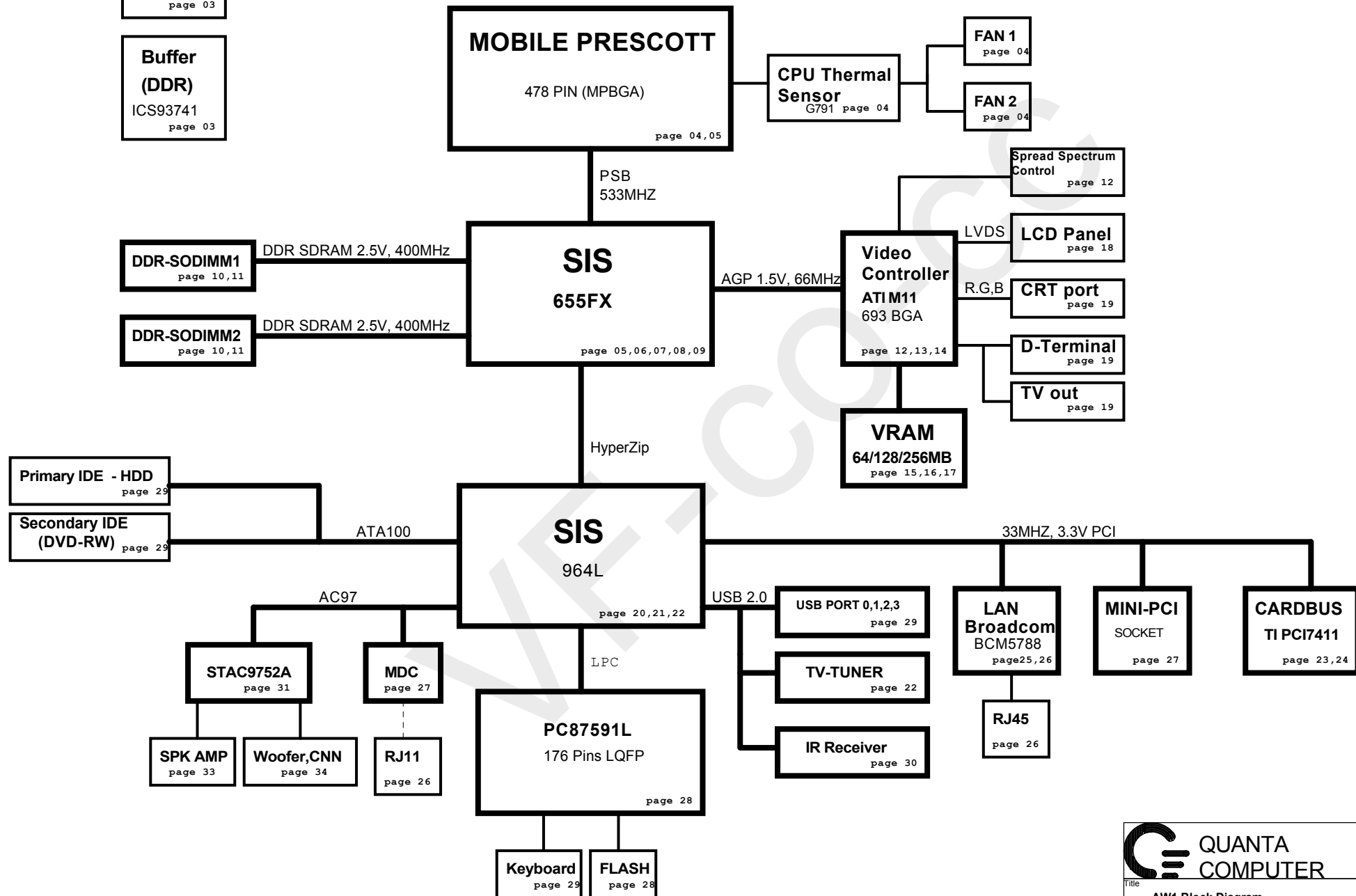


AW1 BLOCK DIAGRAM

01

Mobile Prescott & SIS655FX



01--BLOCK DIAGRAM
 02--Schematic Sheet Number
 03--CLOCK GENERATOR
 04--Mobile Prescott CPU-1
 05--Mobile Prescott CPU-2
 06--NB(655FX HOST & AGP)
 07--NB(655FX UMCA&B)
 08--NB(655FX LINK)
 09--NB(655FX POWER)
 10--SYSTEM DIMM1&2
 11--DDR_TERMINASION
 12--VGA(M11-P AGP INTERFACE)
 13--VGA(M11-P MEM A&B)
 14--VGA(M11-P PWR&GROUN)
 15--VGA(VIDEO MEM A)
 16--VGA(VIDEO MEM B)
 17--VGA(TERMINATION & VGA ROM)
 18--PANEL I/F,LED
 19--CRT&S-VIDEO
 20--964L-1 PCI/IDE/Link.SCH
 21--964L-2 LPC/MII/CPU/GPIO.SCH
 22--964L-3 USB/SATA.SCH
 23--PCI7411 PCMCIA CONTROLLER
 24--PCMCIA CNN & 3 IN 1
 25--LAN (BCM5788-10/100/GIGA)

26--LAN (TR & CONNECTOR)
 27--MINI-PCI/MDC
 28--PCU-87591 & FLASH
 29--K/B,USB,HDD,CDROM
 30--RTC,BEEP,T/P,BUTTON,RECEIVE
 31--AUDIO(STAC9752A,MIC CON)
 32--AUDIO(AMP & POWER & HP CON)
 33--AUDIO (Power AMP, SP CON)
 34--AUDIO (Sub Woofer AMP)
 35--1.5V/1.8V/2.5V & DDRVTT
 36--VGA POWER
 37--DC/DC 3V/5V
 38--BATTERY CHARGER
 39--BATTERY CONNECTOR
 40--CPU POWER-1
 41--CPU POWER-2

2. PCI & IRQ & DMA Description :

IDSEL	CHIP
AD17	Mini PCI Wireless LAN
AD23	R5C551(1394&CardBus)
AD21	Lan/ Broadcom 4401/5788

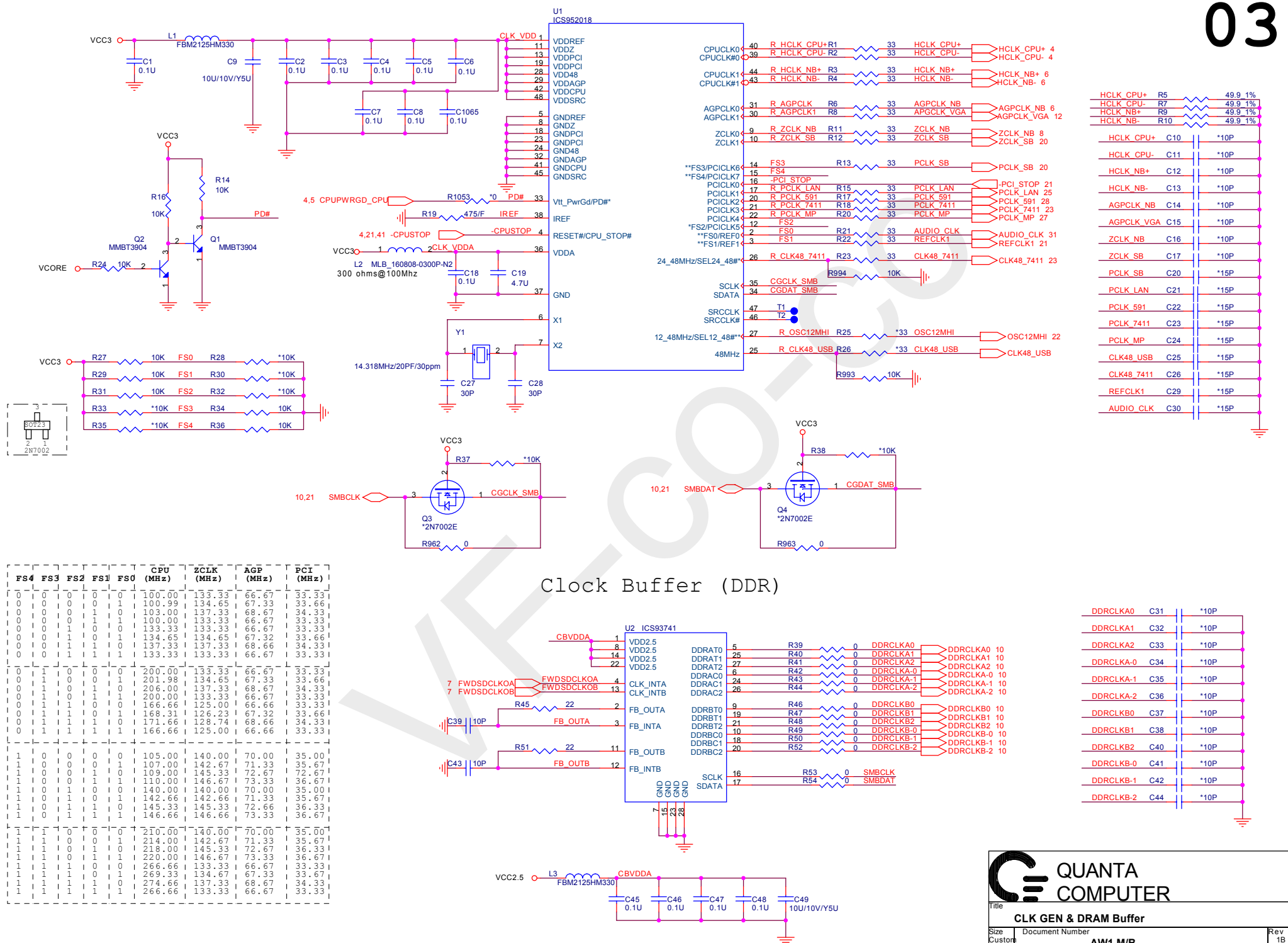
PCIINT	CHIP
IRQA	MiniPCI / CardBus
IRQB	MiniPCI / IEEE1394/ AC97
IRQC	Intel 82540EM
IRQD	ICH4-M Embedded USB2.0

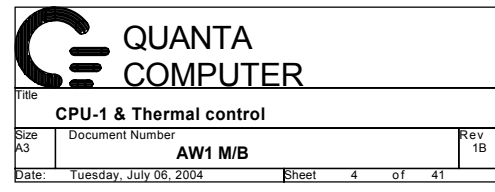
BUSMASTER

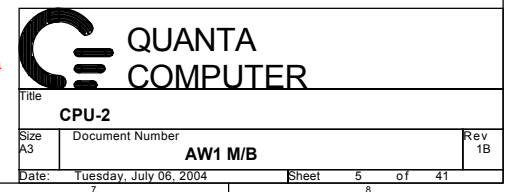
REQ	CHIP
REQ0 / GNT0	Mini PCI Lan
REQ1 / GNT1	R5C551(1394&CardBus)
REQ2 / GNT2	Mini PCI Modem
REQ3 / GNT3	
REQ4 / GNT4	Intel 82540EM

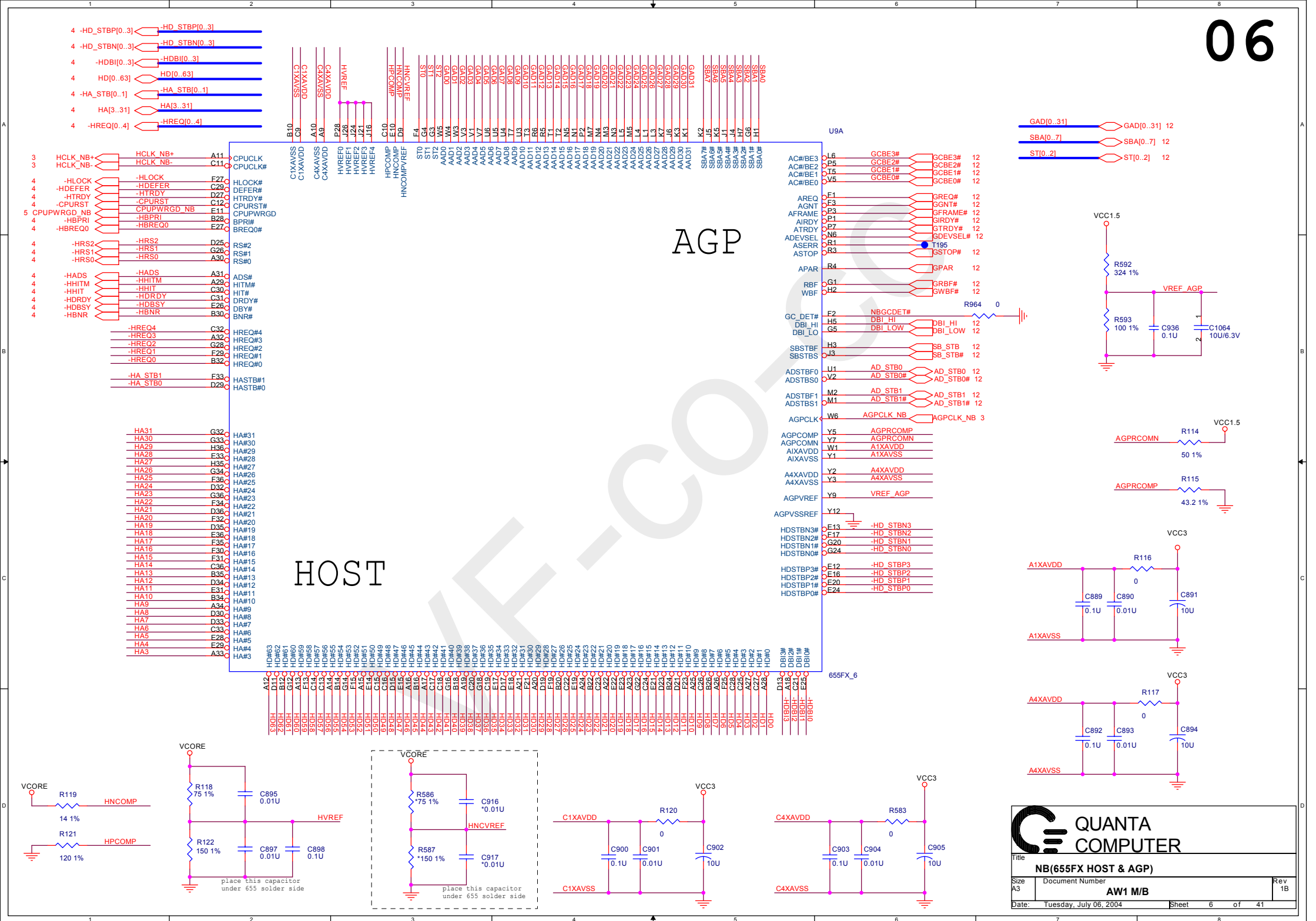
IRQ Channel	Description
IRQ0	System timer
IRQ1	Keyboard
IRQ2	(Casacde)
IRQ3	LAN / MODEM
IRQ4	Serial Port
IRQ5	AUDIO / VGA / USB
IRQ6	FLOPPY DISK
IRQ7	LPT
IRQ8	RTC
IRQ9	ACPI
IRQ10	FIR (Disable by default) (MODEM/LAN)
IRQ11	Cardbus
IRQ12	PS/2 mouse
IRQ13	FPU
IRQ14	HDD
IRQ15	CDROM

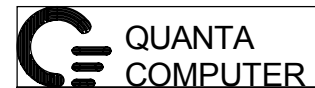
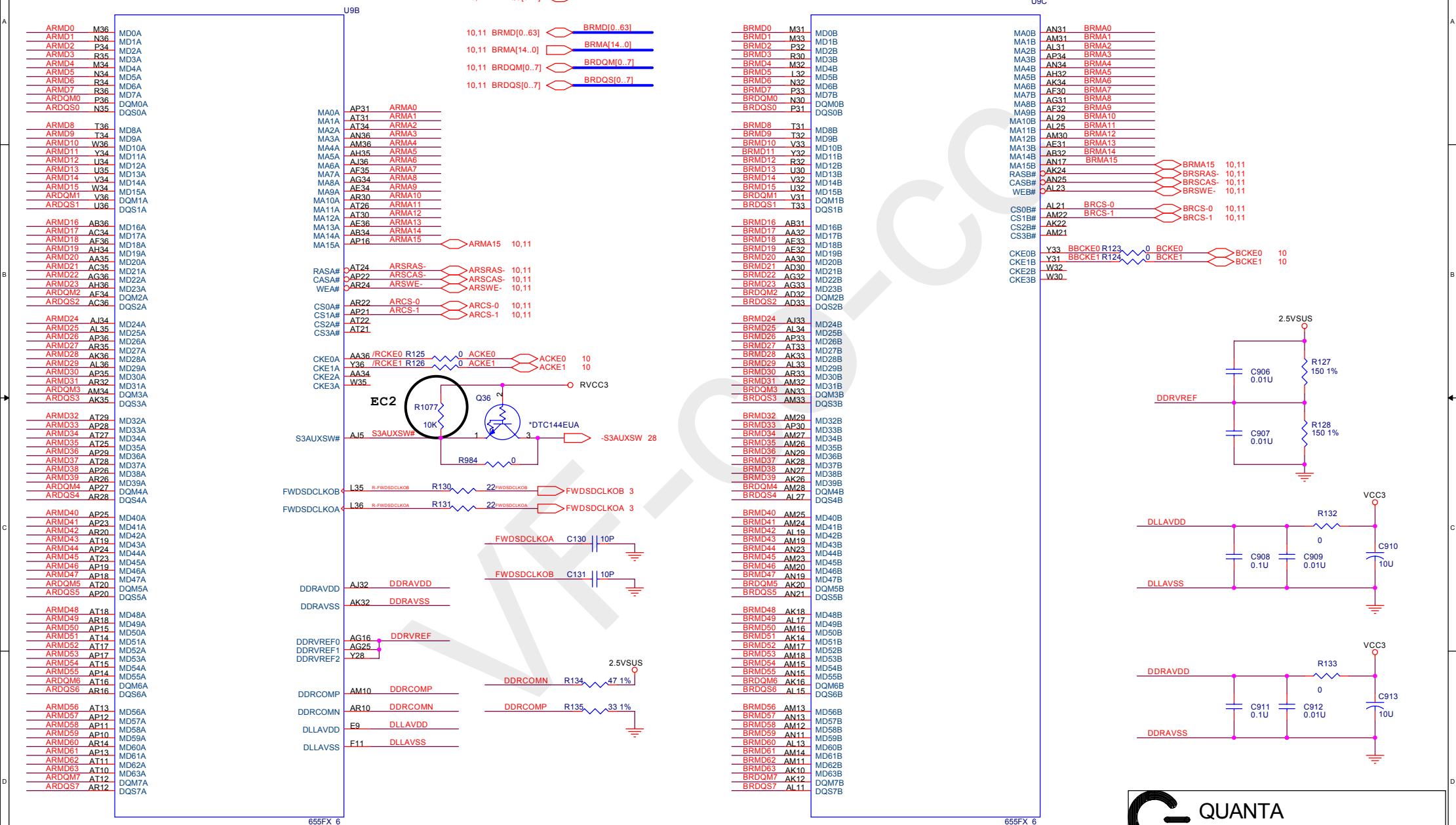
DMA Channel	Device
DMA0	FIR (disable by default) (MODEM / LAN)
DMA1	ECP
DMA2	FLOPPY DISK
DMA3	AUDIO
DMA4	(Cascade)
DMA5	Unused
DMA6	Unused
DMA7	Unused

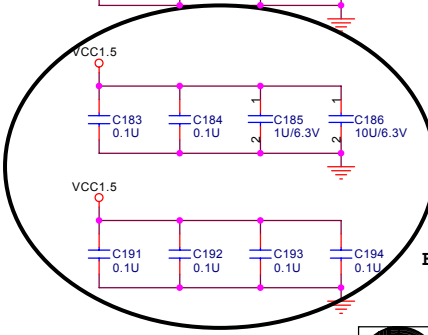
















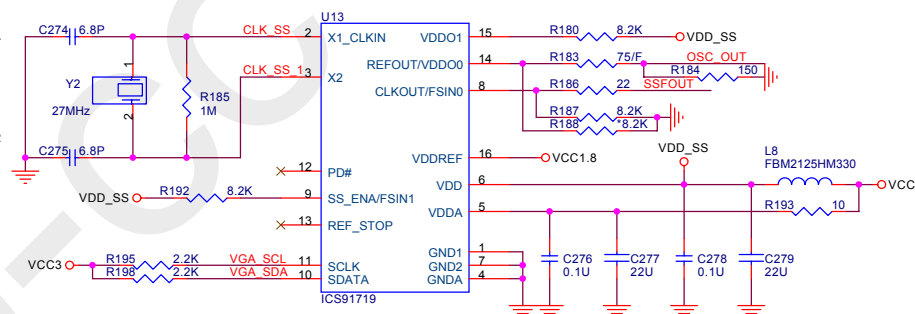
Strapping

```
VMEM 64/128 MB no stuff
```

GPI01	GPI00	AGP 1X CLOCK FEEDBACK PHASE ADJUST WITH RESPECT TO REFCLK
LO	LO	REFCLK SLIGHTLY EARLIER THAN FEEDBACK (DEFAULT)

GPIO3	GPIO2	CLOCK PHASE ADJUSTMENT BETWEEN X1 AND X2 CLK
LO	LO	0 TAP DELAY (DEFAULT)

Spread Spectrum Control

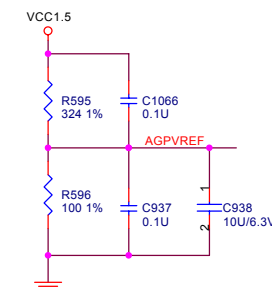


INPUT SELECT FUNCTIONALITY

PIN14	PIN15	REF VOLTAGE
0	1	1.8V

REF VOLTAGE SELECT FUNCTIONALITY

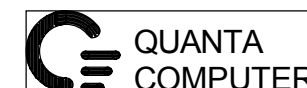
FS_IN1	FS_IN0	MHZ
1	0	27.00 IN/OUT



LOCATION	R953,R954,R955,R956,R957,R958,R959,R960,R961,R962,R963,R964,R965,R966,R967,R968,R969,R970,R971,R972,R973,R974,R975,R976,R977,R978,R979,R980,R981,R982,R983,R984,R985,R986,R987,R988,R989,R990,R991,R992,R993,R994,R995,R996,R997,R998,R999,R1000
VRAM SIZE	U23,C522,R304,R307,R310,R311,R312,R313,R314,R315,R316,R317,R318,R319,R320,R321,R322,R323,R324,R325,R326,R327,R328,R329,R330,R331,R332,R333,R334,R335,R336,R337,R338,R339,R340,R341,R342,R343,R344,R345,R346,R347,R348,R349,R350,R351,R352,R353,R354,R355,R356,R357,R358,R359,R360,R361,R362,R363,R364,R365,R366,R367,R368,R369,R370,R371,R372,R373,R374,R375,R376,R377,R378,R379,R380,R381,R382,R383,R384,R385,R386,R387,R388,R389,R390,R391,R392,R393,R394,R395,R396,R397,R398,R399,R400
64/128	CANCEL
256	MOUNT

	VRAM1	VRAM0	VRAM		
	0	0	SAMSUNG 4M*32	4pcs	64MB
	1	0	SAMSUNG 8M*32	8pcs	256MB
t	0	1	HYNIX 4M*32	8pcs	128MB
	1	1	HYNIX 8M*32	4pcs	128MB

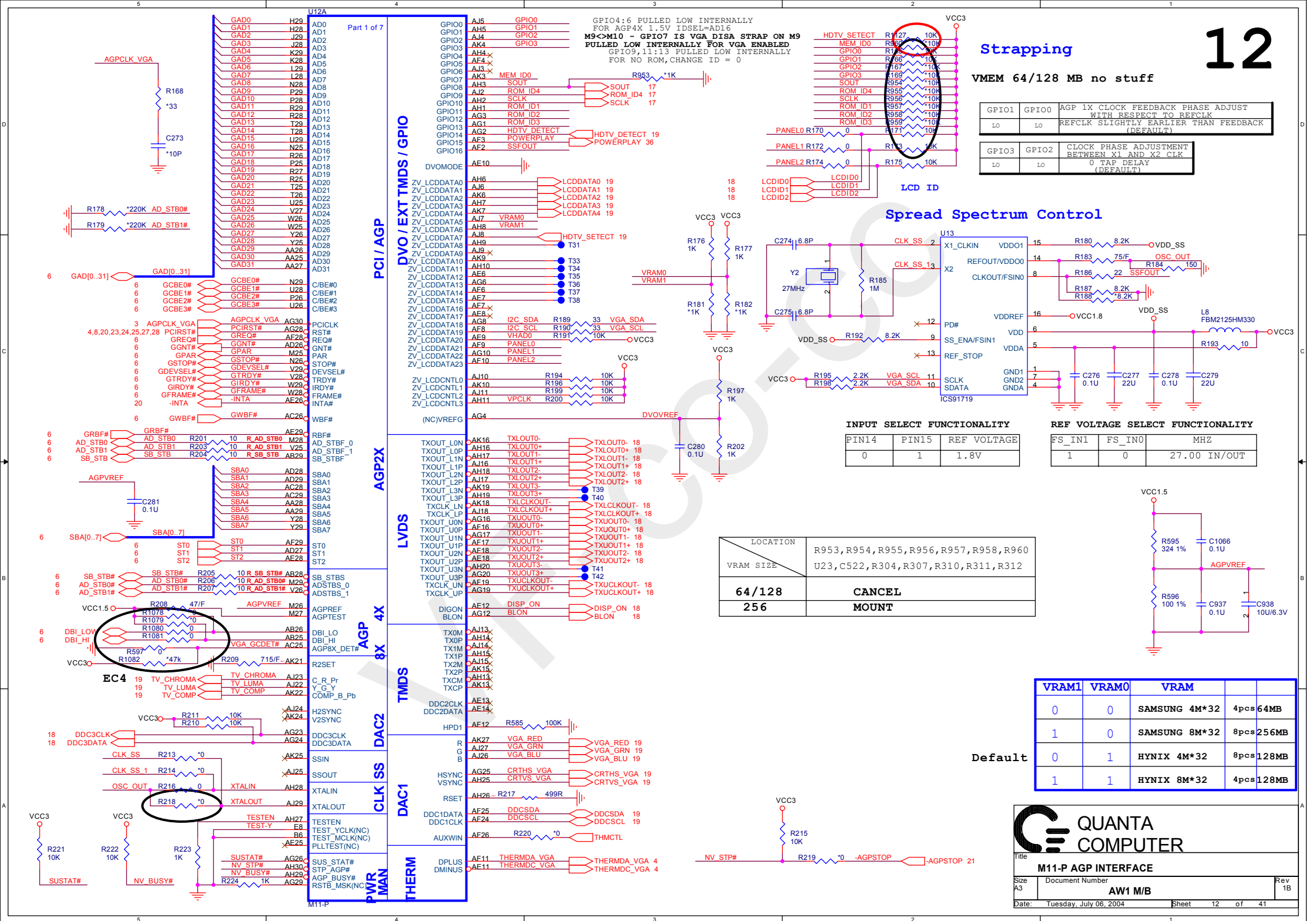
Default

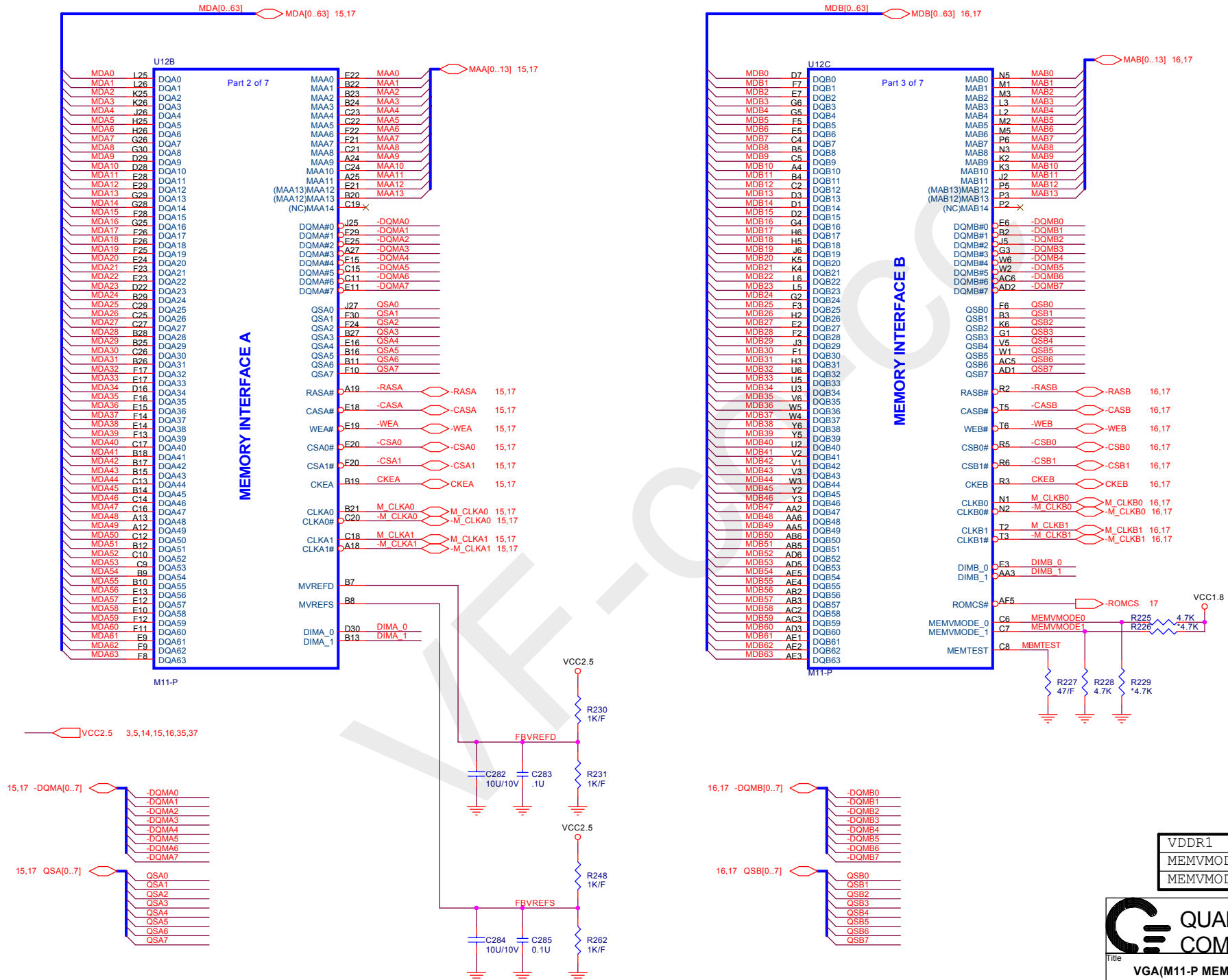


Title

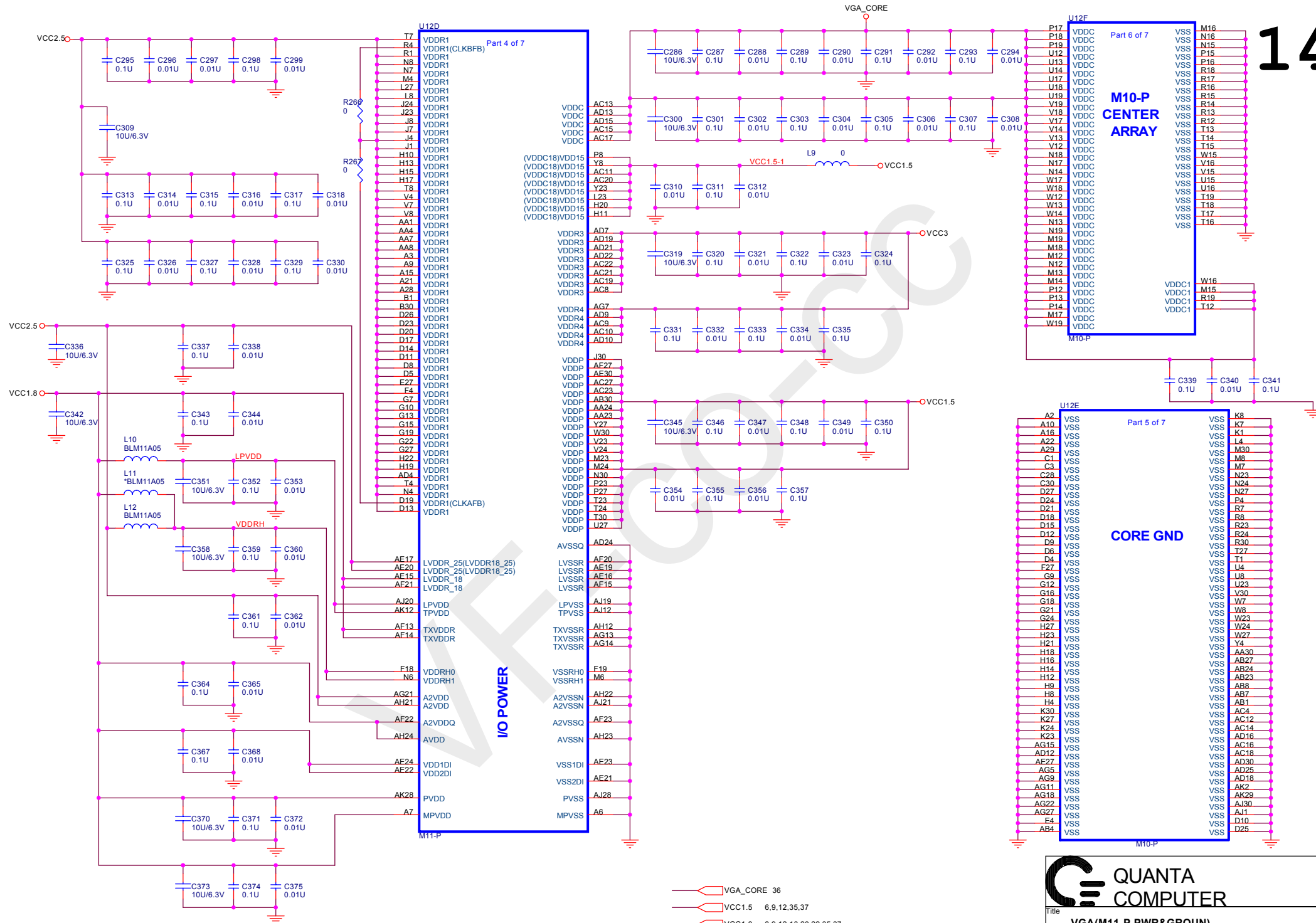
M11-P AGP INTERFACE

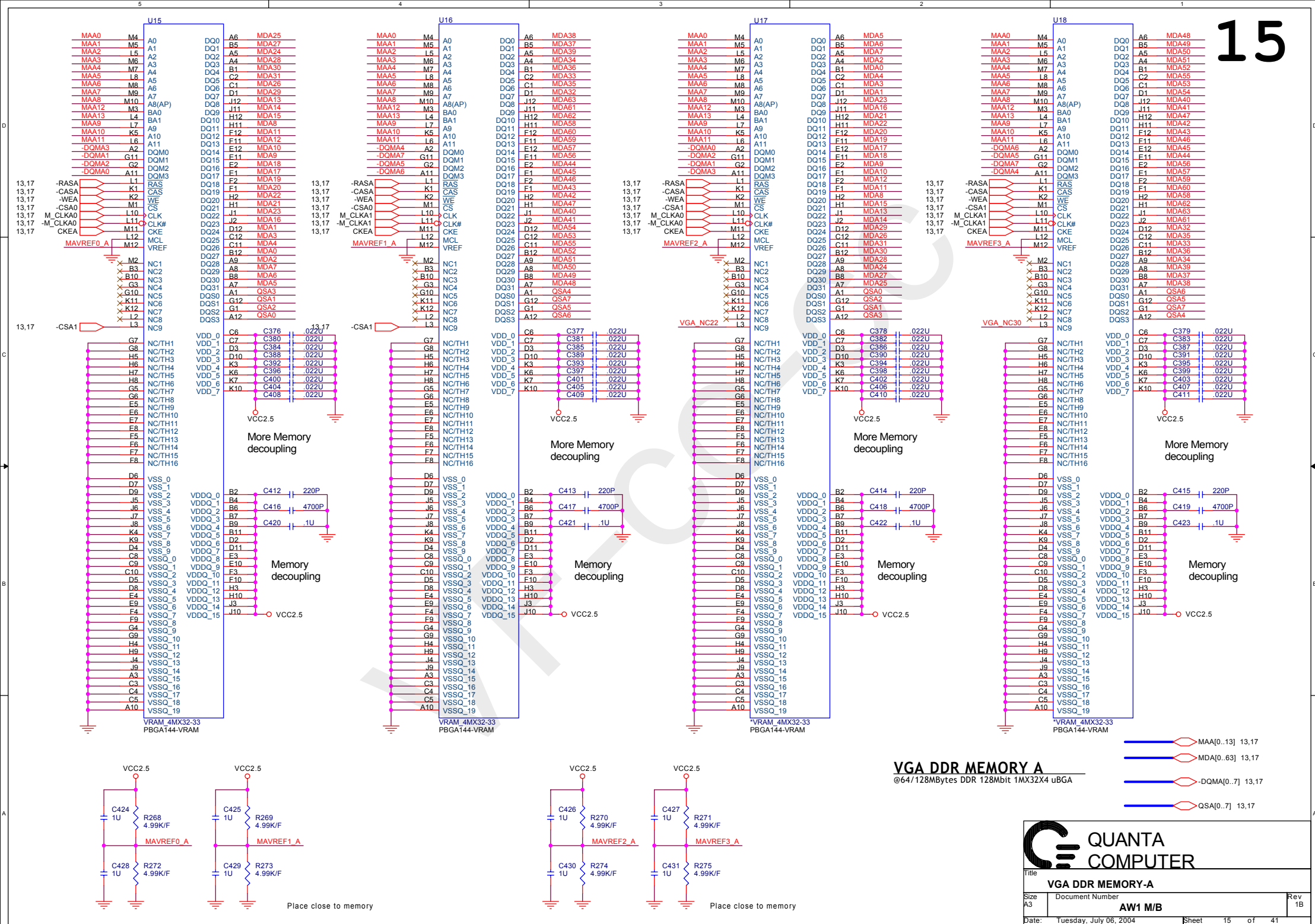
Size A3	Document Number AW1 M/B	Rev 1E
Date: Tuesday, July 06, 2004	Sheet 12 of 41	

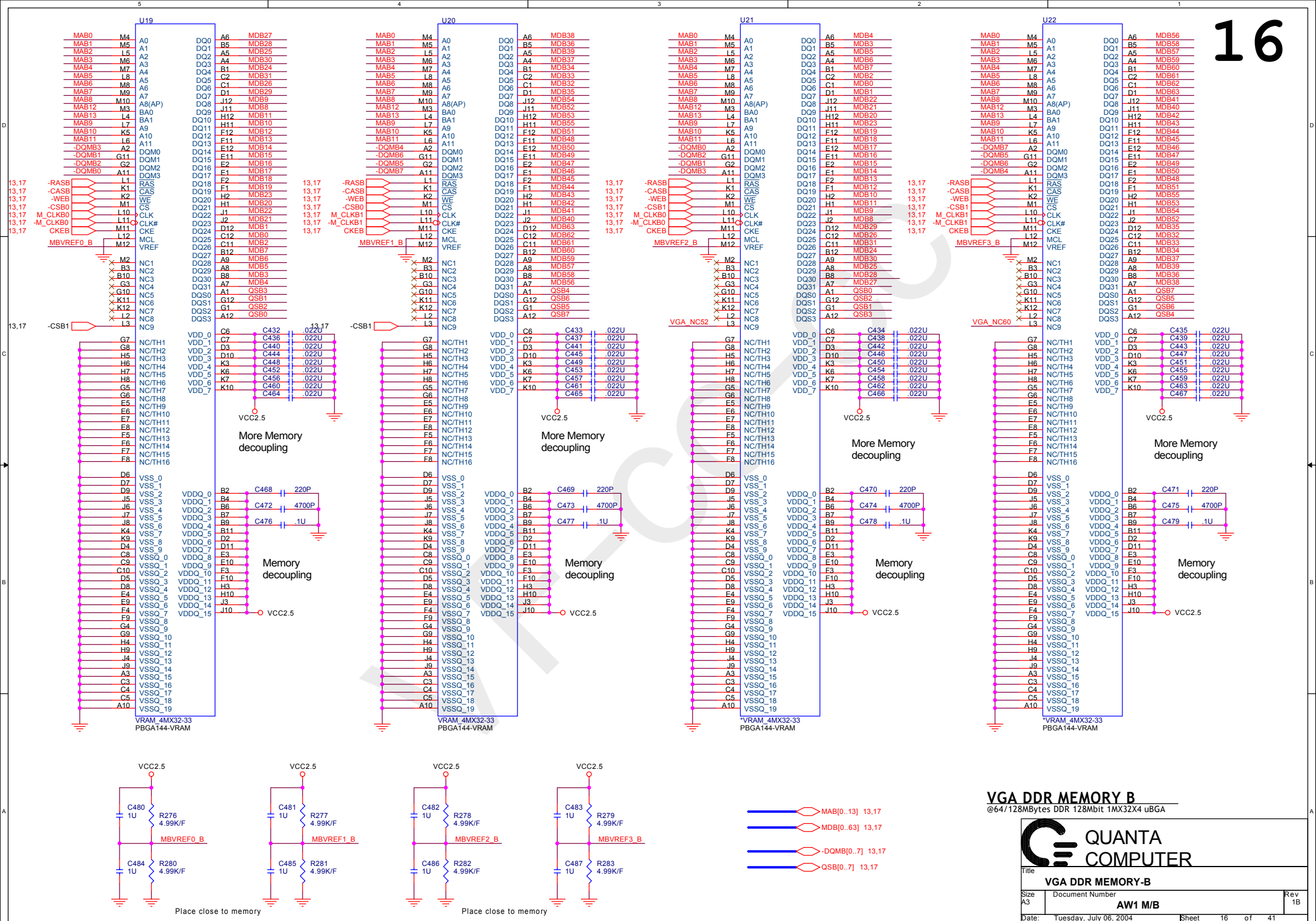




QUANTA
COMPUTER

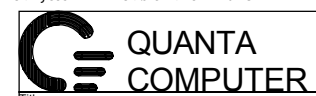




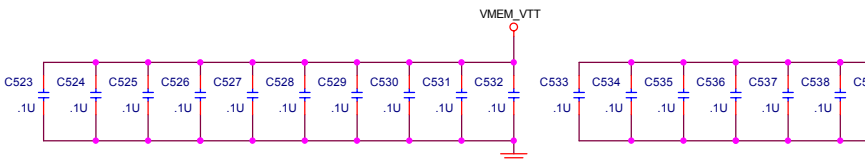


VGA DDR MEMORY B

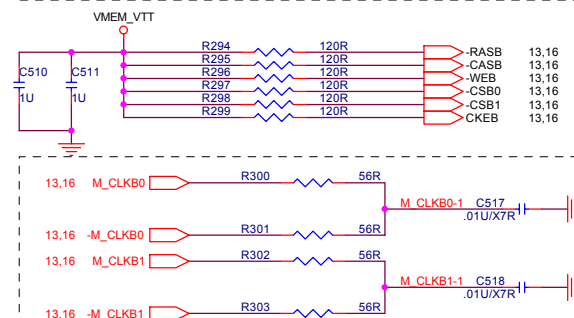
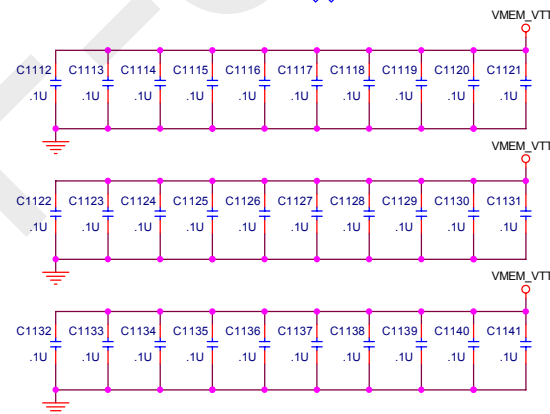
@64/128MBytes DDR 128Mbit 1MX32x4 uBGA



Title			VGA DDR MEMORY-B	
Size	Document Number		Rev	
A3	AW1 M/B		1B	
Date:	Tuesday, July 06, 2004		Sheet	16 of 41

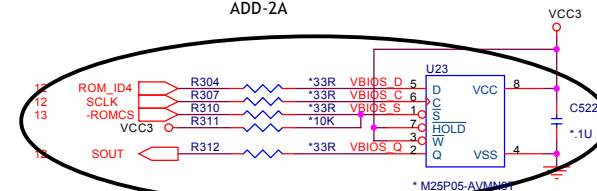


Place at nets mid point



At least a 2.5:1 spacing between the pair
These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory

ADD-2A



* M25P05-AVMN0

```
VMEM 64/128 MB no stuff
```

f SERIAL ROM



QUANTA
COMPUTER

DDR VRAM PULL UP

Size

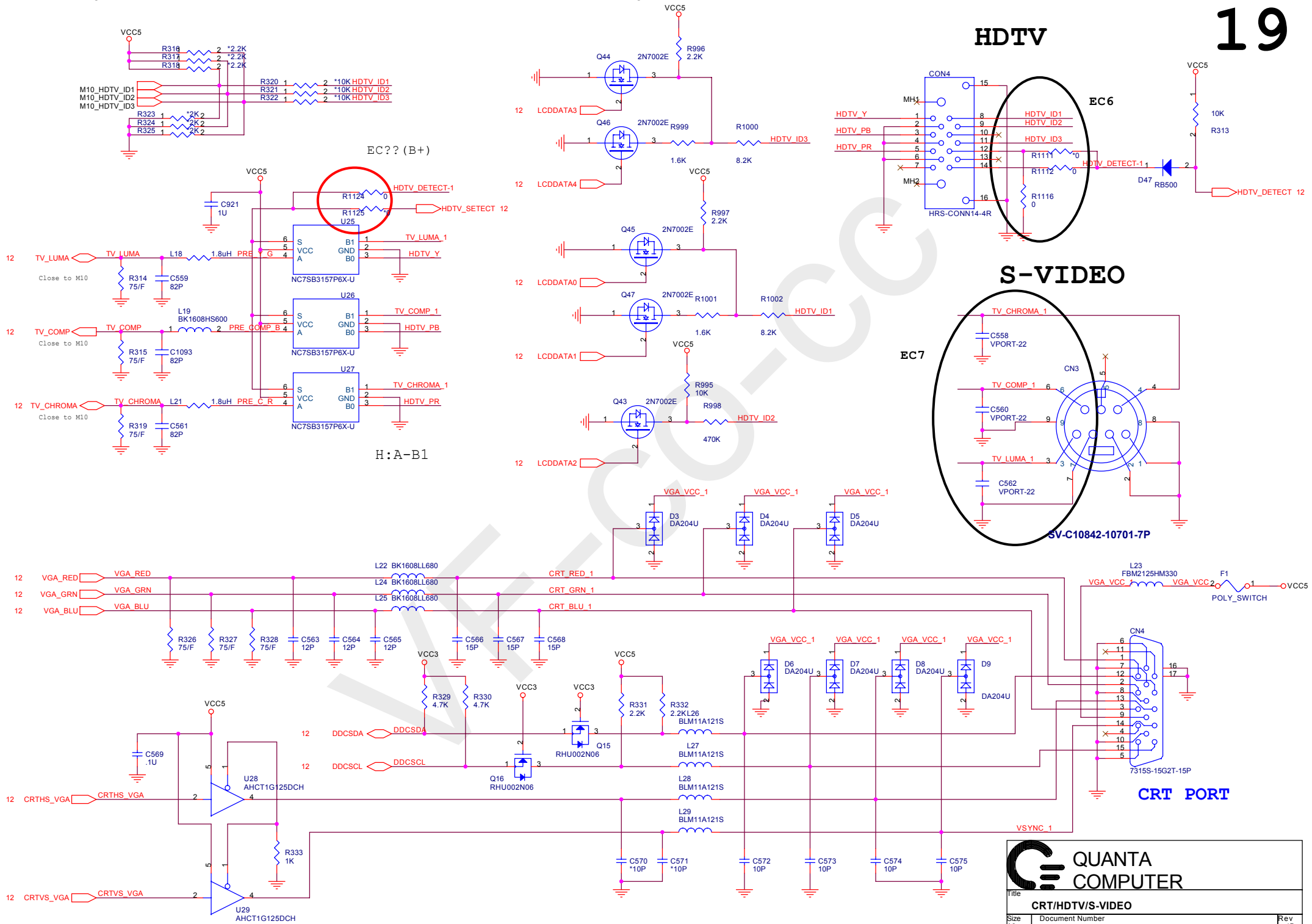
Document Number

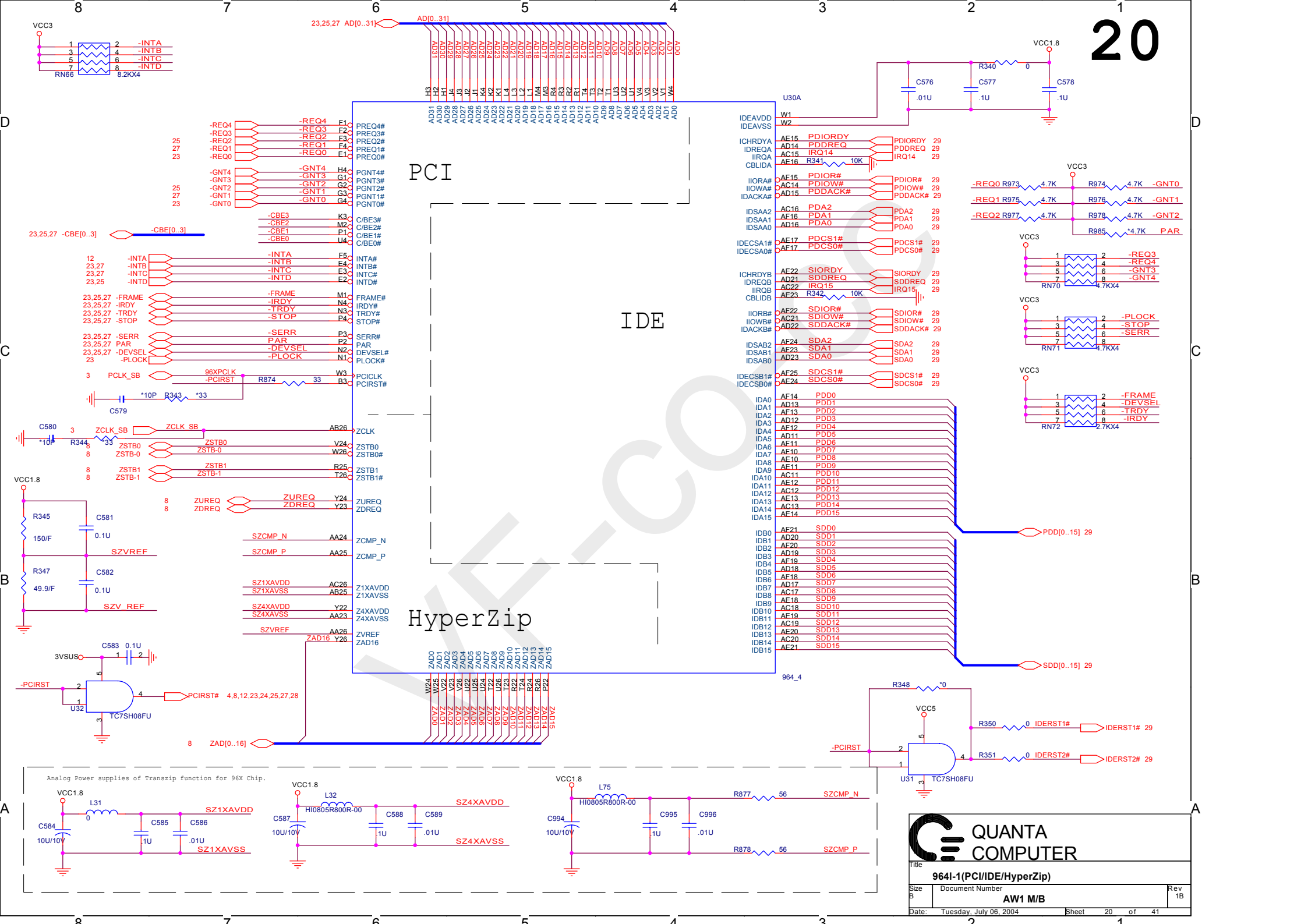
AW1 M/E

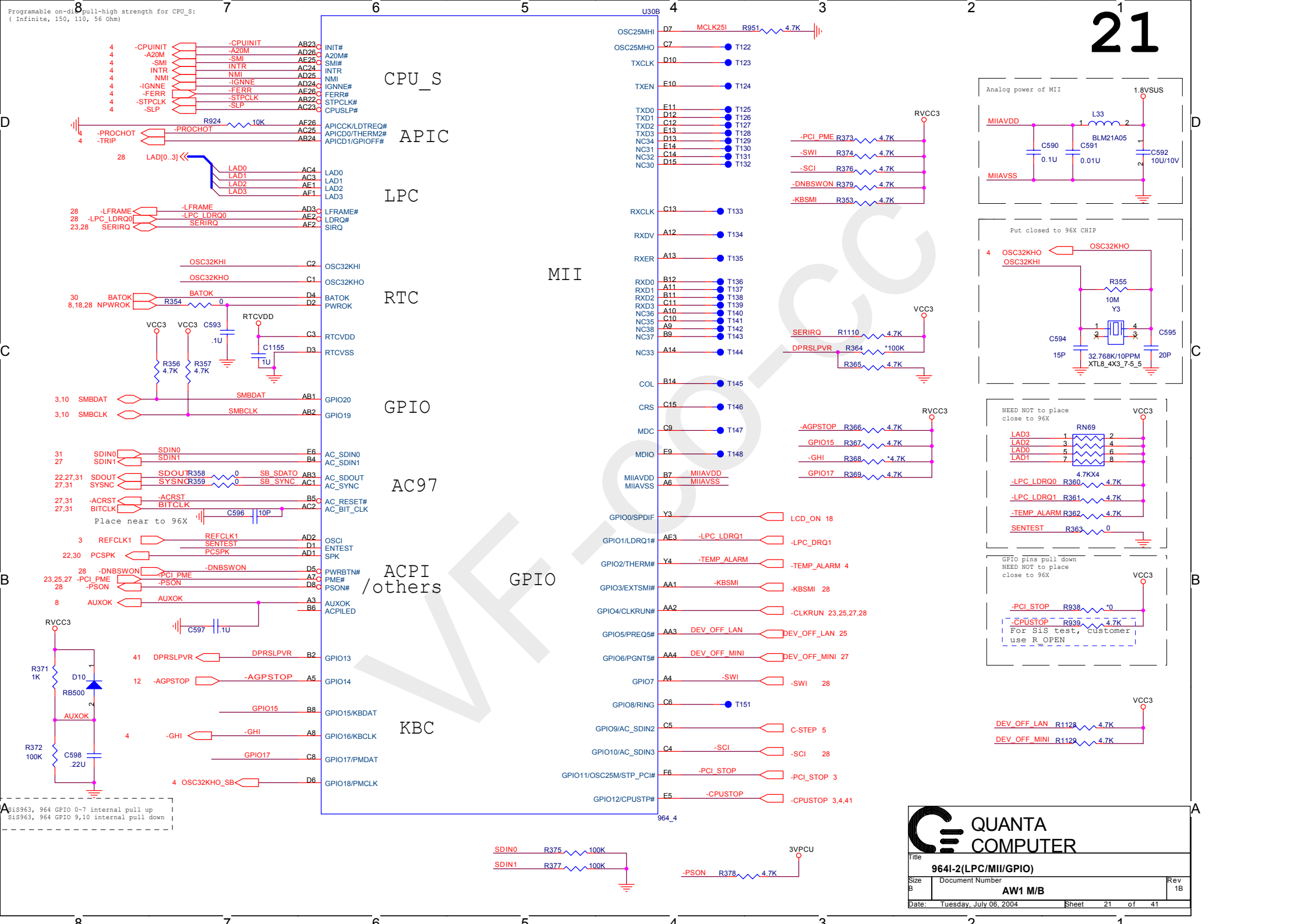
Date: Tuesday, July 06, 2004

She

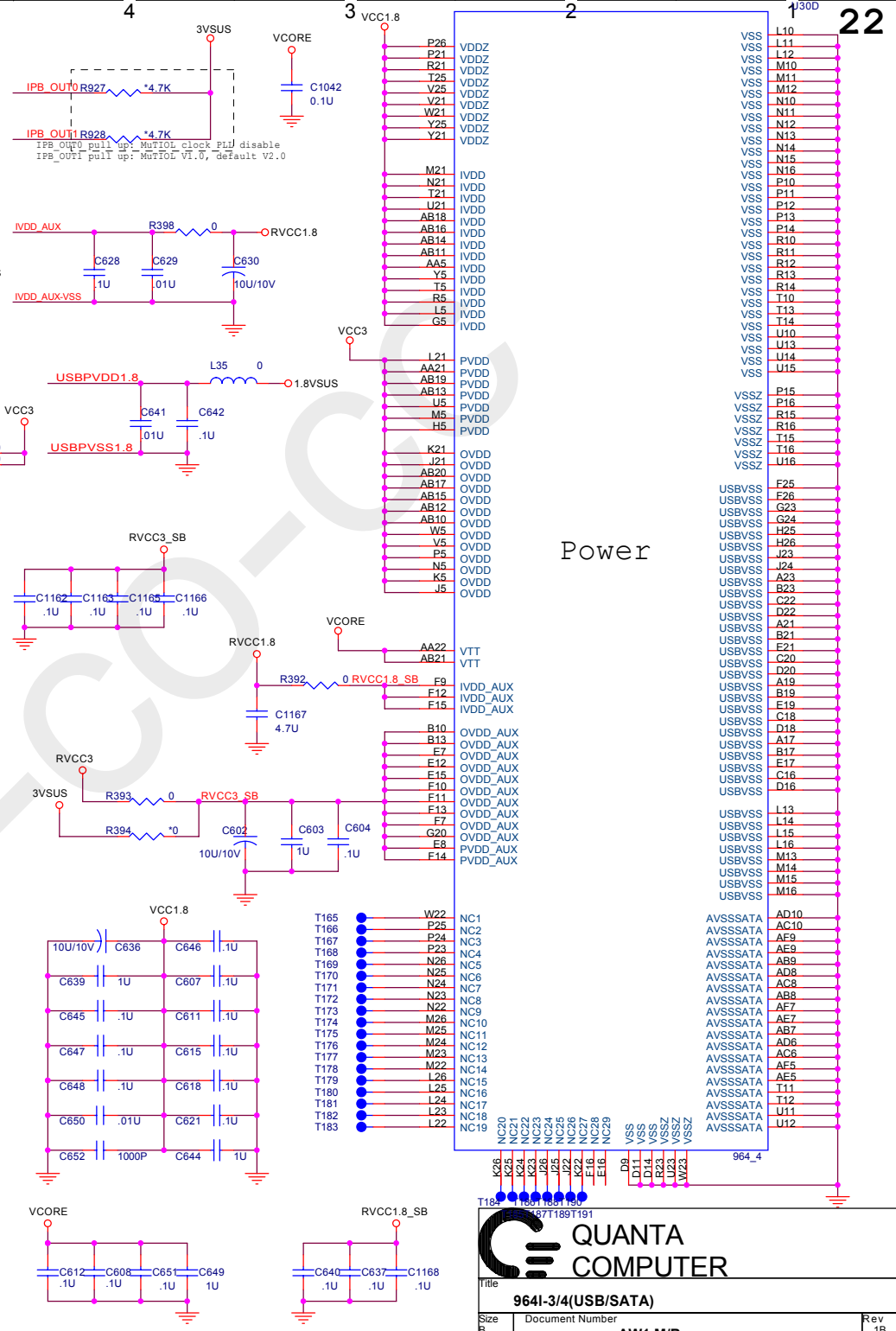
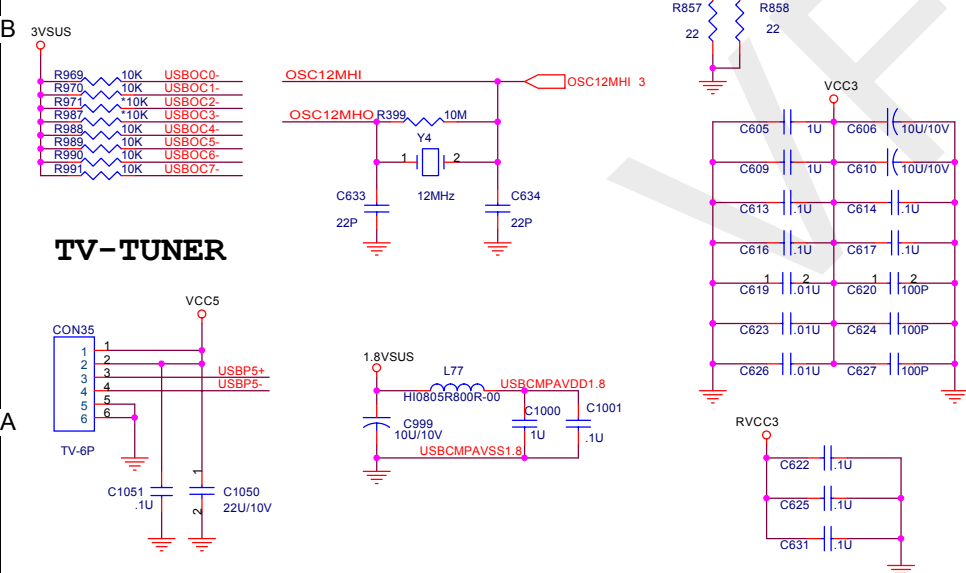
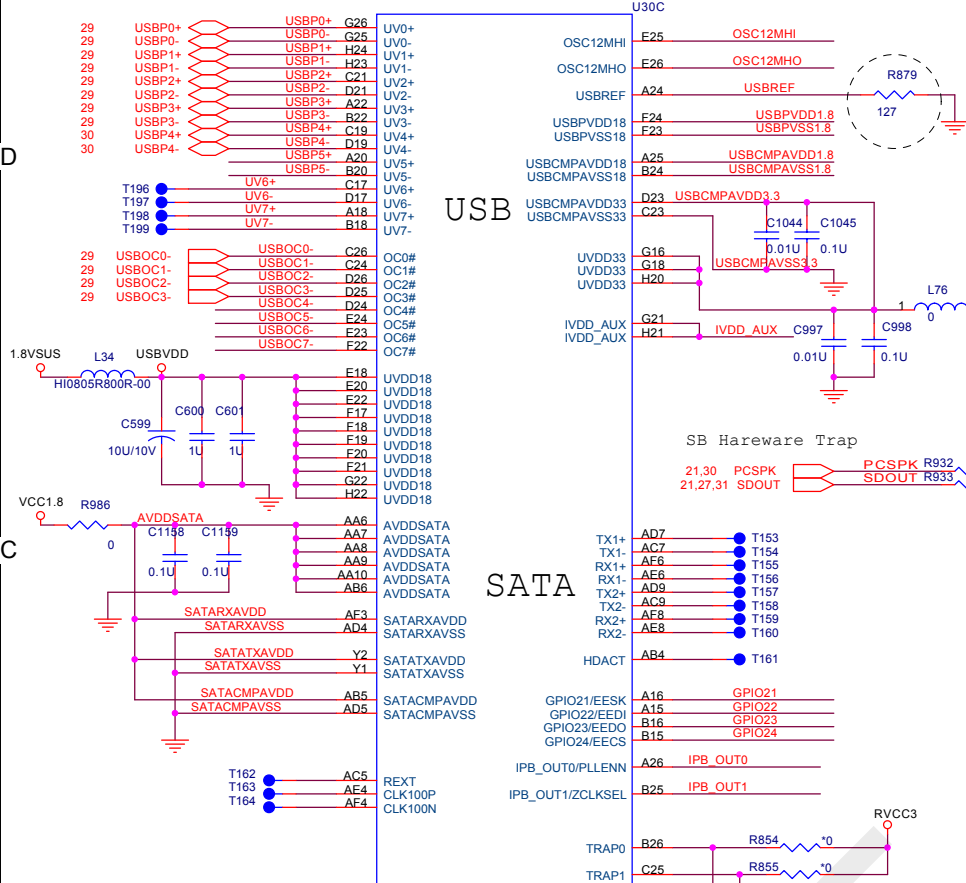
17

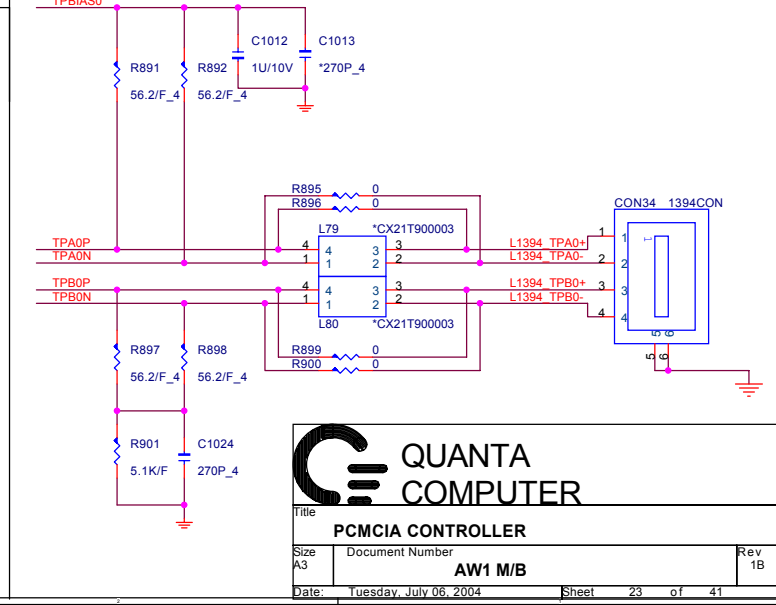
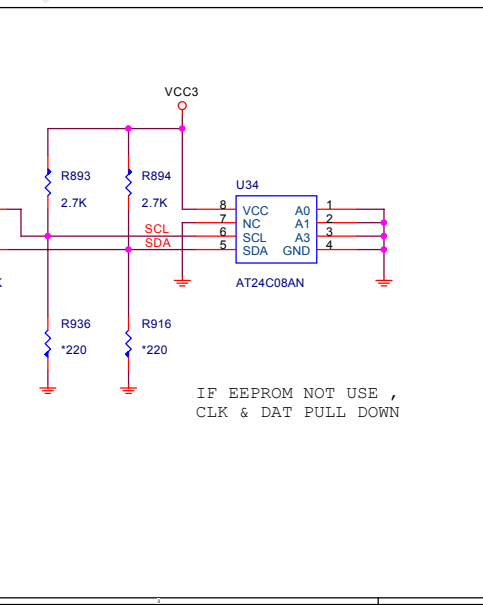
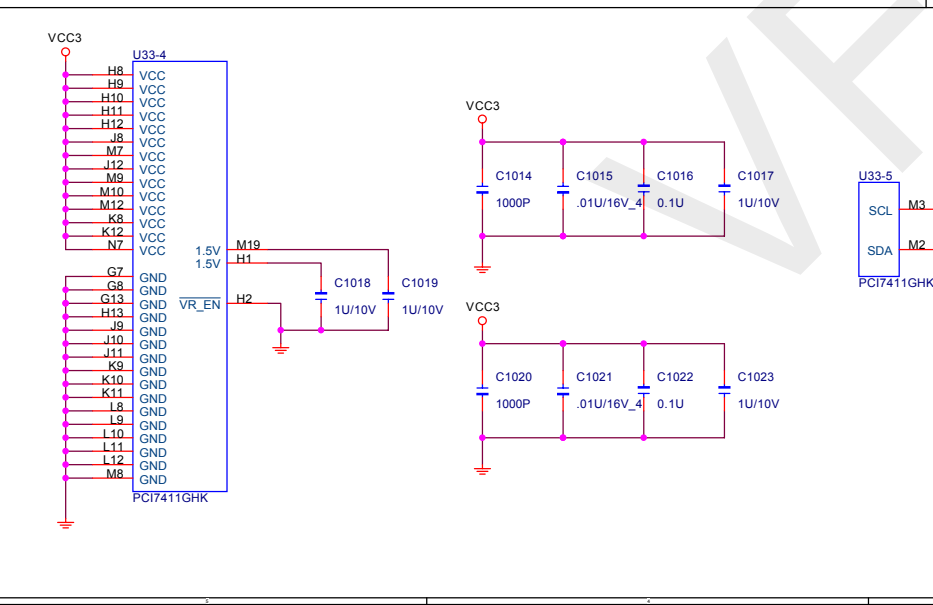
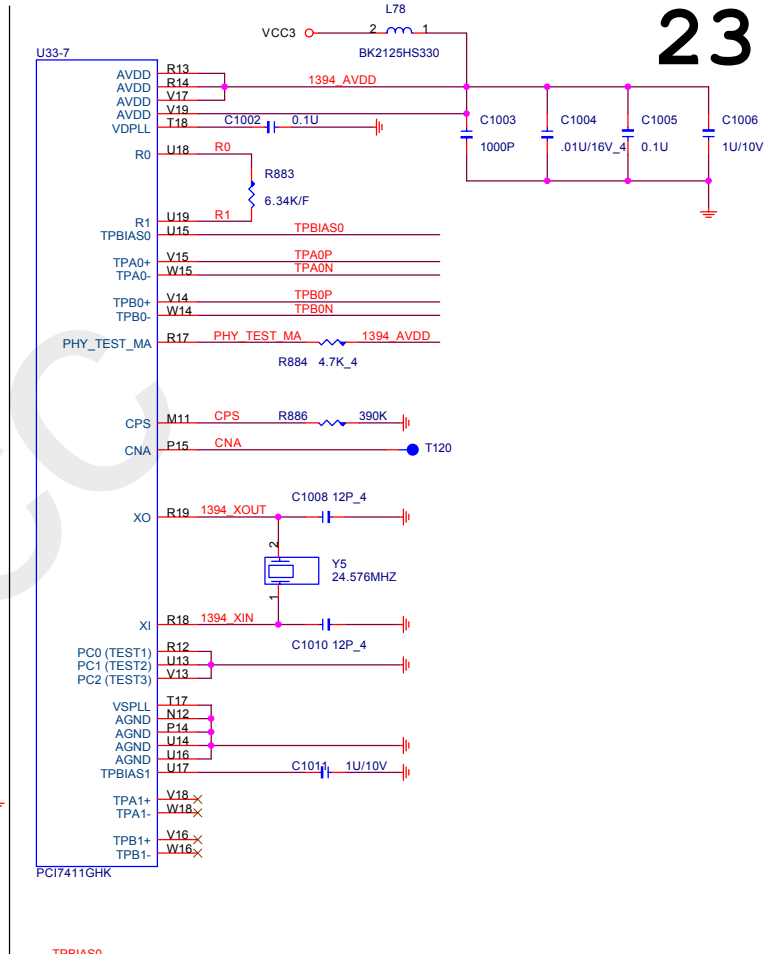
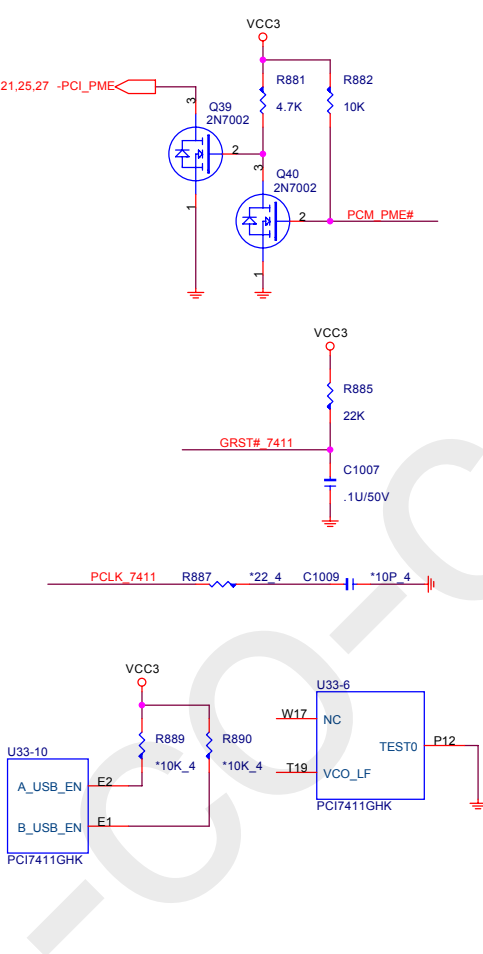
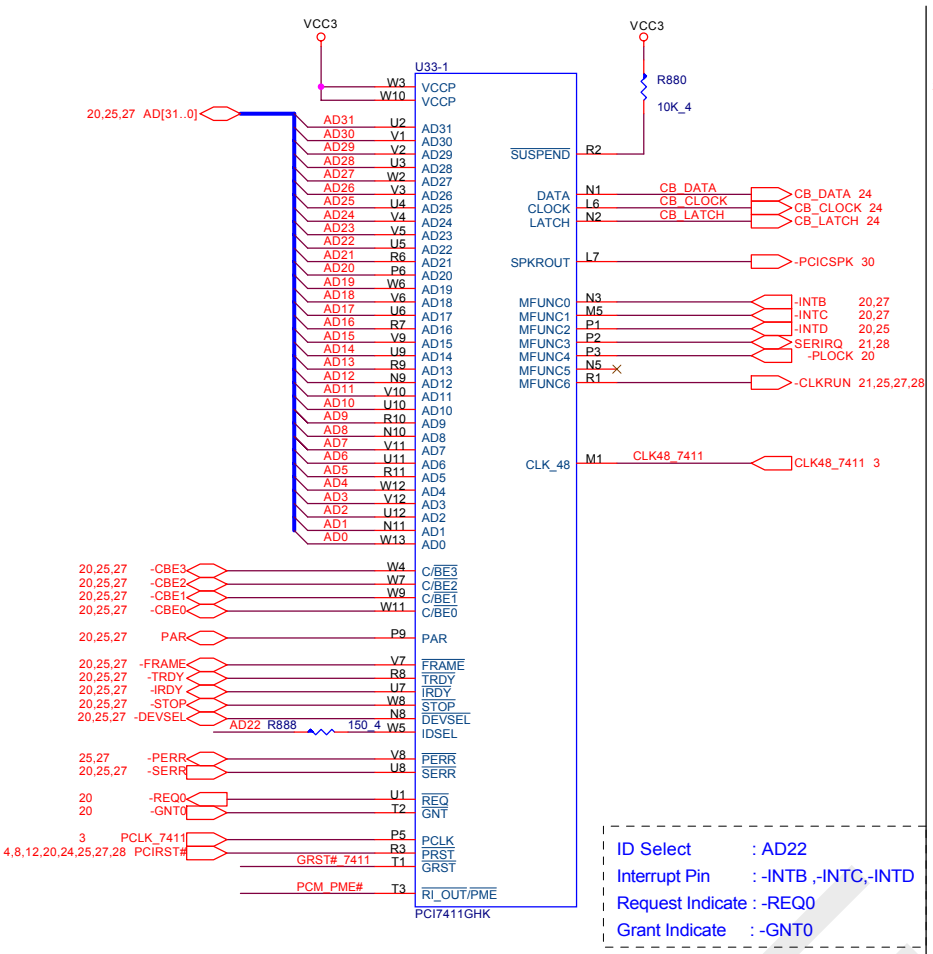


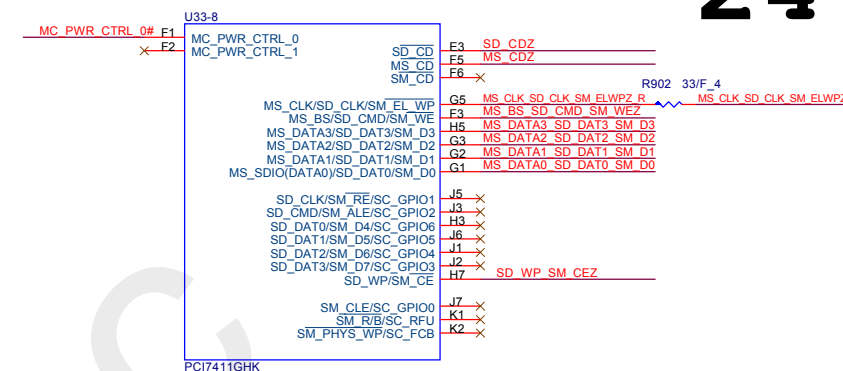
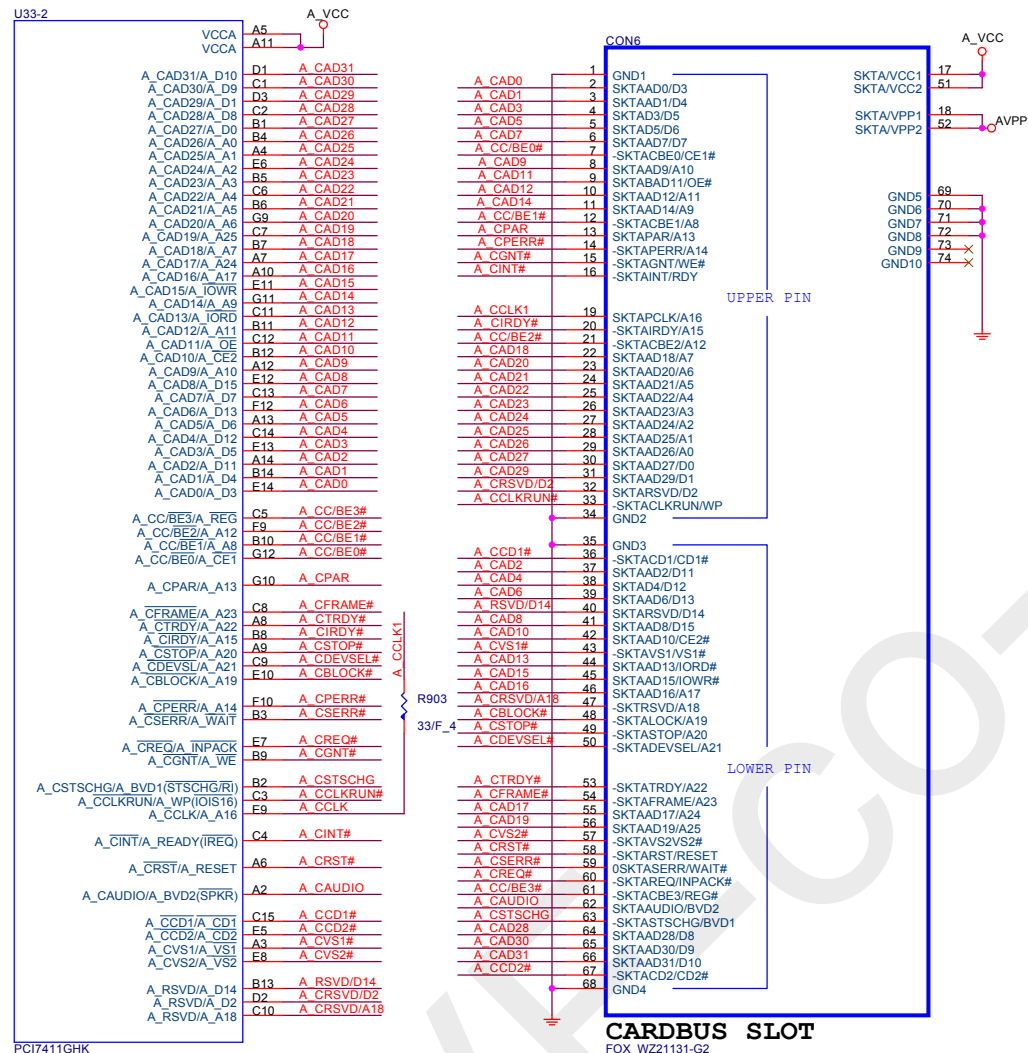




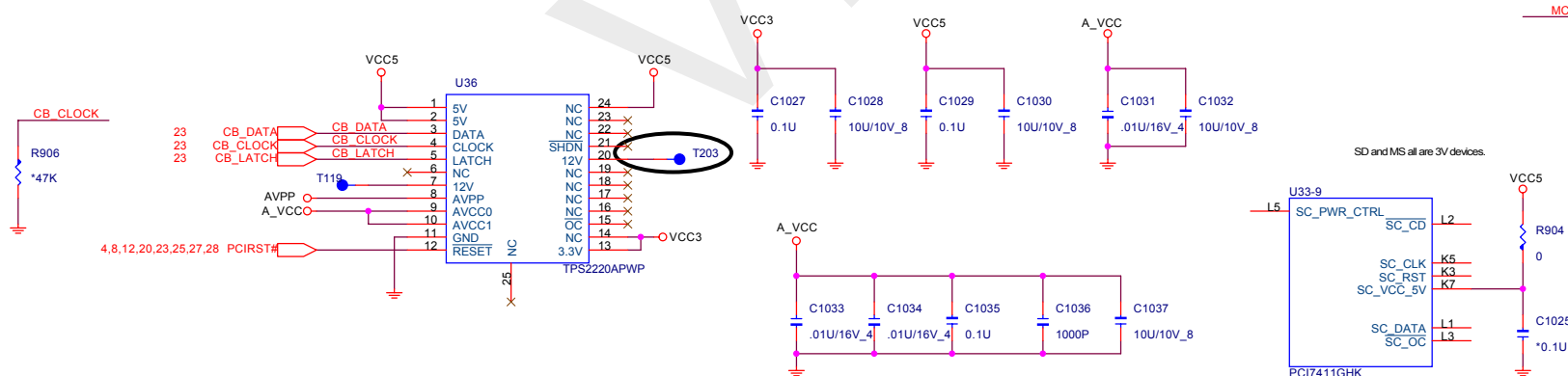
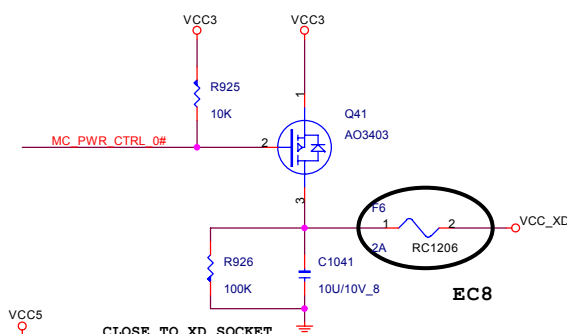
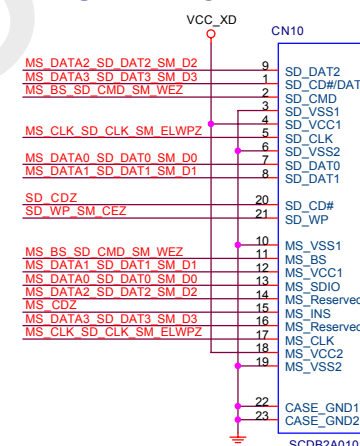
For SiS964L







3 IN 1 CARD READER



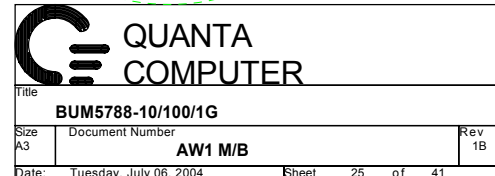
BCM4401--10/100
BCM5788--10/100/GIGA

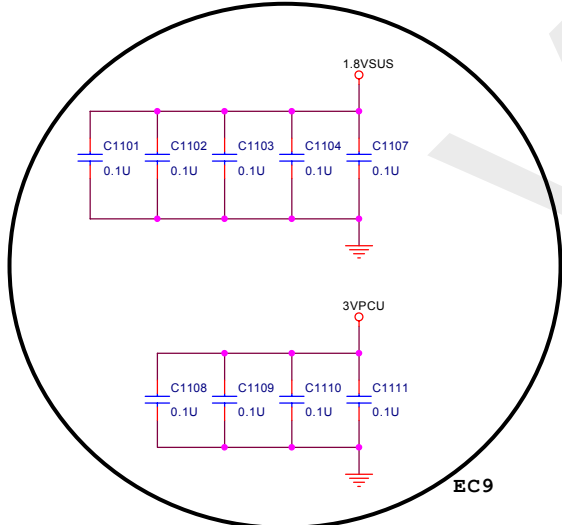
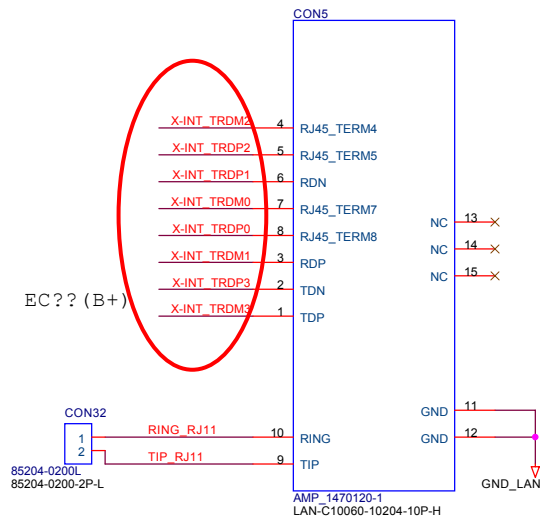
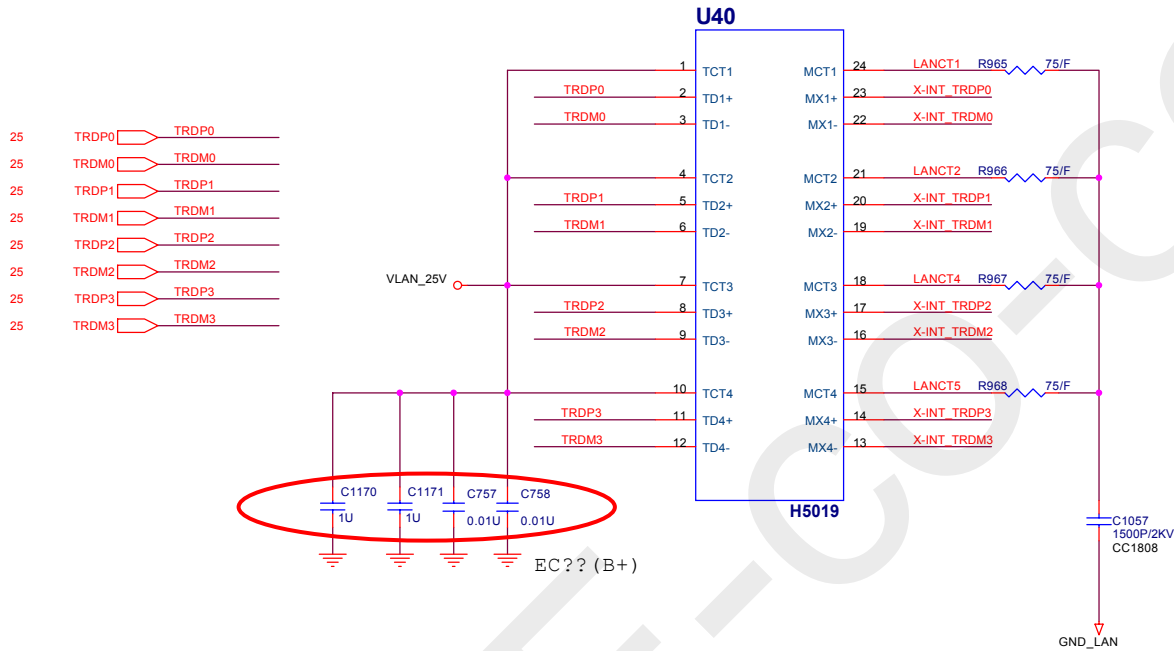
FOR 5788--VCC3
FOR 4401--RVCC3

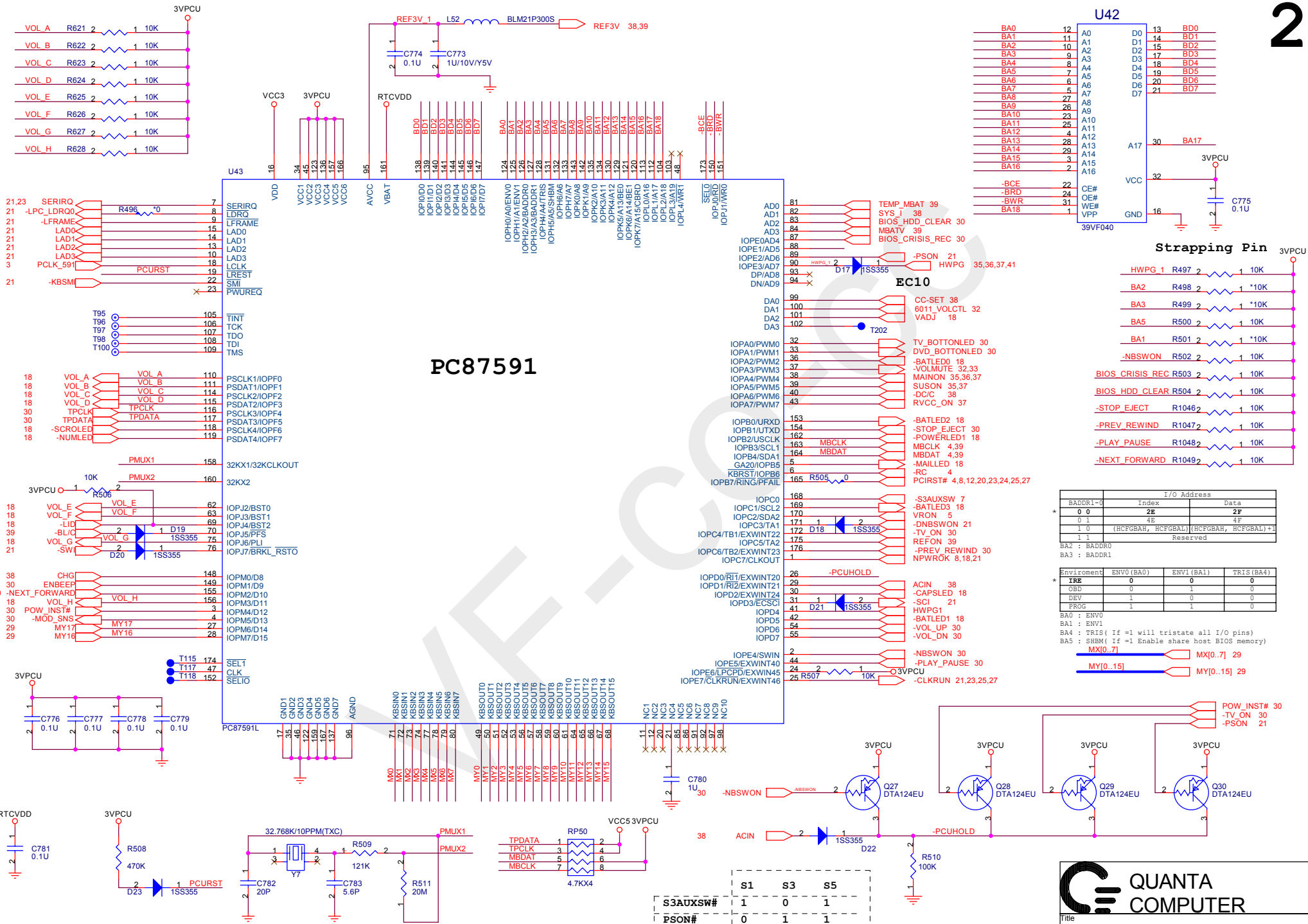
```

ID Select      : AD16
Interrupt Pin   : INTD#
Request indicates : REQ2#
Grant indicates  : GNT2#

```





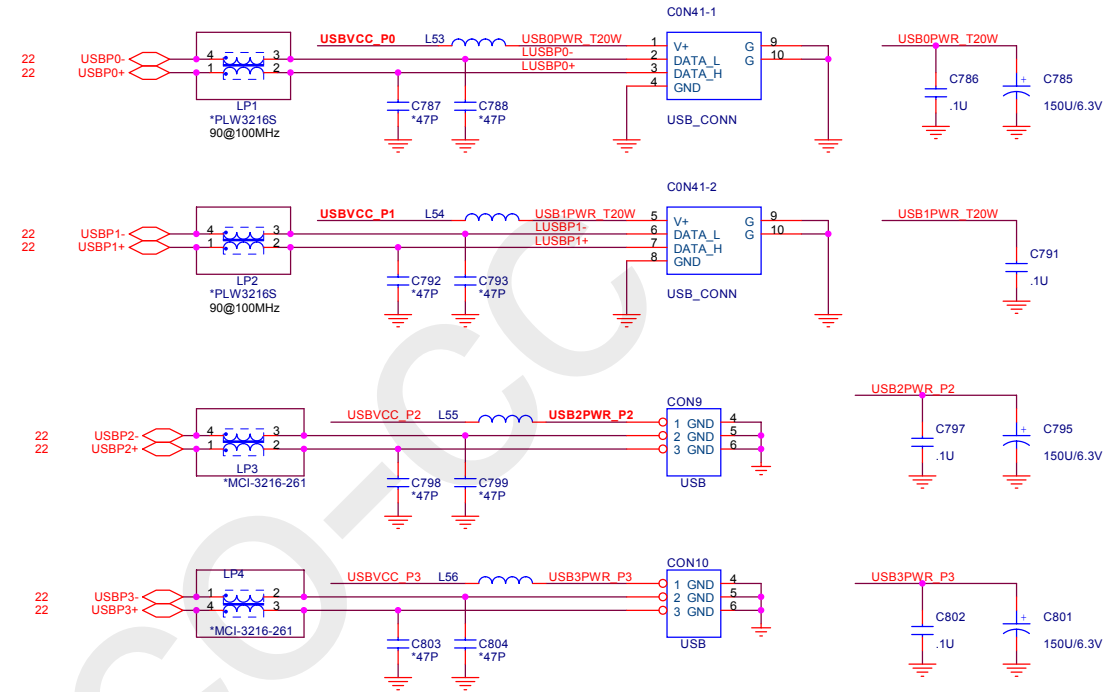
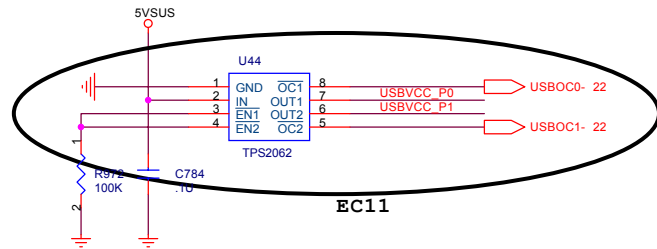


USB Power and Over current

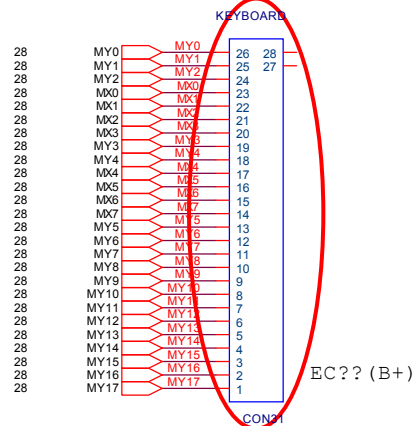
USB Connector

Place near the
USB connector

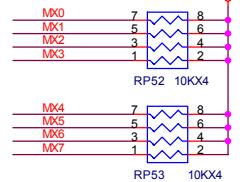
29



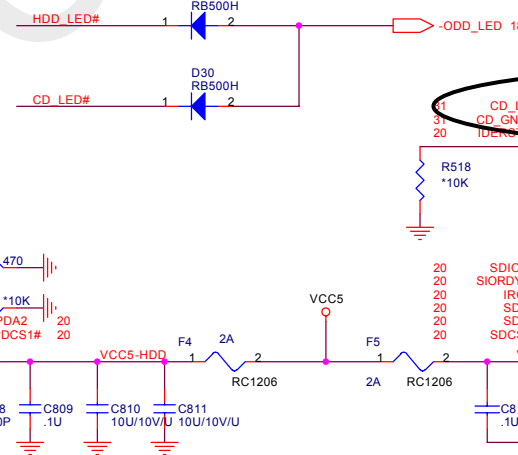
CD-ROM CONNECTOR



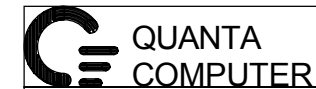
KEYBOARD



HDD CONNECTOR

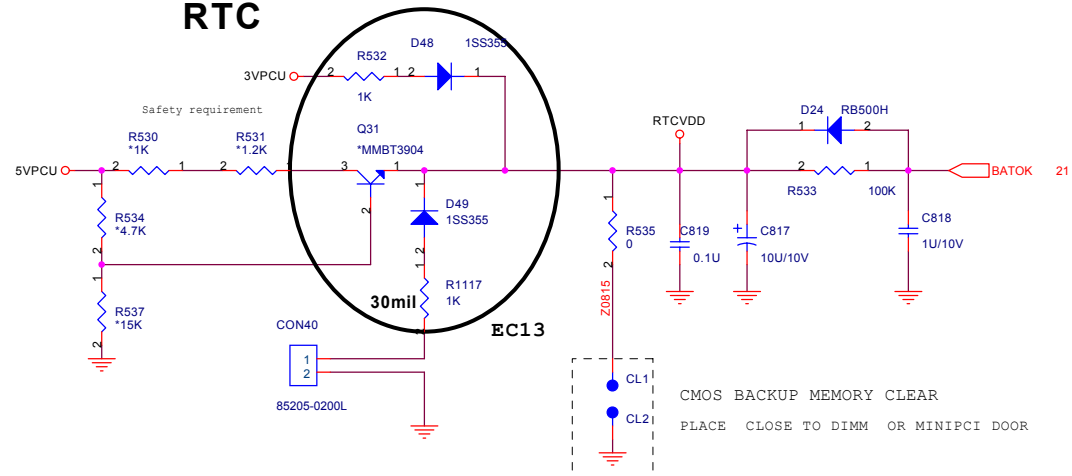


EC12

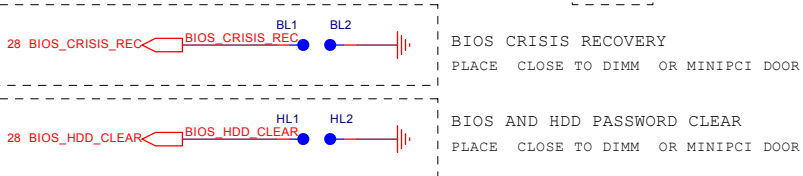


Title			KB/USB/HDD/CDROM	
Size	Document Number		Rev 1B	
A3	AW1 M/B			
Date:	Tuesday, July 06, 2004		Sheet	29 of 41

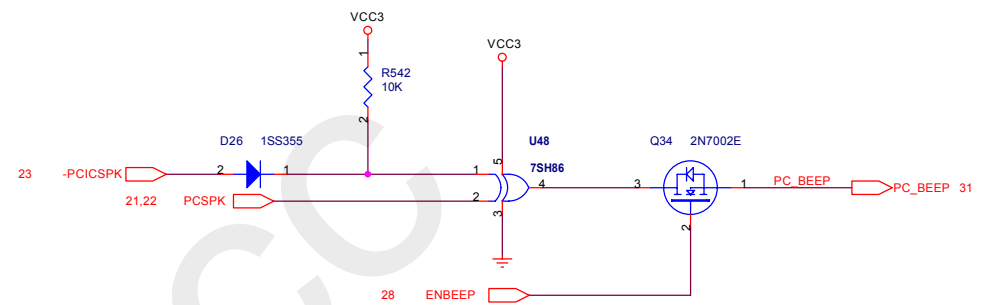
RTC



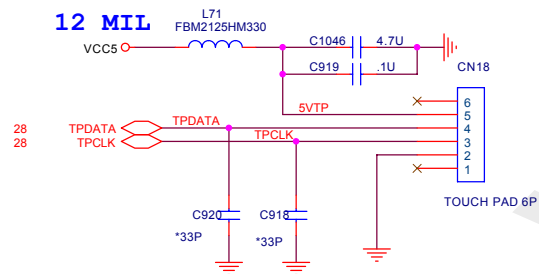
CMOS BACKUP MEMORY CLEAR
PLACE CLOSE TO DIMM OR MINIPCI DOOR



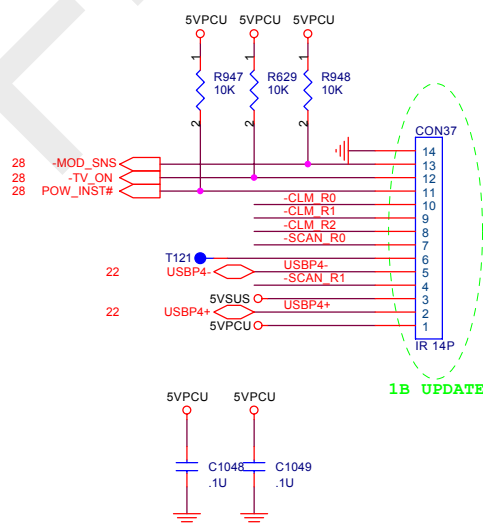
PC-BEEP



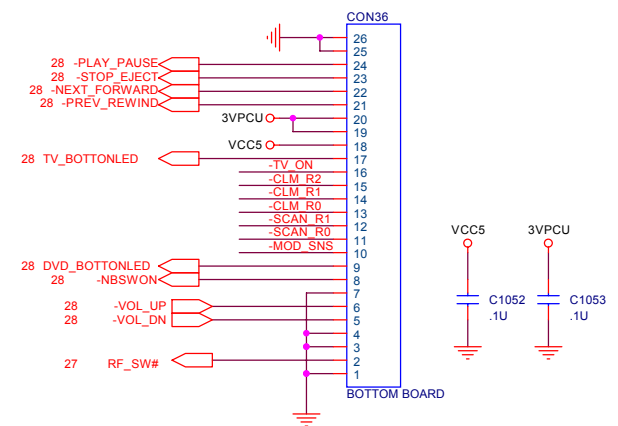
TOUCH PAD



TO RECEIVER MODULE



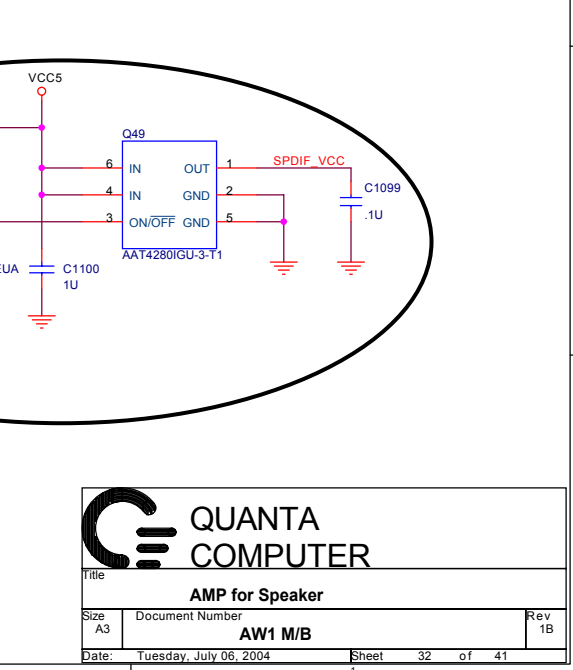
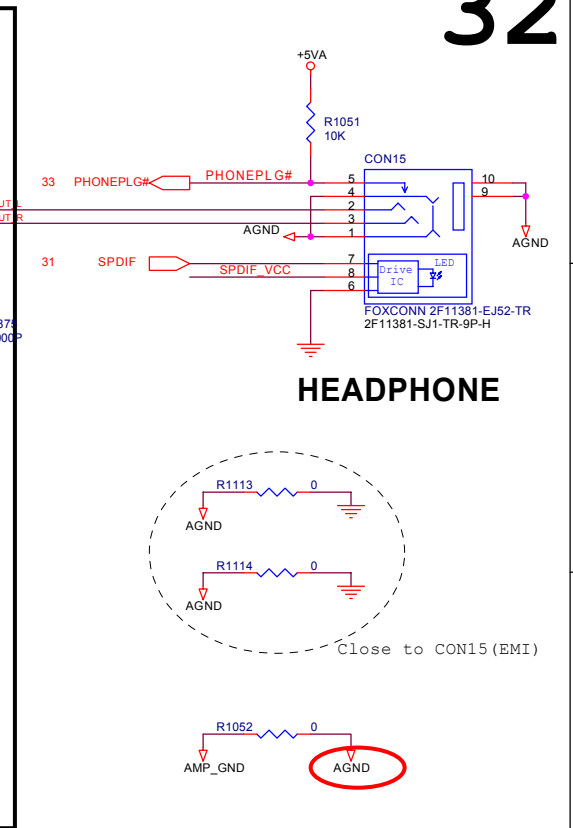
BUTTON BOARD



QUANTA
COMPUTER

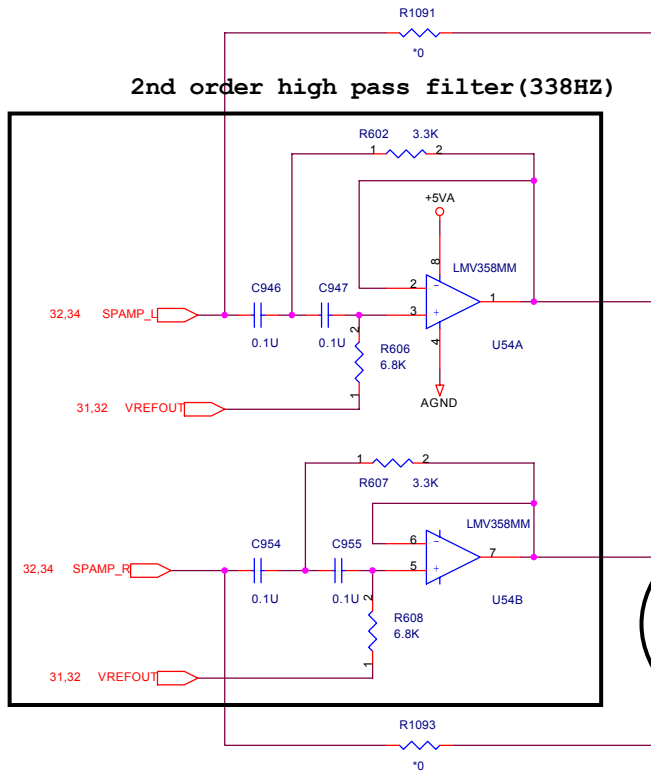
Title		
FAN/RTC/PCBEEP		
Size	Document Number	Rev
A3	AW1 M/B	1B
Date:	Tuesday, July 06, 2004	Sheet 30 of 41



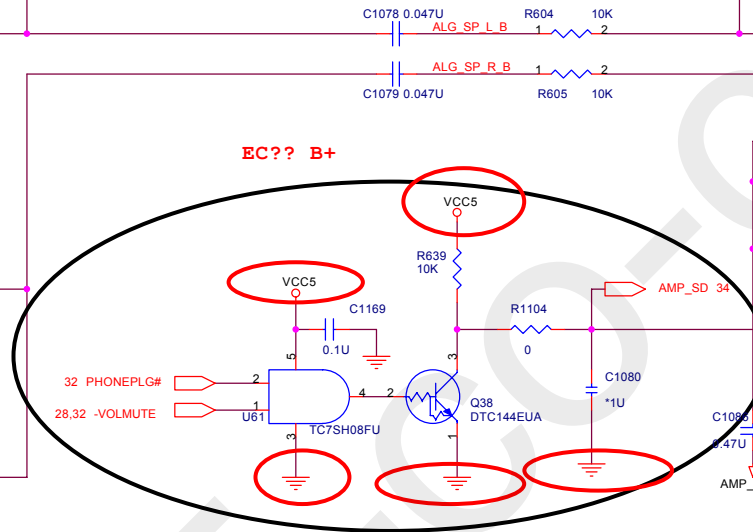


Cutoff:12.4KHz

2nd order high pass filter(338HZ)

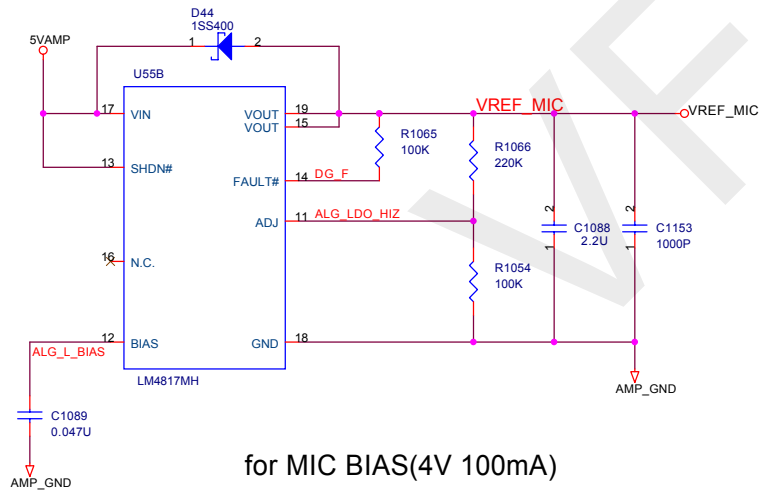


Cutoff:338Hz

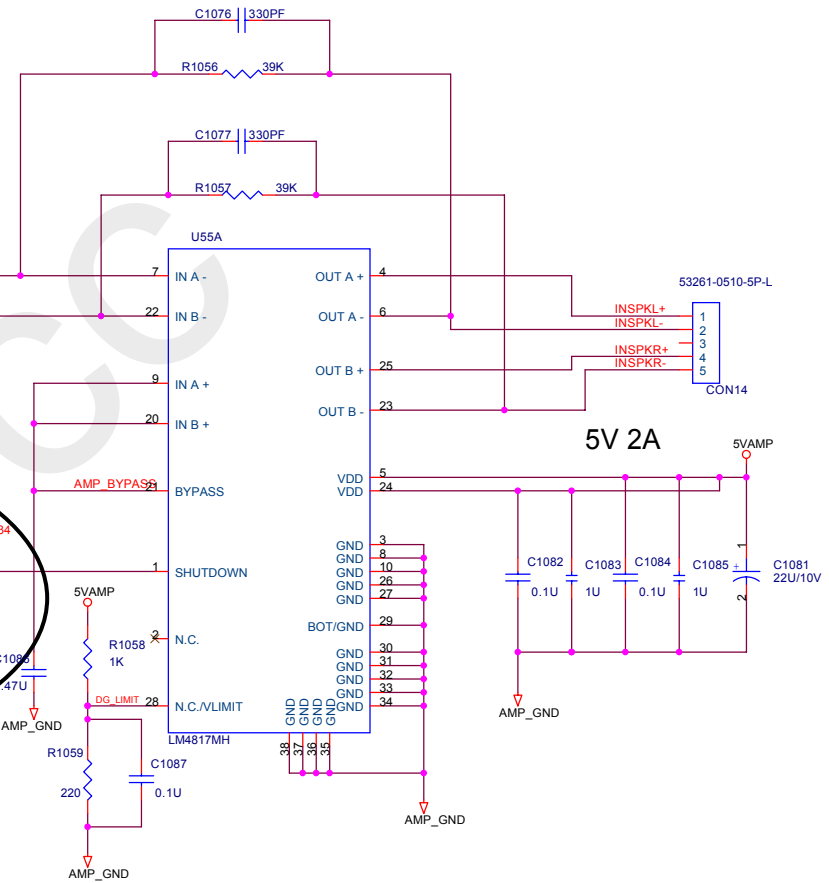
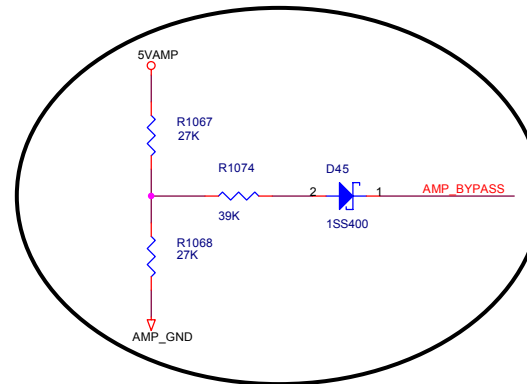


EC?? B+

EC17



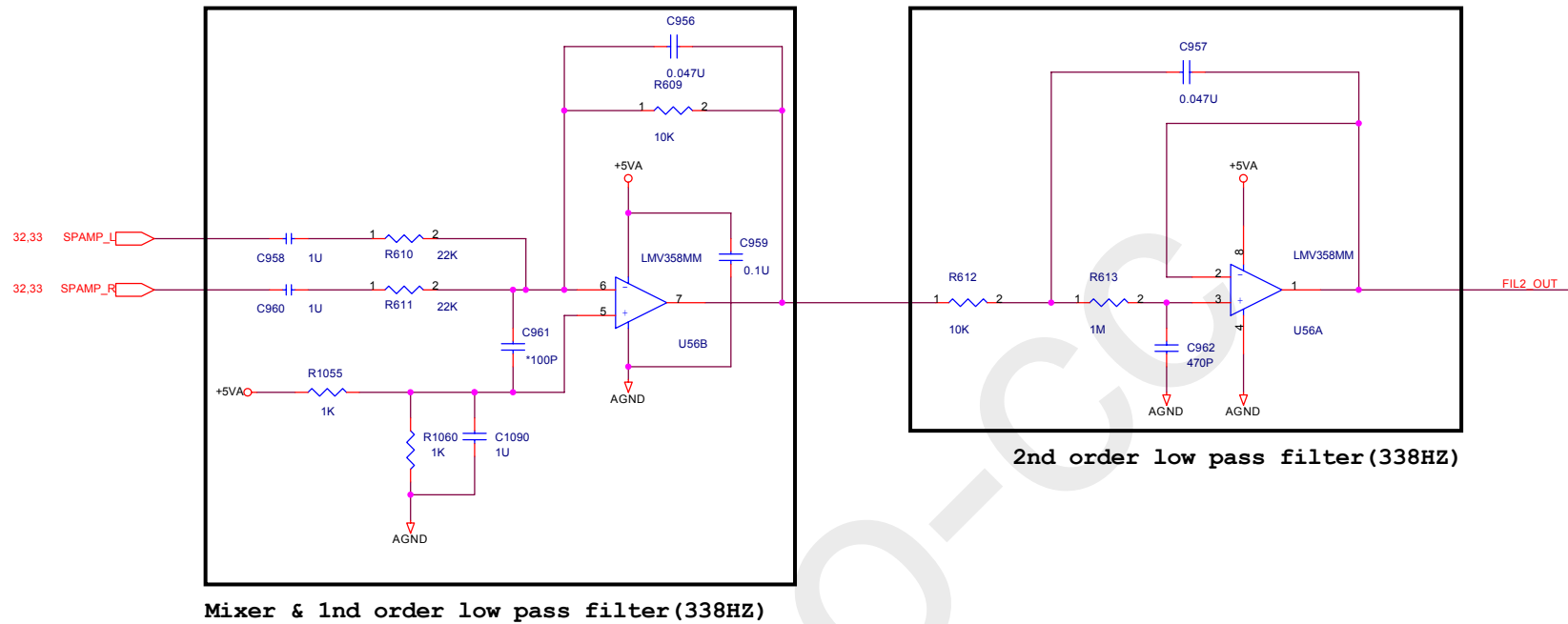
for MIC BIAS(4V 100mA)



5V 2A

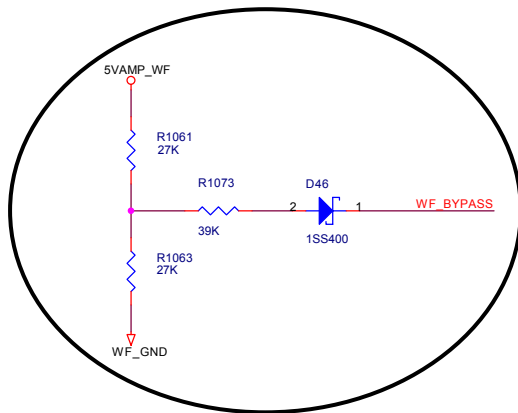
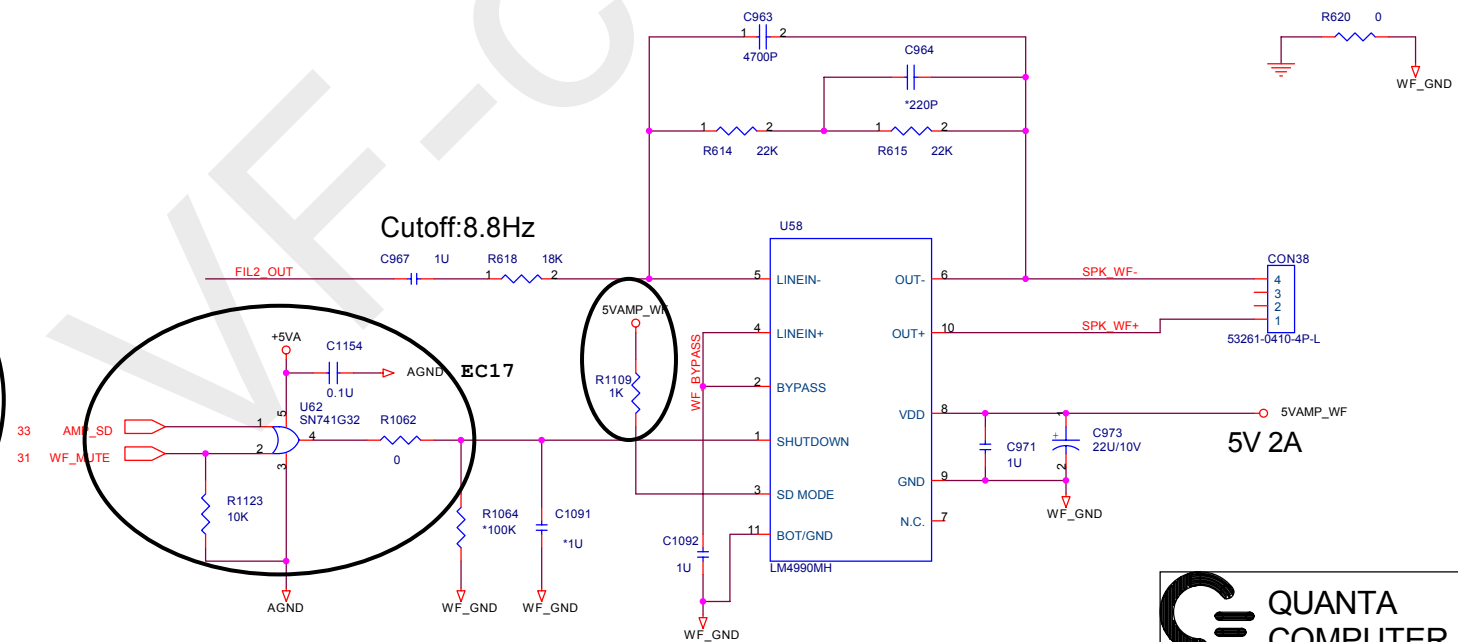
QUANTA
COMPUTER

Title		
AUDIO (Power AMP, SP CON)		
Size	Document Number	Rev
A3	AW1 M/B	1B
Date:	Tuesday, July 06, 2004	Sheet 33 of 41

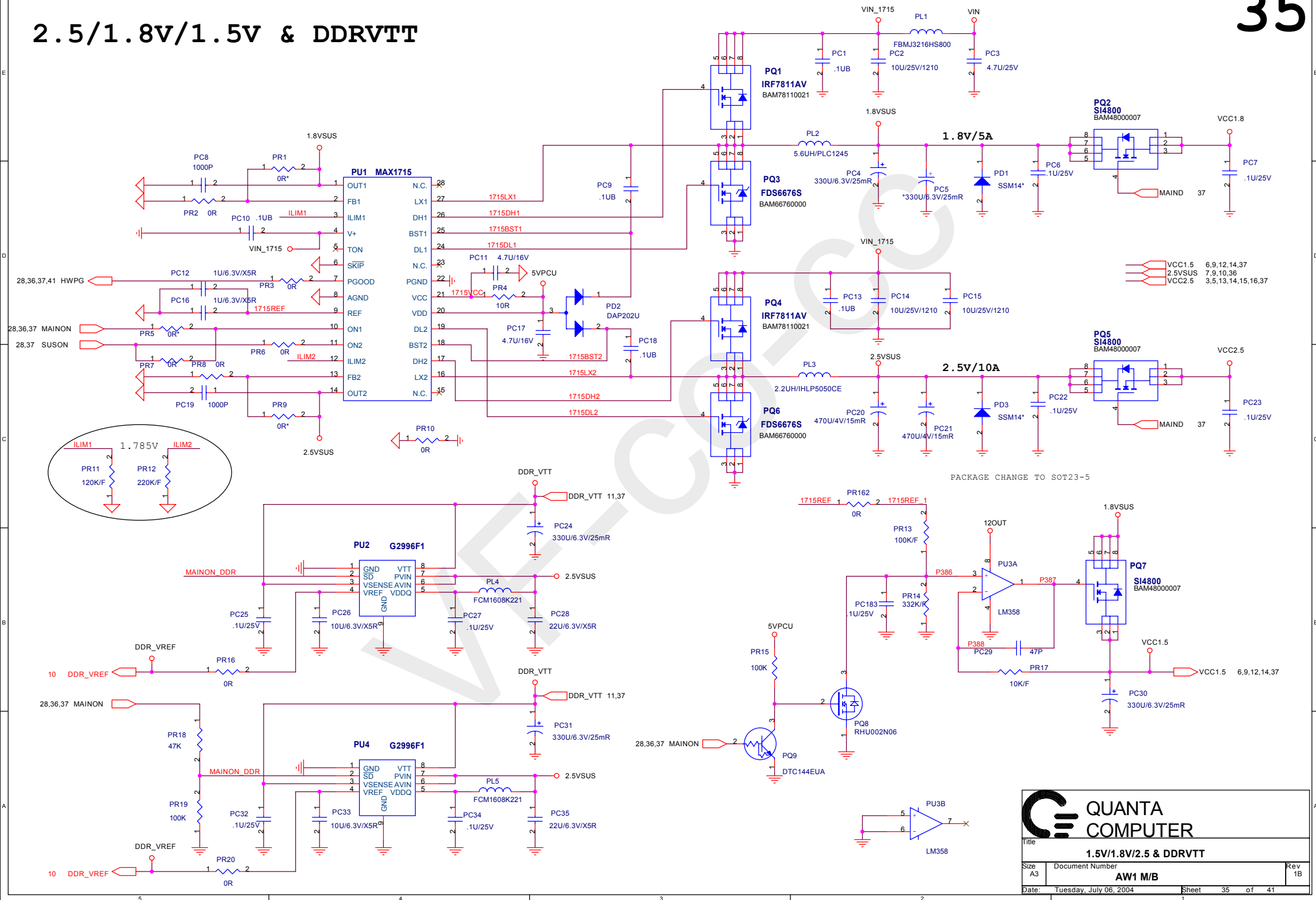


2nd order low pass filter(338Hz)

Cutoff:770Hz

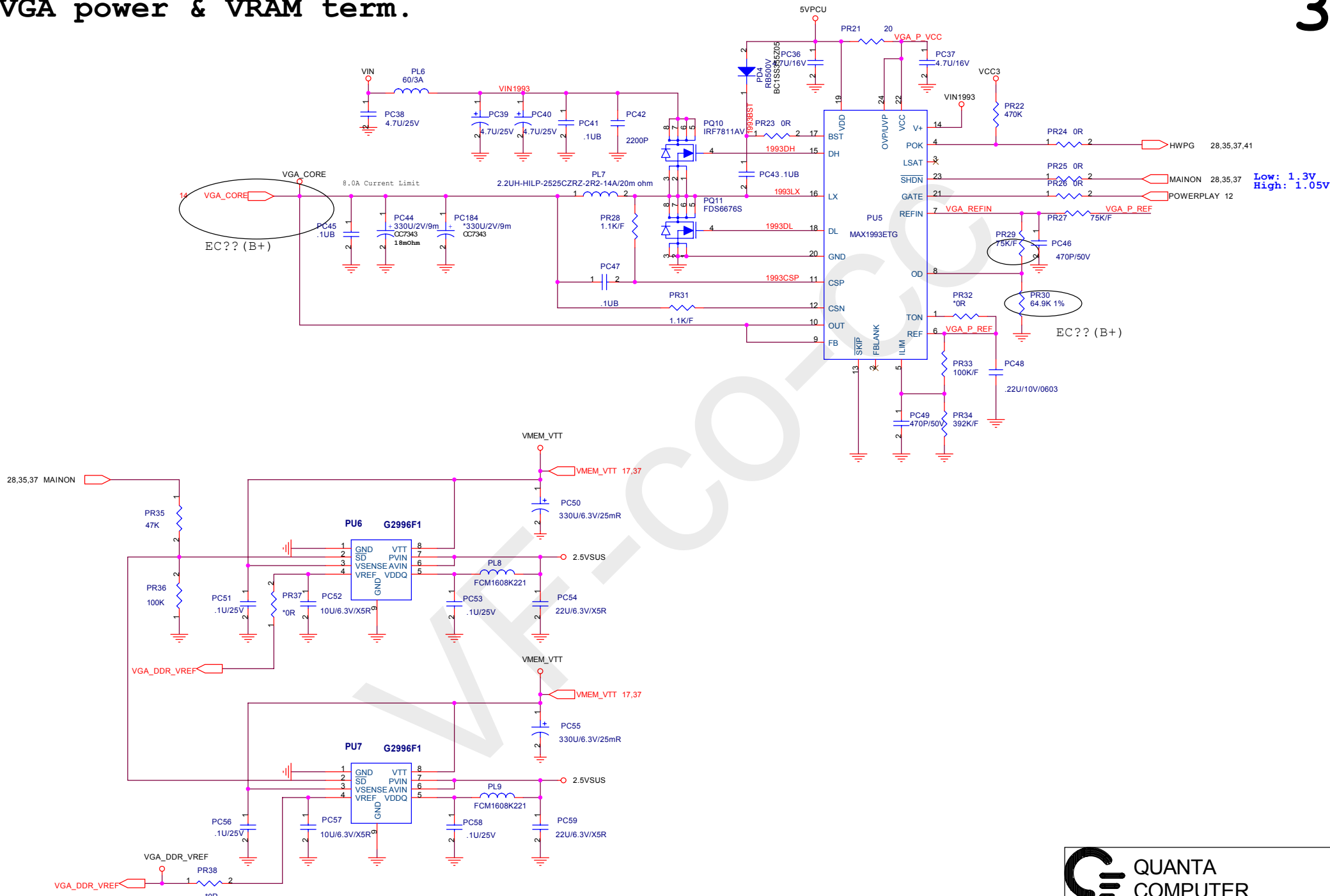


2.5/1.8V/1.5V & DDRVTT



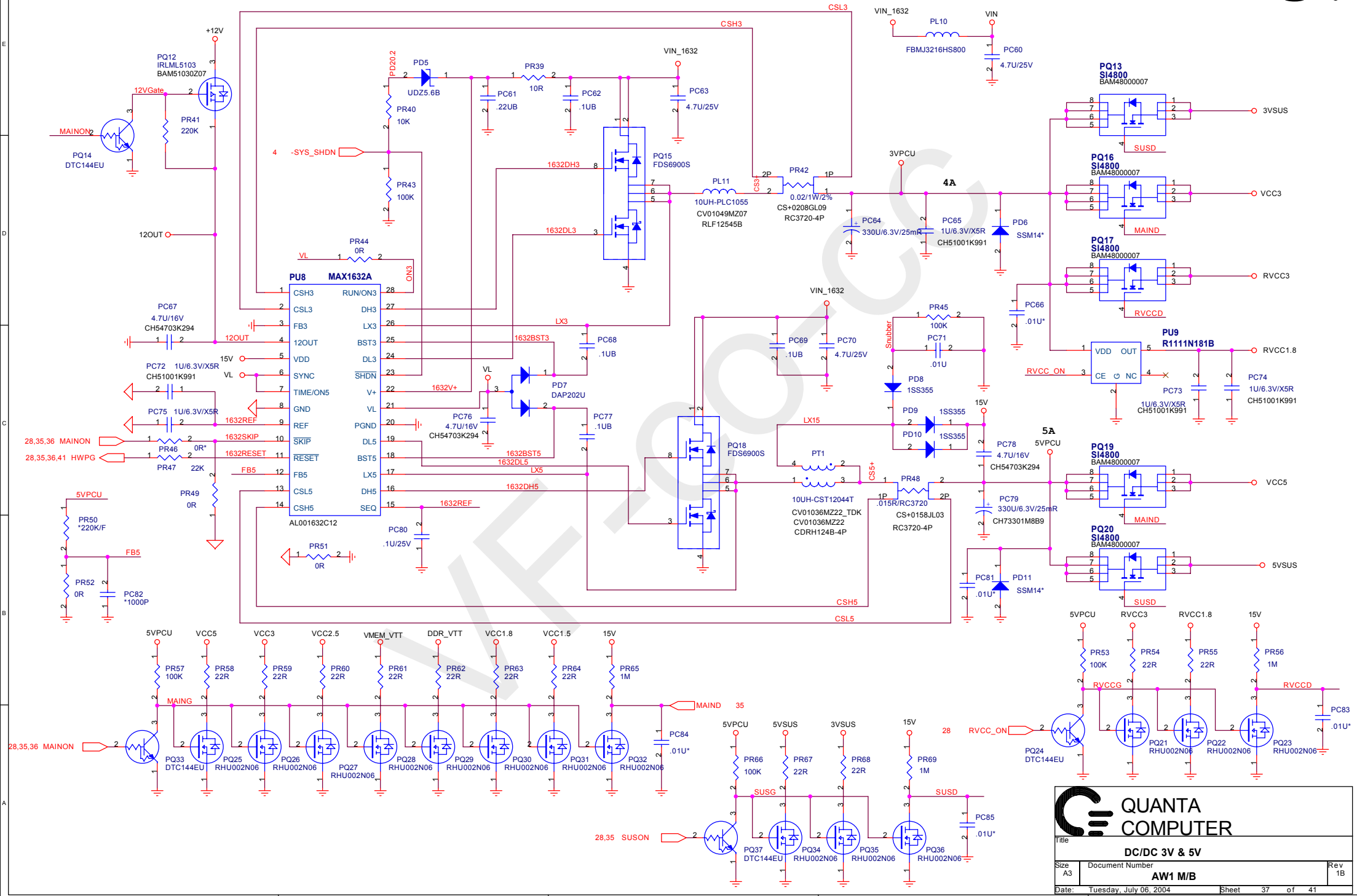
VGA power & VRAM term.

36



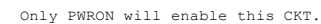
DC/DC 3.3V&5V

37

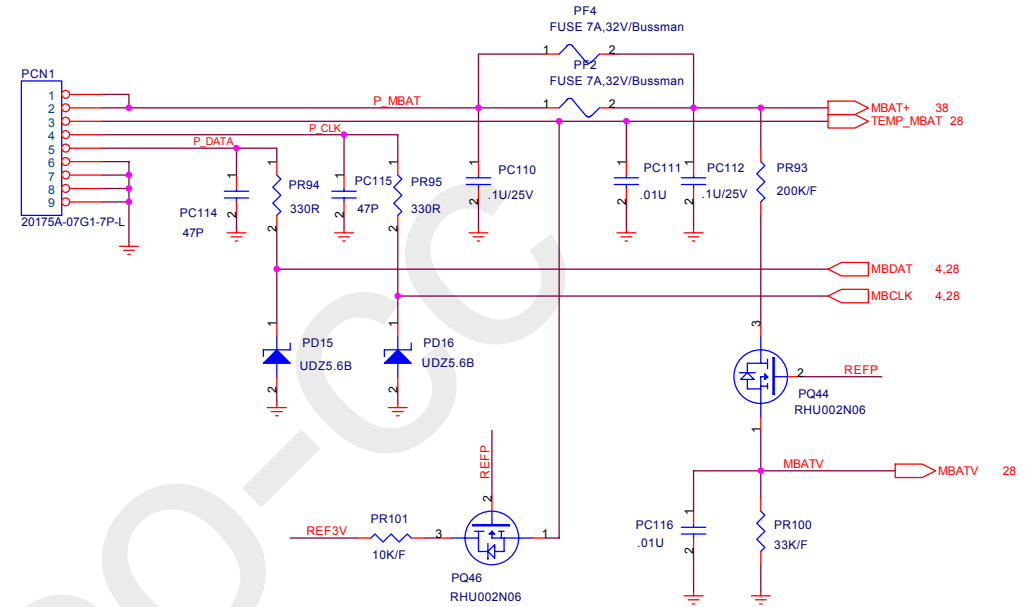
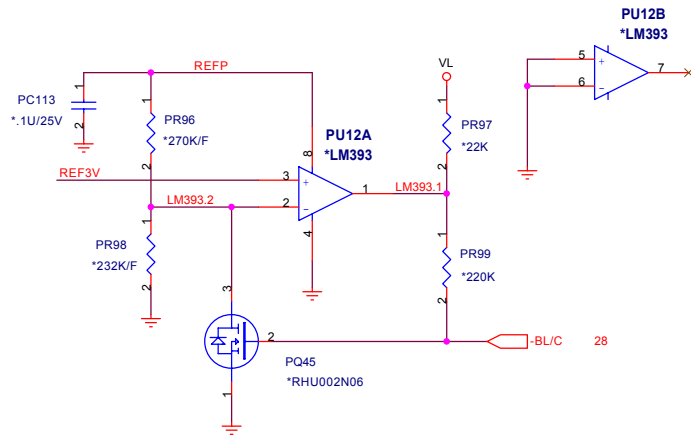


150W/ 19V/ 8A

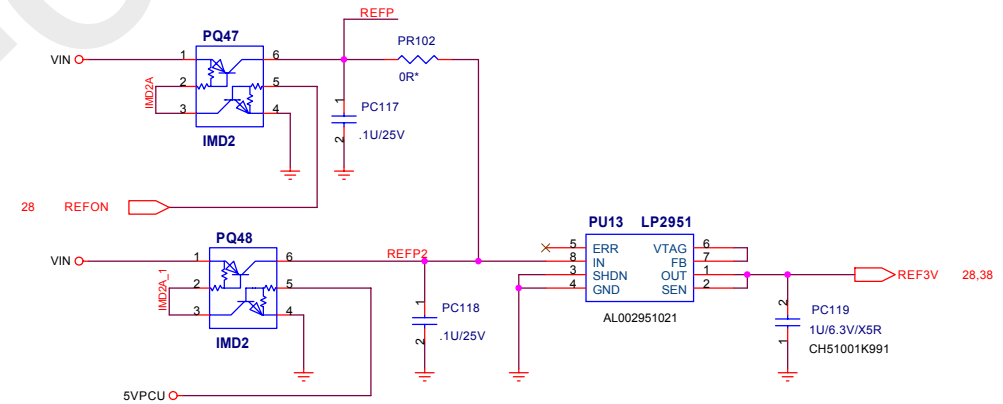
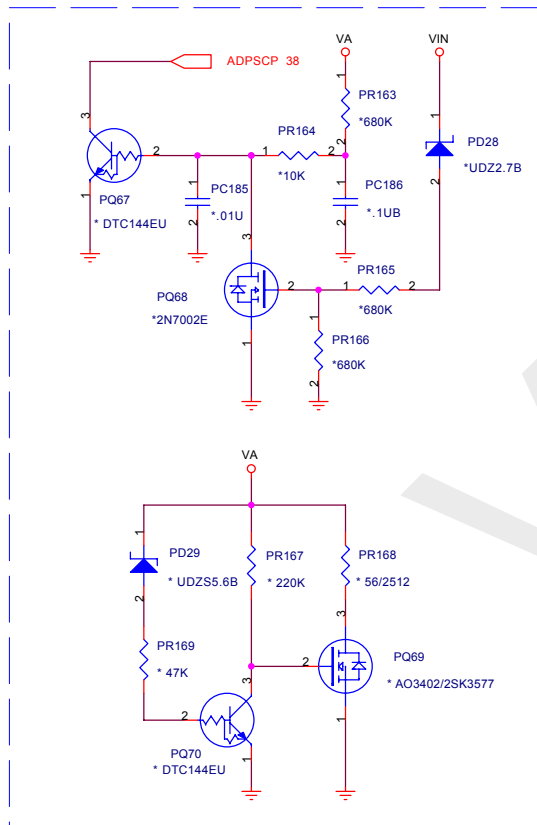
38

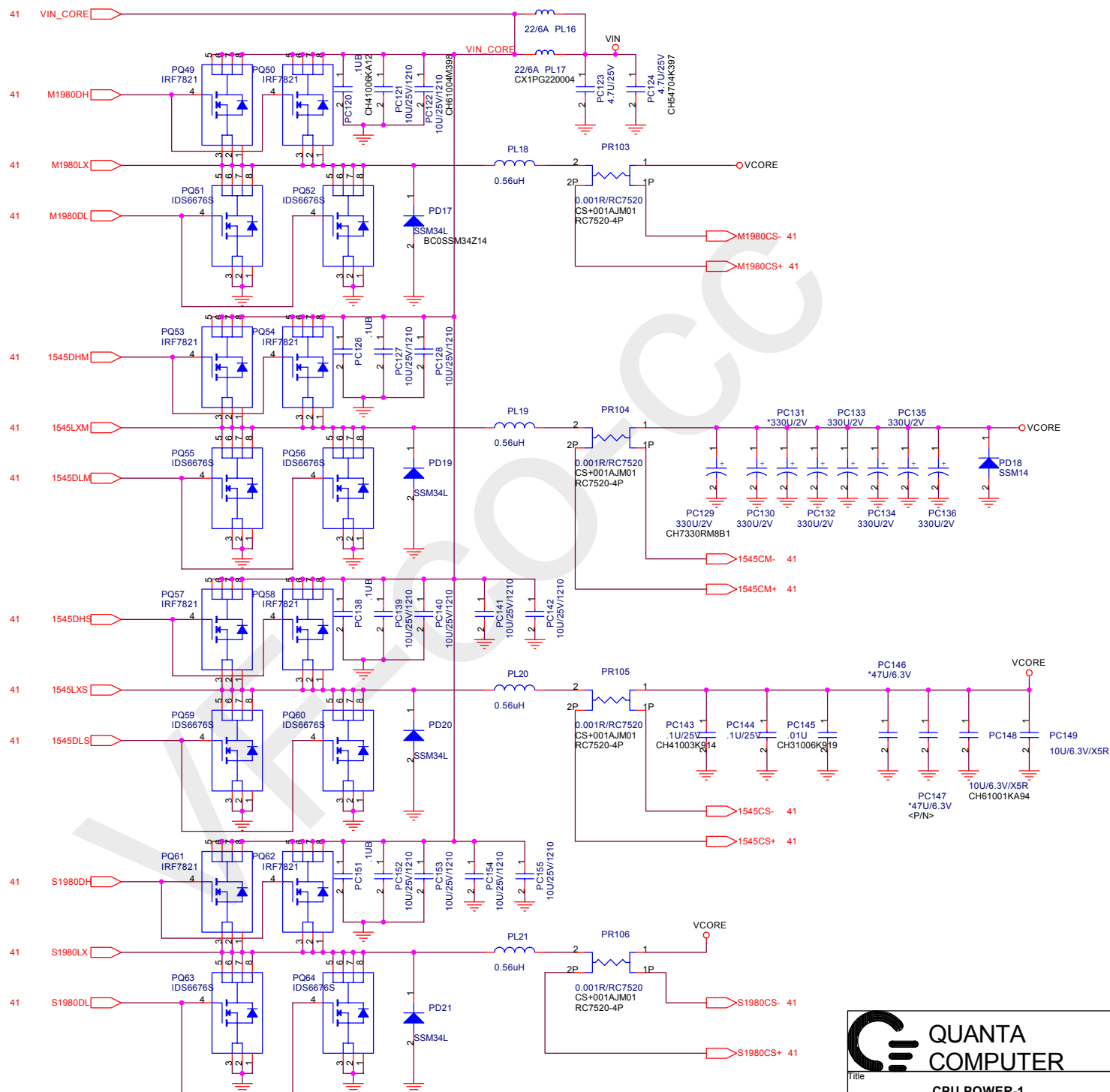


Battery Connector



SCP CKT





THIS PART SHOULD NOT CONTAIN ANY SUBSTANCES WHICH ARE SPECIFIED IN SS-00259-1.



QUANTA
COMPUTER

Title		
CPU POWER-1		
Size	Document Number	Rev
Custom	AW1 M/B	1B
Date:	Tuesday, July 06, 2004	Sheet 40 of 41



QUANTA
COMPUTER

CPU POWER-2

