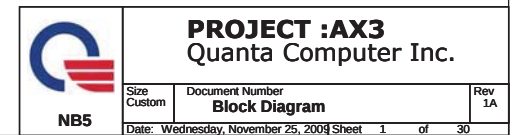
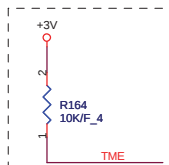
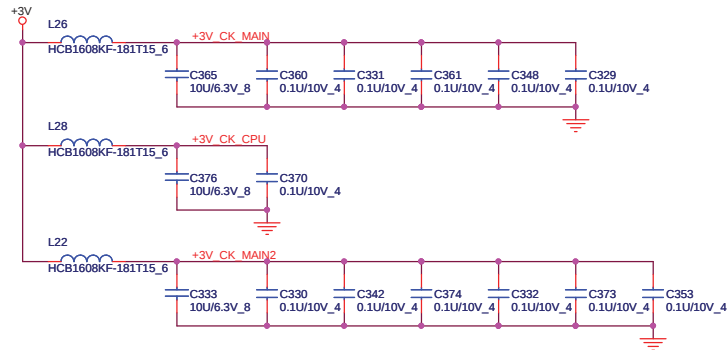


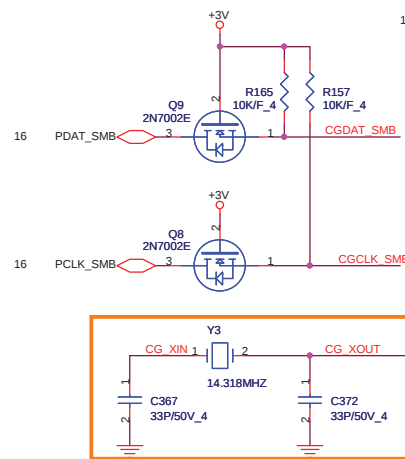
LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT



3,4,5,6,8,9,14,17,27,28 +1.05V
4,6,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,24,28,30 +3V

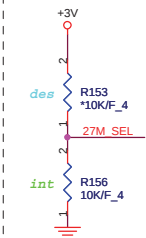


0=overclocking
of CPU and
SRC Allowed
1 = overclocking
of CPU and SRC
not Allowed

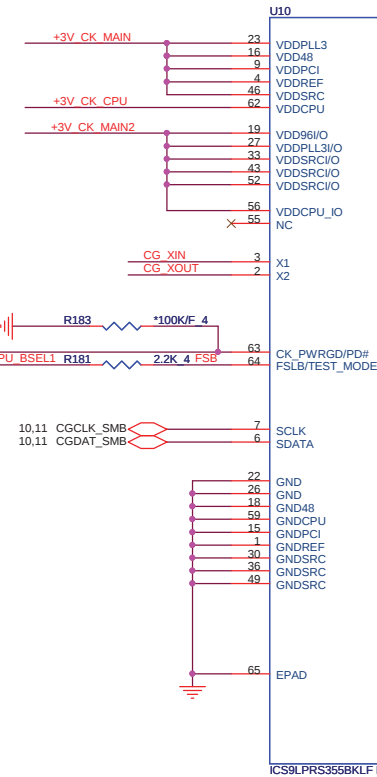
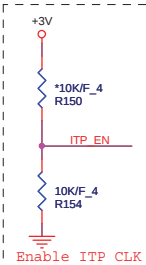


DB:Change from 27P to 33P(TXC suggestion)

27M_SEL PIN13	PIN20	PIN21	PIN24	PIN25
0=UMA	DOT96T	DOT96C	SRCT1/LCDT_100	SRCT1/LCDT_100
1 = External VGA	SRCT0	SRCC0	27Mout-NSS	27Mout-SS



0=UMA
1 = External VGA



CK505

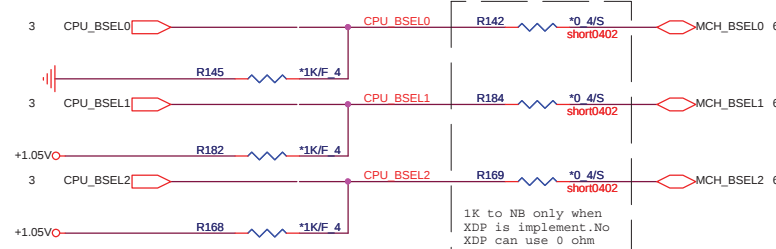


CK505 QFN64

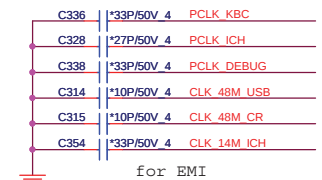
Silego SLG8SP513VTR AL8SP513000
Realtek RTM875N-606-VD-GR AL000875000
ICS ICS9LPRS355BKLF ALPRS355000

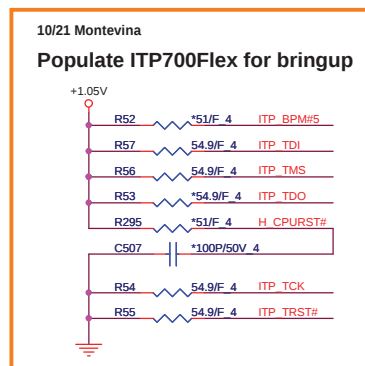
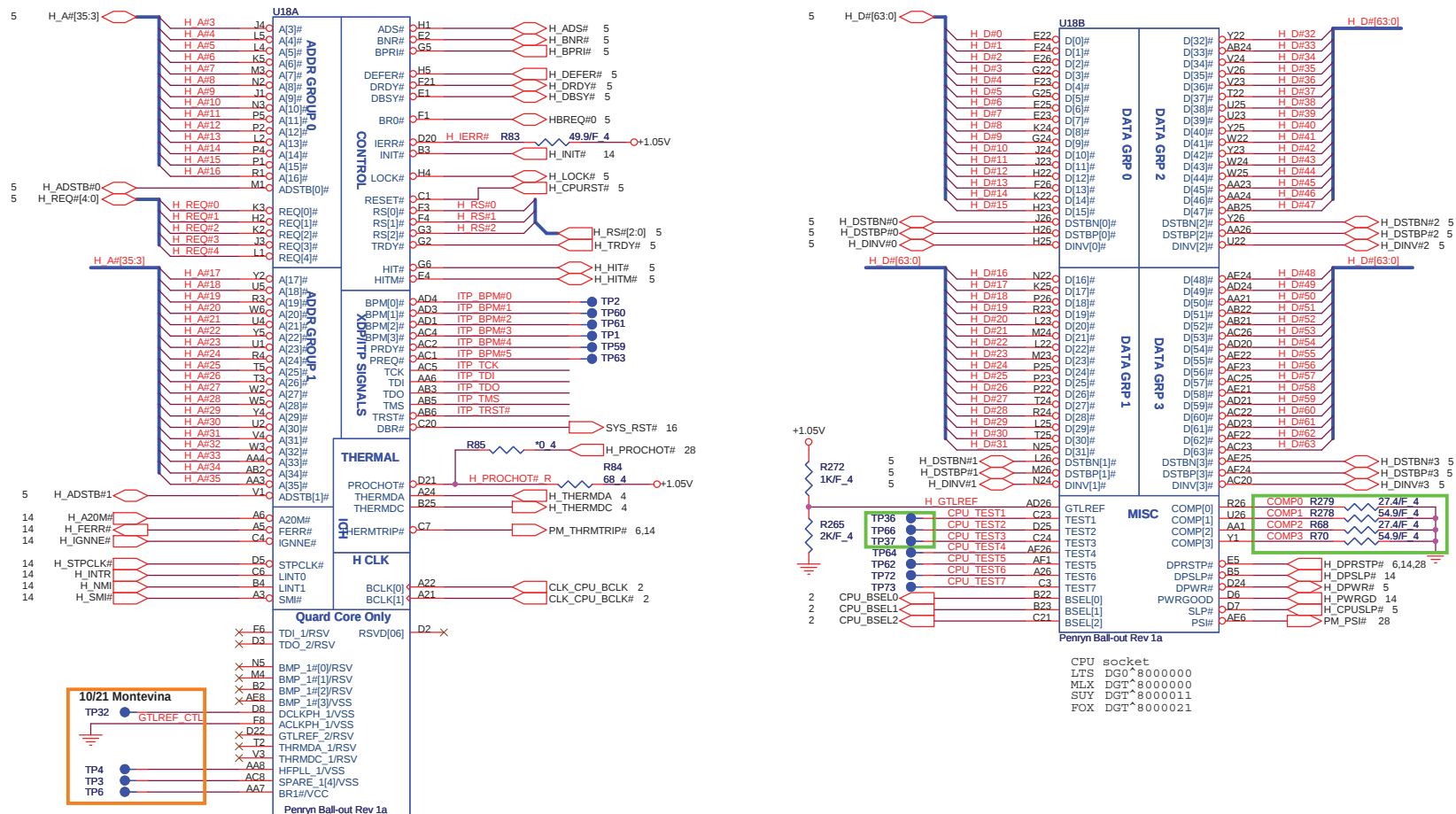


CPU Clock select

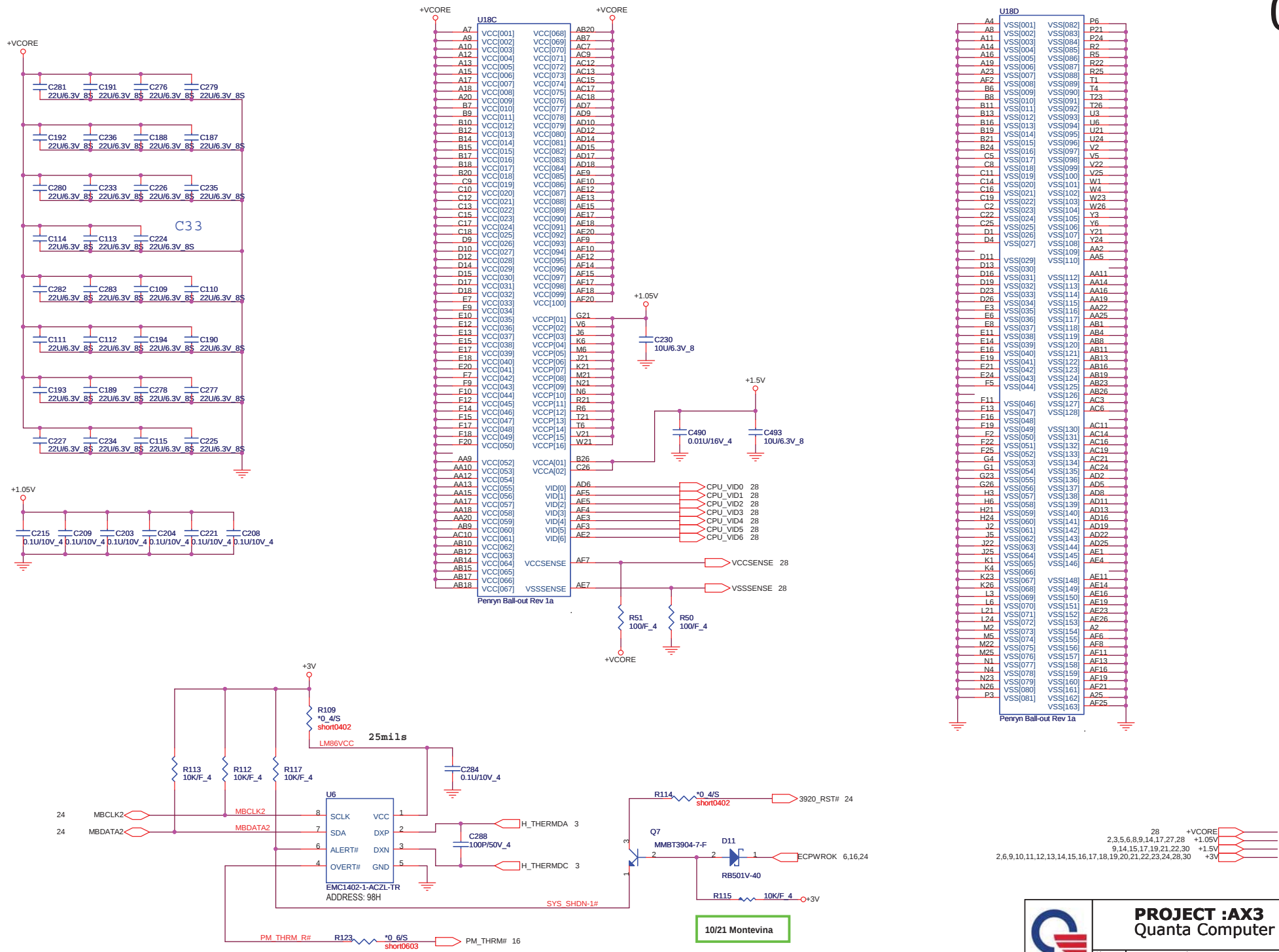


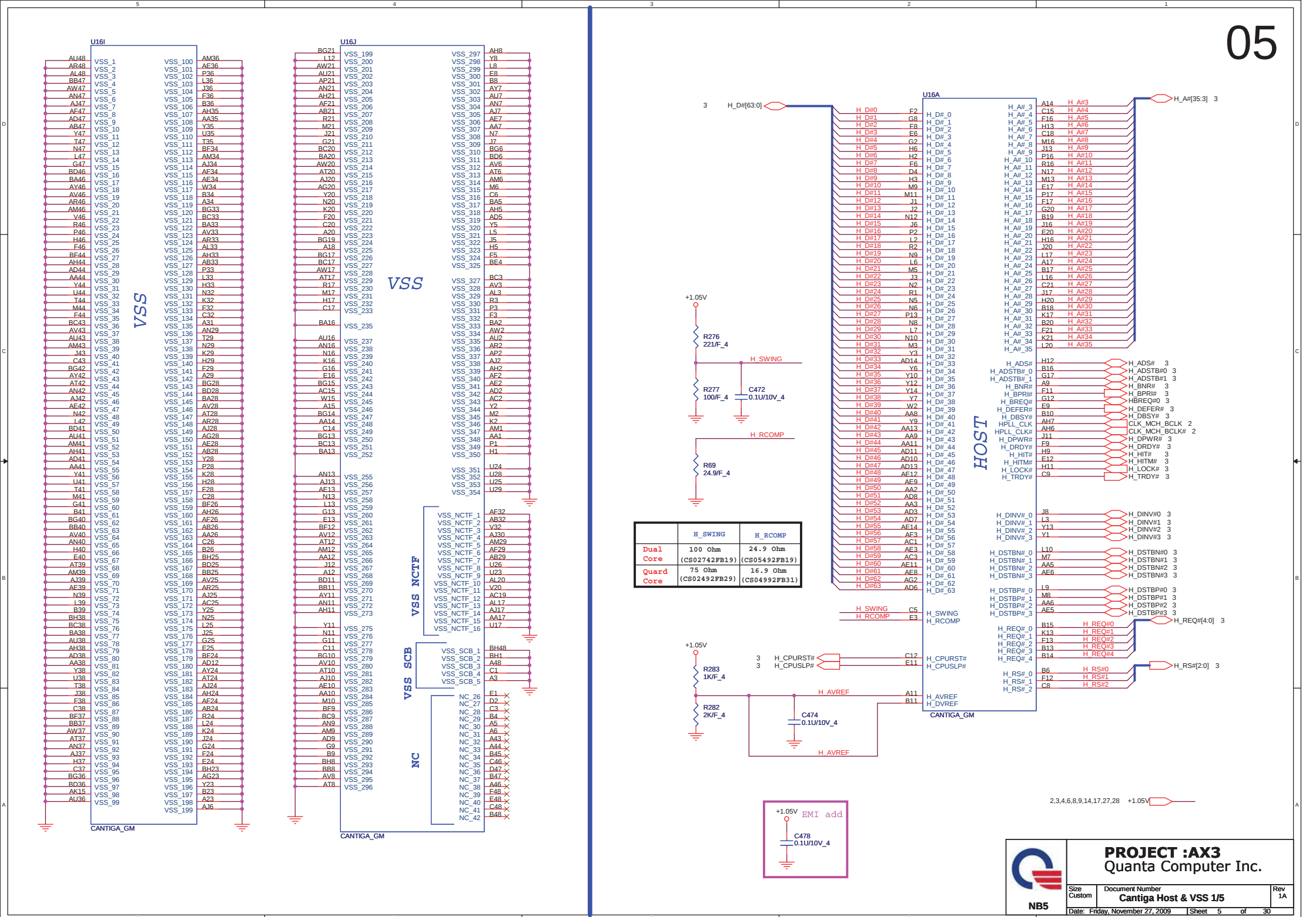
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33

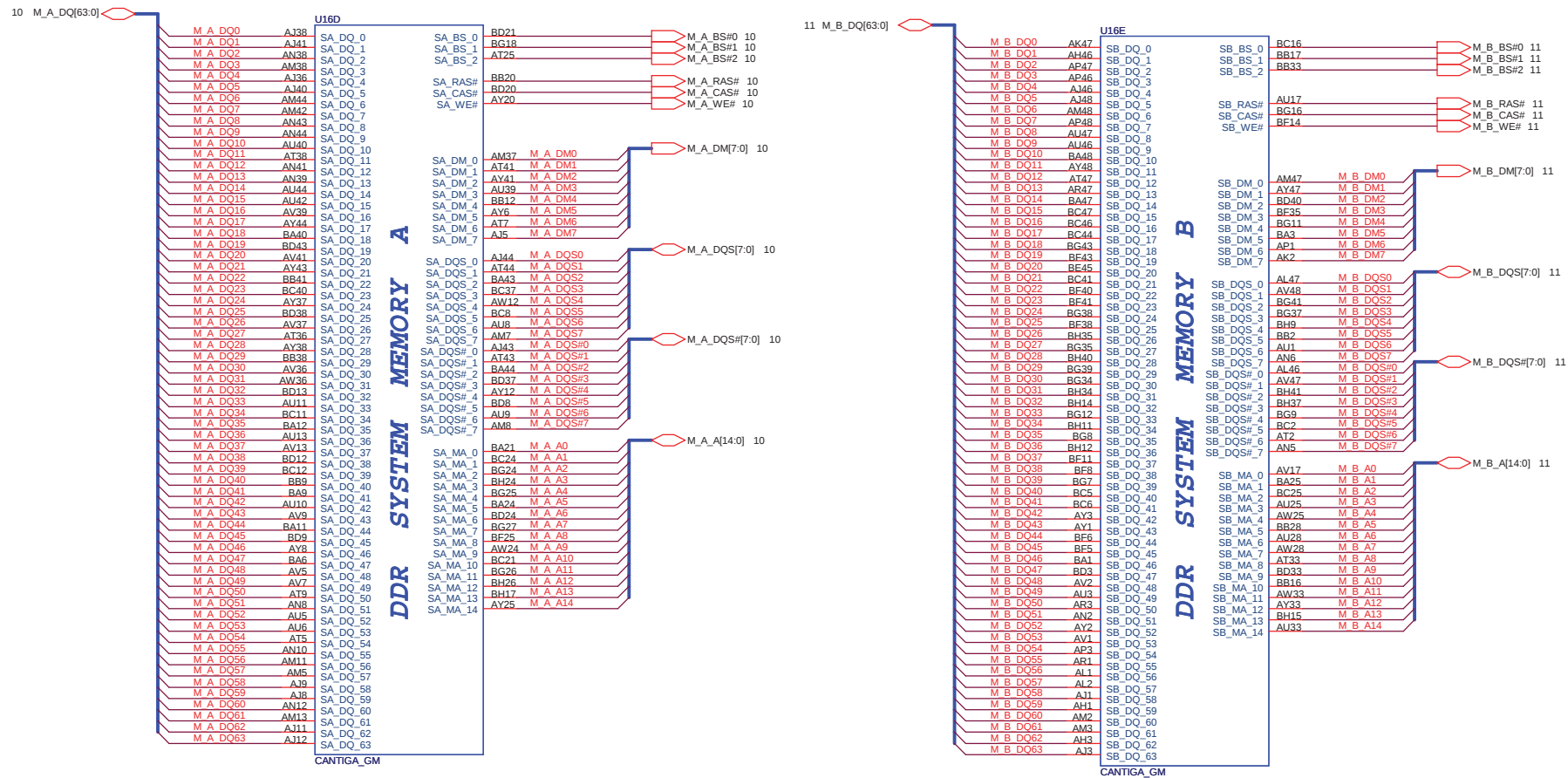




2,4,5,6,8,9,14,17,27,28 +1.05V

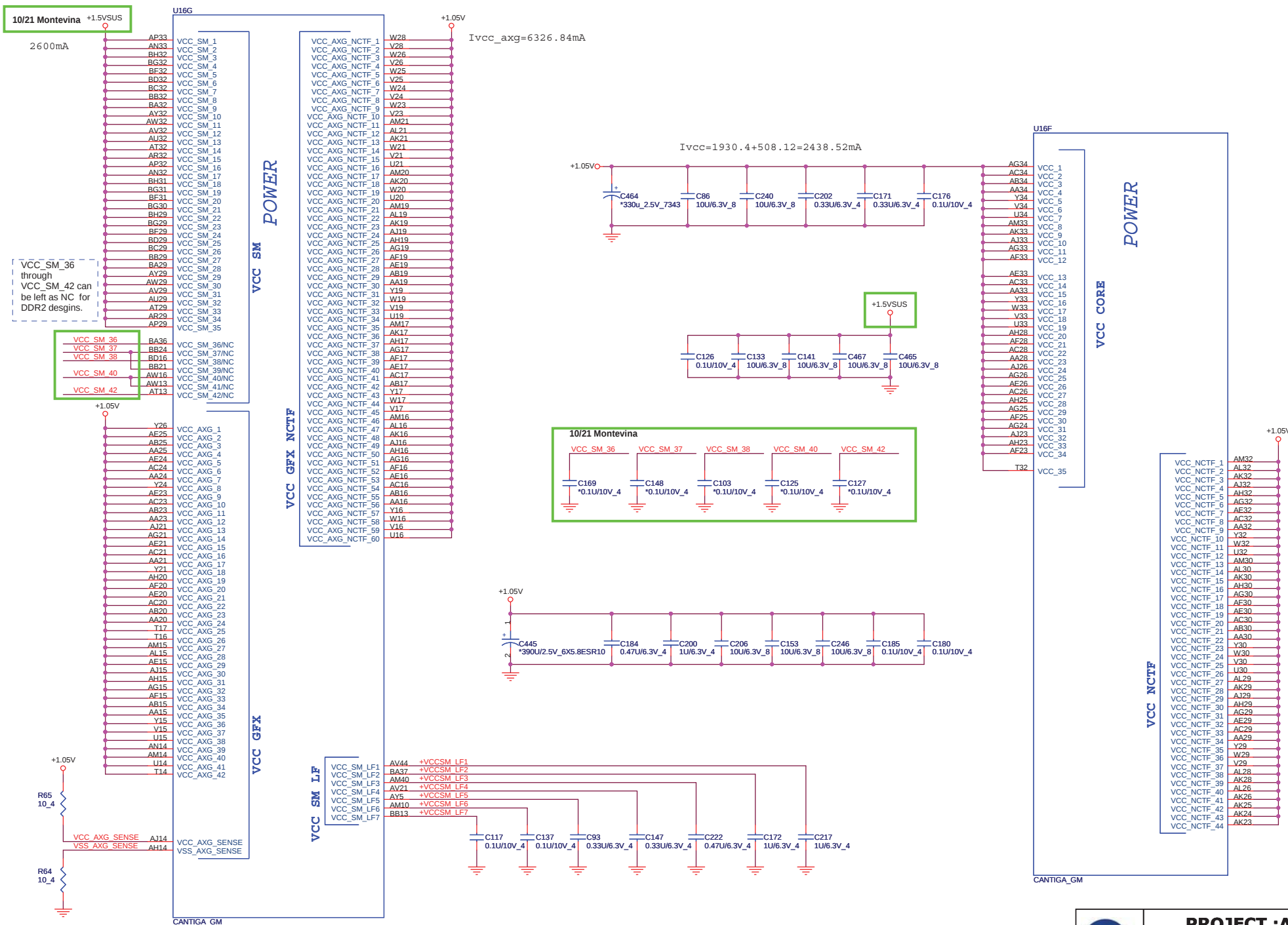




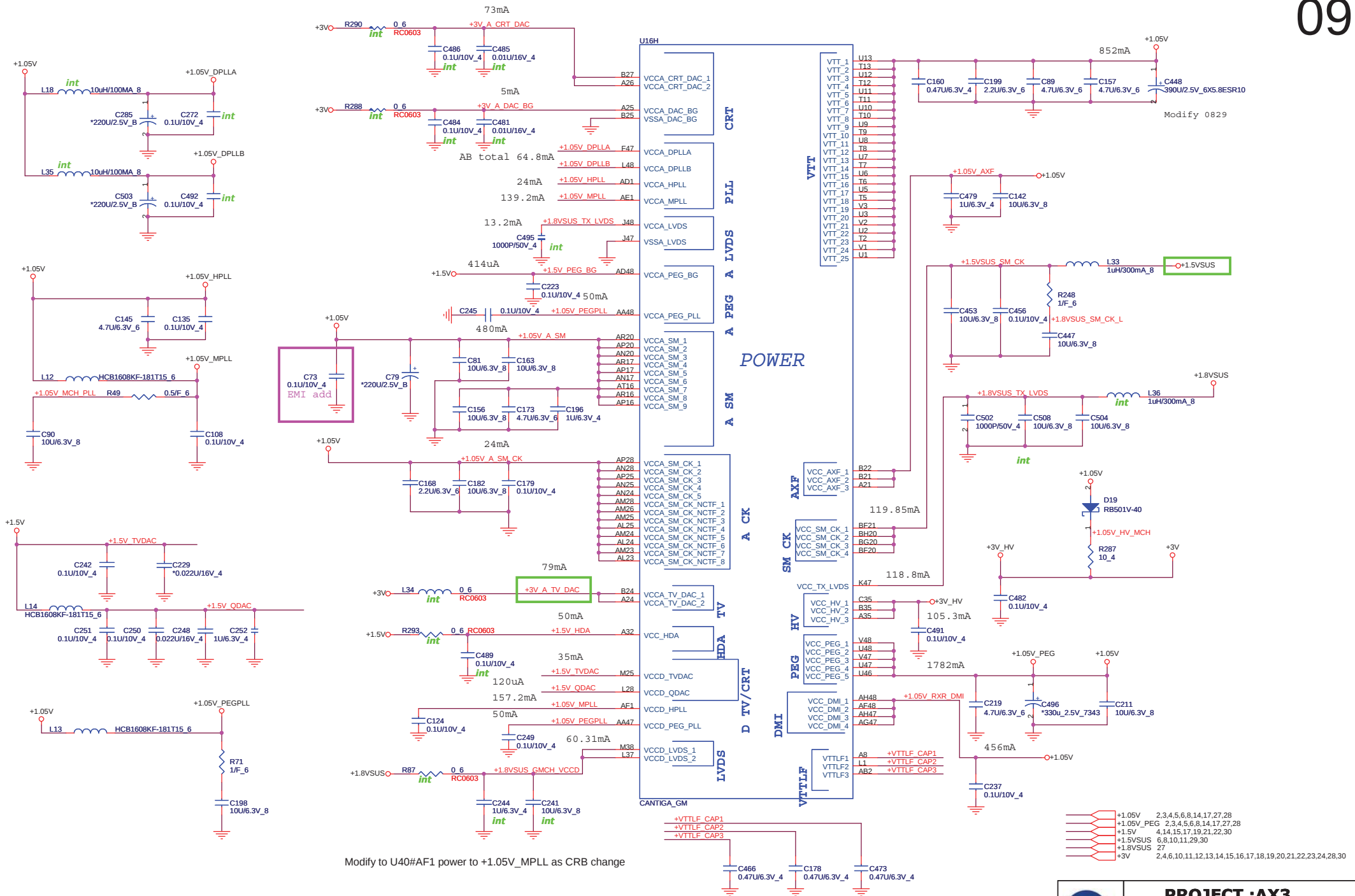


PROJECT :AX3
Quanta Computer Inc.

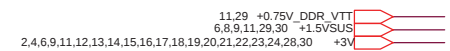
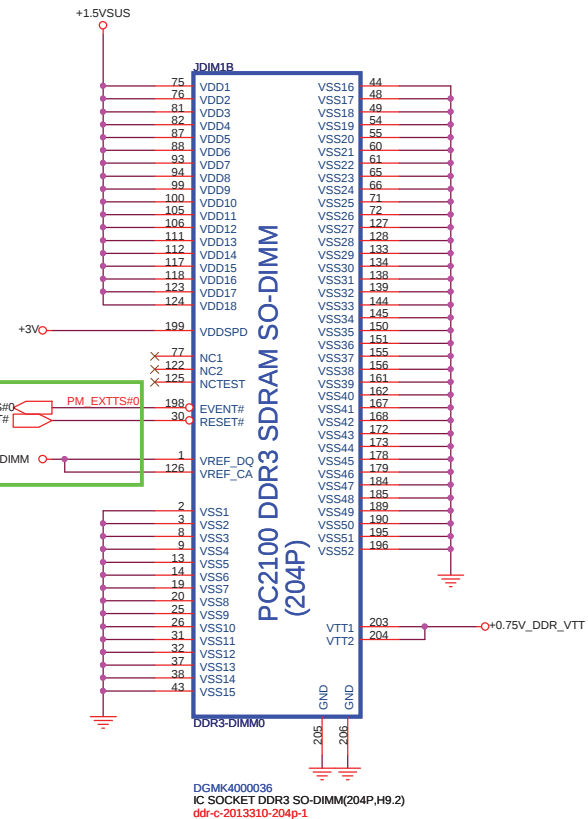
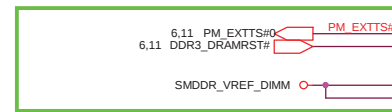
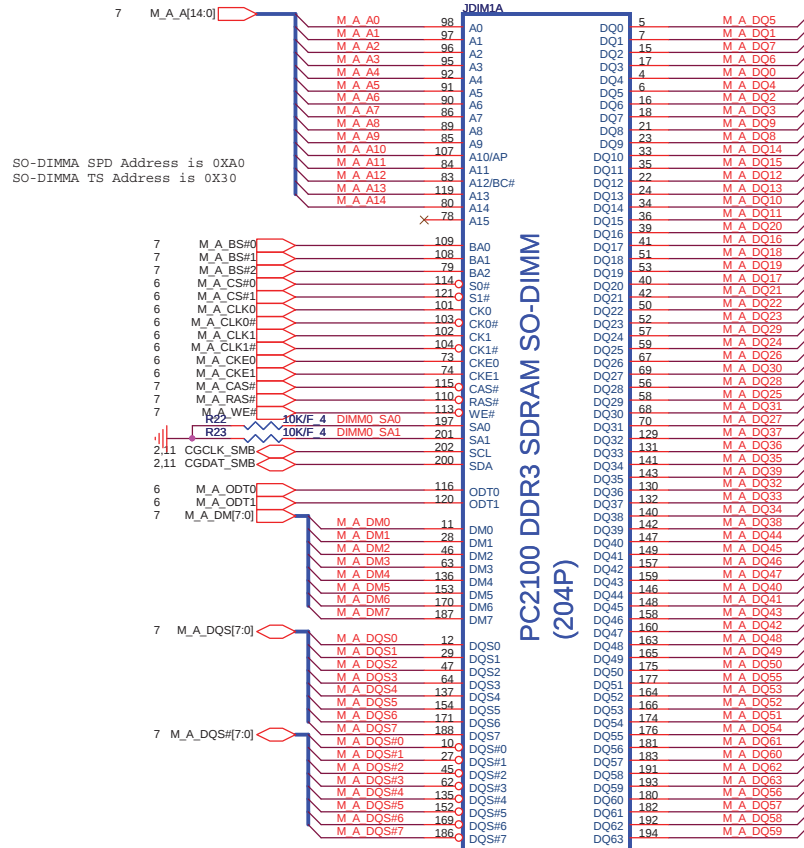
Size Custom	Document Number Cantiga DDR3 3/5	Rev 1A
Date: Friday, November 27, 2009		Sheet 7 of 30



2,3,4,5,6,9,14,17,27,28 +1.05V
6,9,10,11,29,30 +1.5VSUS

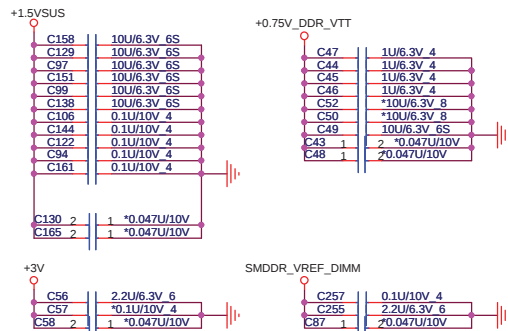


PROJECT :AX3
Quanta Computer Inc.



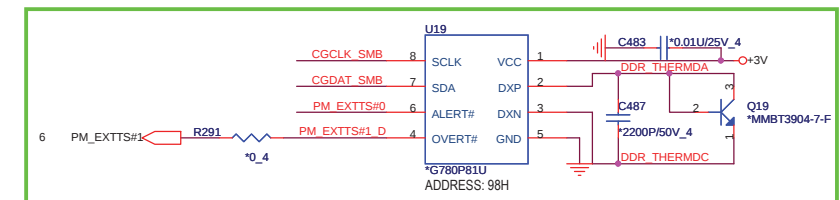
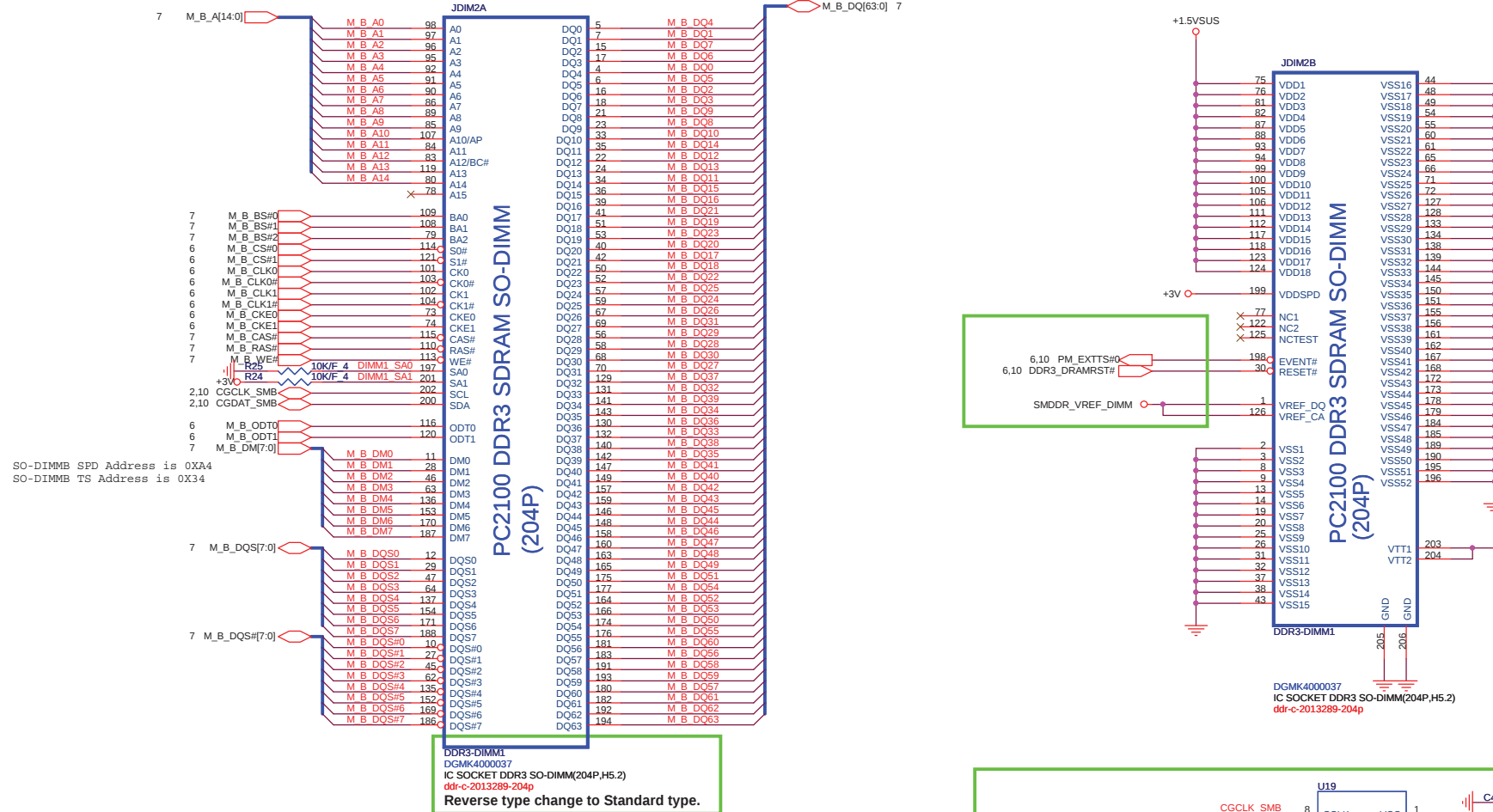
Place these Caps near So-Dimm0.

Some Projects replace 10UF 0805 by 4.7UF 0603
It can cost down 30%

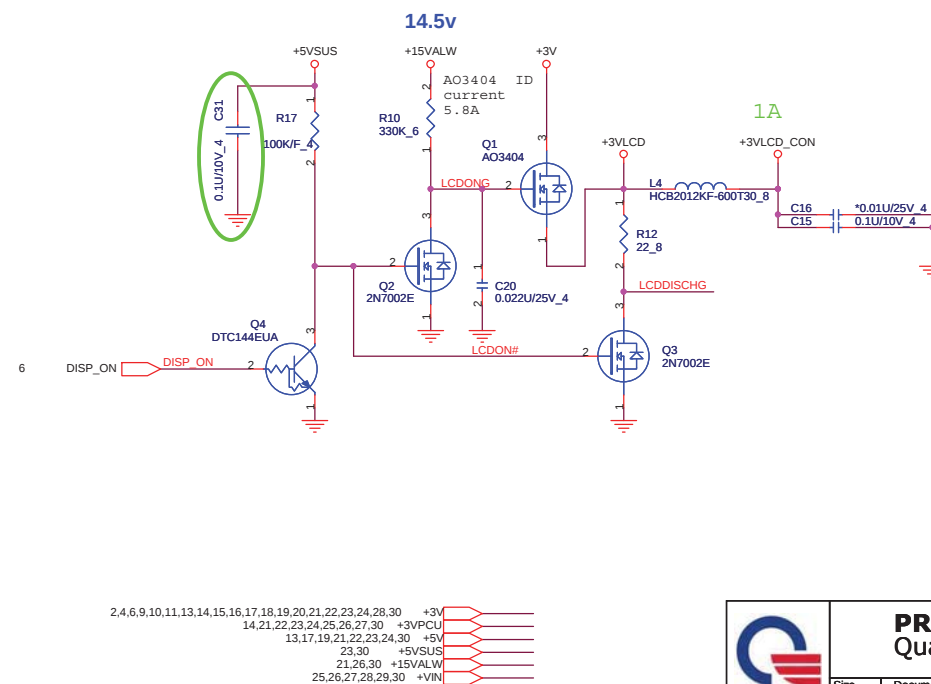
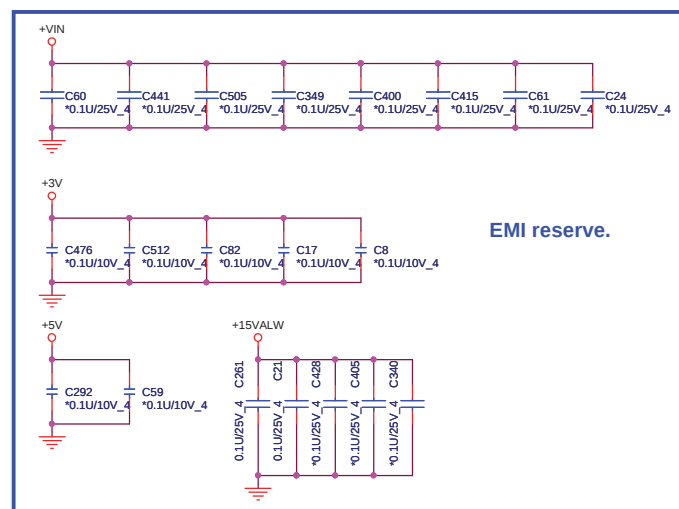
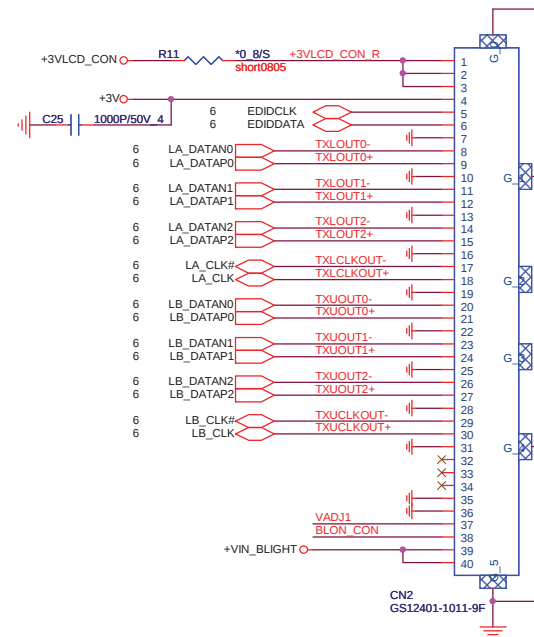
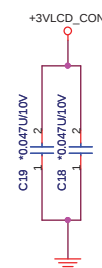
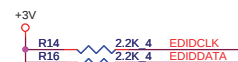
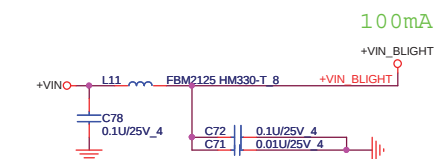
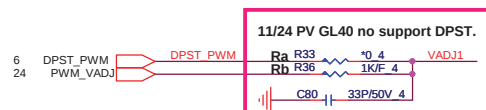
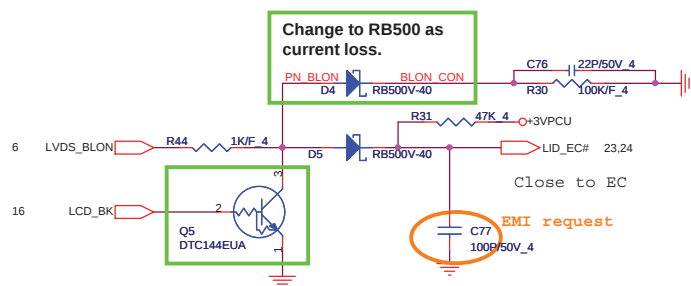


PROJECT :AX3
Quanta Computer Inc.

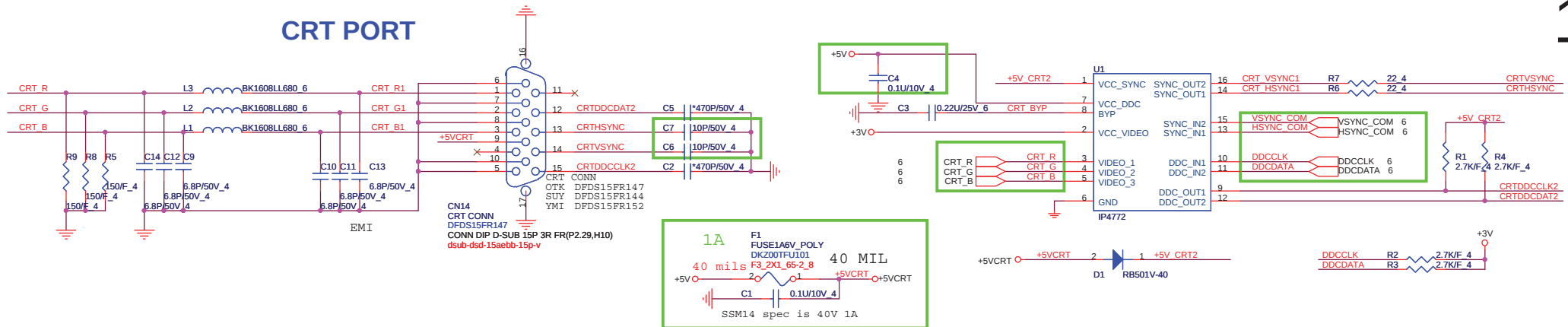
Size	Document Number	Rev
Custom	DDR3 DIMM-0	1A
Date: Friday, November 27, 2009	Sheet 10 of 30	



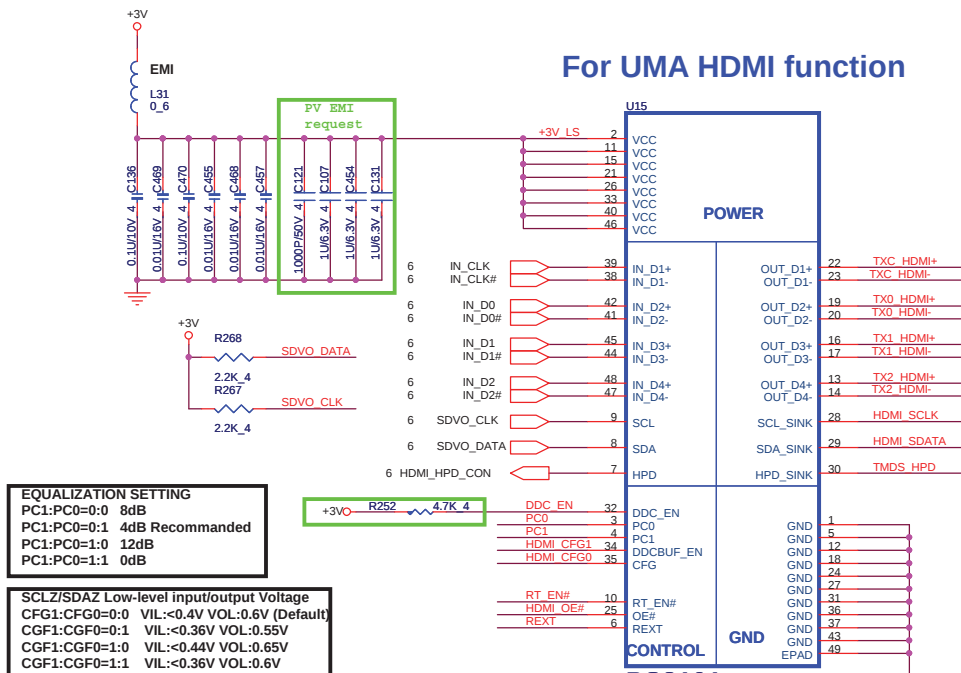
LID Switch



CRT PORT



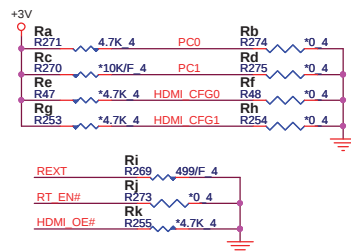
For UMA HDMI function



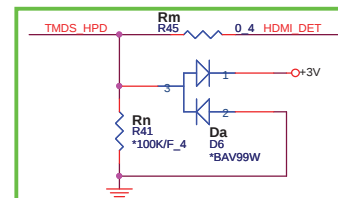
EQUALIZATION SETTING
PC1:PC0=0:0 8dB
PC1:PC0=0:1 4dB Recommended
PC1:PC0=1:0 12dB
PC1:PC0=1:1 0dB

SCLZ/SDAZ Low-level input/output Voltage
CFG1:CFG0=0:0 VIL:<0.4V VOL:0.6V (Default)
CFG1:CFG0=0:1 VIL:<0.36V VOL:0.55V
CFG1:CFG0=1:0 VIL:<0.44V VOL:0.65V
CFG1:CFG0=1:1 VIL:<0.36V VOL:0.6V

Signals	R	PDT	CHR	PIM
PC0	Ra	4.7K	NC	NC
	Rb	NC	NC	0
PC1	Rc	NC	10K	NC
	Rd	NC	NC	0
HDMI_CFG0	Re	NC	10K	NC
	Rf	NC	NC	0
HDMI_CFG1	Rg	NC	10K	NC
	Rh	NC	NC	0
REXT	Ri	499	1.2K	0
RT_EN#	Rj	NC	NC	0
HDMI_OE#	Rk	NC	0	0



Detect	R	PDT	CHR	PIM
IC	Rm	0	20K	0
	Rn	NC	47K	NC
NB (Page6)	Da	NC	Stuff	NC
	Ro	NC	20K	20K
	Rp	NC	7.5K	7.5K
	Rq	0	0	0

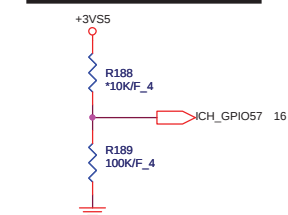


2,4,6,9,10,11,12,14,15,16,17,18,19,20,21,22,23,24,28,30
12,17,19,21,22,23,24,30



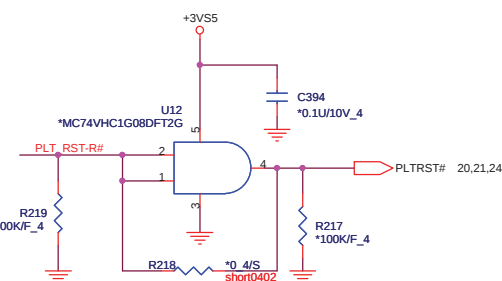
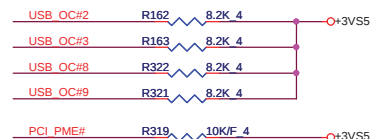
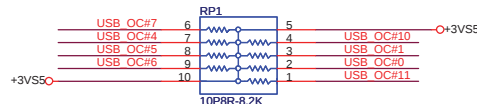
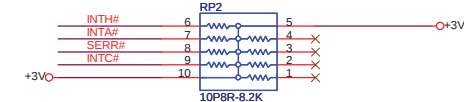
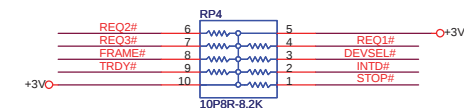
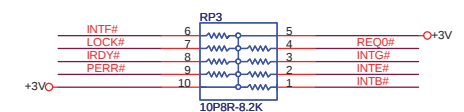
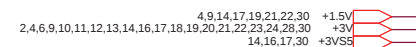
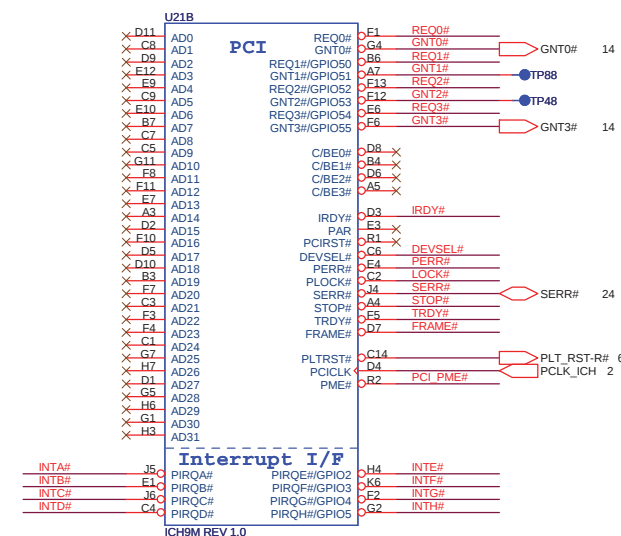
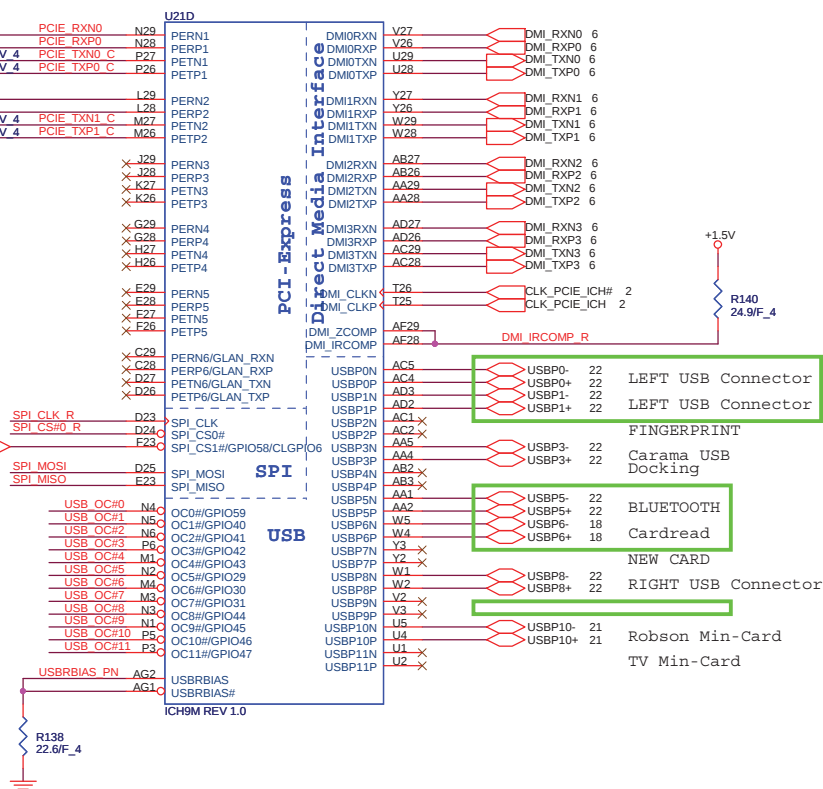
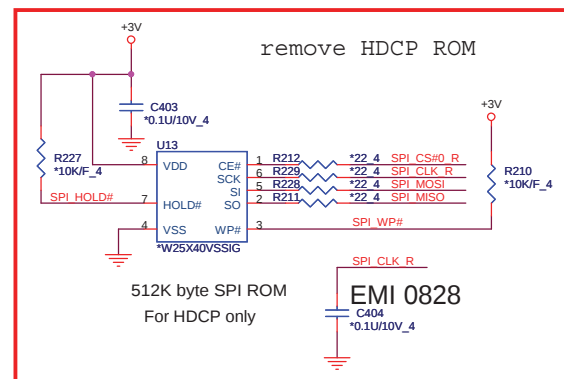
PROJECT :AX3
Quanta Computer Inc.

Size Custom Document Number CRT/HDMI Conn Rev 1A
Date: Friday, November 27, 2009 Sheet 13 of 30



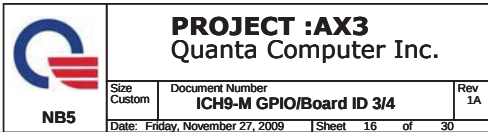
MINI CARD PCI-E (WLAN)

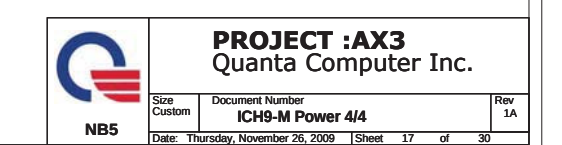
PCIE-LAN

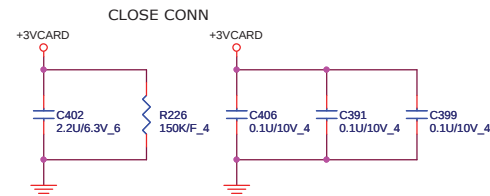
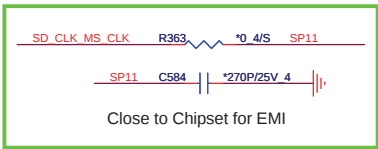


PROJECT :AX3
Quanta Computer Inc.

Size Custom	Document Number ICH9-M DMI/PCIE/USB 2/4	Rev 1A
Date: Friday, November 27, 2009	Sheet 15 of 30	

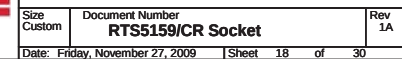


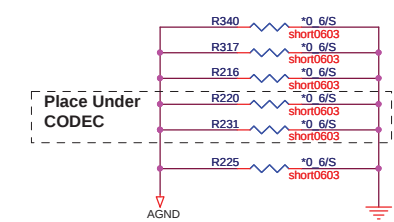
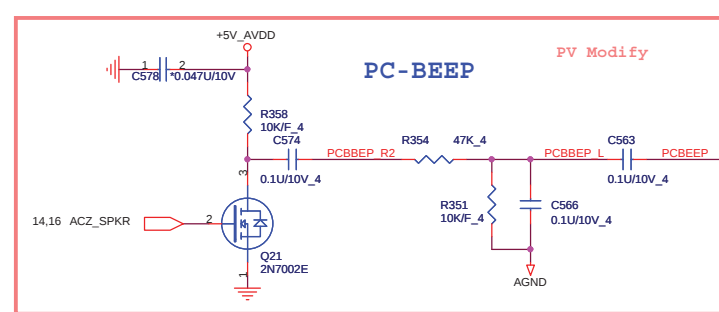
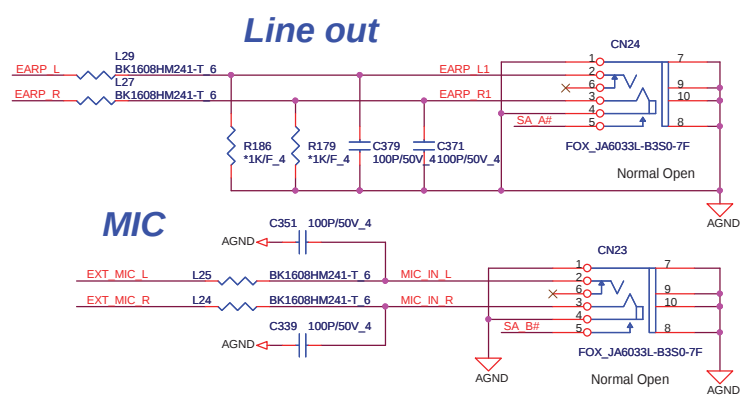
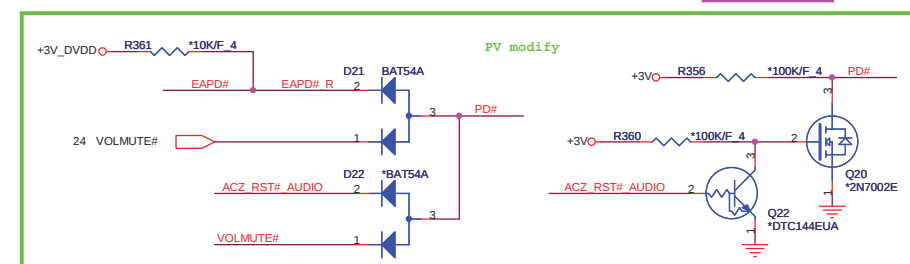
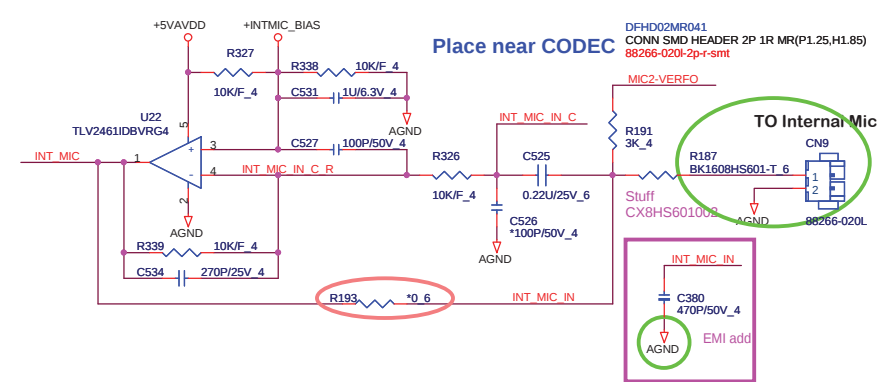
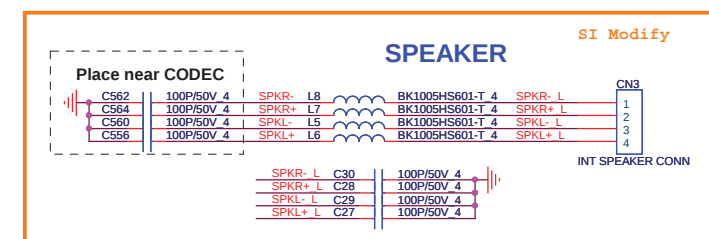
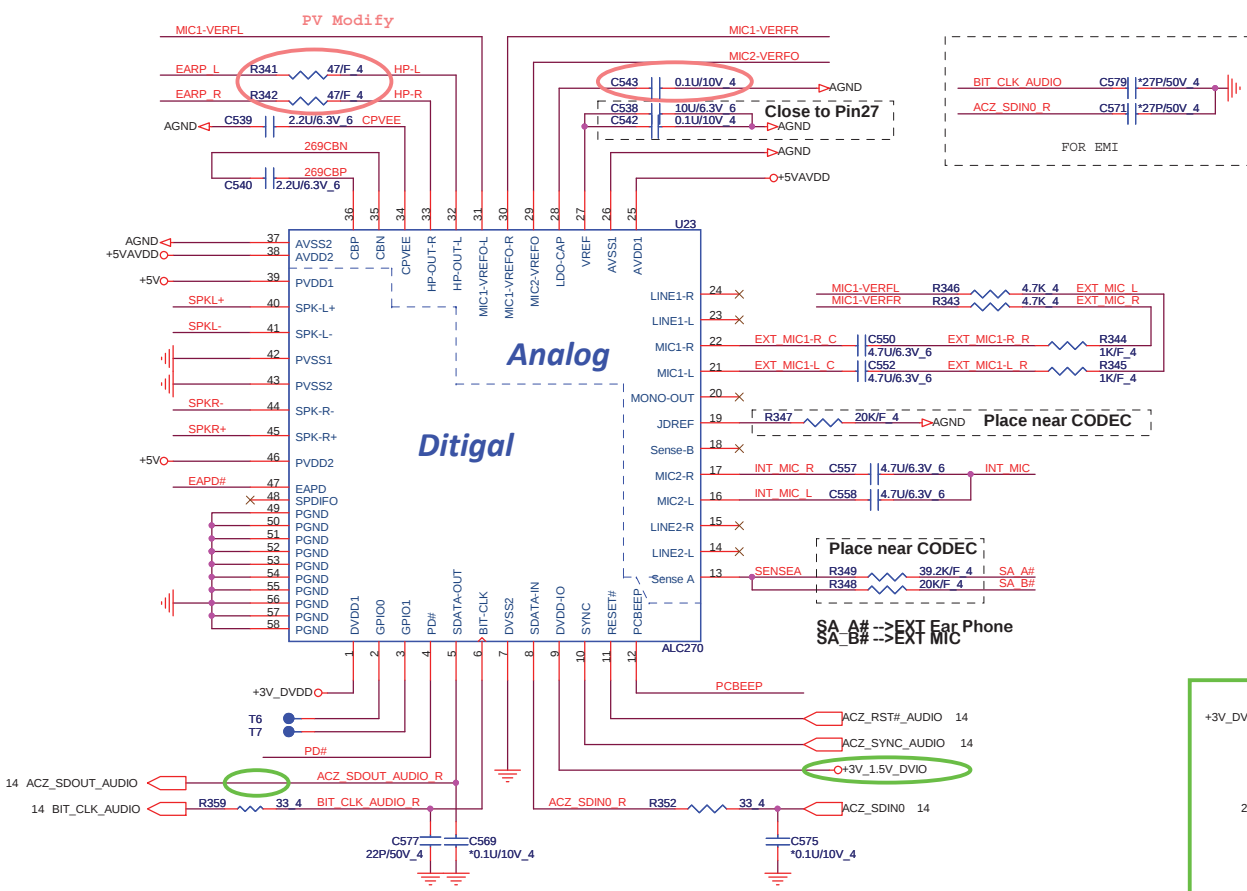
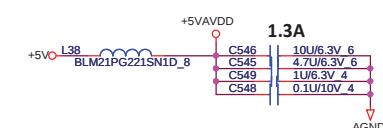
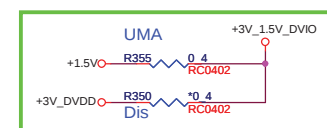
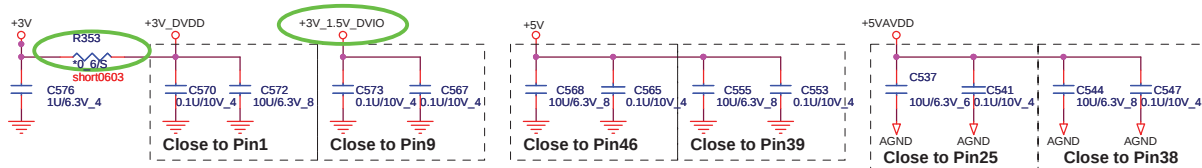


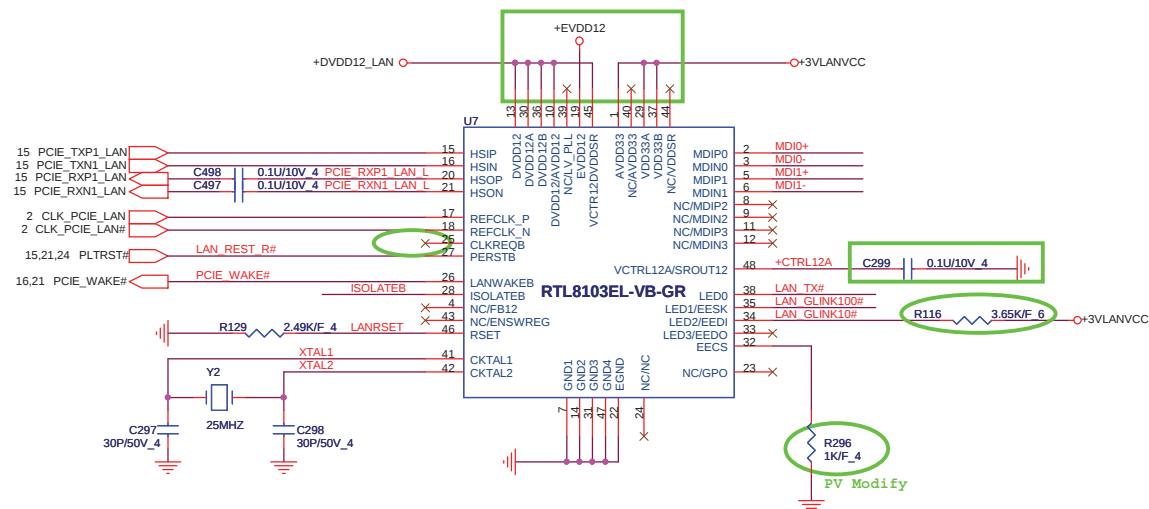


	SD/MMC	MS	XD
SP0			
SP1			XD_CD#
SP2	SD_WP		
SP3	SD_CD#		
SP4	SD_DAT1		XD_DA
SP5		MS_BS	XD_D5
SP6		MS_D1	XD_D3
SP7	SD_DAT0	MS_D0	XD_D6
SP8	SD_DAT7	MS_D2	XD_D7
SP9		MS_INS#	
SP10	SD_DAT6	MS_D3	XD_D2
SP11	SD_CLK	MS_SCLK	XD_D1
SP12	SD_DAT5		XD_D0
SP13	SD_DAT4		XD_WP#
SP14			XD_B#
SP15	SD_DAT3		XD_RE#
SP16	SD_DAT2		XD_RE#
SP17			XD_ALE
SP18			XD_CE#
SP19			XD_CLE

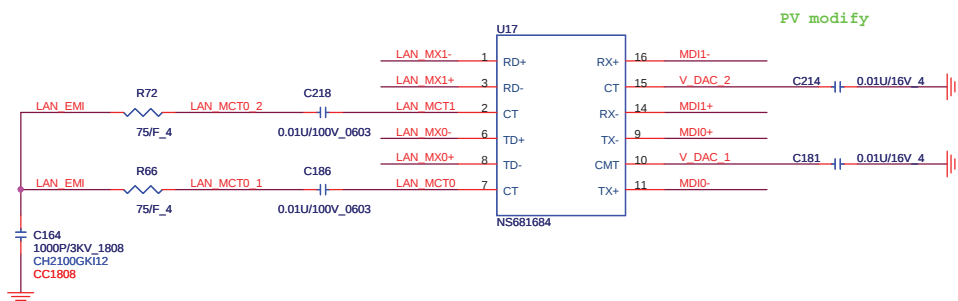
+3V



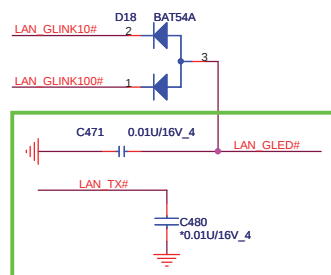




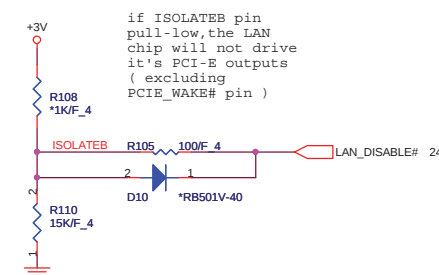
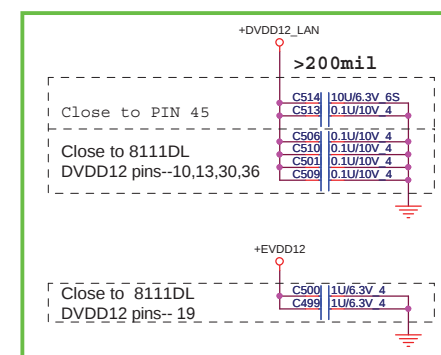
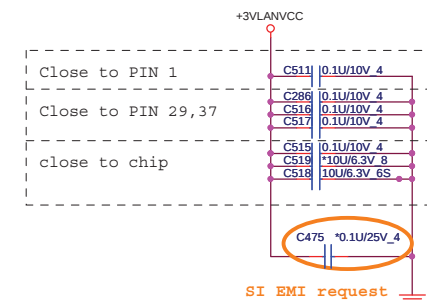
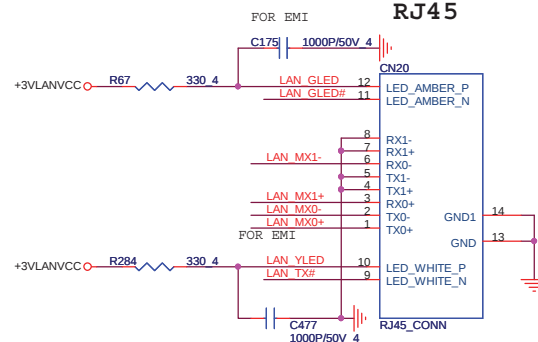
Transformer for 10/100



Lan Con.

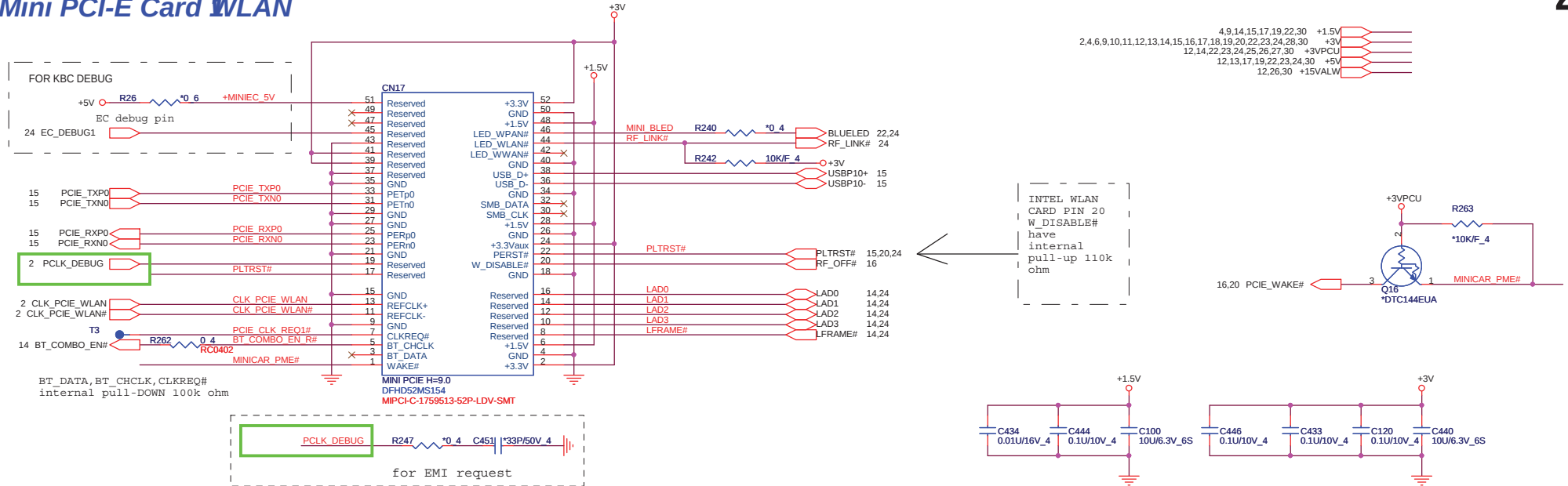


RJ45

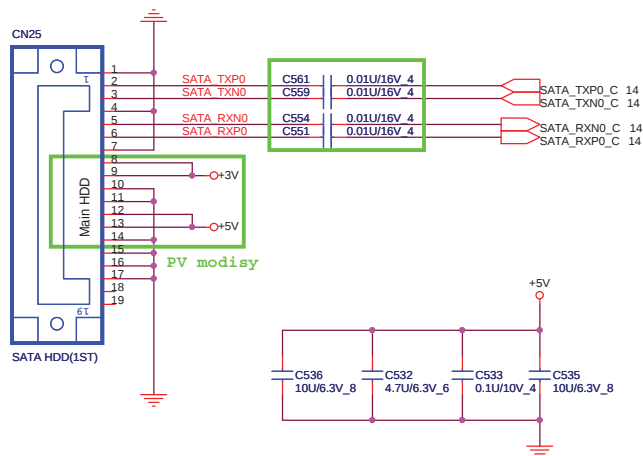


2,4,6,9,10,11,12,13,14,15,16,17,18,19,21,22,23,24,28,30 +3V
30 +3VLANVCC

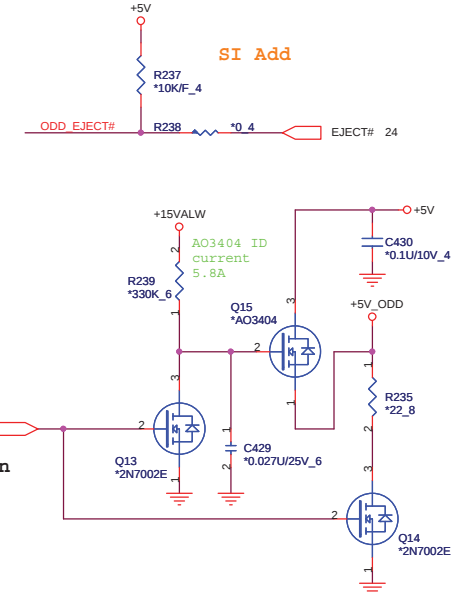
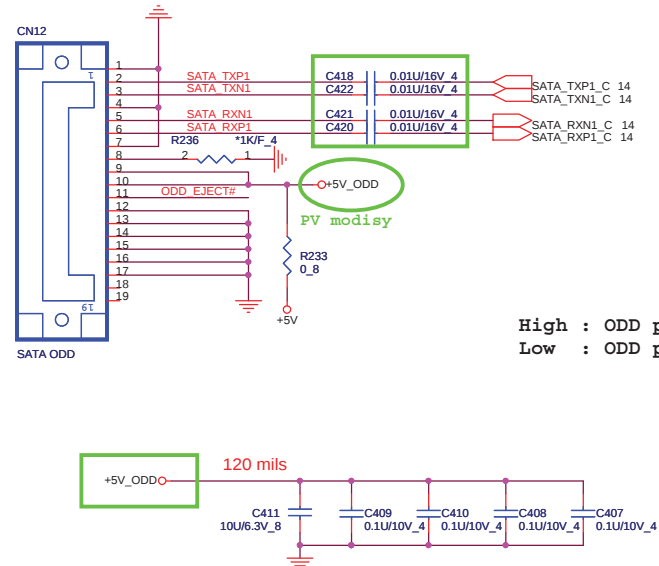
Mini PCI-E Card WLAN



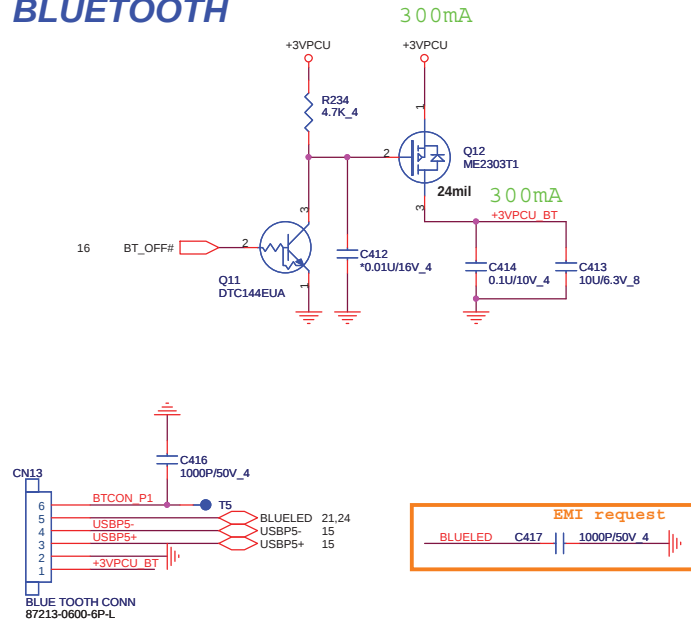
SATA HDD CONNECTOR



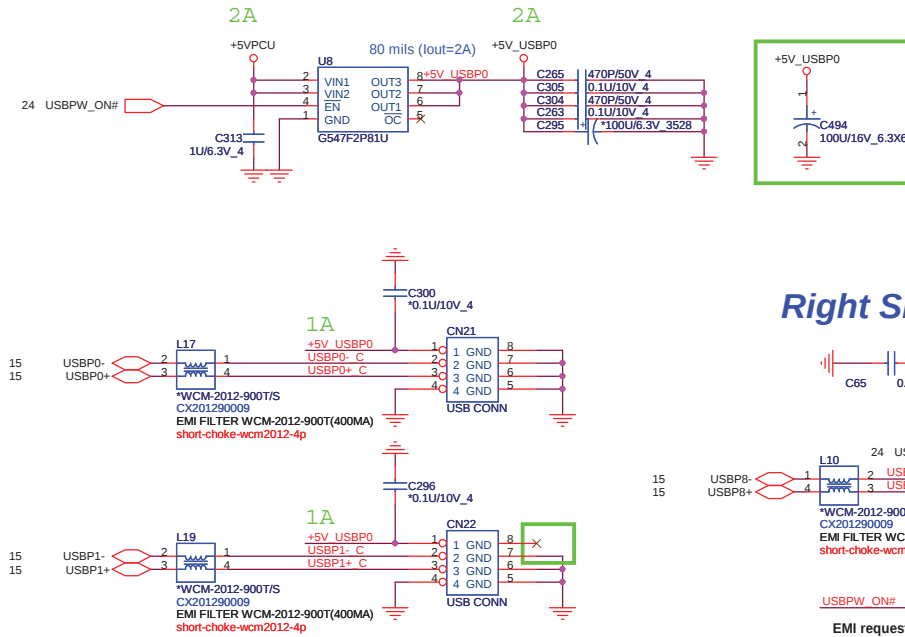
SATA ODD CONNECTOR



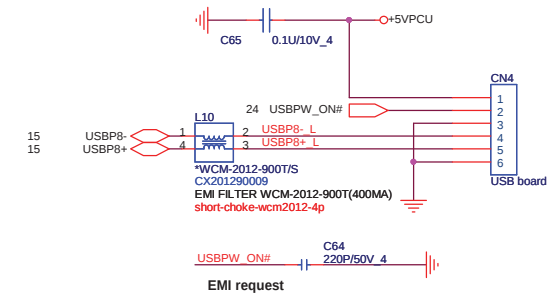
BLUETOOTH



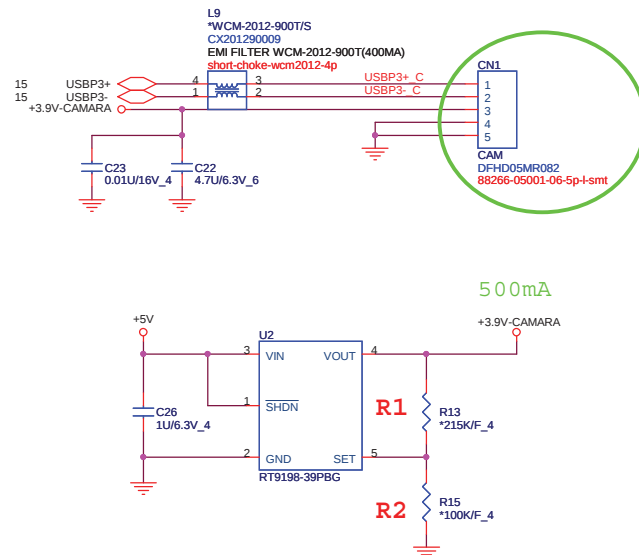
LEFT SIDE USBX1



Right SIDE USBX1

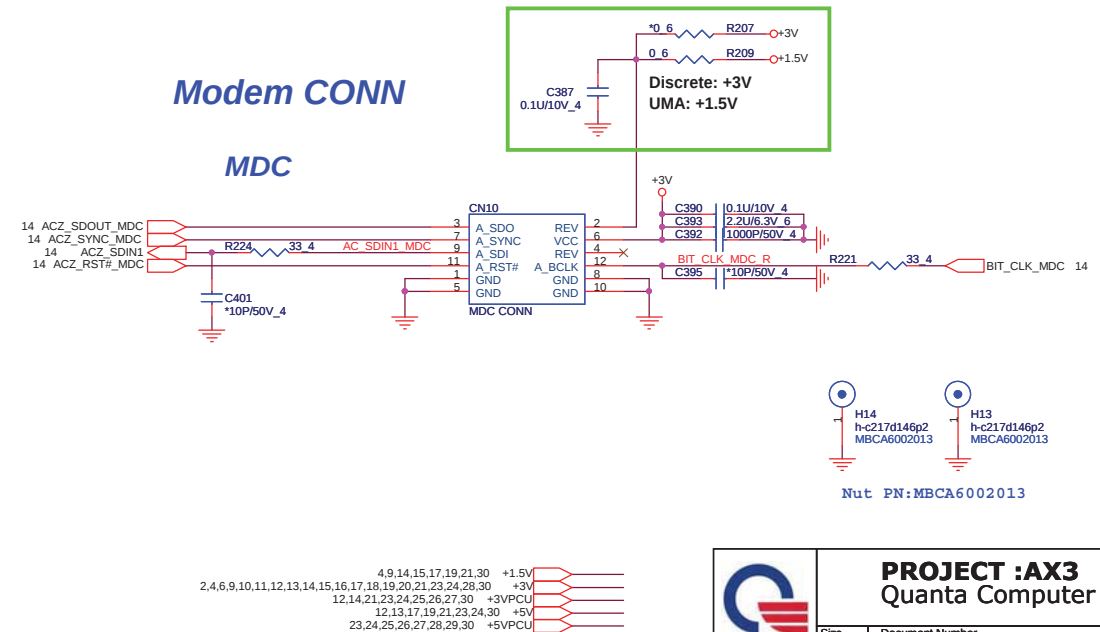


CAMERA



Modem CONN

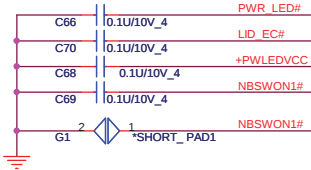
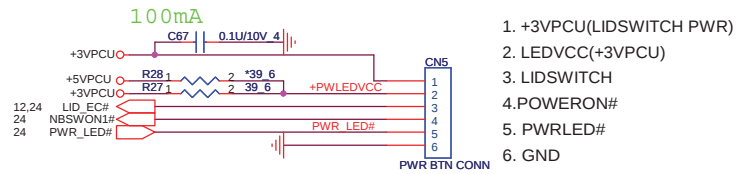
MDC



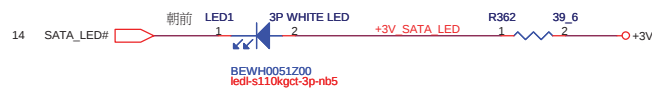
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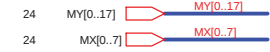
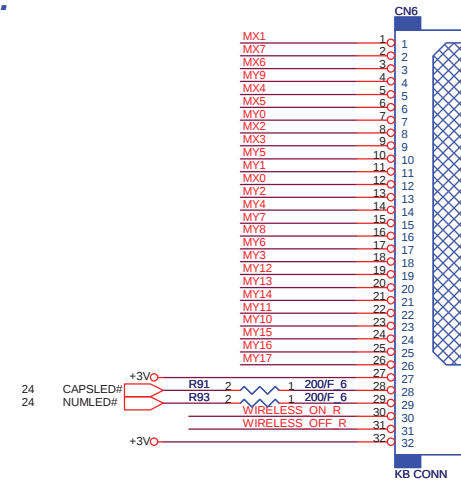
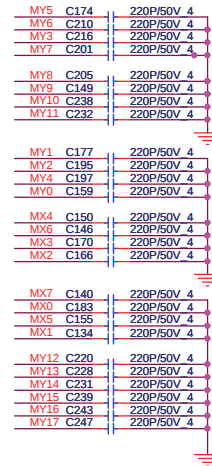
POWER BOTTON CONNECT



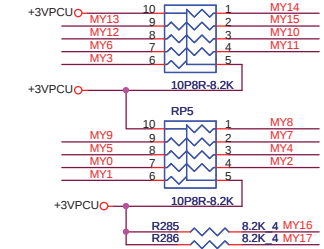
SATA LED



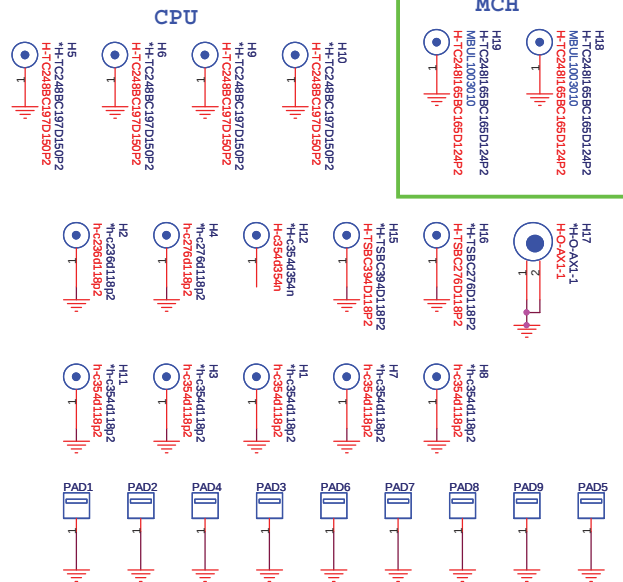
KEYBOARD Con.



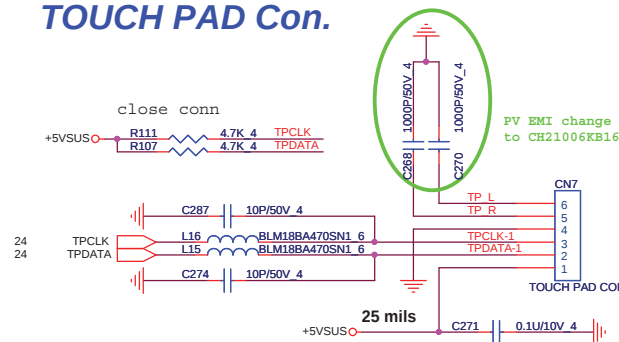
KEYBOARD PULL-UP



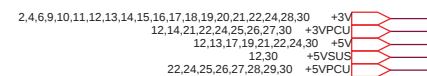
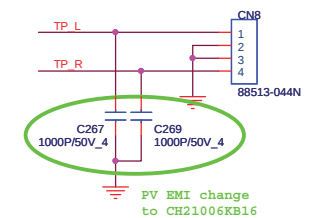
HOLE & PAD

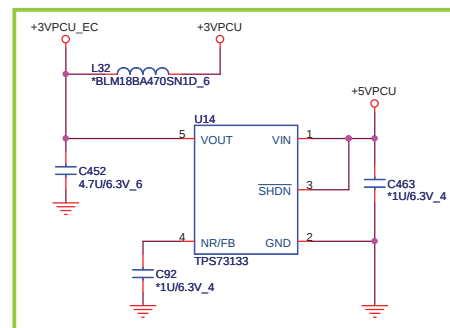
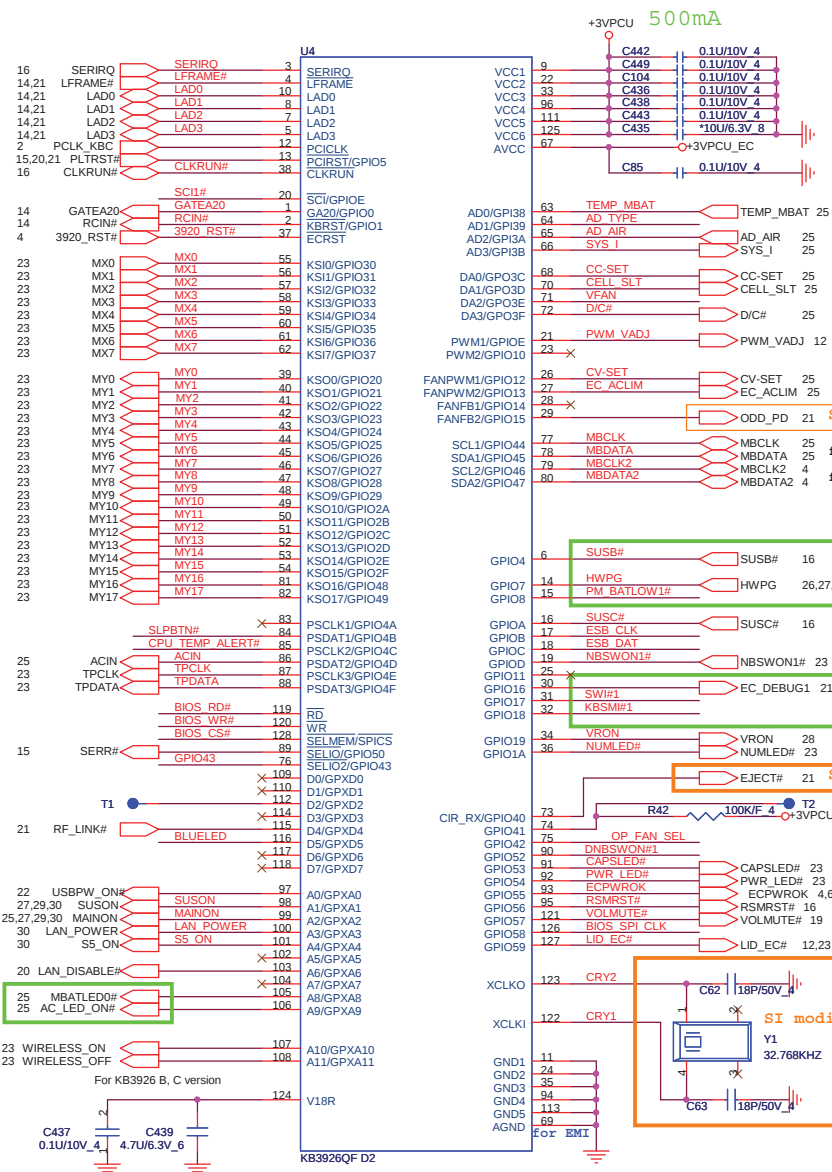


TOUCH PAD Con.

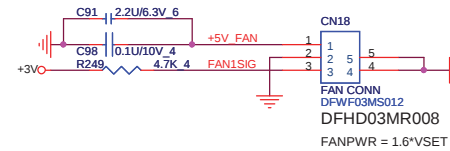
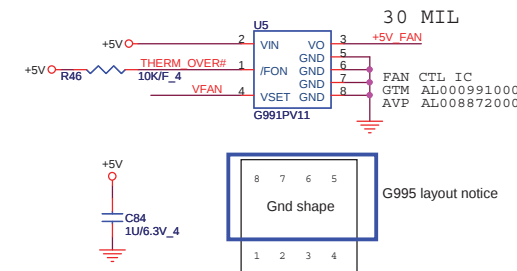


To TOUCH PAD SW board

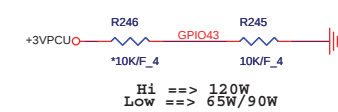




CPU FAN



Adapter select for EC

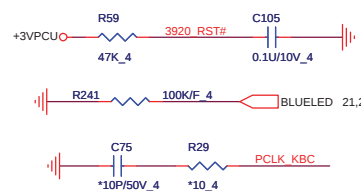


GPIO42 control fan table

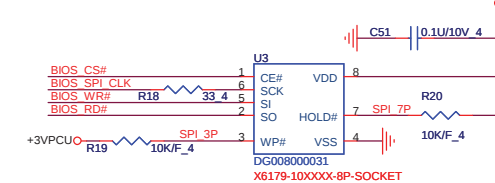
11/24 PV modify board ID.



Project Model	GPIO42
AX 14"	High
AX 15.6"	Low
AX 17.3"	Middle (1.5V)



1M byte SPI EC ROM



Socket: DG008000031
WINBOND AKE3GZNON00
EON AKE3GP0Q00
AIT AKE3GP0801
MAX AKE5GFK0Z09

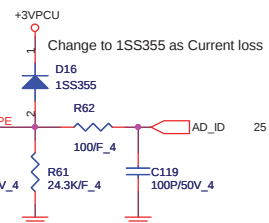
2,4,6,9,10,11,12,13,14,15,16,17,18,19,20,21,22,23,28,30
12,14,21,22,23,25,26,27,30 +3VPCU
12,13,17,19,21,22,23,30 +5V
22,23,25,26,27,28,29,30 +5VPCU



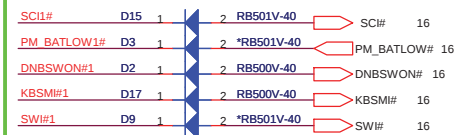
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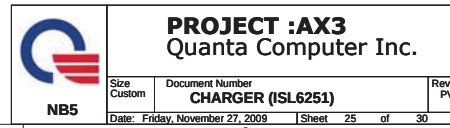
adapter Type check



Change to RB500 as Current loss

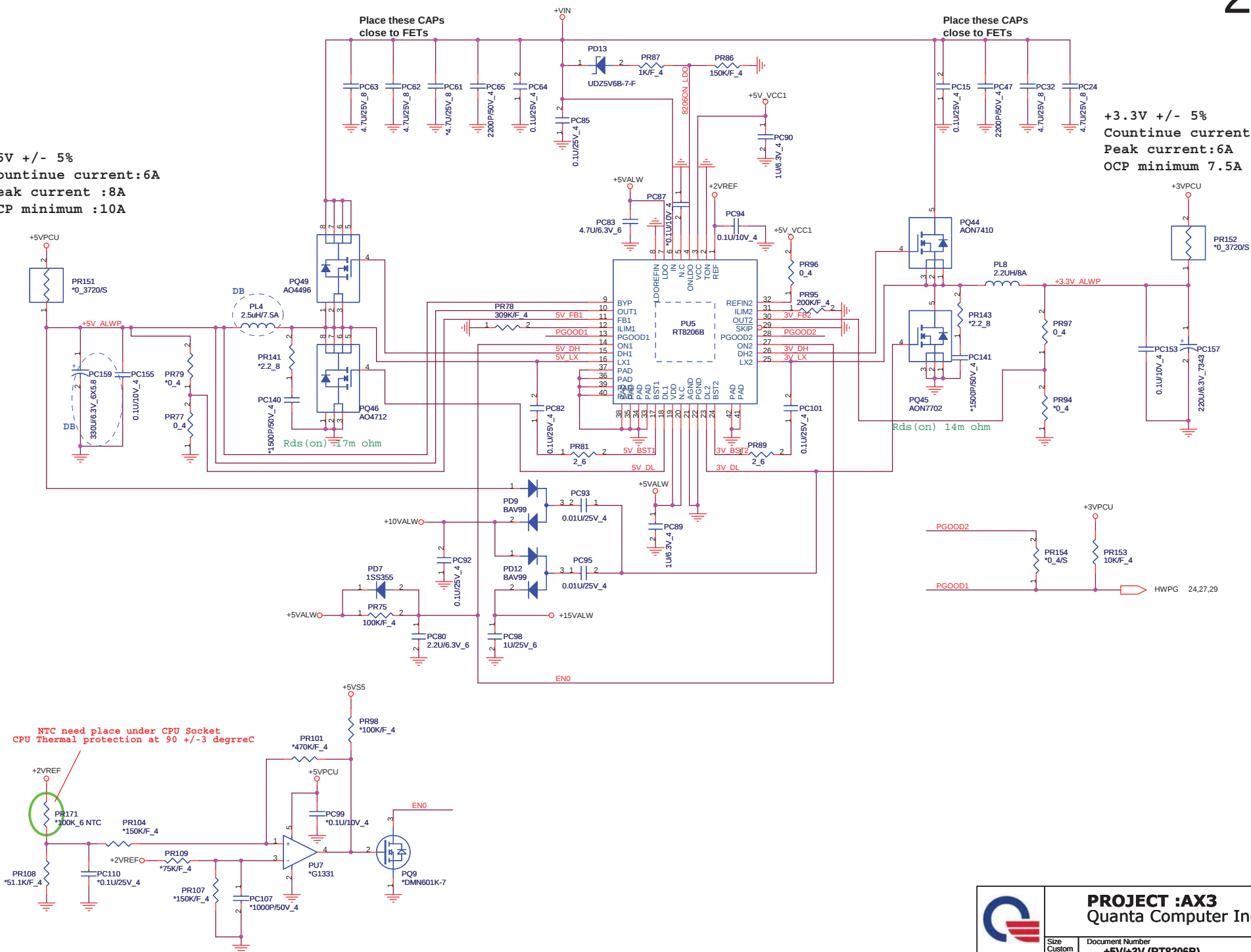


Add Pin 117,103 for DSM,116 for Bluetooth
Delete T10 and tie pin 117 from Lan for DSM



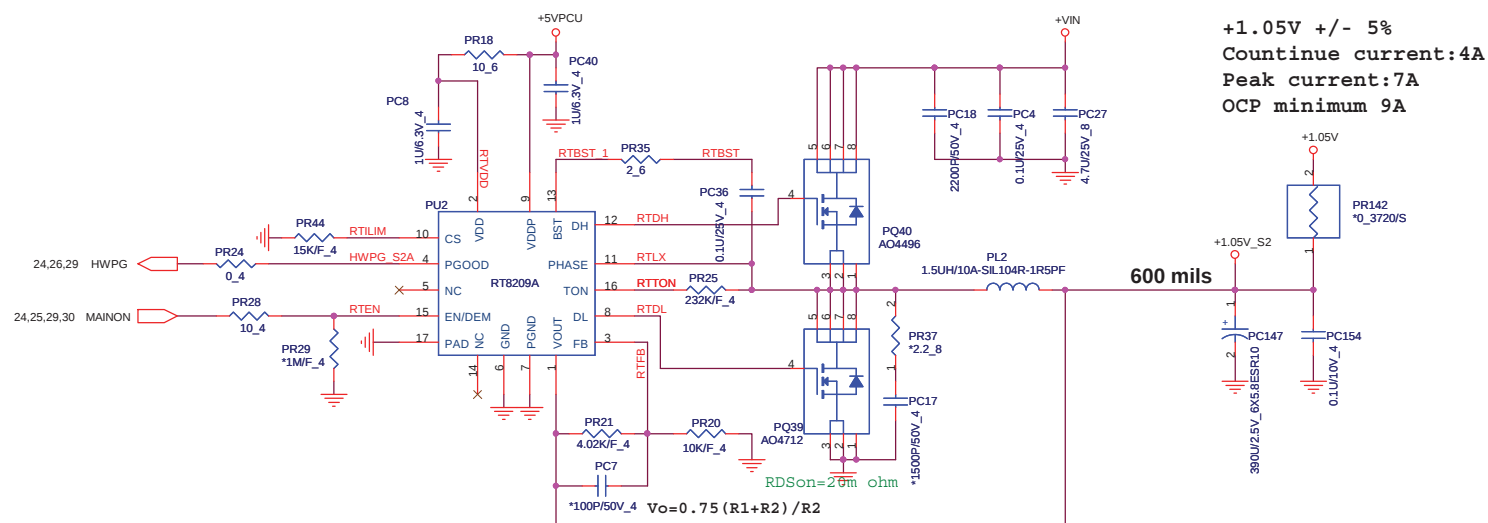
+5V +/- 5%
Countinue current:6A
Peak current :8A
OCP minimum :10A

+3.3V +/- 5%
Countinue current:5A
Peak current:6A
OCP minimum 7.5A

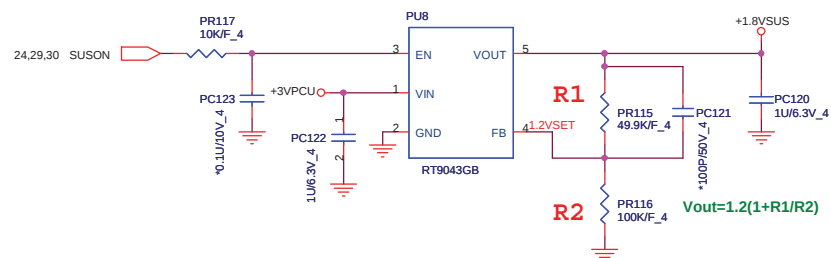


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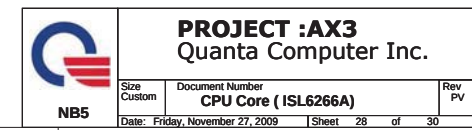


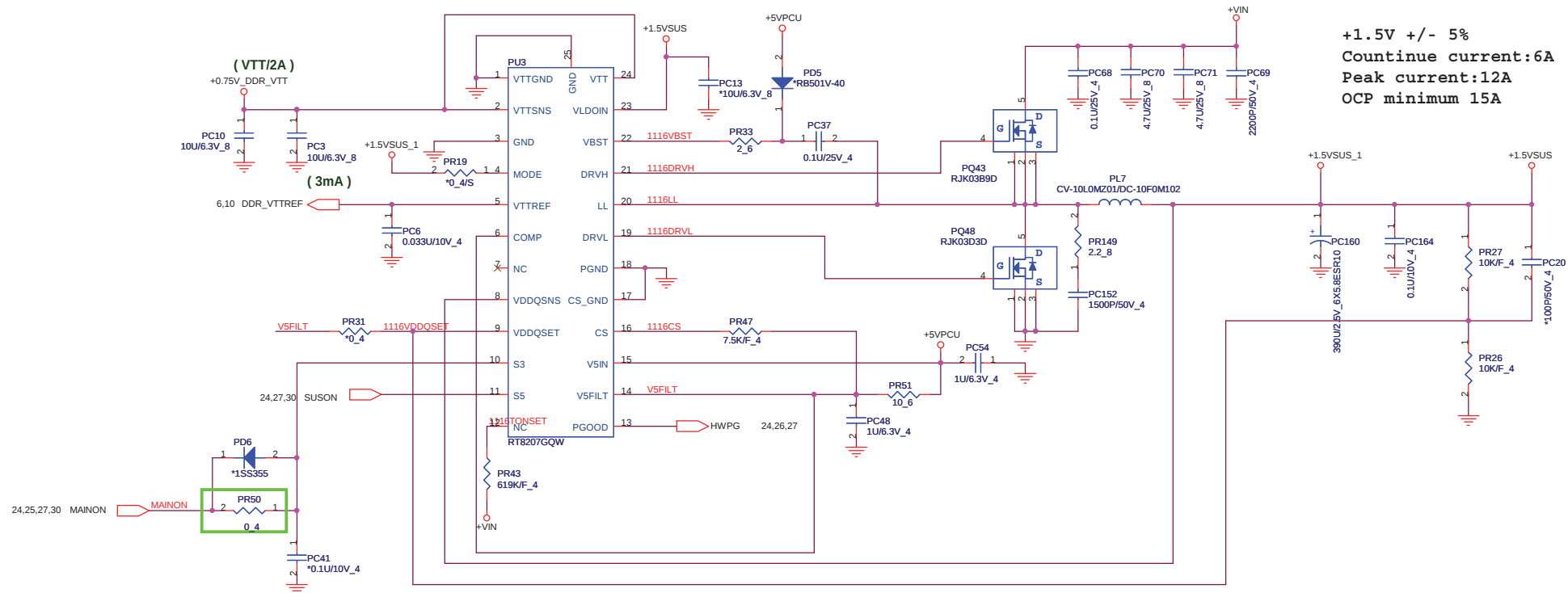
$+1.8V \pm 5\%$
 Continuous current: 150mA
 Peak current: 600mA



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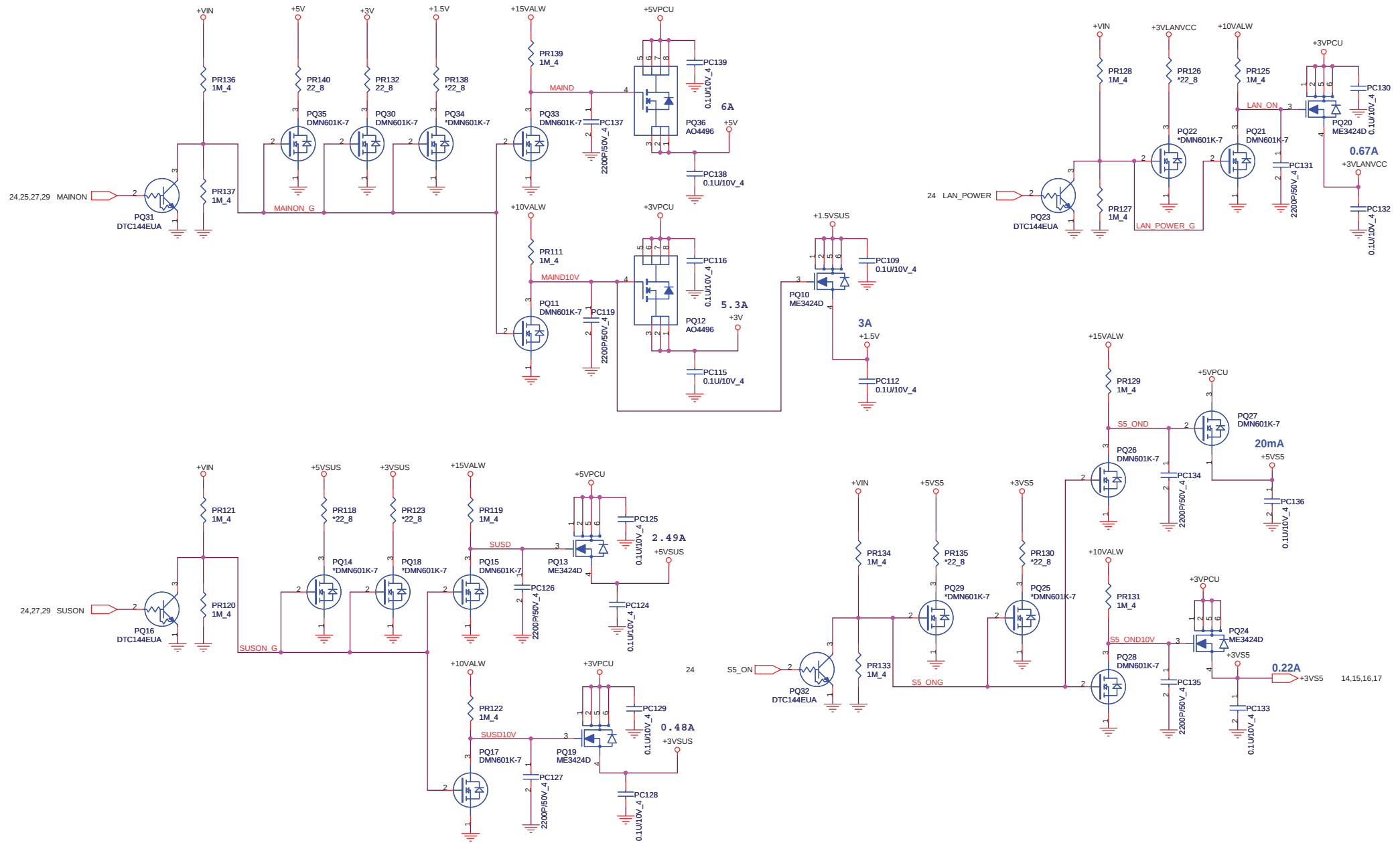
Size Custom	Document Number +1.05V/+1.8V (RT8204C)	Rev PV
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Quanta Computer Inc.

Size Custom	Document Number DDR3 (RT8207)	Rev PV
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PROJECT :AX3

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Size	Document Number	Rev
Custom	DISCHARGE/3VS5/5VS5/LAN	PV
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