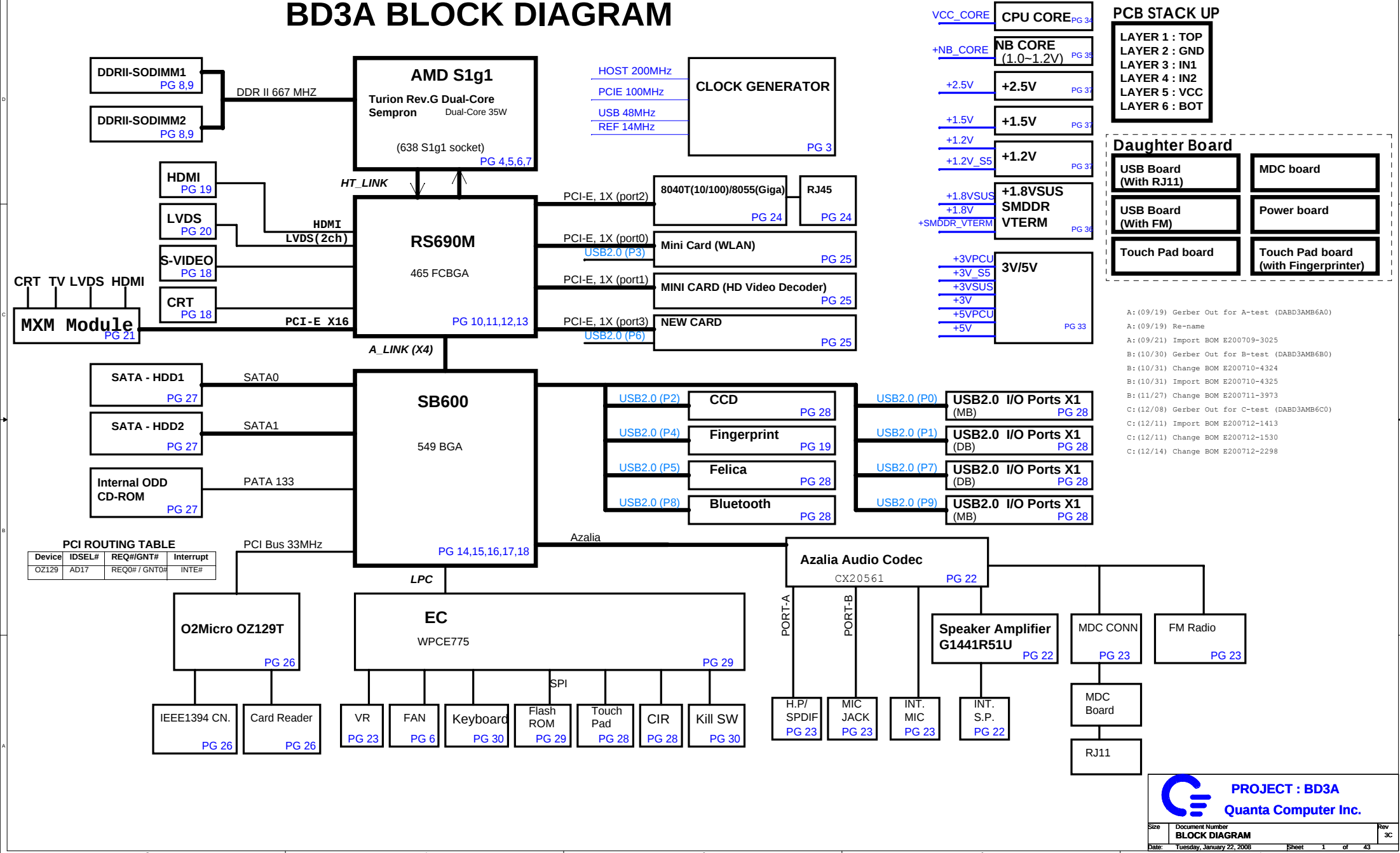
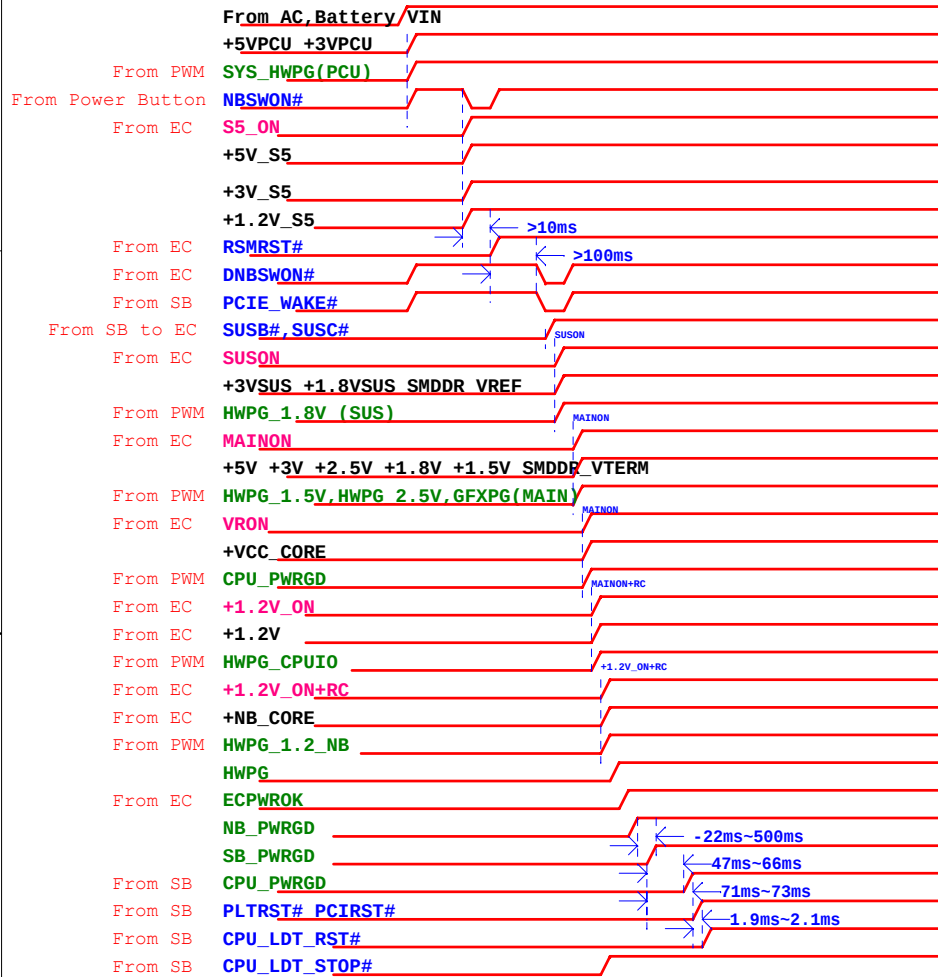


BD3A BLOCK DIAGRAM



BD3A Power On Sequence

BOM naming rule



*Note: EC will sampling SUSB# & SUSC# every 5ms.

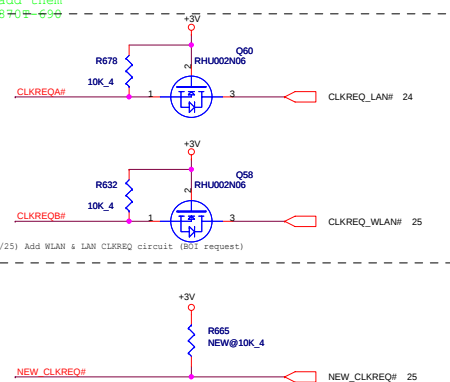
AMD SB600 SMBUS Table

	CLK GEN	RAM	Mini Card (HD-Decoder)	Mini-card(WL)	New Card	HDMI
SB600 SDATA0/SCLK0 (+3V)	V	V	V	V	V	
SB600 SDATA0/SCLK0 (+3V S5)						V
Power	+3V	+3V	+3V	+3V (Atheros)	+3V	+3V S5
Reserve MOS ckt	V	V	V	V	V	V

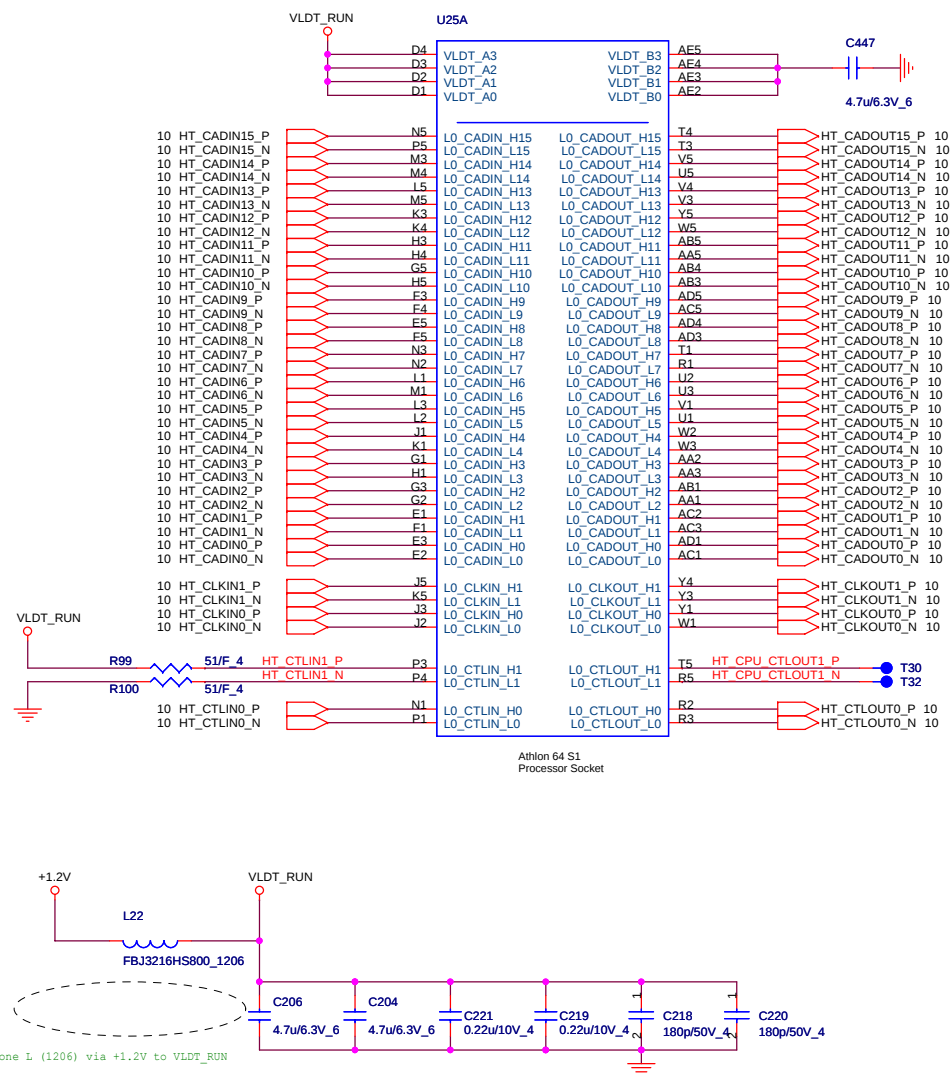
Items	Function	BTO	Name	Description
1	CIR	v	CIR@	
2	HDMI port	v	HDM@	
3	HDMI transmitter	v	SI@	Silicon image SiI 1392/1932
4	HDMI-CEC	v	CEC@	Renesas R8C/1B
5	Discrete VGA		EV@	External VGA stuff
6	UMA		IV@	Internal VGA stuff
7	New Card		NEW@	
8	RJ11	v	MD@	Modem
9	RJ45-10/100		40@	Marvell 8040T(10/100)
10	RJ45-1000		55@	Marvell 8055(Giga)
11	Option for RJ45-10/100 and RJ45-1000		40@55@	Option for 8040/8055
12	TV	v	TV@	
13	Cardbus		CB@	
14	FM transmitter	v	FM@	
15	Mainstream ID LED		MID@	
16	Low cost ID LED		LID@	
17	CCD	v	CCD@	
18	INT MIC	v	I_MIC@	
19	AMD Hyper Flash		HF@	Only for AMD platform
20	North bridge(690MC/RS780MC)		MC@	Only for AMD platform
21	North bridge(RX780)		RX@	Only for AMD platform
22	PowerXpress		PX@	Only for AMD platform
23	PowerXpress with UMA SKU		PX@IV@	Only for AMD platform
24	PowerXpress with Discrete VGA SKU		PX@EV@	Only for AMD platform
25	Power player/Power Shift		PP@	Only for AMD platform

EC SMBUS Table

	Battery	CPU thermal Sensor	EC EEPROM	VGA thermal Sensor	Touch Sensor	HDMI CEC
EC775 SDATA1/SCLK1 (+3VPCU)	V					
EC775 SDATA2/SCLK2 (+3VPCU)		V	V			
EC775 SDATA3/SCLK3 (+3VPCU)				V	V	V
EC775 SDATA4/SCLK4 (+3VPCU)						
Power	+3VPCU	+3V	+3VPCU	+3V	+3VPCU	+5VPCU
Reserve MOS ckt	X	V	X	V	X	V



FS2	FS1	FS0	CPU	SRCCLK [2:1]	HTT	PCI	USB	COMMENT
0	0	0	Hi-Z	100.00	Hi-Z	Hi-Z	48.00	Reserved
0	0	1	X	100.00	X/3	X/6	48.00	Reserved
0	1	0	180.00	100.00	60.00	30.00	48.00	Reserved
0	1	1	220.00	100.00	36.56	73.12	48.00	Reserved
1	0	0	100.00	100.00	66.66	33.33	48.00	Reserved
1	0	1	133.33	100.00	66.66	33.33	48.00	Reserved
1	1	1	200.00	100.00	66.66	33.33	48.00	Normal Turion/Sempron operation



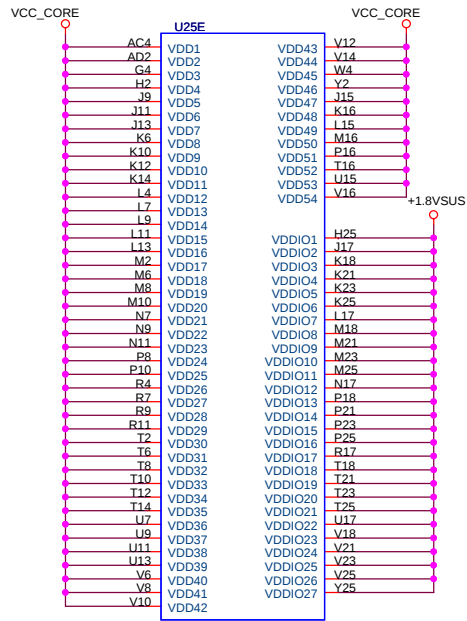
PROJECT : BD3A
Quanta Computer Inc.

5

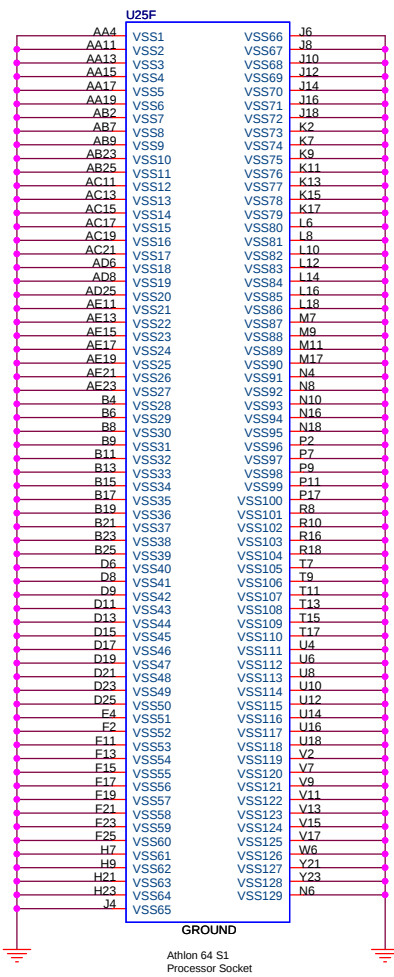
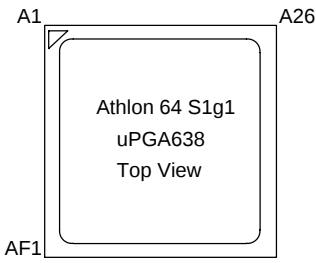


Size	Document Number TURION 64 CTRL & DEBUG	Rev 3C
Date:	Tuesday, January 22, 2008	Sheet 6 of 43

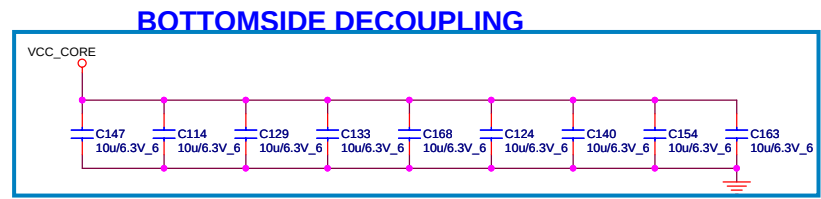
PROCESSOR POWER AND GROUND



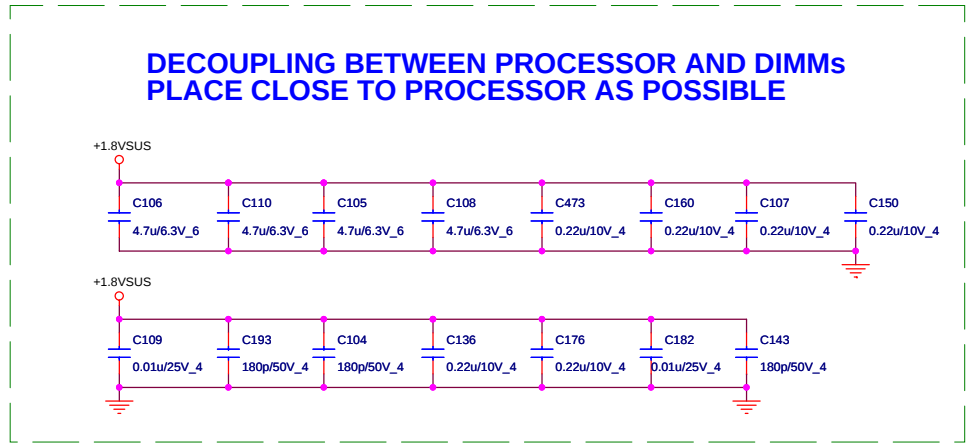
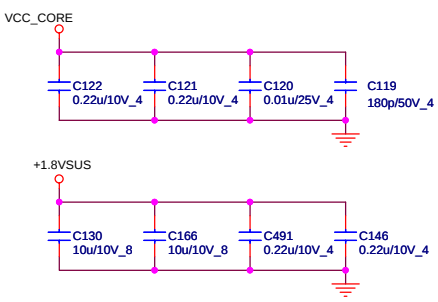
Athlon 64 S1 Processor Socket

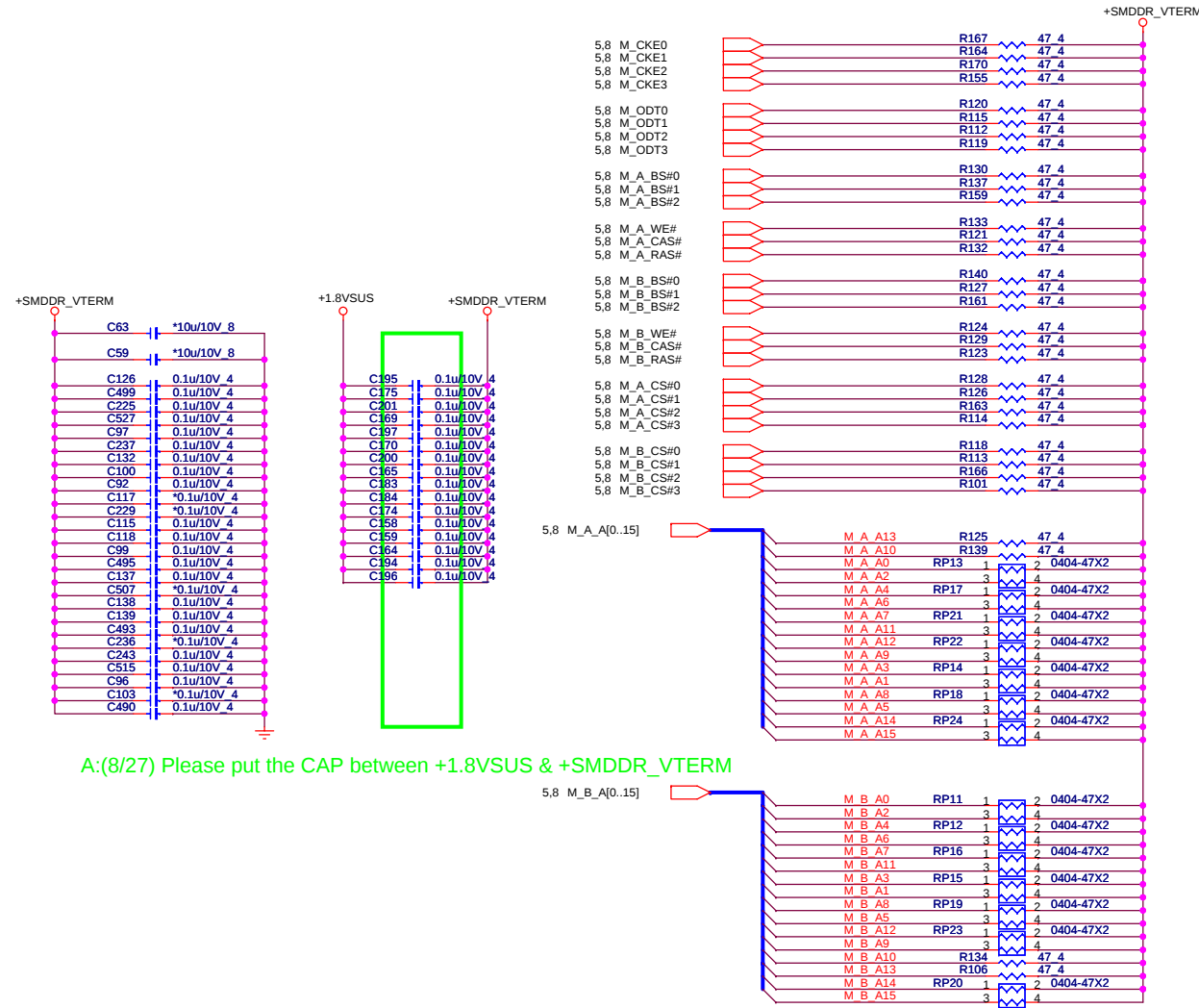


Athlon 64 S1 Processor Socket

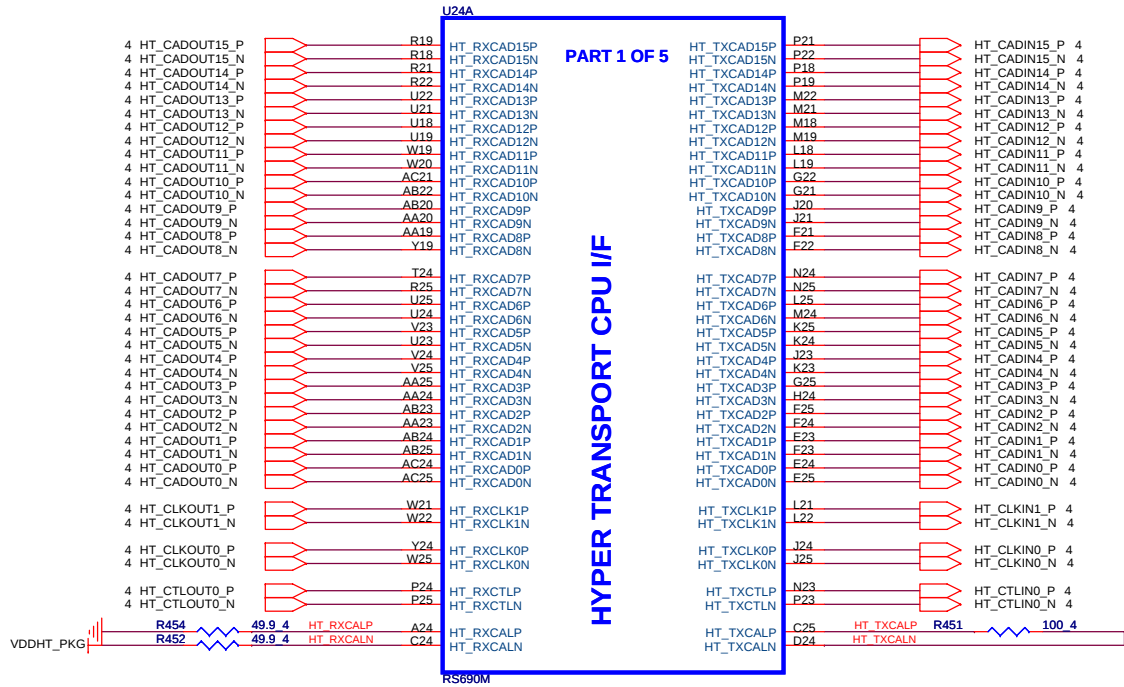


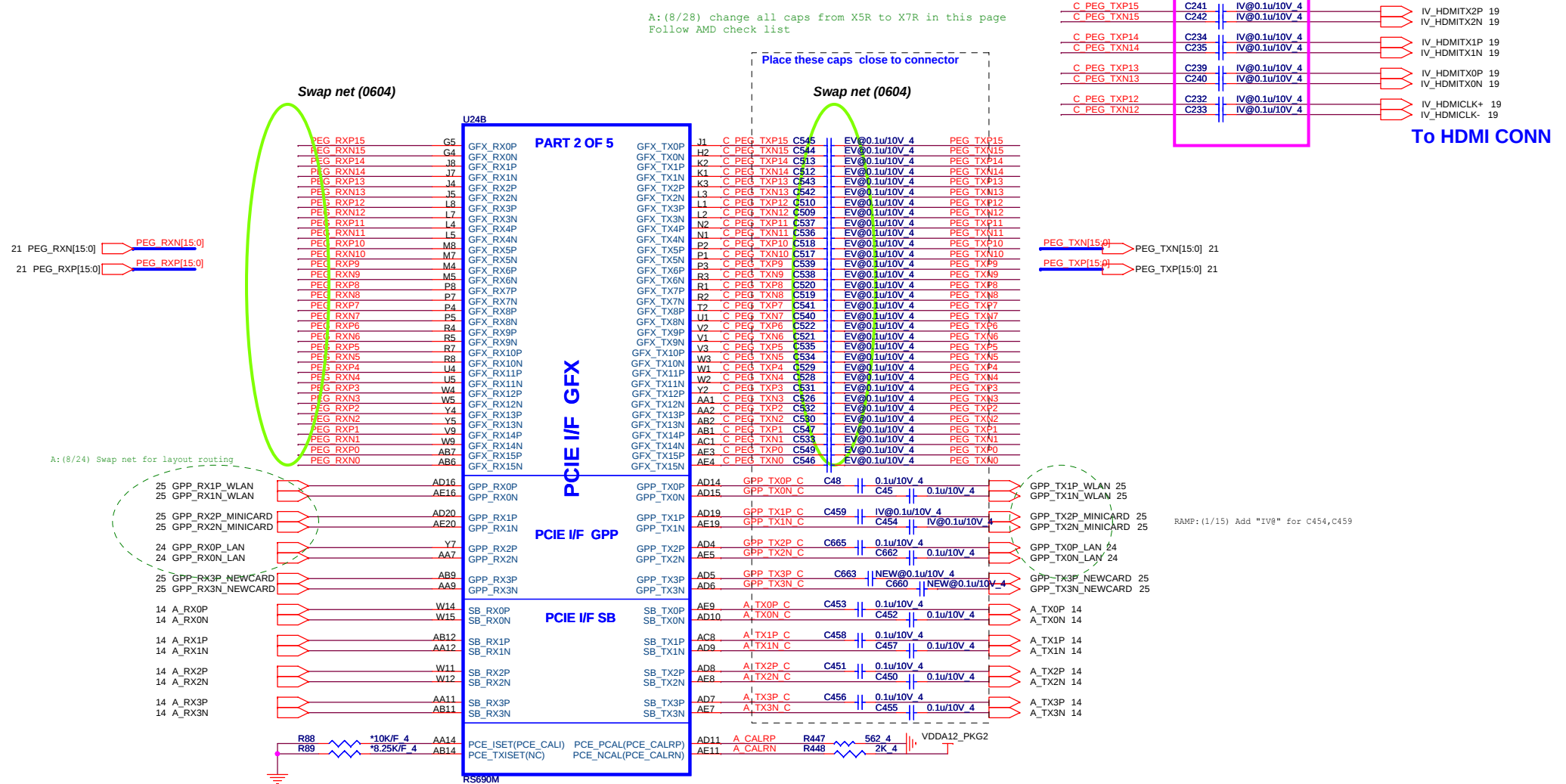
PP 0103



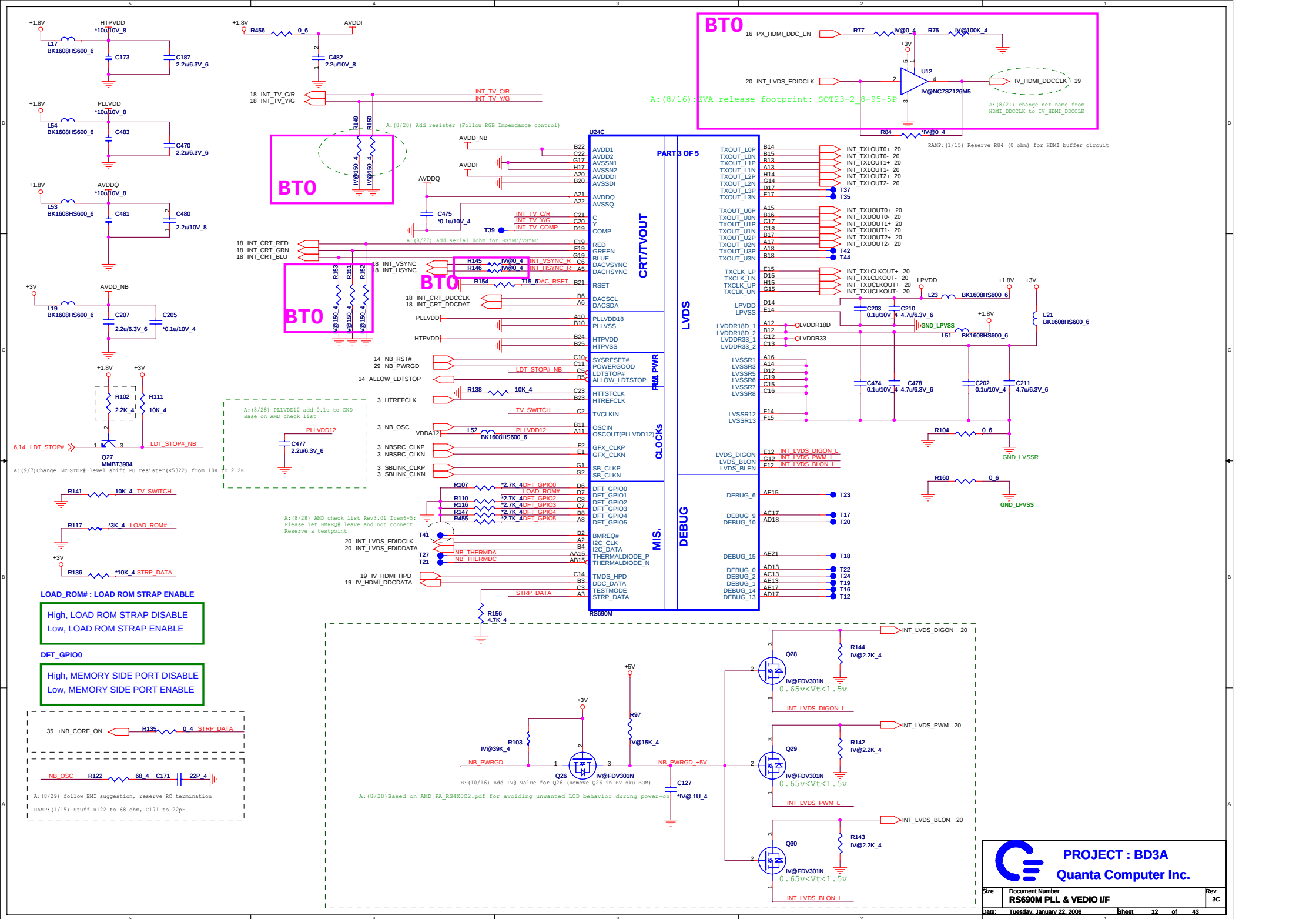


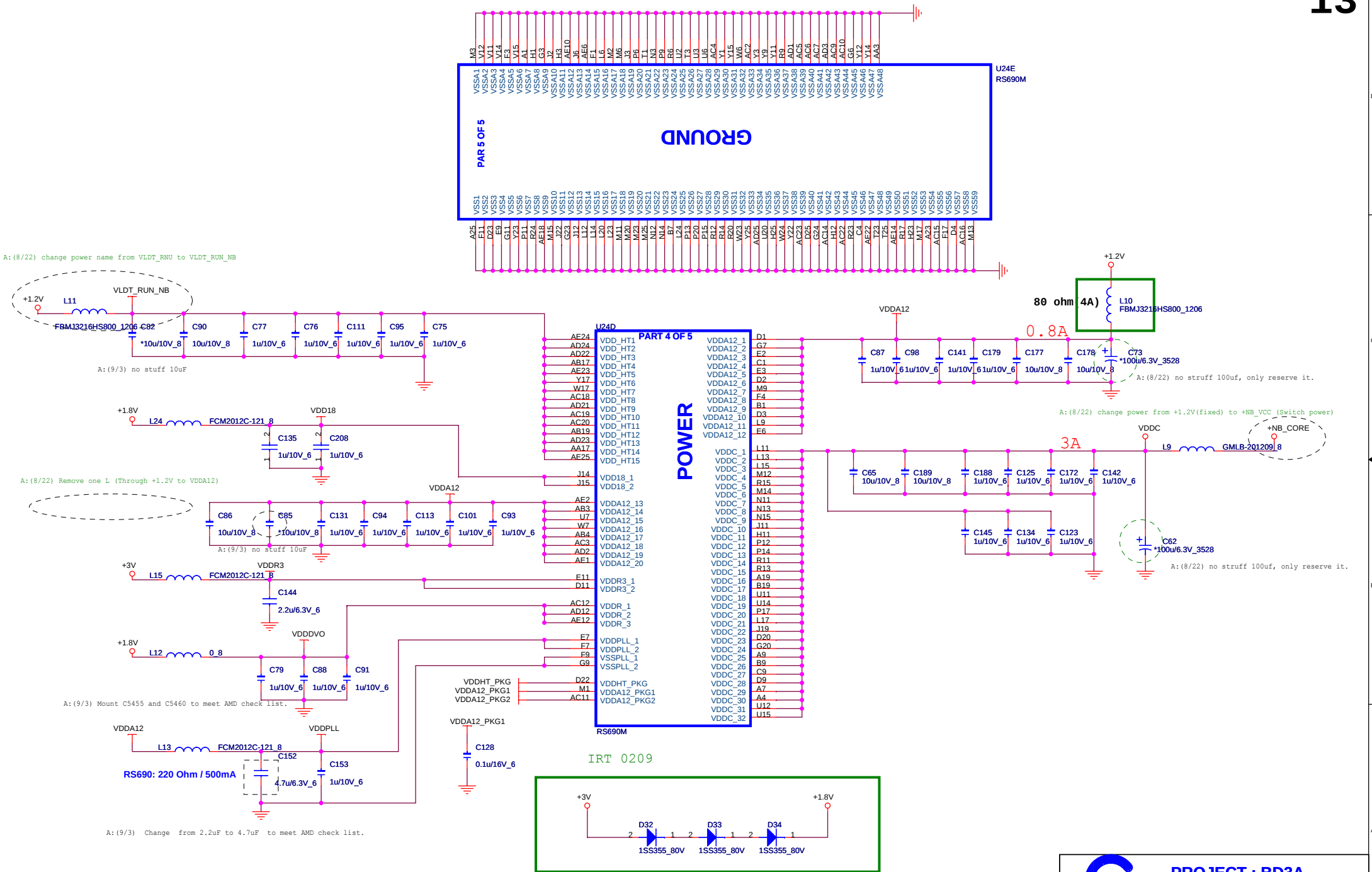
A:(8/27) Please put the CAP between +1.8VSUS & +SMDDR_VTERM





PROJECT : BD3A
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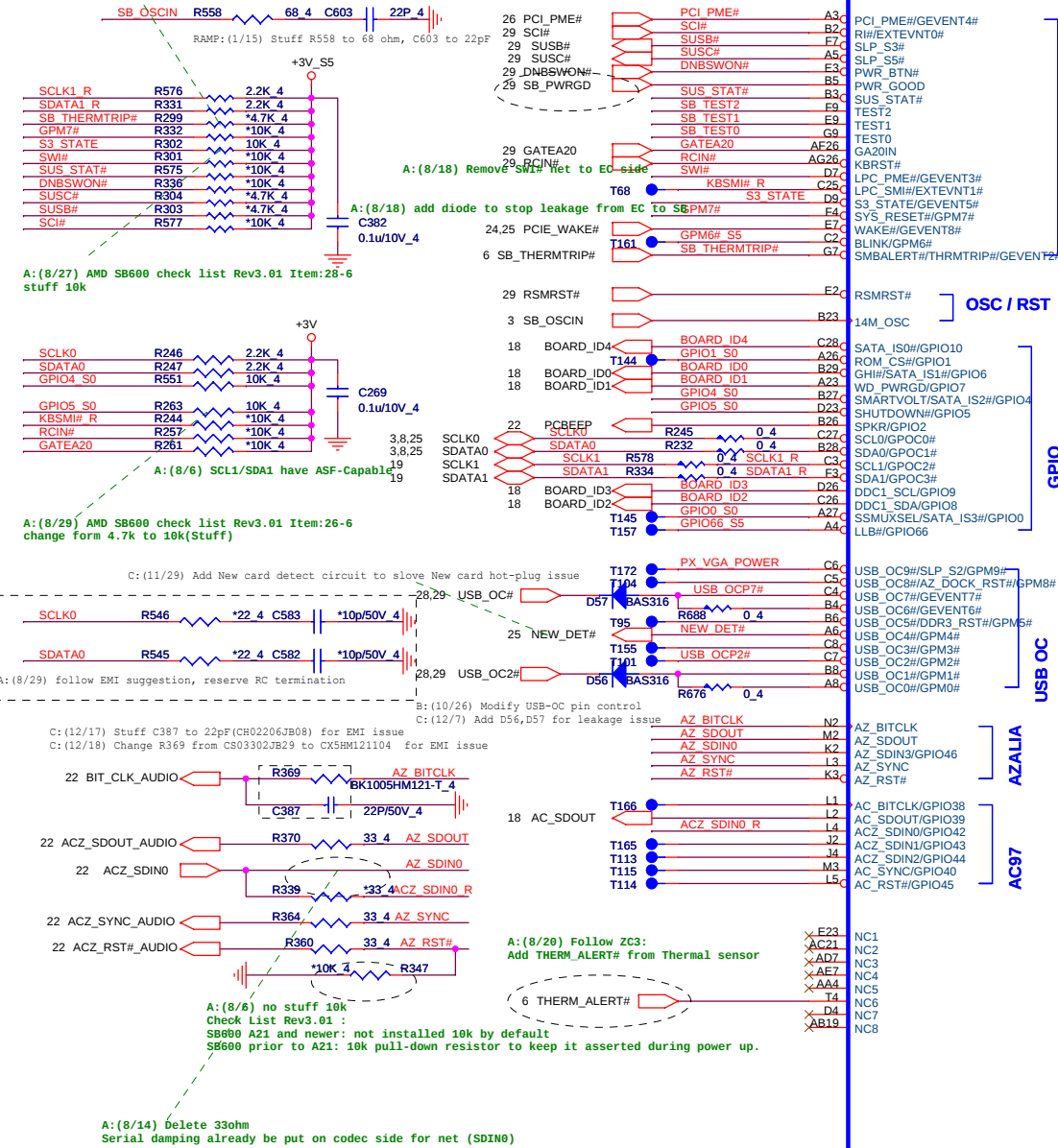






A:(8/27) no stuff 4.7k
AMD SB600 check list Rev3.01 Item:24-17
SB600 have internal PU resistor

A:(8/14) change net name from SBPWROK to ECPWROK



Part 4 of 4

ACPI / WAKE UP
EVENTS

USB INTERFACE

OSC / RST

GPIO

USB OC

USB PWR

AZALIA

AC97

SB600 SB 23x23mm

Part 4 of 4

ACPI / WAKE UP
EVENTS

USB INTERFACE

OSC / RST

GPIO

USB OC

USB PWR

AZALIA

AC97

USBCLK A17 USBCLK 3

USB_RCOMP A14 USB_RCOMP R275 11.8K/F 4

USB_ATEST1 A11 T151

USB_ATEST0 A10 T152

USB_HSDP9+ H12 BT_USB8+ 28

USB_HSDM9- G12 BT_USB8- 28

USB_HSDP8+ E12 R473 0 4

USB_HSDM8- D12 R475 0 4

NEW_USB8P6 25

NEW_USB8P6- 25

USB_HSDP7+ E14 R461 0 4

USB_HSDM7- D14 R472 0 4

USB8P0+ 28

USB8P0- 28

USB_HSDP6+ G14 R476 0 4

USB_HSDM6- H14 R498 0 4

USB8P7+ 28

USB8P7- 28

USB_HSDP5+ D16 FELICA_USB8P5+ 28

USB_HSDM5- F16 FELICA_USB8P5- 28

USB_HSDP4+ D18 FP_USB8P4+ 28

USB_HSDM4- E18 FP_USB8P4- 28

USB_HSDP3+ G16 WL_USB8P3+ 25

USB_HSDM3- H16 WL_USB8P3- 25

USB_HSDP2+ G18 R670 0 4

USB_HSDM2- H18 R671 0 4

CCD_USB2P+ 20

CCD_USB2P- 20

USB_HSDP0+ D19 USB8P1+ 28

USB_HSDM0- E19 USB8P1- 28

USB_HSDP0+ G19 USB8P9+ 28

USB_HSDM0- H19 USB8P9- 28

AVDDTX_0 B9

AVDDTX_1 B11

AVDDTX_2 B13

AVDDTX_3 B16

AVDDTX_4 B18

AVDDTX_5 A9

AVDDTX_6 B10

AVDDTX_7 B12

AVDDTX_8 B14

AVDDTX_9 B17

AVDDTX_10

AVDDTX_11

AVDDTX_12

AVDDTX_13

AVDDTX_14

AVDDTX_15

AVDDTX_16

AVDDTX_17

AVDDTX_18

AVDDTX_19

AVDDTX_20

AVDDTX_21

AVDDTX_22

AVDDTX_23

AVDDTX_24

AVDDTX_25

AVDDTX_26

AVDDTX_27

AVDDTX_28

AVDDTX_29

AVDDTX_30

AVDDTX_31

AVDDTX_32

AVDDTX_33

AVSS_USB_1 A16

AVSS_USB_2 C9

AVSS_USB_3 C10

AVSS_USB_4 C11

AVSS_USB_5 C12

AVSS_USB_6 C13

AVSS_USB_7 C14

AVSS_USB_8 C16

AVSS_USB_9 C17

AVSS_USB_10 C18

AVSS_USB_11 C19

AVSS_USB_12 C20

AVSS_USB_13 D11

AVSS_USB_14 D12

AVSS_USB_15 E11

AVSS_USB_16 E12

AVSS_USB_17 F11

AVSS_USB_18 F12

AVSS_USB_19 F14

AVSS_USB_20 F16

AVSS_USB_21 F18

AVSS_USB_22 F19

AVSS_USB_23 F21

AVSS_USB_24 G11

AVSS_USB_25 G12

AVSS_USB_26 H11

AVSS_USB_27 H12

AVSS_USB_28 J11

AVSS_USB_29 J12

AVSS_USB_30 J14

AVSS_USB_31 J16

AVSS_USB_32 J18

AVSS_USB_33 J19

To Bluetooth

To New Card

To M/B USB

To M/B USB

To Felica

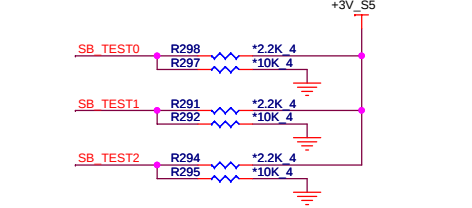
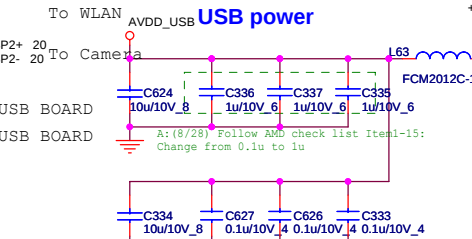
To Finger Printer

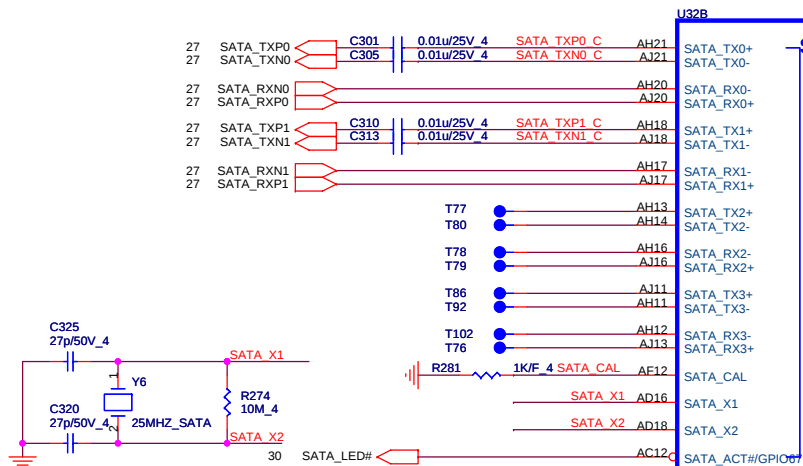
To WLAN

To Camera

To USB BOARD

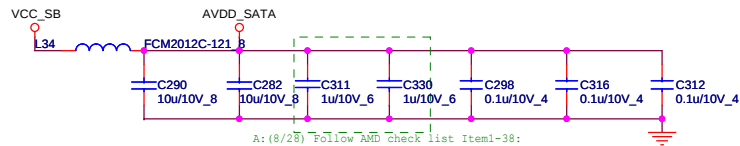
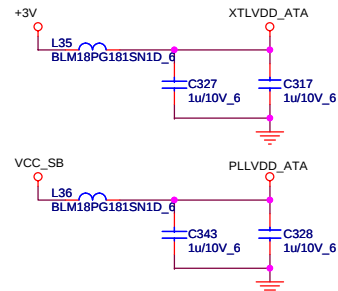
To USB BOARD





B: (10/31) change C320, C325 to 27p

SATA Power



A: (8/28) Follow AMD check list Item1-38:
Change from 0.1u to 1u

U32B

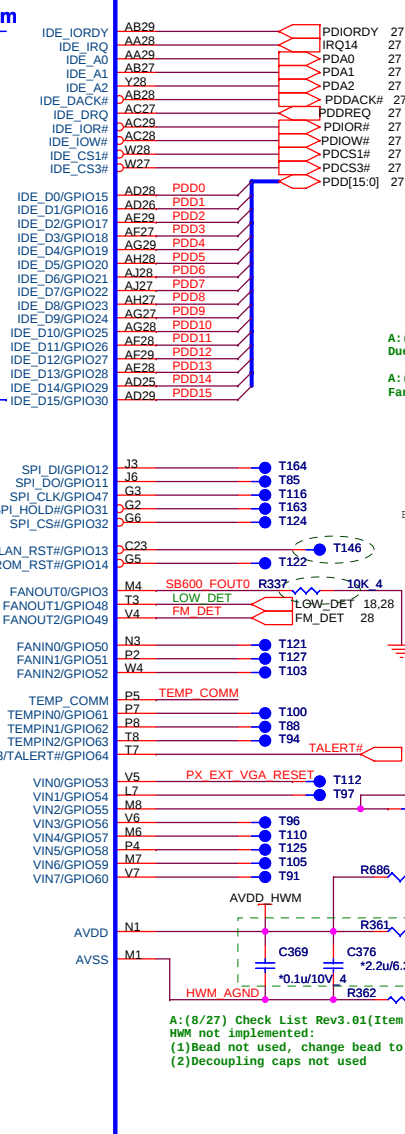
SB600 SB 23x23mm
Part 2 of 4

SERIAL ATA

SERIAL ATA POWER

SPI ROM

HW MONITOR



A: (8/14) Remove LAN_RST net. Reserve a test point.
Due to LAN_RST net already be deleted in LAN side.

A: (8/6) Check List Rev3.01:
Fan Control Not Implemented: Used as GPIO3 or terminated

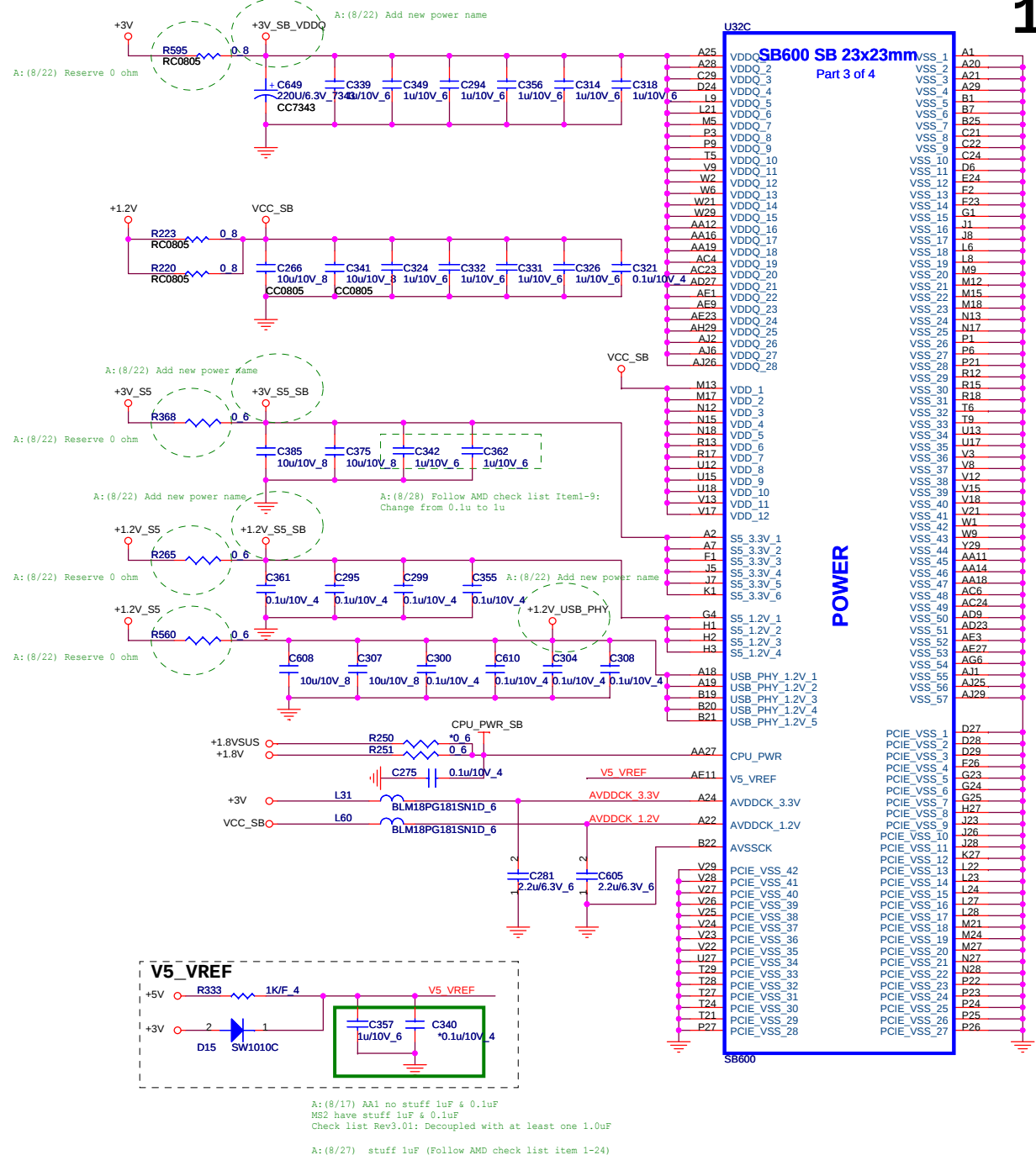
B: (10/18) add FM_DET to GPIO48
B: (10/25) add R673 (no stuff) for debug use

A: (8/27) Check List Rev3.01: 22-11
Let TEMP_COMM connect to GND
when Temp Monitor not implementing

A: (8/6) Check List Rev3.01: Tie to a +3V_S5 rail
Change AVDD from +3V to +3V_S5 (MS2/AA1: +3V for A-test)

B: (10/26) Add R686 to +3V, no stuff R361 for leakage issue

A: (8/27) Check List Rev3.01 (Item 1-33, 1-34):
HWM not implemented:
(1) Bead not used, change bead to 0 ohm (no stuff)
(2) Decoupling caps not used



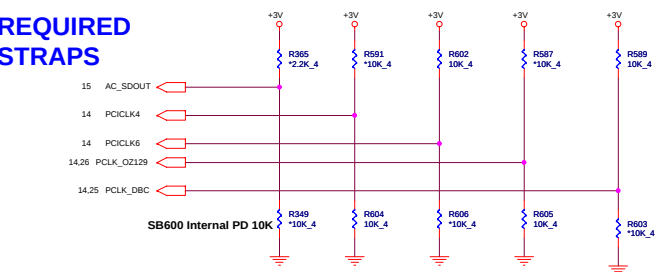
A: (8/17) AA1 no stuff 1uF & 0.1uF
MS2 have stuff 1uF & 0.1uF
Check list Rev3.01: Decoupled with at least one 1.0uF

A: (8/27) stuff 1uF (Follow AMD check list item 1-24)



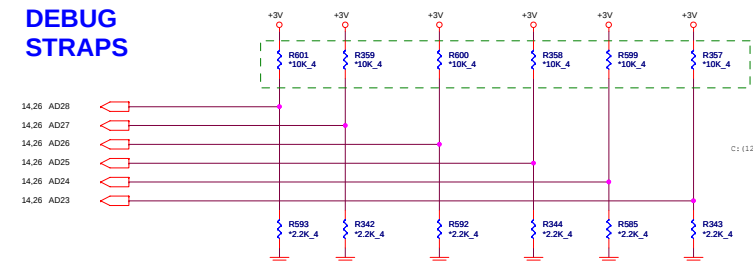
PROJECT : BD3A
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REQUIRED STRAPS



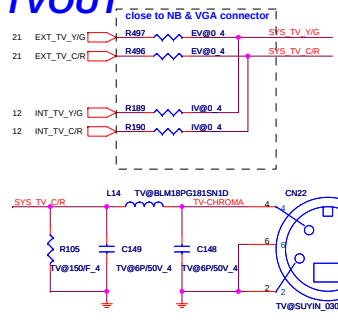
	AC_SDOUT	PCICLK4	PCICLK6	PCCLK_OZ129	PCCLK_DBC
PULL HIGH	USE DEBUG STRAPS	USE INT. PLL48	CPU IF=K8	ROM TYPE: H, H = PCI ROM H, L = SPI ROM L, H = LPC ROM L, L = FWH ROM	DEFAULT
PULL LOW	IGNORE DEBUG STRAPS	USE EXT. 48MHZ	CPU IF=P4		DEFAULT

DEBUG STRAPS



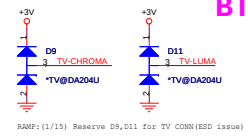
	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET	USE PCI PLL	USE ACPI BCLK	USE IDE PLL	USE DEFAULT PCIE STRAPS	BOOTFAILTIMER DISABLED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	BOOTFAILTIMER ENABLED

TVOUT



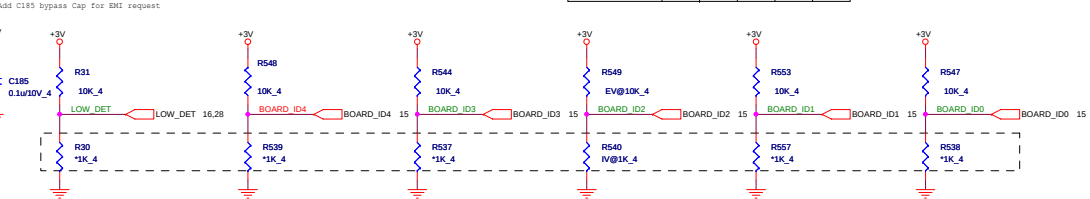
B: (10/24) change CN22 from 7PIN to 4PIN CONN
B: (10/30) change CN22 from footprint from "SV-030018FR004S100FR-RVS-4P-V" to "SV-030018FR004S100FR-RVS-4P-R"
C: (12/11) Change CN22 (S-Video) from DPM04FR296 (Yellow Color) to DPM04FR006 (Black Color)

BT0



RAMP: (1/15) Reserve D9, D11 for TV CONN (ESD Issue)

Board ID	ID4	ID3	ID2	ID1	ID0
NEW CARD					H
CARD BUS					L
CCFL Panel				H	
LCD Panel				L	
W/ MXM					
W/O MXM				H	
W/ S-VIDEO				L	
W/O S-VIDEO					
W/ HDMI					
W/O HDMI	H				

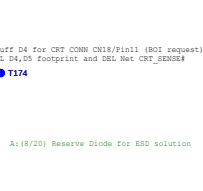
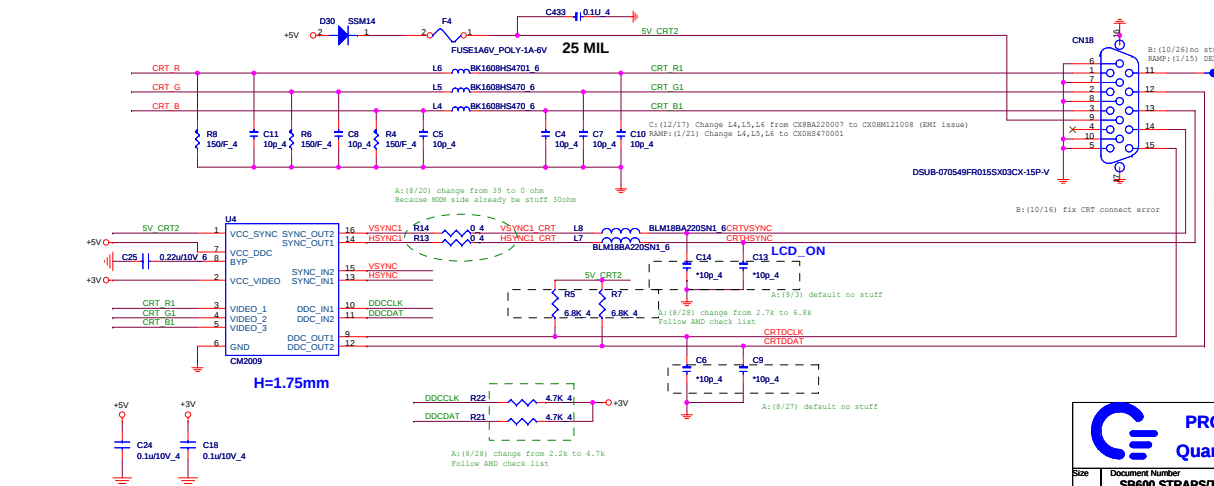
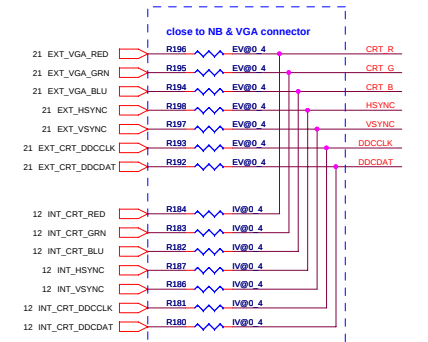


High : Low cost function board (0 key)
Low : Low cost function board (6 key)

C: (11/29) Board ID issue!
(1) Because GPIO pin of South Bridge have internal PU or PD. It will let Board ID become floating (+1.5V) when external circuit PD
(2) Change external Pull down to 1K ohm for all BOARD_ID and LOW_DET circuit

B: (10/30) GPIO8 (BOARD ID2) for UGA/MXM detection use
(1) Stiff R540 (pull low), don't stuff R549 for UGA
(2) Stiff R549 (pull high), don't stuff R540 for MXM

CRT PORT



B: (10/26) no stuff D4 for CRT CONN CN18/Pin11 (NO1 request)
RAMP: (1/15) DEL D4, D5 footprint and DEL NET CRT_SENSE#

A: (8/20) Reserve Diode for ESD solution

B: (11/11) Change CN18 P/N to DPM015PR048 (Follow BL38 & ME)

PROJECT : BD3A

SB600 STRAPS/TVICRT

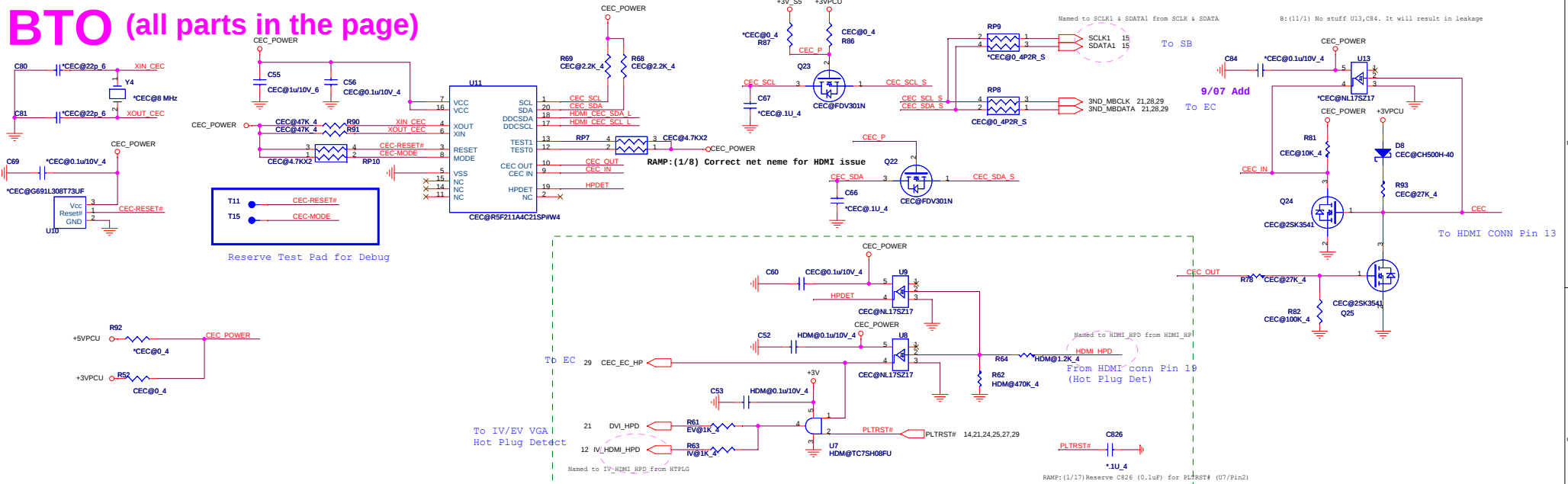
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Date: Tuesday, January 22, 2008

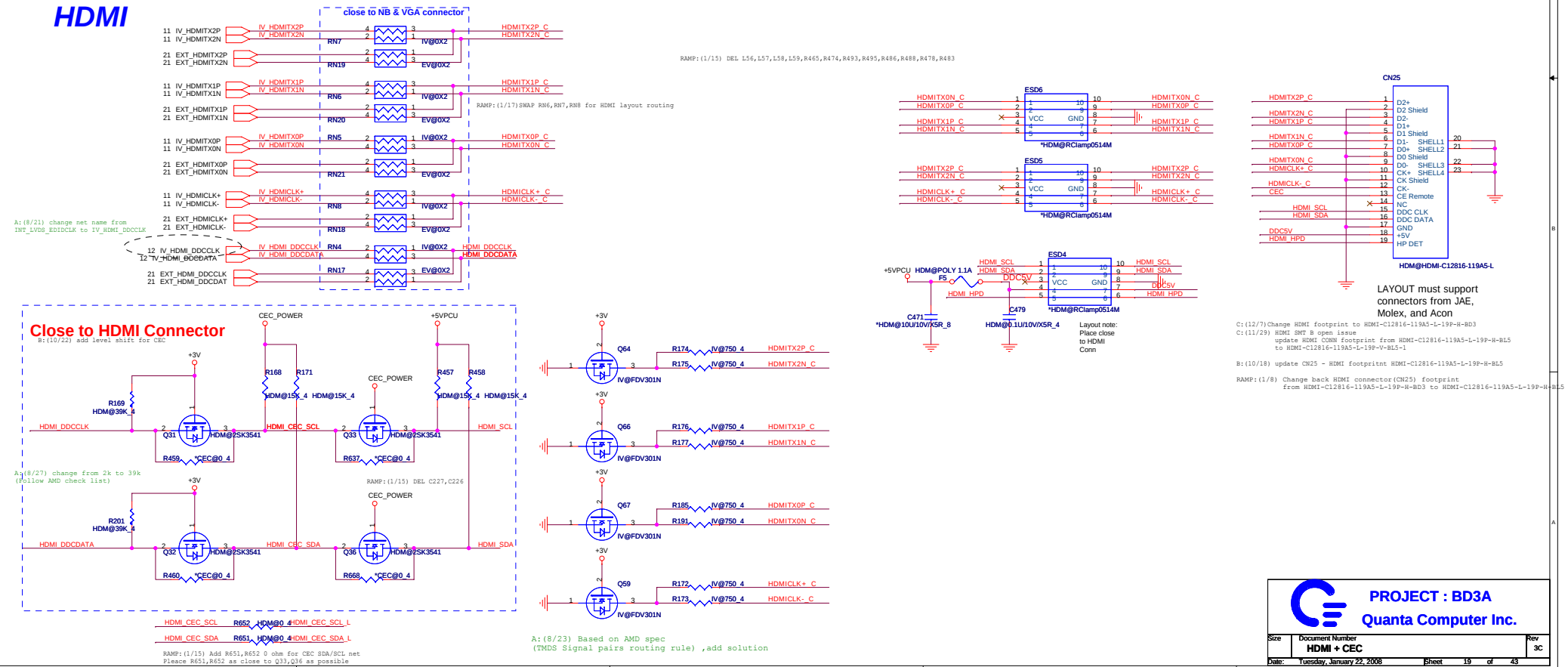
Sheet 18 of 42

Rev 3C

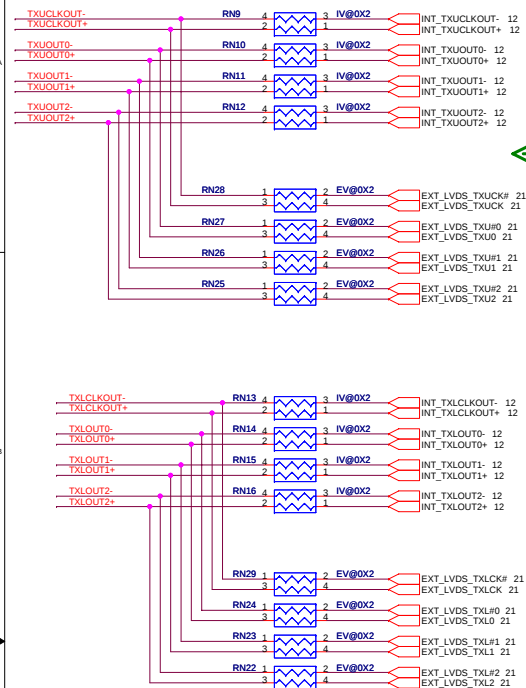
BTO (all parts in the page)



HDMI

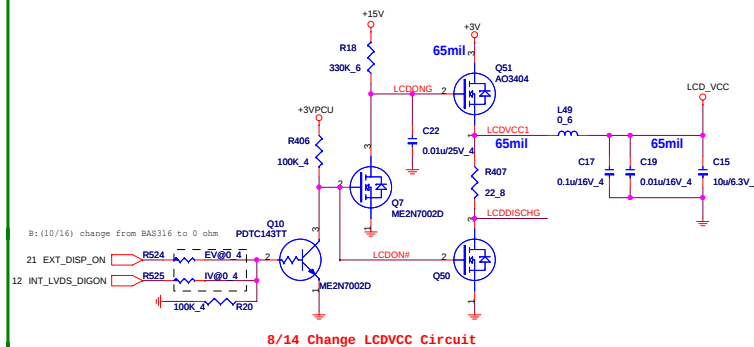
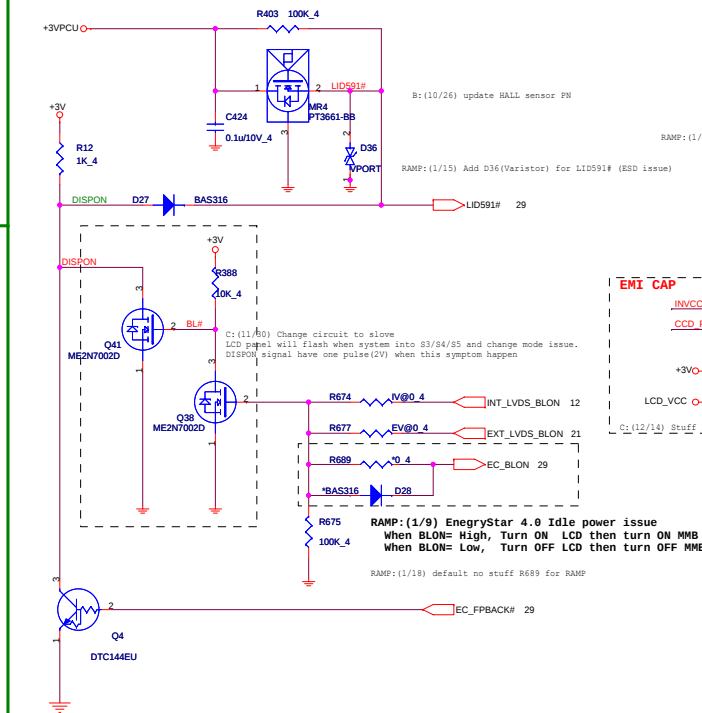


LCD Switch

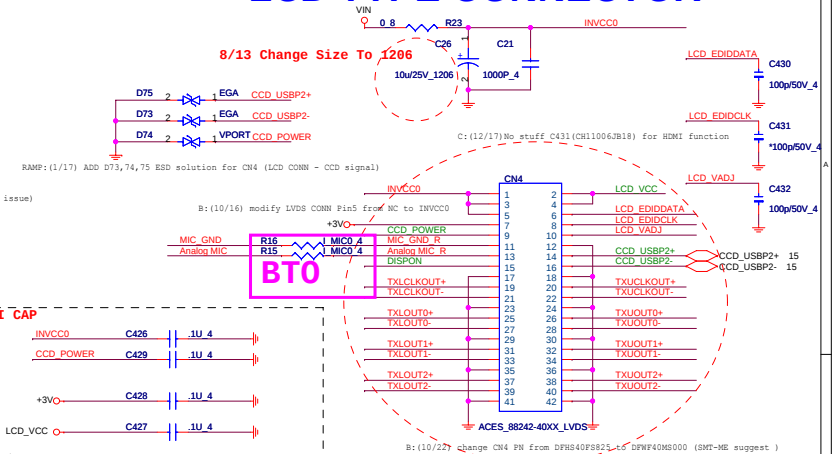


Close to NB & VGA Connector

HALL SENSOR

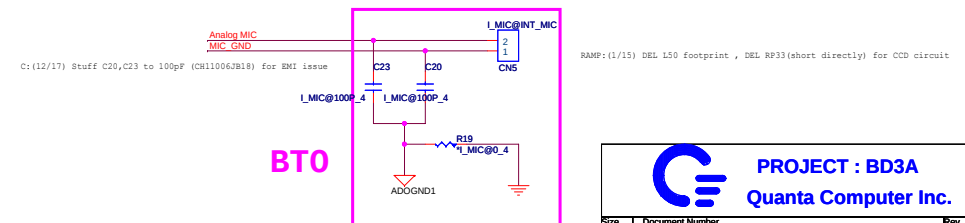
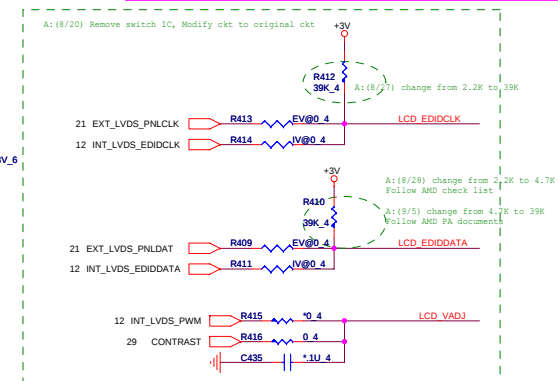
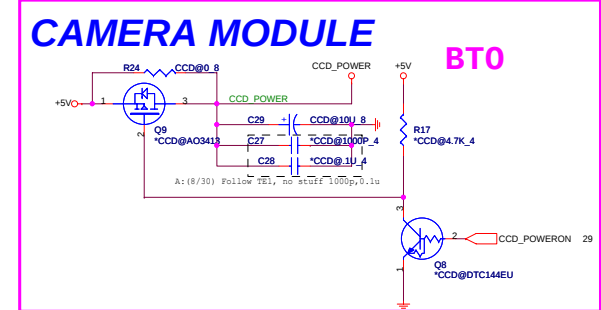


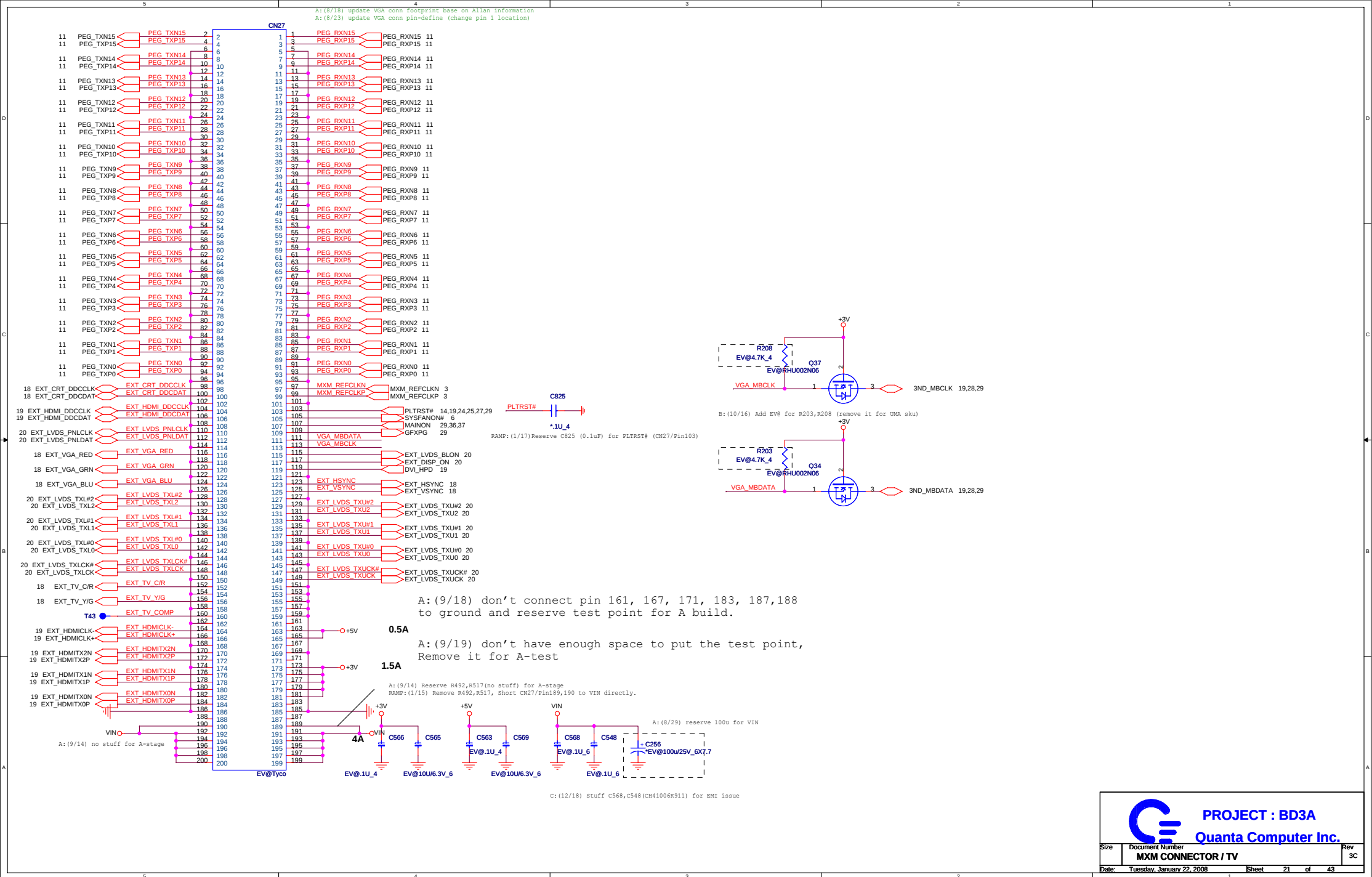
LCD TYPE CONNECTOR



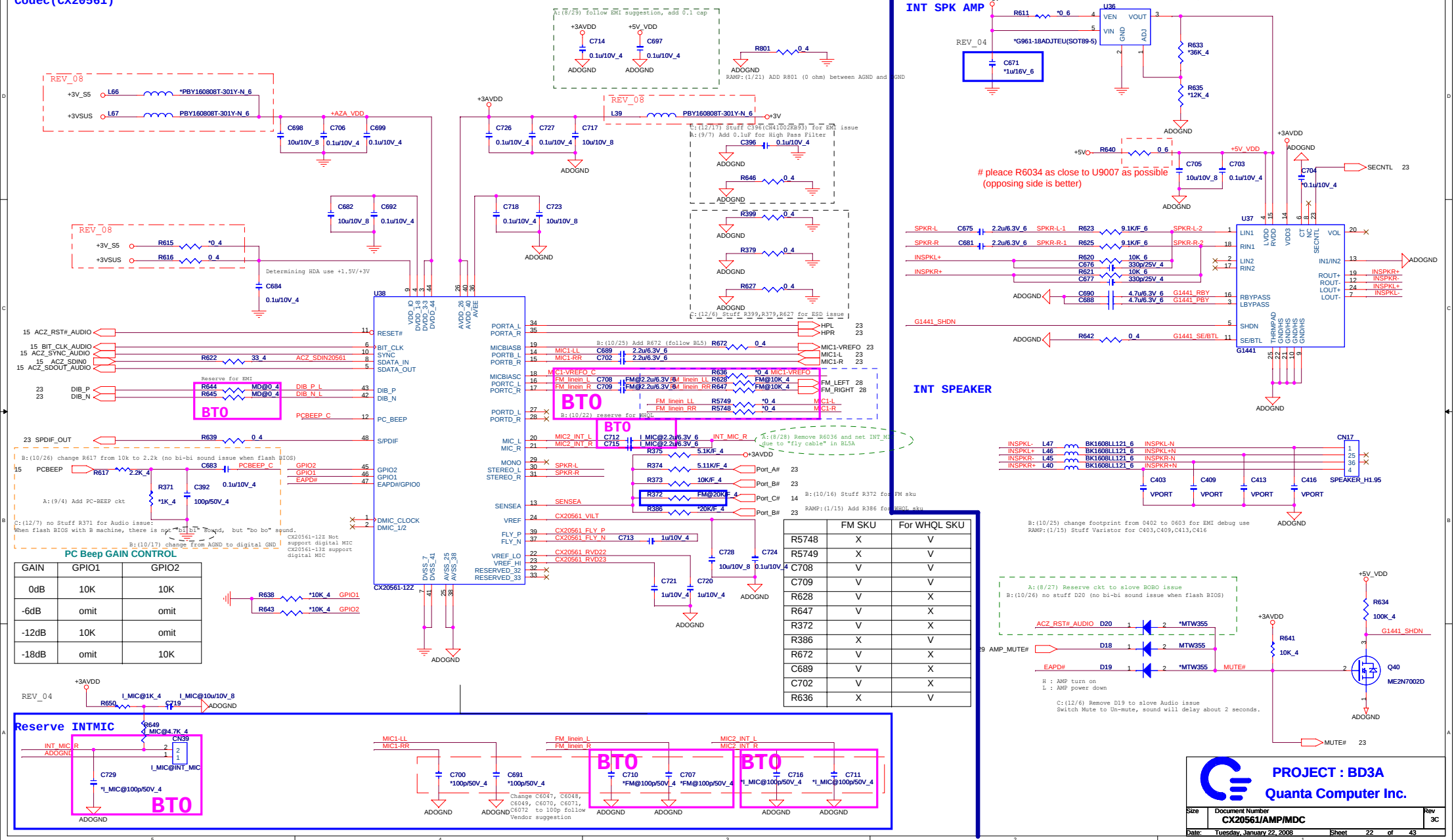
8/14 Modify Pin Define

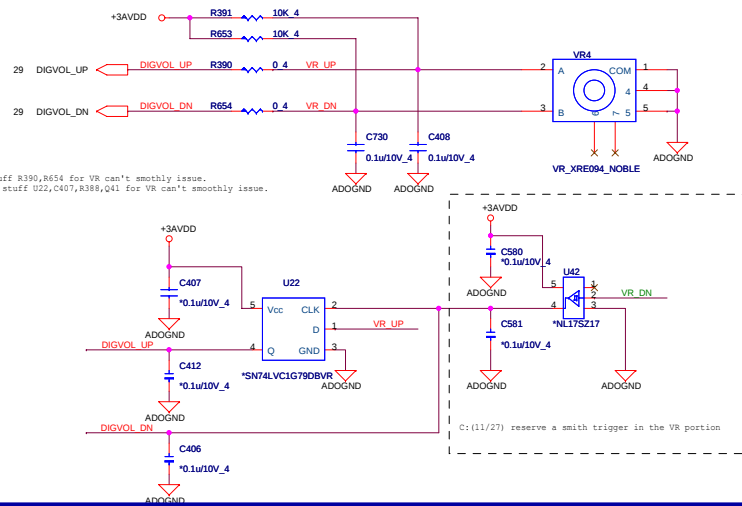
LCD PANEL MODULE



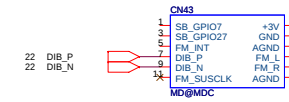


Codec (CX20561)





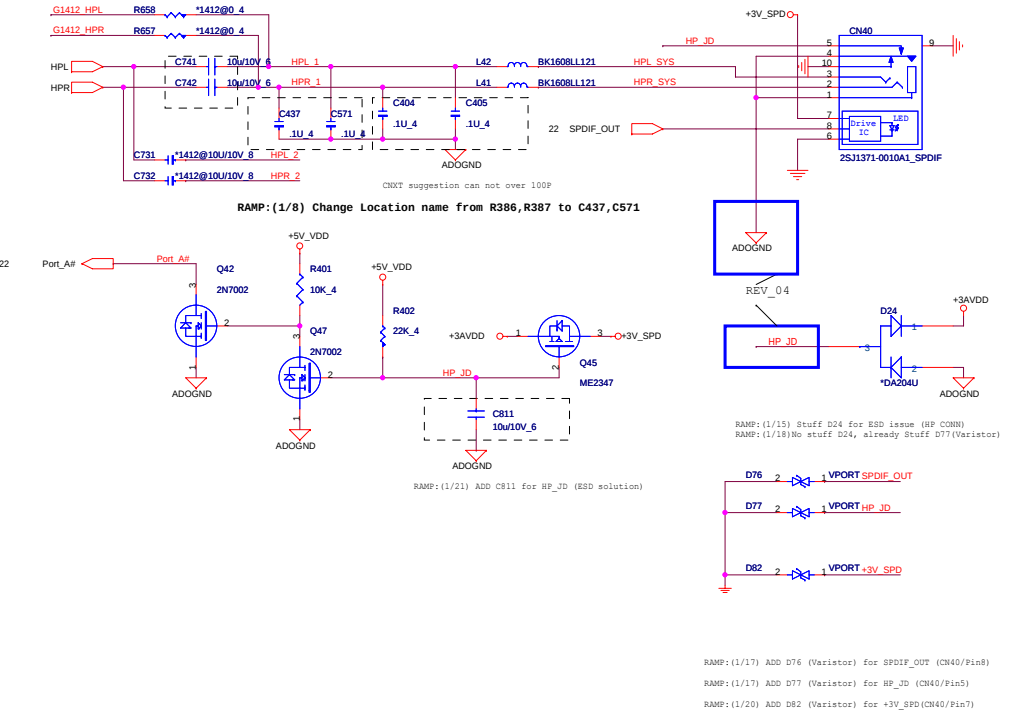
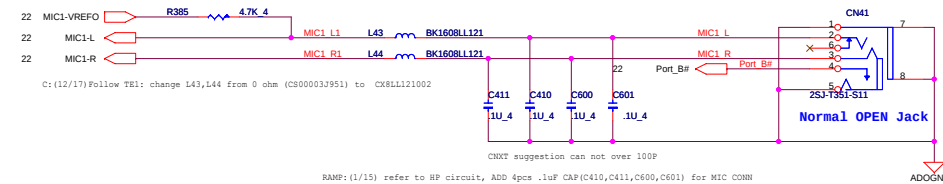
BTO



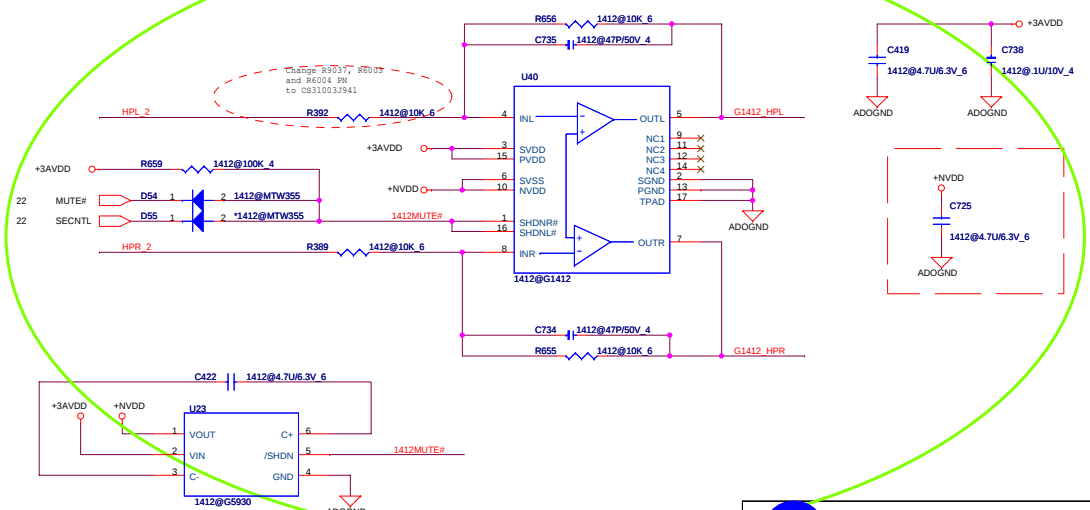
B: (10/16) Remove FM function in MDC CONN

RAMP: (1/15) Change CN43 footprint from MDC-1-179373-2-12P-RUV to MDC-1-179373-2-12P-RUV-BD3A (SMT open issue)

SYSTEM MIC

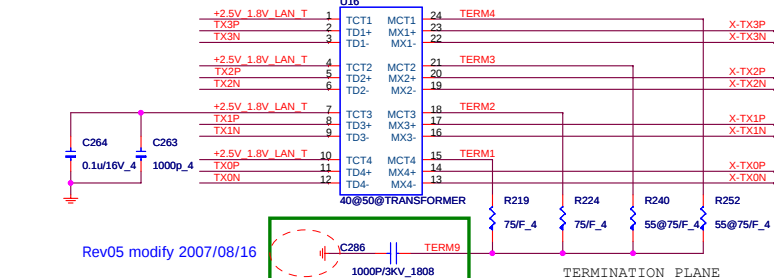
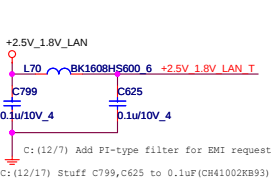
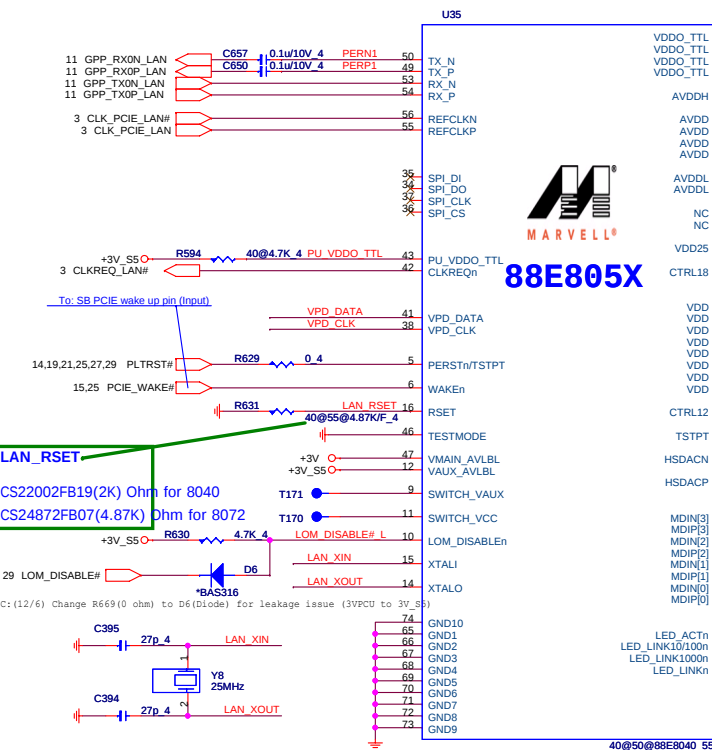
HP Amplifier
REV_05

0903 all AMP parts need to be mount



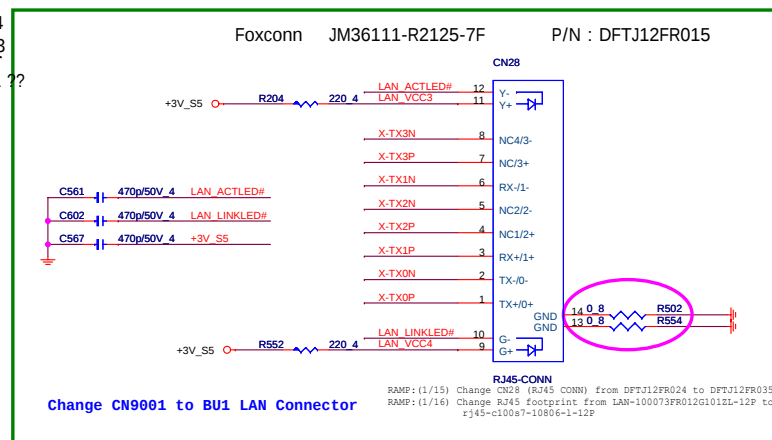
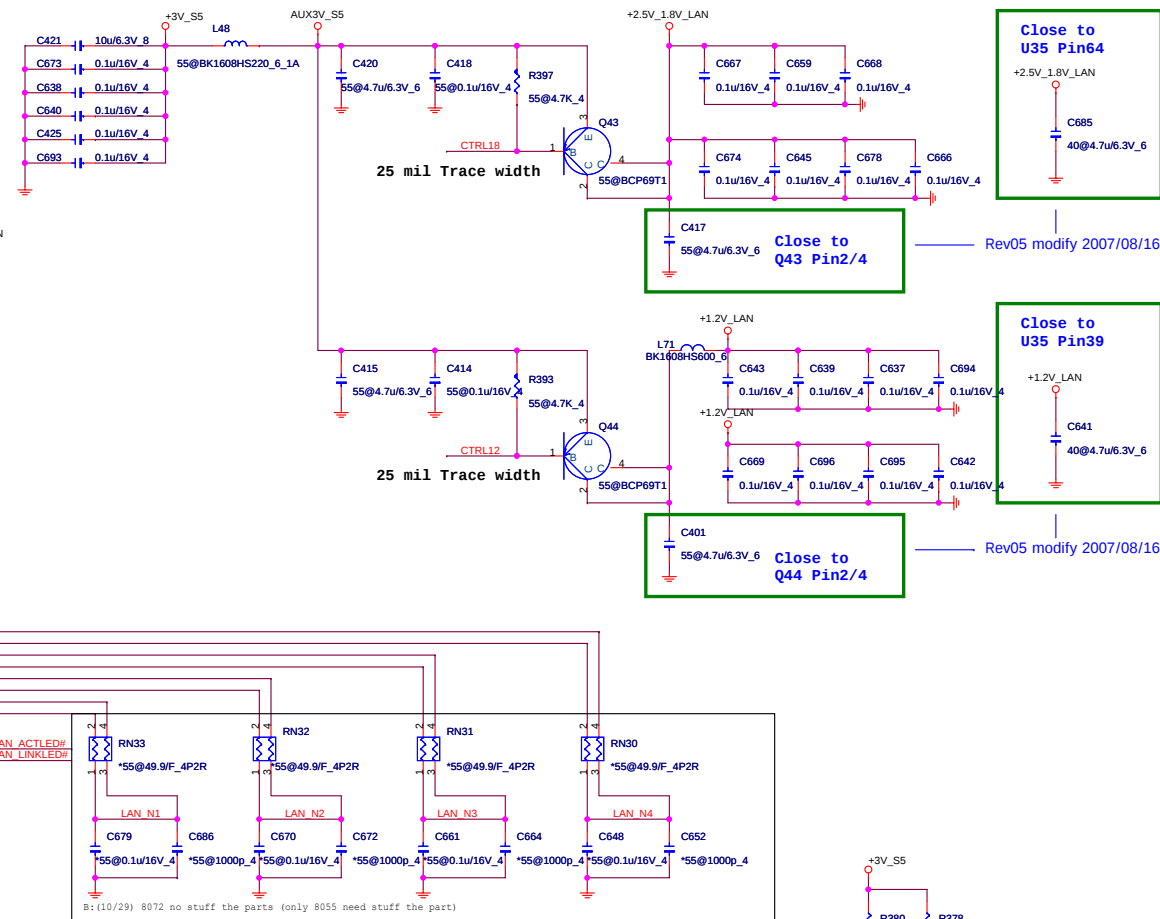
LAN_MARVELL_88E8040/88E8055/88E8072

10/100 : 88E8040T	P/N : AL008040001
GIGA : 88E8055	P/N : AJ080550000
GIGA : 88E8072	P/N : AL008072000

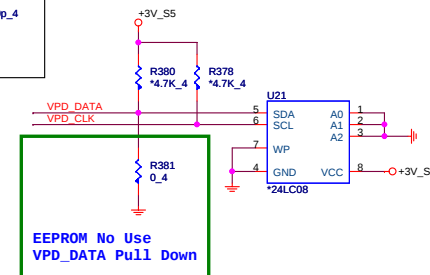


Rev05 modify 2007/08/16

Check by Safety

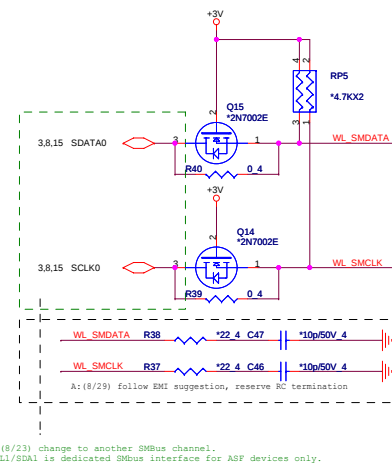
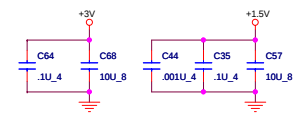
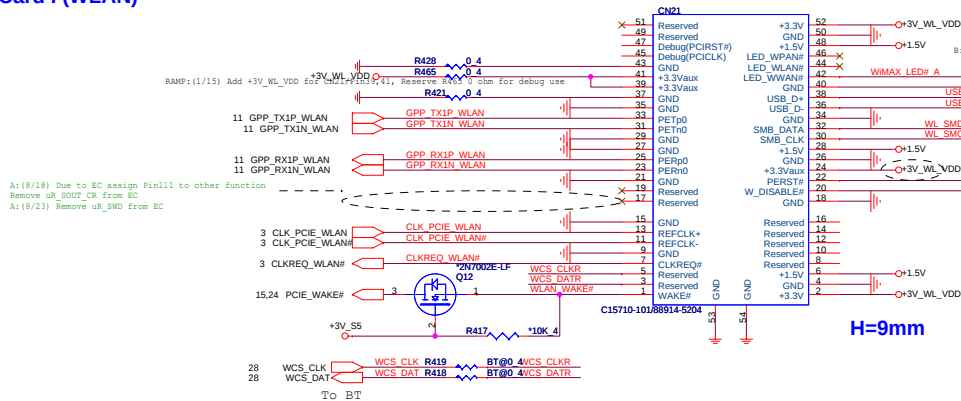


Rev05 modify 2007/08/16

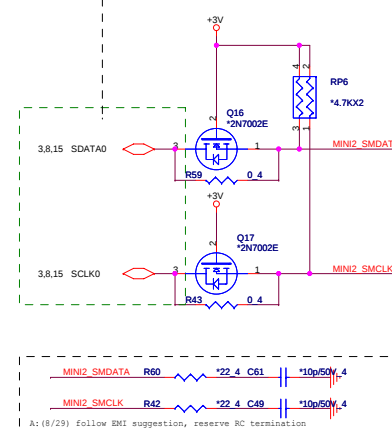
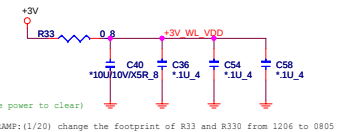
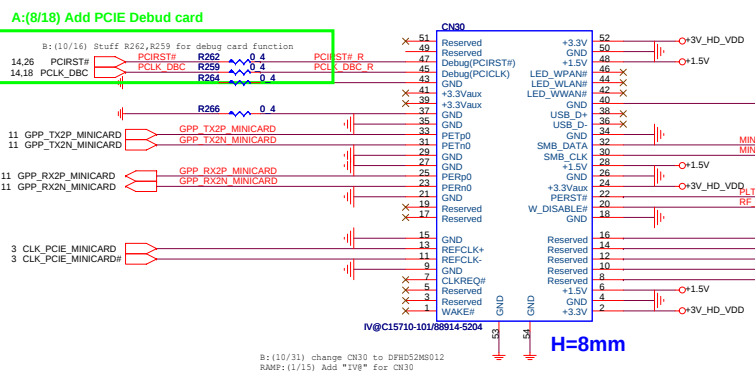


PROJECT : BD3A
Quanta Computer Inc.

MINI-Card I (WLAN)



MINI-Card II (HD Decoder)



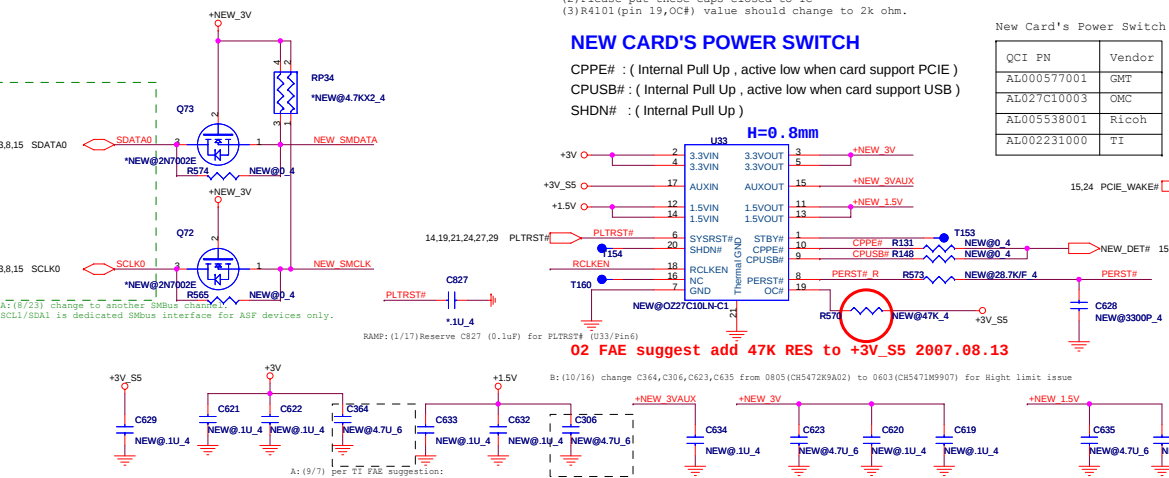
A: (9/7) per TI FAE suggestion:

- (1) Please keep all input and output capacitor value $> 4.8\mu\text{F}$ ($0.1\mu\text{F} + 4.7\mu\text{F}$)
- (2) Please put these caps closed to IC
- (3) R4101 (pin 19, OC#) value should change to $2\text{k}\Omega$.

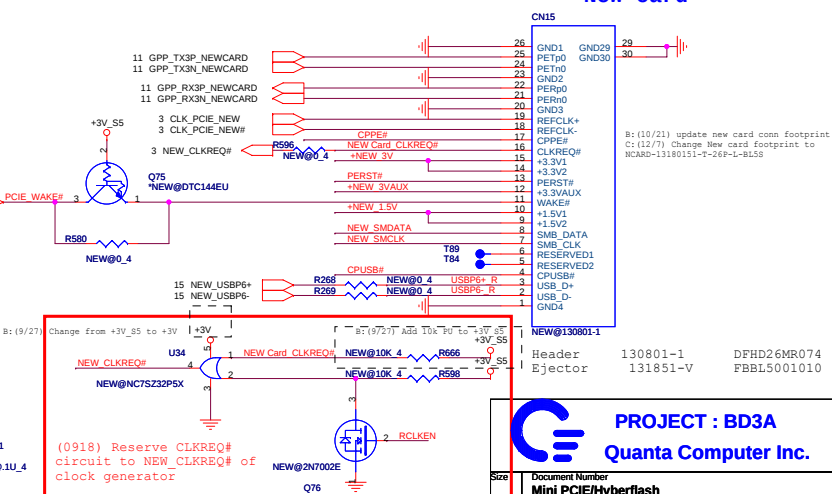
NEW CARD'S POWER SWITCH

CPPE# : (Internal Pull Up , active low when card support PCIE)
 CPUSB# : (Internal Pull Up , active low when card support USB)
 SHDN# : (Internal Pull Up)

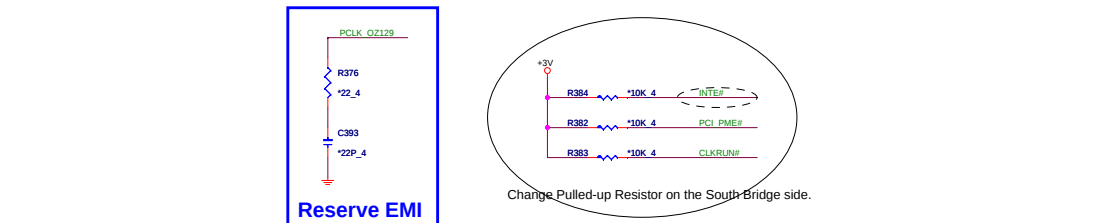
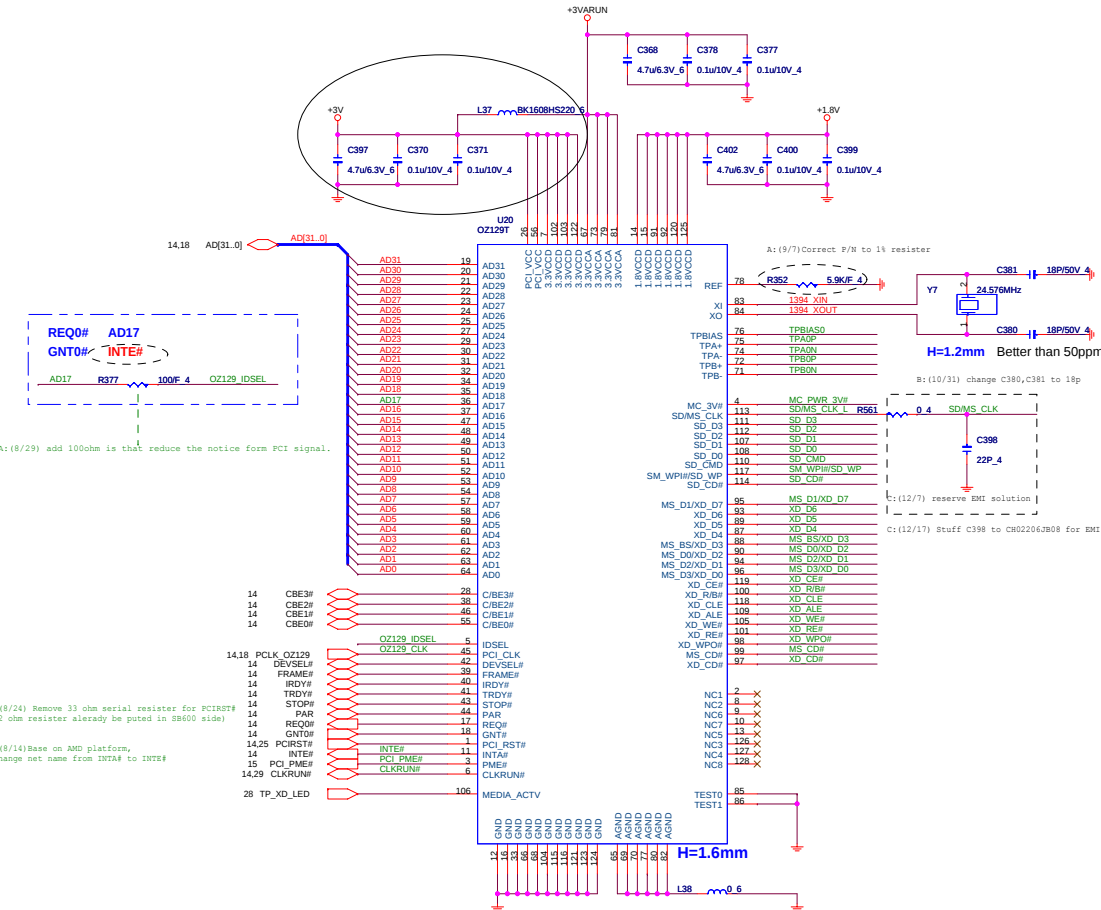
QCI PN	Vendor
AL000577001	GMT
AL027C10003	OMC
AL005538001	Ricoh
AL002231000	TI



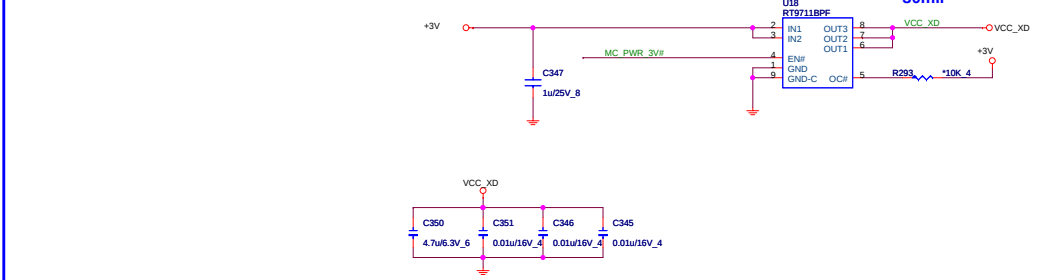
New card



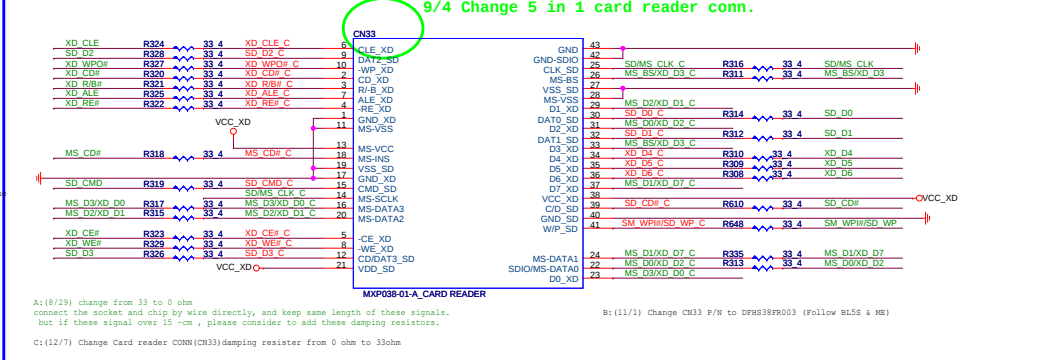
MMC



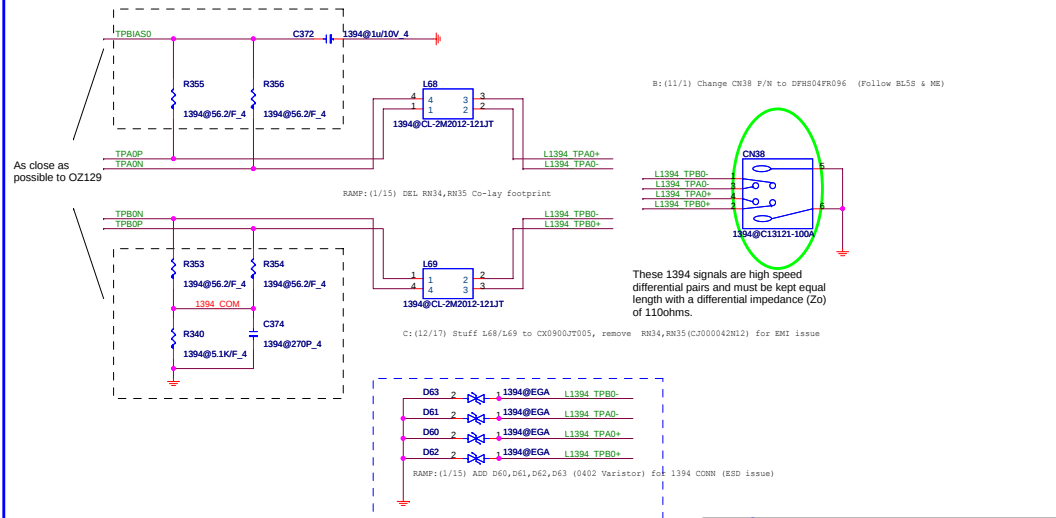
CARDREADER POWER

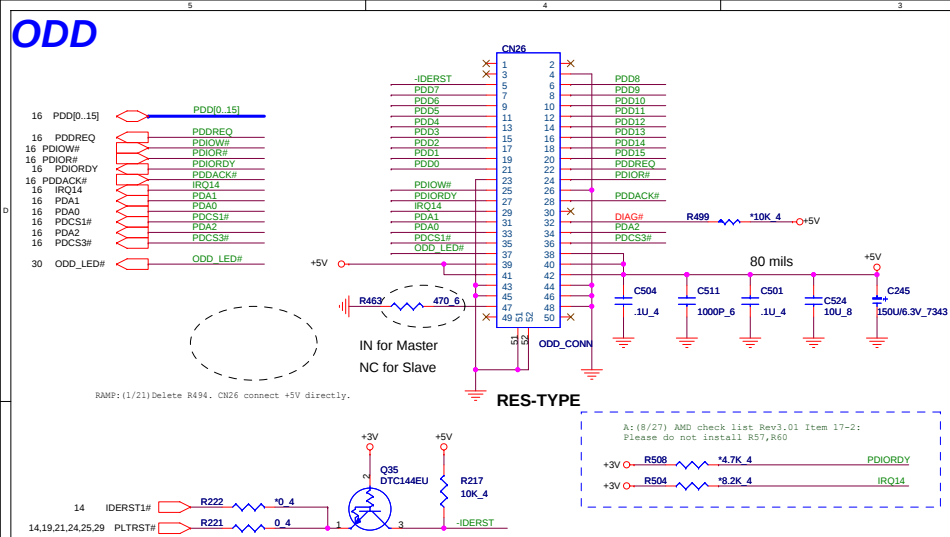


5 IN 1 CARD READER

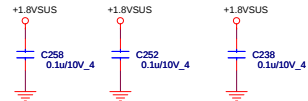


1394



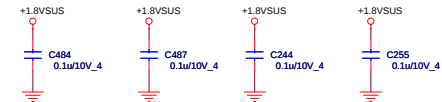


reference power plane, add 4 CAP near SB600



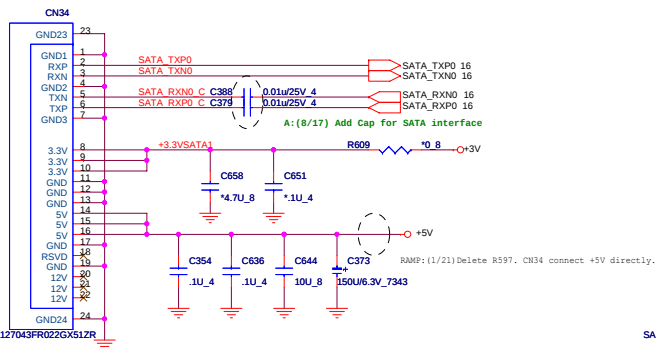
A: (9/17) Add 0.1u to stitch Plane (+1.8VSUS to GND)

reference power plane, add 4 CAP near IDE CONN

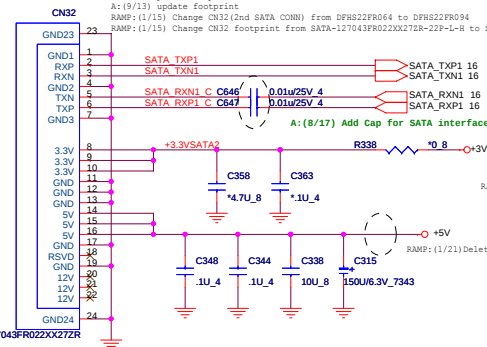


A: (9/13) Add 0.1u to stitch Plane (+1.8VSUS to GND)

SATA HDD



2'nd SATA HDD



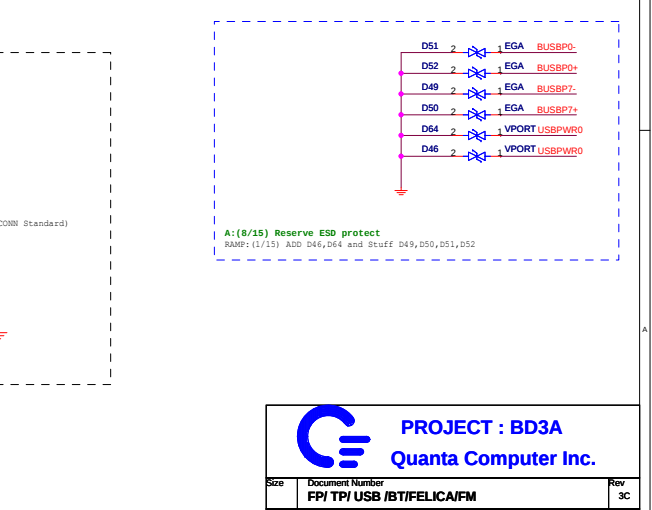
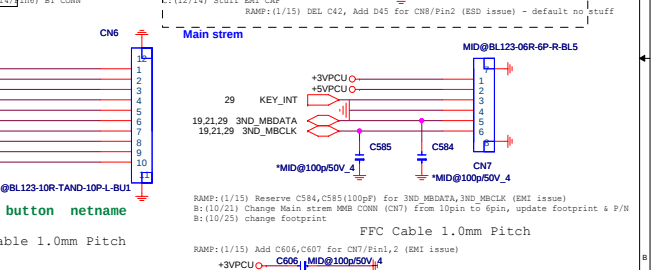
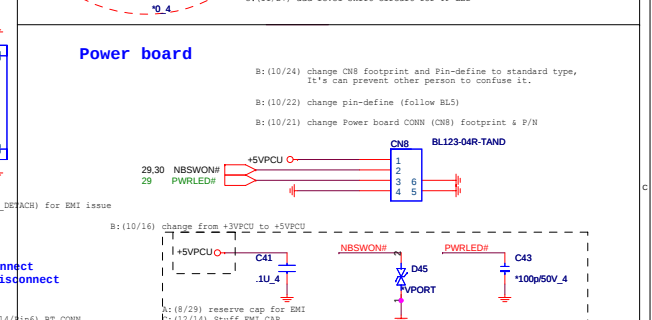
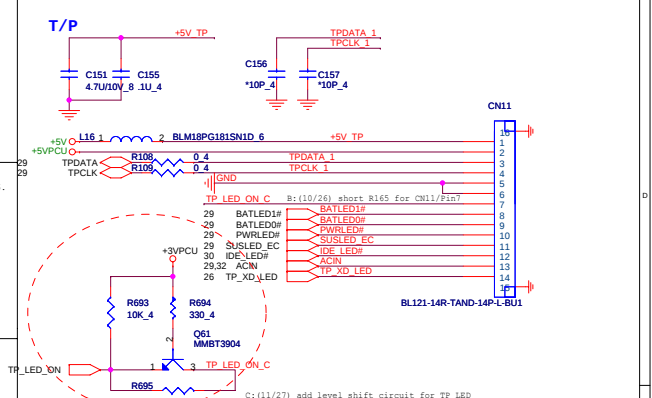
RAMP: (1/21) ADD D91, D92 (0603 Varistor) for two SATA CONN (CN32&CN34) +5V power pin

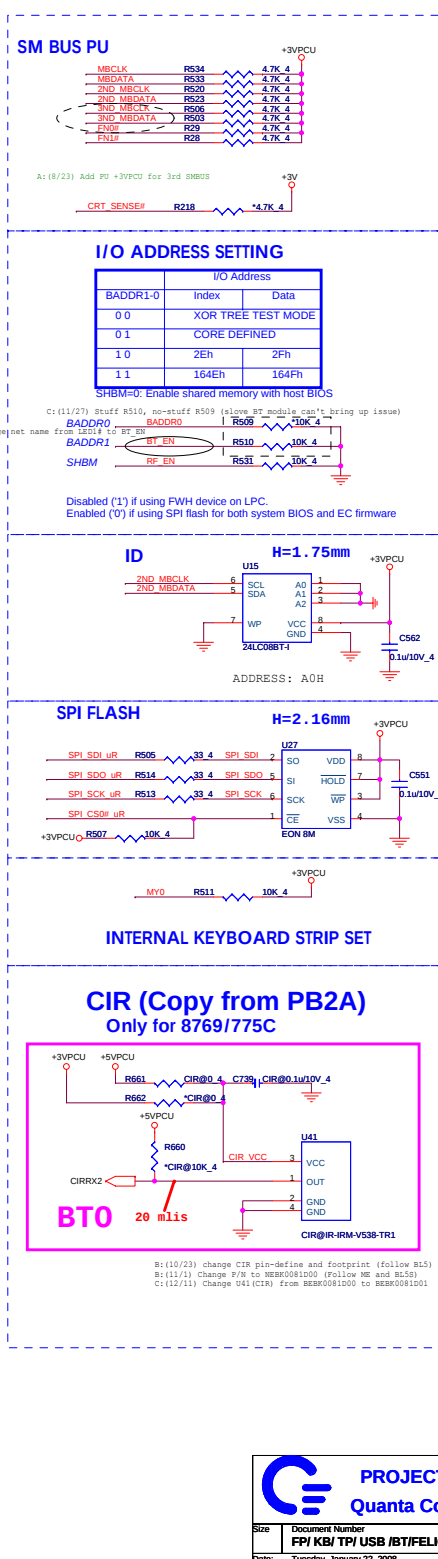
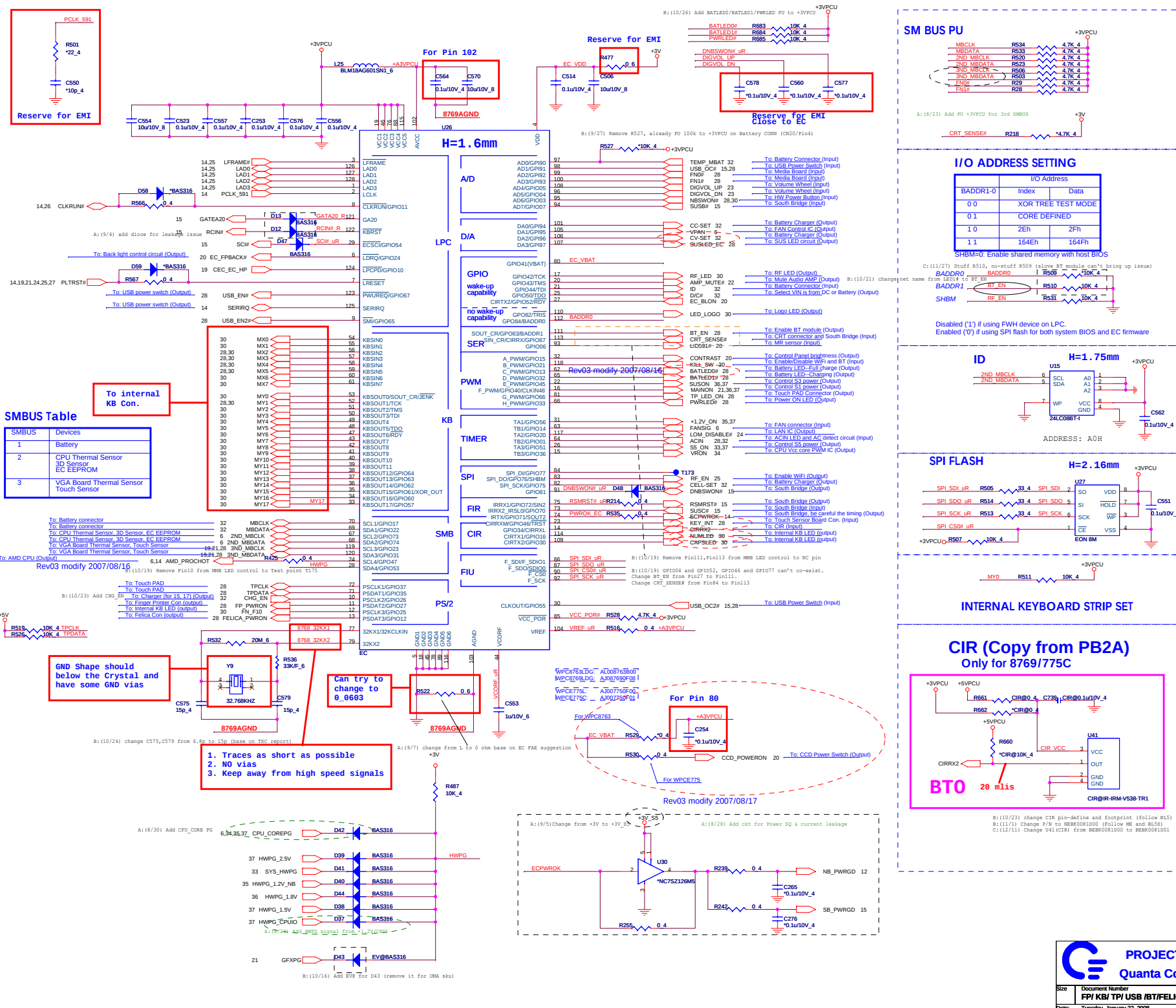
RAMP: (1/20) ADD D83-90 for two SATA CONN (E3D issue)



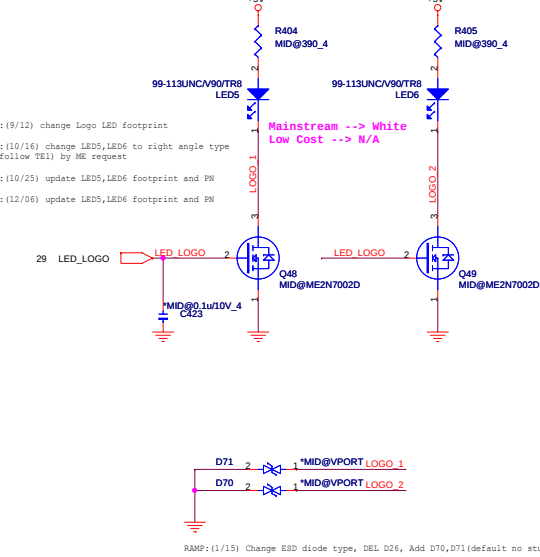
PROJECT : BD3A
Quanta Computer Inc.

Size	Document Number	Rev
	SATA / PATA	3C
Date:	Tuesday, January 22, 2008	Sheet 27 of 43

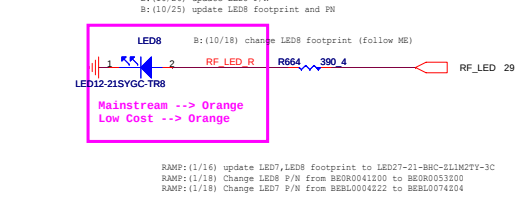




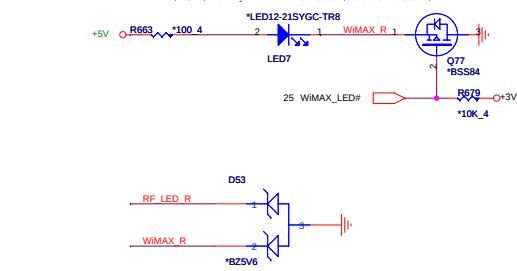
LOGO LED



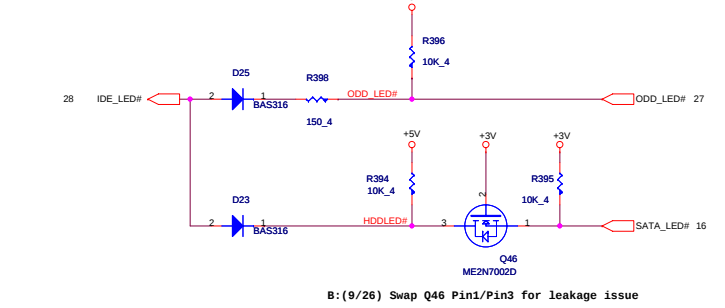
W-LAN&BT



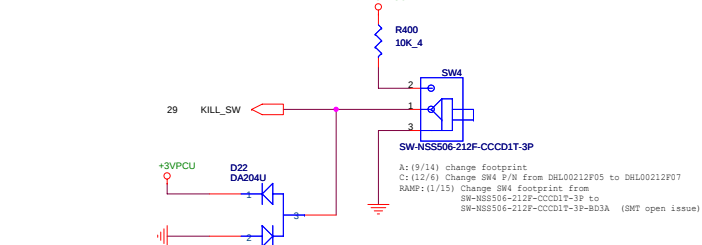
WiMAX LED



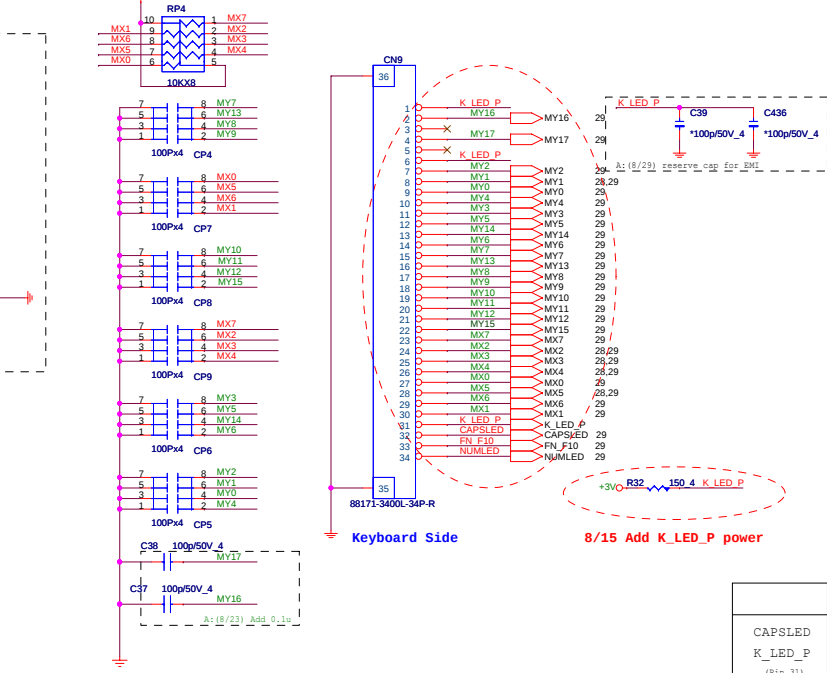
IDE LED



Kill SW



Keyboard

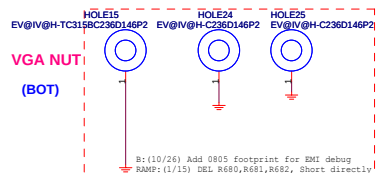


A: (8/27) Confirm with EC FAE, MY no need External PU resistor

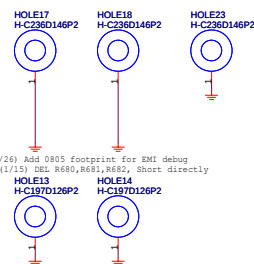
	15"	17"
CAPSLED K_LED_P (Pin 31)	V	V
FN_F10 K_LED_P (Pin 6)	V	
NUMLED K_LED_P (Pin 1)	V	V

HOLE

Take care NUT P/N base on IV/EV sku



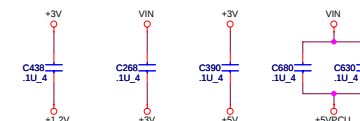
RAMP: (1/16) Add "MD0" for Hole32, only stuff for Modern SKU



NUT P/N Control Table

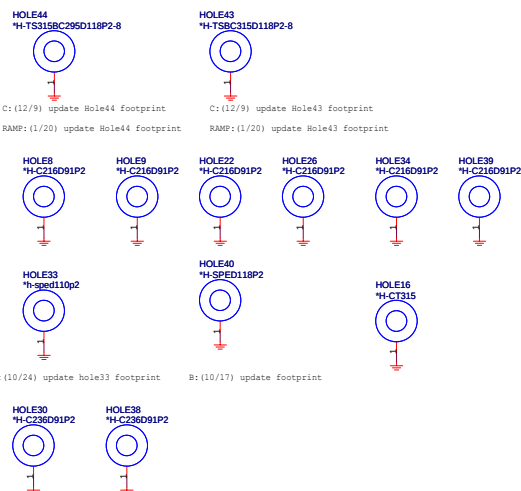
	UMA sku	EV sku
HOLE15	FBBL5002010	FBBD3017010
HOLE24	FBBL5002010	FBBD3017010
HOLE25	FBBL5002010	FBBD3017010

Stitch CAP



E-SATA NUT
(BOT)

```
RAMP:(1/15) No stuff E-SATA NUT for REV.G platform. Remove Hole31,Hole37
RAMP:(1/21) DEL HOLE31,37 (E-SATA NUT)
```



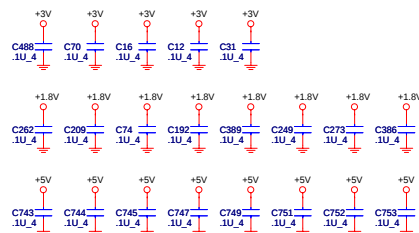
EMI CAP.

A:(9/17) Base ON EMI mail(9/13) Item5

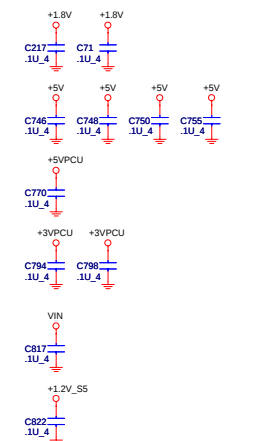
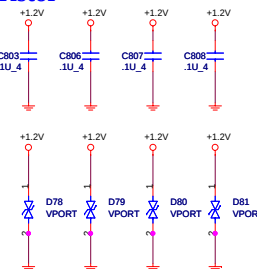
A:(9/12) Base ON EMI mail(9/13) Item1

B:(10/25) EMI Solution:

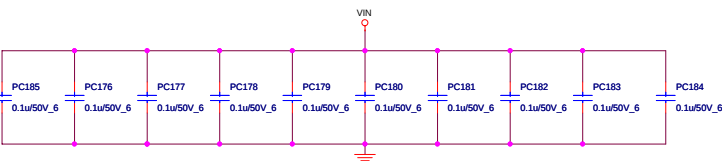
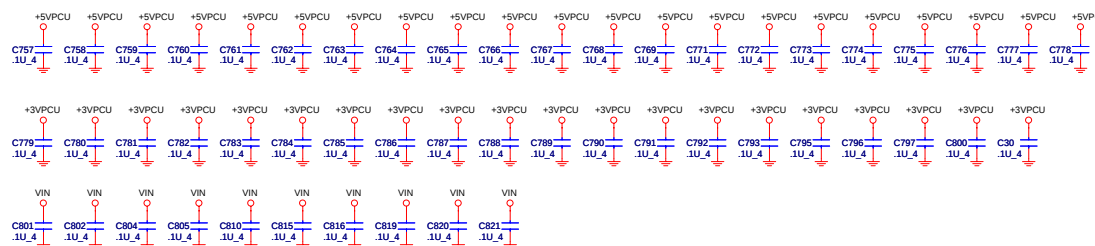
C: (12/14) Stuff EMI CAP



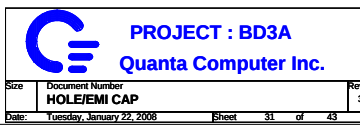
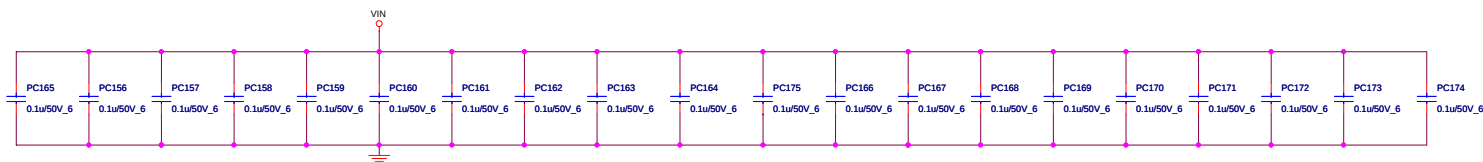
RAMP: (1/18) ADD C803,C806,C807,C808,D78~81 for +1.2V (ESD issue)

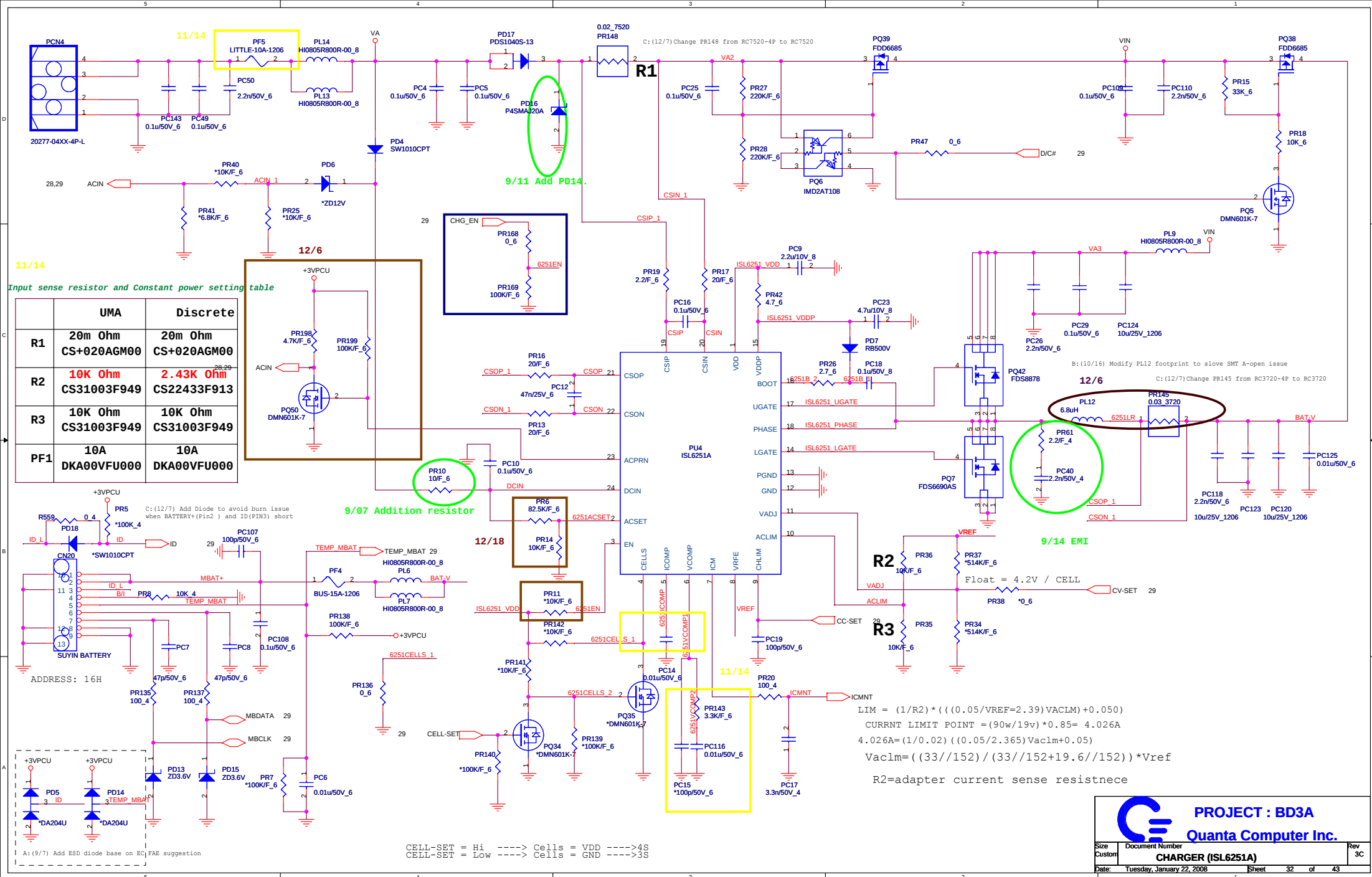


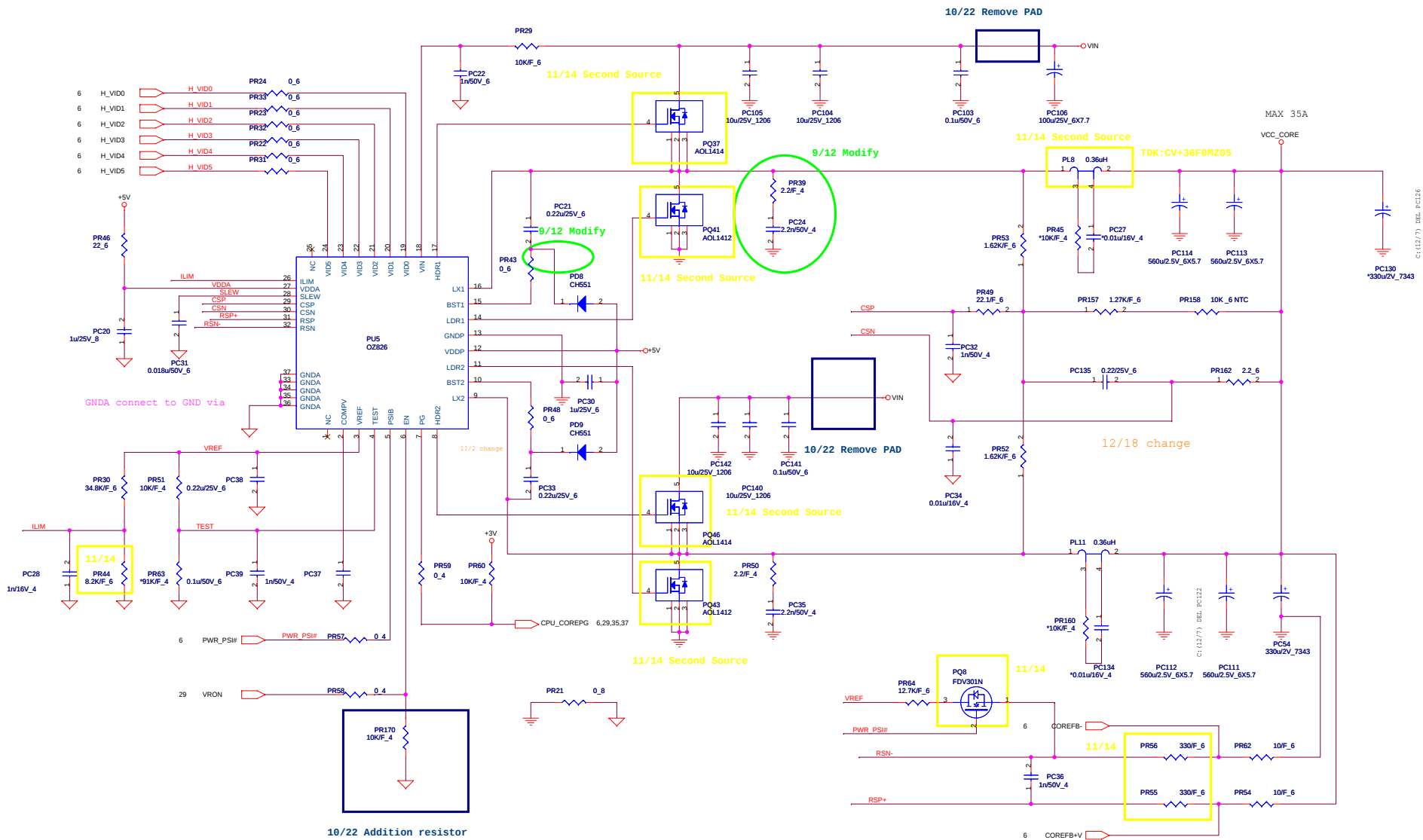
C:(12/18) Stuff C217,C71,C746,C748,C750,C755,C770,C794,C798,C817,C822 = 0.1uf (CH41002KB93)
for EMI issue

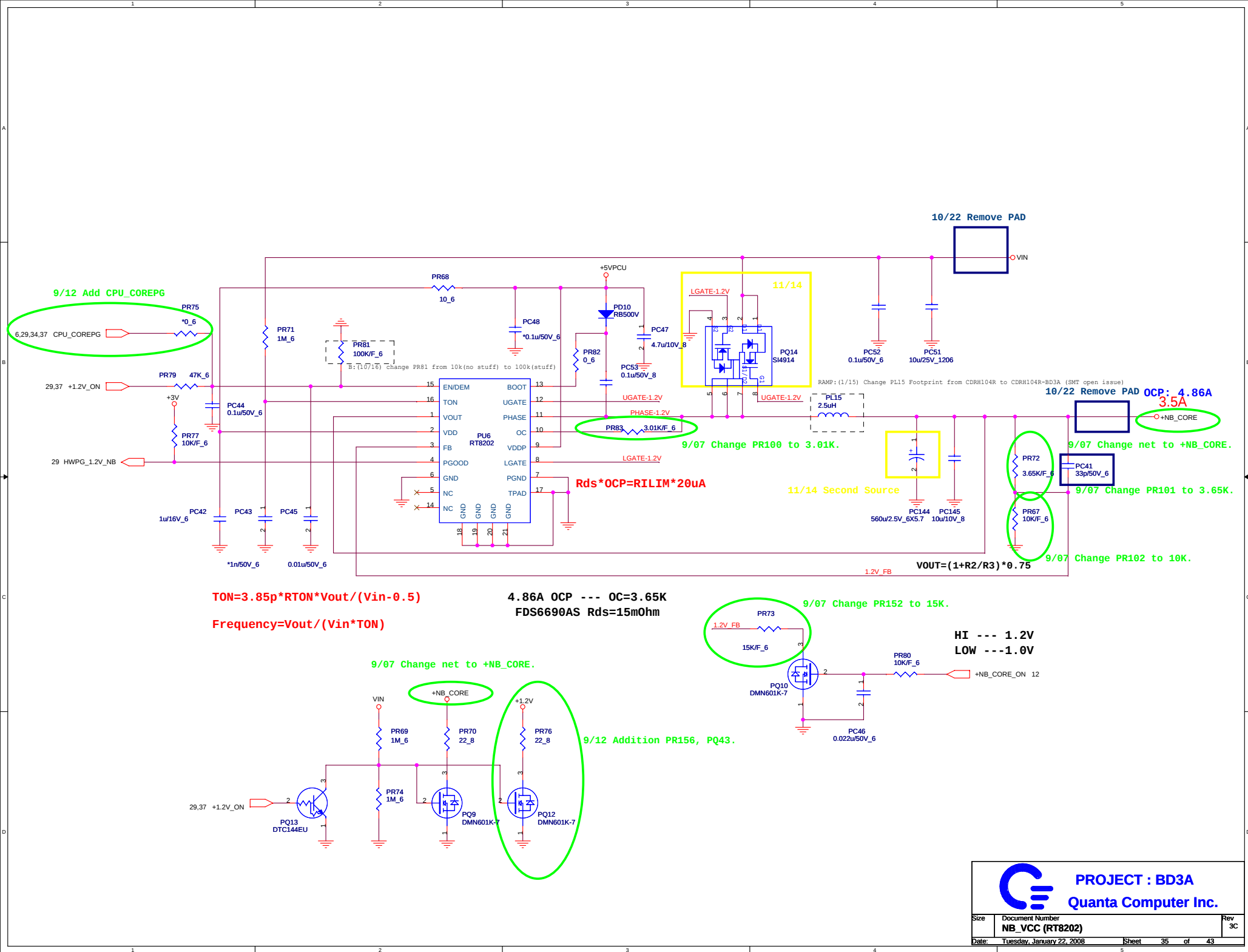


12/6 Add EMI CAP for Power Circuit



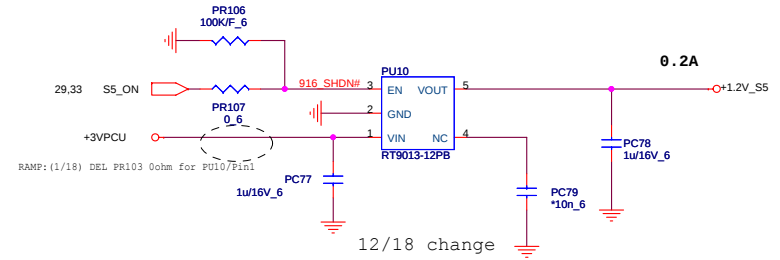
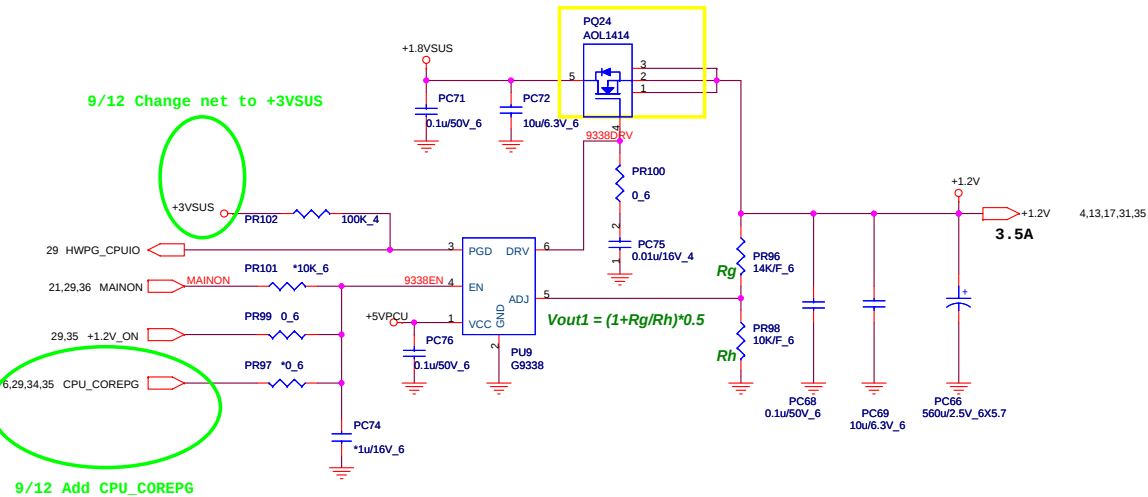




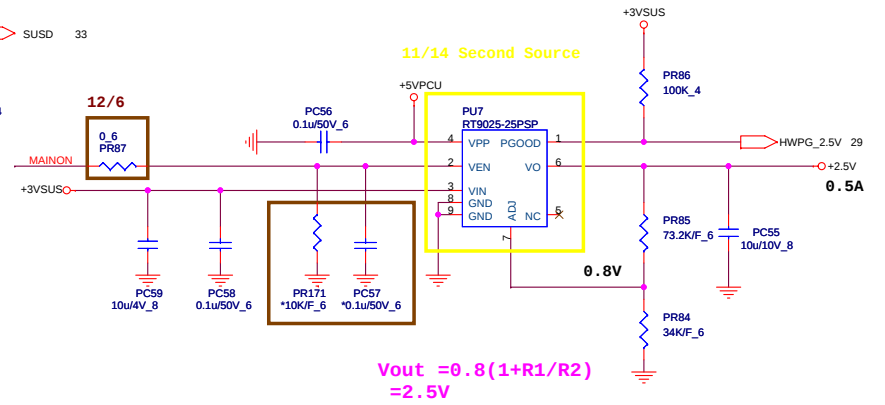
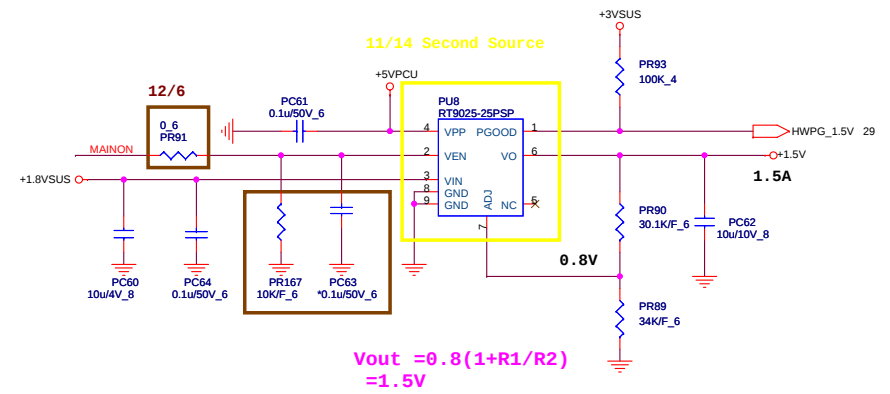
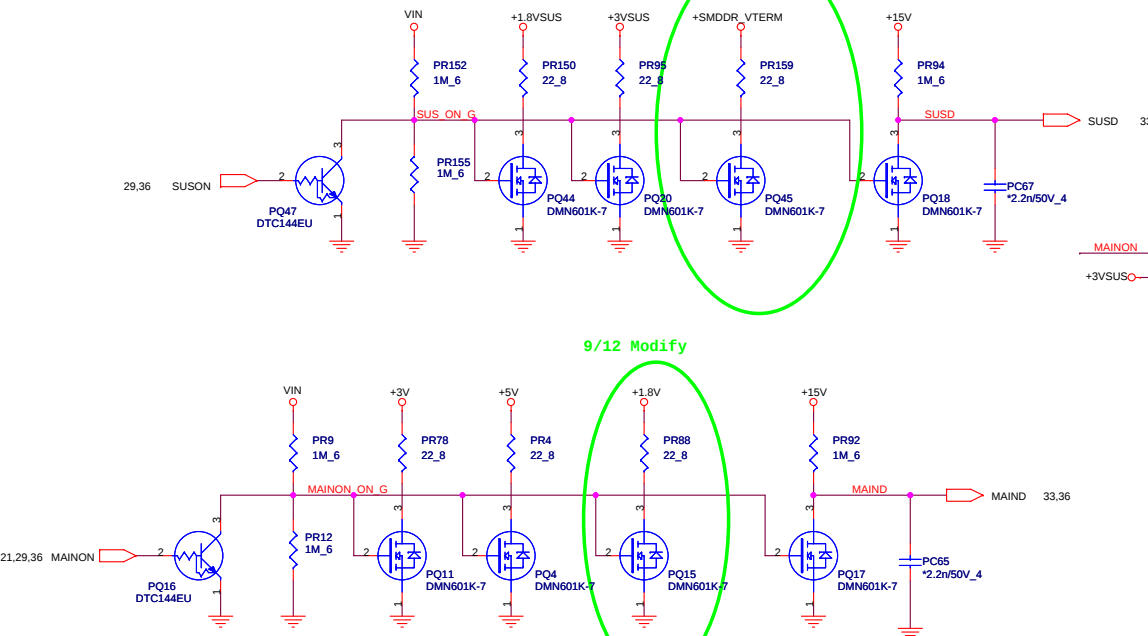


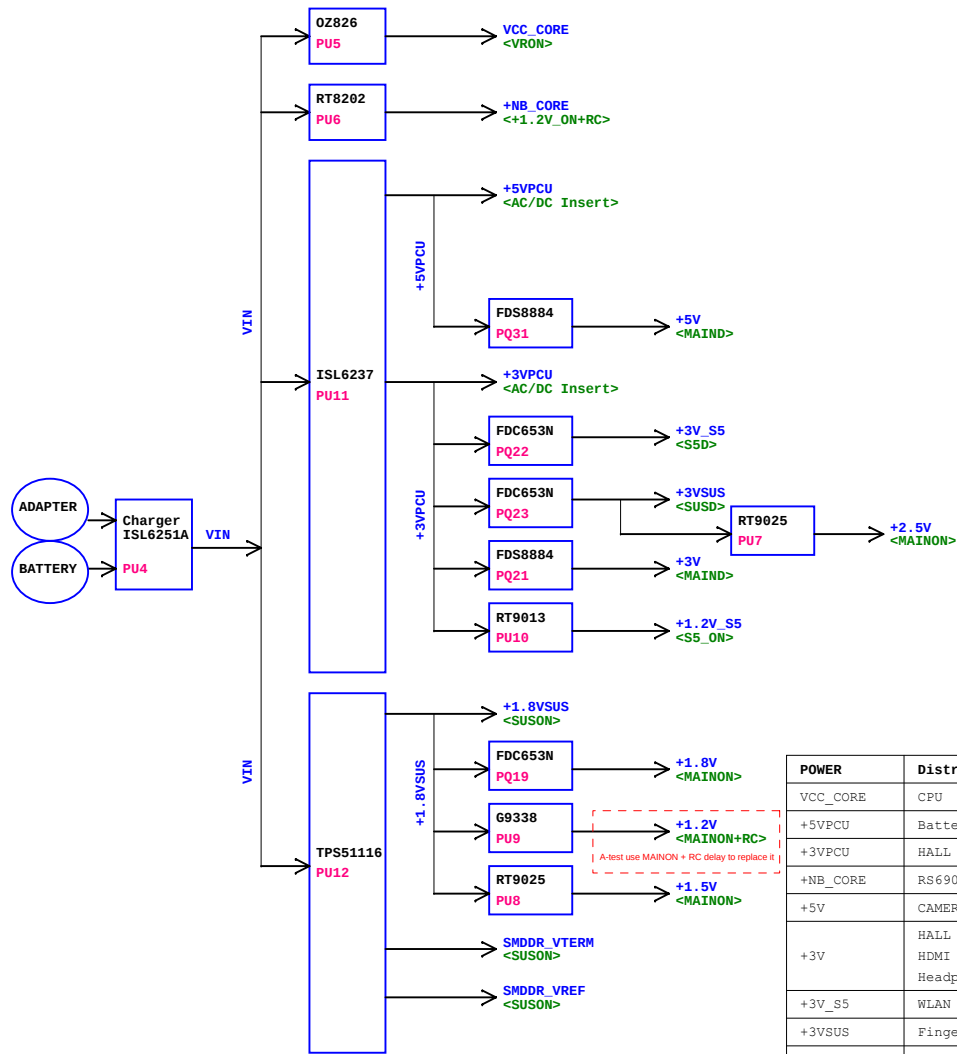
11/14 Second Source

9/12 Change net to +3VSUS

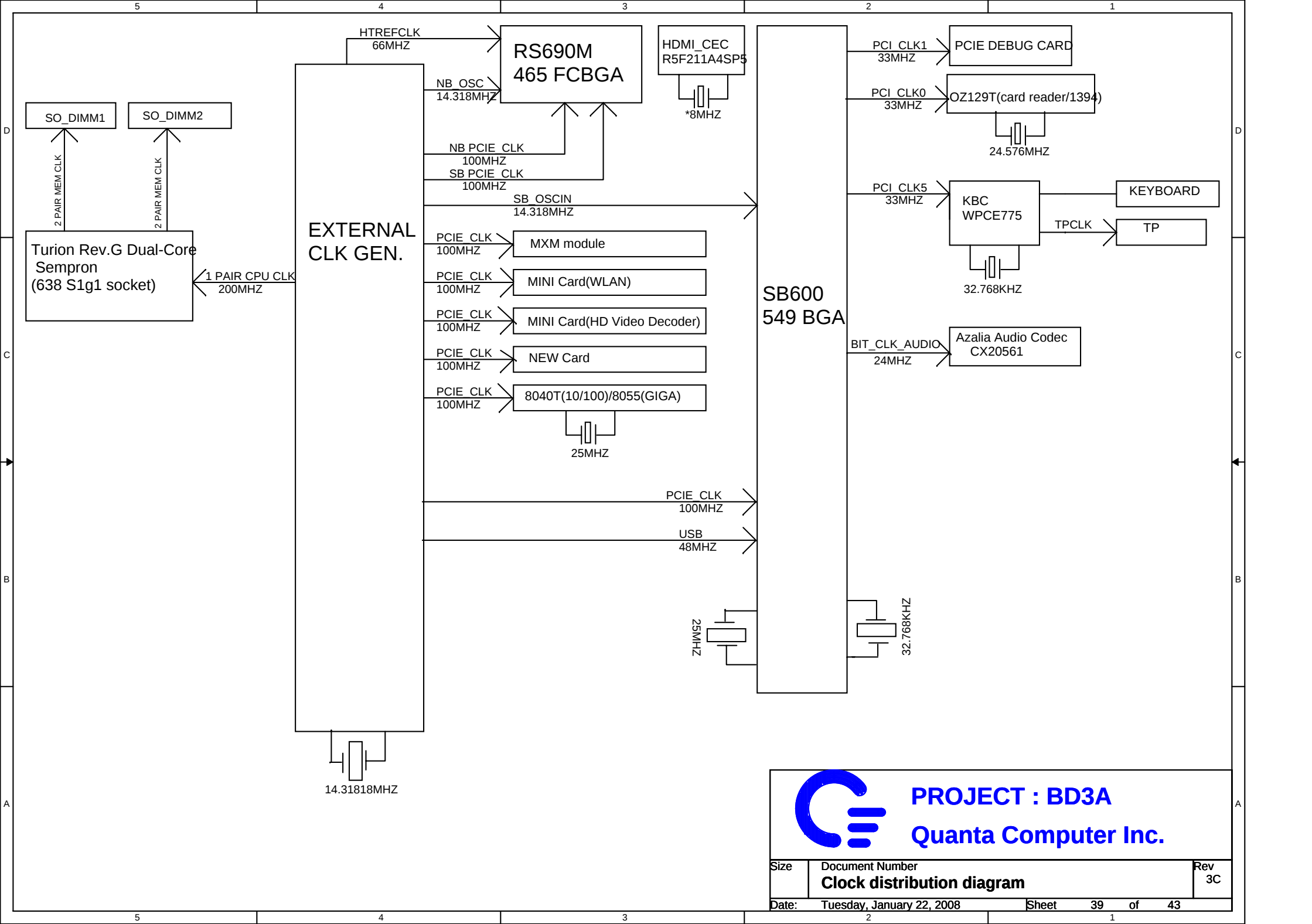


9/12 Modify





POWER	Distribution
VCC_CORE	CPU
+5VPCU	Battery LED , Power LED , USB , CIR , RTC
+3VPCU	HALL SENSOR , Battery LED , RF LED , kill SW , Jumper LED , KB , Power Board , EC , ID , SPI Flash , CIR
+NB_CORE	RS690M
+5V	CAMERA , Card Reader LED , ODD/HDD LED , Felica , T/P , T/sensor , CRT , HDMI , SB600 , CPU FAN , MXM , Headphone , EC , INT SPK AMP
+3V	HALL SENSOR , LCD PANEL , LVDS , WLAN , HD Decoder , NEW CARD , KB , KB LED , XD LED , Blue tooth , Touch sensor , Card Reader (OZ129) , ODD/HDD , HDMI , CRT , TVOUT , REQUIRED STRAPS , DEBUG STRAPS , SB600 , RS690M , DDR , CPU Thermal monitor , CPU FAN , CLK , MXM , VR , FM Tuner MDC , Headphone , EC , LAN , Codec(CX 20561)
+3V_S5	WLAN , NEW CARD , SB600 , MXM , LAN
+3VSUS	Finger print , SB600
+2.5V	CPU
+1.2V_S5	SB600
+1.8VSUS	SB600 , DDR , CPU , HDT
+1.8V	SB600 , LCD , LVDS , RS690M
+1.2V	SB600 , RS690M , CPU , WLAN , HD Decoder , NEW CARD
+SMDDR_VTERM	DDR , CPU
+SMDDR_VREF	DDR
+5V_S5	



Model	REV	DATE	CHANGE LIST	NOTE
BD3A	A1A	20070921	FIRST RELEASED : Import BOM ECN E200709-3025	ECN Release
	B2A	20070921	Page14: change C285 to CH6221M9A07 (CH6222M9A01 EOL issue)	Circuit modify
		20070926	Page30: Swap Q46 Pin1/Pin3 (leakage issue)	Circuit modify
		20070927	Page03: Add 10k(R665) PU to +3V (NEW Card ckt issue)	Circuit modify
		20070927	Page25: Reserve R666 PU to +3V_S5 for U34/Pin1 (NEW Card ckt issue)	Circuit modify
		20070927	Page25: Change U34/Pin5 from +3V_S5 to +3V (NEW Card ckt issue)	Circuit modify
		20070927	Page29: Remove R527, already PU 100k to +3VPCU on Battery CONN (CN20/Pin4)	Circuit modify
		20070927	Page14: Safety request design with two resistor (two 8.66K),Add R667. (RTC circuit issue)	Circuit modify
		20070928	Page14: Change R582 from 0603 to 0402 (RTC circuit issue)	Circuit modify
		20071016	Page12: Add "IV@" value for Q26 (Remove Q26 in EV sku BOM)	Circuit modify
		20071016	Page18: fix CRT connect error	Circuit modify
		20071016	Page20: Remove PX function,change D6,D7 to R524,R525 (0 ohm)	Circuit modify
		20071016	Page20: Remove PX function, Modify backlight control ckt (follow original design)	Circuit modify
		20071016	Page20: Modify LVDS CONN Pin5 from NC to INVCC0	Circuit modify
		20071016	Page21: Add "EV@" for R203,R208 (remove it for UMA sku BOM)	Circuit modify
		20071016	Page22: Stuff R372 for FM sku	Circuit modify
		20071016	Page23: Remove FM function in MDC CONN	Circuit modify
		20071016	Page25: Change C364,C306,C623,C635 from 0805(CH5472K9A02) to 0603(CH5471M9907) for Hight limit issue	Circuit modify
		20071016	Page25: Stuff R262,R259 for debug card function	Circuit modify
		20071016	Page28: Change C41 +3VPCU to +5VPCU	Circuit modify
		20071016	Page28: Change USB CONN CN31,CN35 footprint and pin-define (follow USB CONN Standard)	Circuit modify
		20071016	Page29: Add "EV@" for D43 (remove it for UMA sku BOM)	Circuit modify
		20071016	Page32: Modify PL12 footprint to slove SMT A-open issue	Circuit modify
		20071016	Page35: Change PR81 from 10k(no stuff) to 100k(stuff)	Circuit modify
		20071016	Page ALL: Remove PX function	Circuit modify
		20071016	Page28: Add FM CKT	Circuit modify
		20071016	Page30: Change LED5/6 to right angle type	Circuit modify
		20071016	Page22: Remove CN36 (FM tuner CONN)	Circuit modify
		20071017	Page22: change PCBEEP from AGND to GND	Circuit modify
		20071017	Page31: update Hole7,2835,33,40 footprint	Circuit modify
		20071018	Page31: update Hole15 footprint	Circuit modify
		20071018	Page16: Add LOW_DET to GPIO48(SB600)	Circuit modify
		20071018	Page15: change USB sequence for layout route smoothly	Circuit modify
		20071018	Page30: change LED7/8 footprint (follow ME)	Circuit modify
		20071018	Page19: update CN25 HDMI footprint to HDMI-C12816-119A5-L-19P-H-BL5	Circuit modify
		20071019	Page29: Remove EC Pin111,Pin113,Pin10 from MMB LED control Pin to NC pin	Circuit modify
		20071019	Page29: GPIO04 and GPIO52, GPIO46 and GPIO77 can't co-exist. Change BT_EN from Pin27 to Pin111.Change CRT_SENSE# from Pin84 to Pin113	Circuit modify
		20071021	Page25: update new card CONN footprint	Circuit modify
		20071021	Page28: Change Power board CONN (CN8) footprint & P/N	Circuit modify
		20071021	Page28: Change Felica CONN (CN12) footprint and PN	Circuit modify
		20071021	Page28: Change Main strem MMB CONN (CN7) from 10pin to 6pin, update footprint & P/N	Circuit modify
		20071021	Page28: Change FM CONN (CN42) footprint	Circuit modify
		20071022	Page29: change CIR LED footprint and pin-define	Circuit modify
		20071022	Page19: add level shift for CEC circuit	Circuit modify
		20071022	Page22: U38/Pin16,17,18 reserve circuit for WHQL	Circuit modify
		20071022	Page32~37: update power schematic	Circuit modify
		20071022	Page20: Change CN4 PN from DFHS40FS825 to DFWF40MS000 (SMT-ME suggest)	Circuit modify
		20071023	Page29: EC pin10 connect CHG_EN (active high) to battery charger	Circuit modify
		20071023	Page29: EC pin117 connect LOM_DISABLE# to LAN IC.	Circuit modify
		20071023	Page24: LAN IC pin10 need to add "LOM_DISABLE#" signal connect to EC pin117 (to decrease LAN power consumption).	Circuit modify
		20071023	Page28: DEL net"FELICA_DE" in FELICA CONN(CN12/Pin6)	Circuit modify
		20071023	Page16: add FM CONN/Pin7 (LOW_DET) to SB600 GPIO48, add PU R453 10k to +3.3V	Circuit modify
		20071024	Page18: change CN22(S-video) to 4 pin, remove TV_COMP circuit	Circuit modify
		20071024	Page16: change C320,C325 from 10p to 27p (base on TXC report)	Circuit modify
		20071024	Page26: change C381,C380 from 22p to 18p (base on TXC report)	Circuit modify
		20071024	Page29: change C575,C579 from 6.8p to 15p (base on TXC report)	Circuit modify
		20071025	Page15: add R473,R475 (0 ohm) for USB port7 -EMI suggestion	Circuit modify
		20071025	Page15: add R461,R472 (0 ohm) for USB port0 -EMI suggestion	Circuit modify
		20071025	Page15: add R476,R498 (0 ohm) for USB port6 -EMI suggestion	Circuit modify
		20071025	Page15: add R670,R671 (0 ohm) for USB port2 -EMI suggestion	Circuit modify
		20071025	Page30: Modify WiMAX LED circuit (default no stuff)	Circuit modify
		20071025	Page31: Add EMI 0.1uF Solution: (1)+5V *14pcs (2)+5VPCU *22PCS (3)+3VPCU *21PCS (4)VIN *21PCS(5)+1.2V_S5 *1PC	Circuit modify
		20071025	Page22: Change C403,C409,C413,C416 footprint for EMI debug	Circuit modify
		20071025	Page22: Add R611,Remove R632 for Audio circuit	Circuit modify
		20071025	Page03: Add WLAN & LAN CLKREQ circuit (BOI request)	Circuit modify
		20071026	Page31: Add0805 footprint for HOLE15,17,18 (EMI debug)	Circuit modify



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Model	REV	DATE	CHANGE LIST	NOTE
BD3A	B2A	20071026	Page28: Short R165 for CN11/Pin7	Circuit modify
		20071026	Page18: no stuff D4 for CRT CONN CN18/Pin11 (BOI request)	Circuit modify
		20071026	Page29: Add BATLED0/BATLED1/PWRLED PU to +3VPCU	Circuit modify
		20071026	Page16: Add R686 to +3V, no stuff R361 for leakage issue	Circuit modify
		20071026	Page31: Remove all ESD pad	Circuit modify
		20071026	Page15: SWAP USBP7 & USBP9 (BT can't exist with USB)	Circuit modify
		20071026	Page15: Modify USB OC pin control in SB side	Circuit modify
		20071026	Page06: For floating issue,DEL C437, Add 100ohm (R687) to GND	Circuit modify
		20071026	Page20: Change HALL Sensor(MR4)PN	Circuit modify
		20071026	Page30: Change LED7,LED8 PN	Circuit modify
		20071029	Page24: Change Giga LAN frim 8055 to 8072	Circuit modify
		20071029	Page28: Change CN6/Pin1 from MX1 to MX5	Circuit modify
		20071030	Page25: no support WiMAX, no stuff R423	Circuit modify
		20071030	Page33: Stuff PR127 for DDPWRGD(PU11/Pin13,28)	Circuit modify
		20071030	Page06: Change Thermal sensor(U6) from MAX6657 to GMT G781 for Cost down	Circuit modify
		20071030	PageALL: Change T117,T91,T99,T114,T105,T115,T92,T87,T88 footprint from TP3075 to TP3050	Circuit modify
		20071101	Page23: Stuff R390,R654. No stuff U22,C407,R388,Q41 for VR can't smothly issue.	Circuit modify
	C3A	20071126	Page25: Add PCIE Debud card circuit for CN21	Circuit modify
		20071127	Page28: Add level shift circuit for TP LED	Circuit modify
		20071127	Page23: reserve a smith trigger in the VR portion	Circuit modify
		20071127	Page29: Stuff R510, no-stuff R509 (slope BT module can't bring up issue)	Circuit modify
		20071129	Page19: HDMI CONN SMT B open issue : update HDMI CONN footprint from HDMI-C12816-119A5-L-19P-H-BL5 to HDMI-C12816-119A5-L-19P-V-BL5	Circuit modify
		20071129	Page18: Board ID issue:Change external Pull down to 1K ohm for all BOARD_ID and LOW_DET circuit	Circuit modify
		20071129	Page25: Short CPPE#,CPUSB# together and connect to USB_OC4#/GPM4#(ball A6) of SB600	Circuit modify
		20071130	Page19: Change LCD DISPLAY circuit to slope LCD panel will flash when system into S3/S4/S5 and change mode issue.	Circuit modify
		20071205	Page14: Remove Delay PCIE Power for A-Link IF	Circuit modify
		20071206	Page32~37: update power schematic	Circuit modify
		20071206	Page30: update LED5,LED6 footprint and PN	Circuit modify
		20071206	Page24: Change R669(0 ohm) to D6(Diode) for leakage issue (3VPCU to 3V_S5)	Circuit modify
		20071206	Page22: Stuff R399,R379,R627 for ESD issue	Circuit modify
		20071206	Page22: no Stuff R371 for Audio issue:When flash BIOS with B machine, there is not "bi bi" sound, but "bo bo" sound.	Circuit modify
		20071206	Page23: Slove Audio issue:When plug in-out headphone, headphone has bo sound.1. Change R652&R651 to C741,C742(10U/6.3V 0603)	Circuit modify
			2. Stuff R386/R387 to 1K(0402)	Circuit modify
		20071206	Page22: Remove D19 to slove Audio issue Switch Mute to Un-mute, sound will delay about 2 seconds.	Circuit modify
		20071206	Page15: Add D56,D57 for leakage issue (3VPCU to 3V_S5)	Circuit modify
		20071206	Page31: Add EMI CAP 30pcs	Circuit modify
		20071207	Page19: update HDMI footprint to HDMI-C12816-119A5-L-19P-H-BD3	Circuit modify
		20071207	Page32: Change PR148,PR145 footprint to slove Battery charge/discharge fail issue	Circuit modify
		20071207	Page18: Add C185 for EMI issue	Circuit modify
		20071207	Page24: Add PI-type filter for LAN Transformer Power +2.5V_1.8V_LAN (EMI request)	Circuit modify
		20071207	Page24: Add L71 Bead for LAN Power +1.2V_LAN (EMI request)	Circuit modify
		20071207	Page28: Co-layout L72,L73,L74 (commond choke) for USB CONN-Daughter board and BT module CONN (EMI request)	Circuit modify
		20071207	Page32: Add Diode to avoid burn issue when BATTERY+(Pin2) and ID(PIN3) short	Circuit modify
		20071207	Page26: Change Card reader CONN(CN33)damping resister from 0 ohm to 33ohm	Circuit modify
		20071207	Page25: Change New card footprint to NCARD-13180151-T-26P-L-BL5S	Circuit modify
		20071209	Page31: Update Hole43.44 footprint	Circuit modify
		20071211	Page29: Change U41(CIR) from BEBK0081D00 to BEBK0081D01	Circuit modify
		20071211	Page18: Change CN22(S-Video) from DFMD04FR296(Yellow Color) to DFMD04FR006(Black Color)	Circuit modify
		20071214	Page14,20,28,31: Add EMI CAP 83pcs	Circuit modify
		20071217	Page18: Change L4,L5,L6 from CX8BA220007 to CX0HM121008 (EMI issue)	Circuit modify
		20071217	Page15: Stuff C387 to 22pF(CH02206JB08) for EMI issue	Circuit modify
		20071217	Page20: Stuff C20,C23 to 100pF (CH11006JB18) for EMI issue	Circuit modify
		20071217	Page20: No stuff C431(CH11006JB18) for HDMI function	Circuit modify
		20071217	Page28: Remove R613,R612,R624,R626 (CS00003J951); Stuff L73,L74 (CX216900002) for EMI issue	Circuit modify
		20071217	Page28: Remove R276,R278 (CS00003J951); Stuff L72 (CX216900002) for EMI issue	Circuit modify
		20071217	Page26: Stuff L68/L69 to CX0900JT005, remove RN34,RN35(CJ000042N12) for EMI issue	Circuit modify
		20071217	Page26: Stuff C398 to CH02206JB08 for EMI issue	Circuit modify
		20071217	Page23: Follow TE1: change L41,L42,L43,L44 from 0 ohm (CS00003J951) to CX8LL121002	Circuit modify
		20071217	Page24: Stuff C799,C625 to 0.1uF(CH41002KB93) for EMI request	Circuit modify
		20071217	Page23: No stuff R386 and R387(CS21002JB34) to meet HP Jack signal measure and HP plug- unplug haven't happen bobo-sound	Circuit modify
		20071217	Page23: Stuff C404 and C405 to 0.1u(CH41002KB93) to enhance avoid 3G noise and meet HP Jack signal measure	Circuit modify
		20071217	Page22: Stuff C396(CH41002KB93) for EMI issue	Circuit modify
		20071218	Page21: Stuff C568,C548(CH41006K911) for EMI issue	Circuit modify
		20071218	Page31: Stuff C217,C71,C746,C748,C750,C755,C770,C794,C798,C817,C822 = 0.1uF (CH41002KB93) for EMI issue	Circuit modify
		20071218	Page15: Change R369 from CS03302JB29 to CX5HM121104 for EMI issue	Circuit modify
		20071218	Page31: Stuff R680,R681,R682 for ESD issue	Circuit modify



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Model	REV	DATE	CHANGE LIST	NOTE
BD3A	C3C	20080108	Page14: Modify RTC circuit. (Change R581 from 4.7k to 6.8k. Change R571,R667 from 8.66k to 2k)	Circuit modify
		20080108	Page14: Change RTC Battery from VARTA (AHL03001441) to MATSUSHITA (AHL03002005)	Circuit modify
		20080108	Page19: Change back HDMI connector(CN25) footprint from HDMI-C12816-119A5-L-19P-H-BD3 to HDMI-C12816-119A5-L-19P-H-BL5	Circuit modify
		20080108	Page23: Change Location name from R386,R387 to C437,C571 for Head-phone circuit	Circuit modify
		20080108	Page19: Correct HDMI-CEC-SMBUS net name for U11/Pin17,18	Circuit modify
		20080109	Page20: Enegrystar 4.0 Idle power issue, When BLON= High, Turn ON LCD then turn ON MMB. When BLON= Low, Turn OFF LCD then turn OFF MMB	Circuit modify
		20080109	Page29: Reserve D58,D59 for EC leakage issue(Default stuff R566,R567)	Circuit modify
		20080115	Page24: Change CN28 (RJ45 CONN) from DFTJ12FR024 to DFTJ12FR035	Circuit modify
		20080115	Page27: Change CN32(2nd SATA CONN) from DFHS22FR064 to DFHS22FR094	Circuit modify
		20080115	Page10~18: Update U24,U32 (RS690M/SB600) PN	Circuit modify
		20080115	Page27: Change CN32 footprint from SATA-127043FR022XX27ZR-22P-L-H to SATA-127043FR022G285ZR-22P-L	Circuit modify
		20080115	Page30: DEL CN10 (Keyboard CONN) Co-kay footprint 196130-340201-34P-L (SMT open issue)	Circuit modify
		20080115	Page03: Add C572(18pF) for SB_OSCIN , Stuff R558 to 68 ohm, C603 to 22pF (EMI issue)	Circuit modify
		20080115	Page03: Add C186(10pF) for NB_OSC, Stuff R122 to 68 ohm, C171 to 22pF (EMI issue)	Circuit modify
		20080115	Page06: ADD D29,D35 (Varistor) for CN19 Pin1/3 (ESD issue)	Circuit modify
		20080115	Page06: Change Location name from R468 to L75, Change PN from CS00002JB38 to CX8LM25003, Change footprint from RC0402 to RC0603(ESD)	Circuit modify
		20080115	Page06: Add C573,C574 for LDT_RST#, CPU_HT_RESET# (ESD issue)	Circuit modify
		20080115	Page08: DEL R84,R75,C78,C89 footprint	Circuit modify
		20080115	Page25: Add "IV@" for CN30	Circuit modify
		20080115	Page12: Reserve R84 (0 ohm) for HDMI buffer circuit	Circuit modify
		20080115	Page18: Stuff D9,D11 for TV CONN(ESD issue)	Circuit modify
		20080115	Page18,29: DEL D4,D5 footprint and DEL CRT_SENSE# net, No stuff R218	Circuit modify
		20080115	Page20: Add D36(Varistor) for LID591#(MR4/Pin2) (ESD issue)	Circuit modify
		20080115	Page21: Remove R492,R517, Short CN27/Pin189,190 to VIN directly.	Circuit modify
		20080115	Page28: Reserve C584,C585(100pF) for 3ND_MBDATA,3ND_MBCLK (EMI issue)	Circuit modify
		20080115	Page11: Add "IV@" for C454,C459	Circuit modify
		20080115	Page22: Stuff Varistor for C403,C409,C413,C416 (ESD issue)	Circuit modify
		20080115	Page23: Refer to HP circuit, ADD 4pcs .1uF CAP(C410,C411,C600,C601) for MIC CONN	Circuit modify
		20080115	Page26:ADD D60,D61,D62,D63 (0402 Varistor) for 1394 CONN (ESD issue)	Circuit modify
		20080115	Page26: DEL RN34,RN35 Co-lay footprint (1394 circuit)	Circuit modify
		20080115	Page28: Remove Felica circuit (No-stuff Q57,Q56,C476,R450,CN12,R178,R179)	Circuit modify
		20080115	Page28: Change L72 from CX216900002 to cx163210007, DEL R276,R278 Co-lay footprint (BT circuit)	Circuit modify
		20080115	Page28: Add C78 (100pF) to CN14/Pin9 (USB_DETACH) for EMI issue	Circuit modify
		20080115	Page28: DEL C42, Add D45 for CN8/Pin2 (ESD issue) - default no stuff	Circuit modify
		20080115	Page28: Change L73,L74 from CX216900002 to cx163210007,DEL R613,R612,R624,R626 Co-lay footprint (USB-daughter circuit)	Circuit modify
		20080115	Page28: Stuff L64,L65 to cx163210007,DEL R607,R608,R572,R569 Co-lay footprint (USB-MB circuit)	Circuit modify
		20080115	Page28: ADD D46,D64 for USB CONN, and Stuff D49,50,51,52 (ESD issue)	Circuit modify
		20080115	Page30: DEL debug LED circuit, DEL R210,LED4	Circuit modify
		20080115	Page31: No stuff E-SATA NUT for REV.G platform. Remove Hole31,Hole37	Circuit modify
		20080115	Page31: DEL R680,R681,R682, Short directly for Hole15,17,18	Circuit modify
		20080115	Page19: Add R651,R652 0 ohm for CEC SDA/SCL net	Circuit modify
		20080115	Page19: DEL L56,L57,L58,L59,R465,R474,R493,R495,R486,R488,R478,R483,C226,C227 for HDMI circuit	Circuit modify
		20080115	Page25: Add +3V_WL_VDD for CN21/Pin39,41, Reserve R465 0 ohm for debug use	Circuit modify
		20080115	Page08: Change CN23 footprint from DDR-C-292564-200P to DDR-C-292564-200P-BD3A (SMT open issue)	Circuit modify
		20080115	Page08: Change CN24 footprint from DDR-C-1734071-200P to DDR-C-1734071-200P-BD3A (SMT open issue)	Circuit modify
		20080115	Page30: Change SW4 footprint from SW-NSS506-212F-CCCD1T-3P to SW-NSS506-212F-CCCD1T-3P-BD3A (SMT open issue)	Circuit modify
		20080115	Page23: Change CN43 footprint from MDC-1-179373-2-12P-RUV to MDC-1-179373-2-12P-RUV-BD3A (SMT open issue)	Circuit modify
		20080115	Page23: Stuff D24 for ESD issue (HP CONN)	Circuit modify
		20080115	Page31: ADD C823,C824 for VIN and +NB_CORE (EMI issue)	Circuit modify
		20080115	Page35: Change PL15 Footprint from CDRH104R to CDRH104R-BD3A (SMT open issue)	Circuit modify
		20080115	Page22: Add R386 for ViSTA WHQL circuit	Circuit modify
		20080115	Page20: DEL L50 footprint , DEL RP33(short directly) for CCD circuit	Circuit modify
		20080115	Page28: Add C606,C607 for CN7/Pin1,2 (EMI issue)	Circuit modify
		20080116	Page31: Add "MD@" for Hole32, only stuff for Modern SKU	Circuit modify
		20080116	Page31: Follow BL5, update Hole footprint (13pcs)	Circuit modify
		20080116	Page24: Change RJ45 footprint from LAN-100073FR012G101ZL-12P to rj45-c100s7-10806-1-12P	Circuit modify
		20080116	Page30: Update LED7,LED8 footprint to LED27-21-BHC-ZL1M2TY-3C	Circuit modify
		20080117	Page19: SWAP RN6,RN7,RN8 for HDMI layout routing	Circuit modify
		20080117	Page28: ADD D65,66,67 ESD solution for CN13 (Finger printer CONN)	Circuit modify
		20080117	Page28: ADD D68,69,72 ESD solution for CN14 (BT CONN)	Circuit modify
		20080117	Page20: ADD D73,74,75 ESD solution for CN4 (LCD CONN - CCD signal)	Circuit modify
		20080117	Page21: Reserve C825 (0.1uF) for PLTRST# (CN27/Pin103)	Circuit modify
		20080117	Page25: Reserve C827 (0.1uF) for PLTRST# (U33/Pin6)	Circuit modify
		20080117	Page14: Reserve C828 (0.1uF) for PLTRST# (R348)	Circuit modify
		20080117	Page19: Reserve C826 (0.1uF) for PLTRST# (U7/Pin2)	Circuit modify
		20080117	Page23: Connect pin 9,10 of CN40(Headphone) toD-GND.	Circuit modify



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