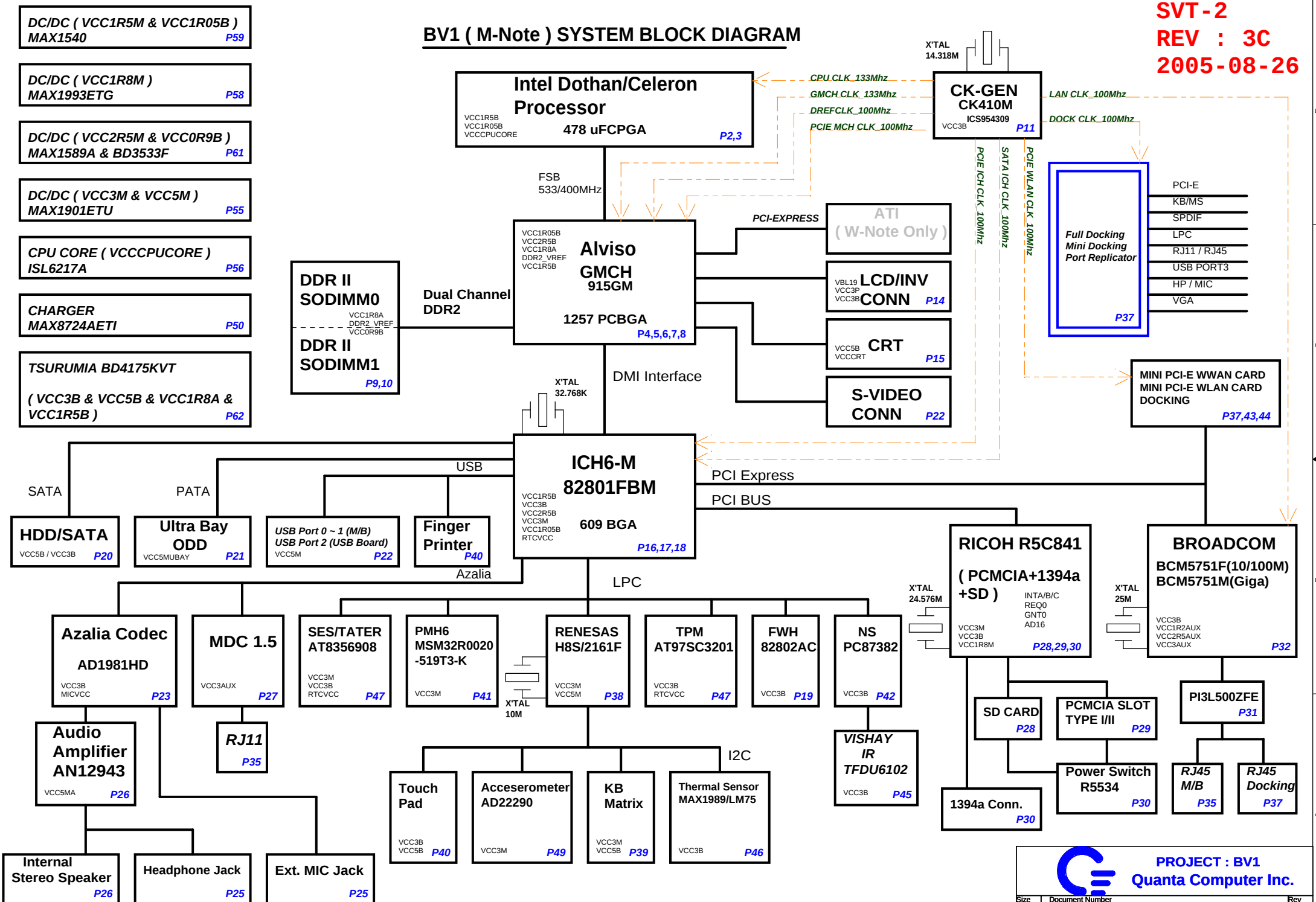
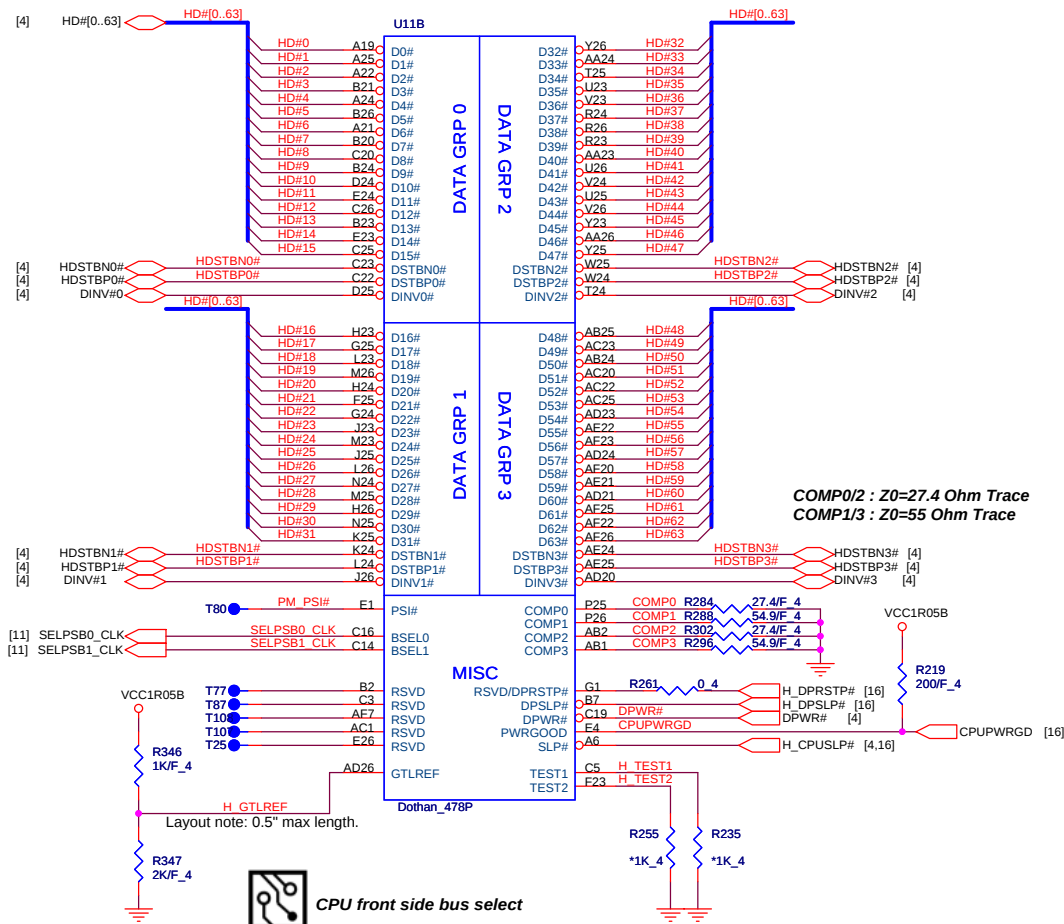
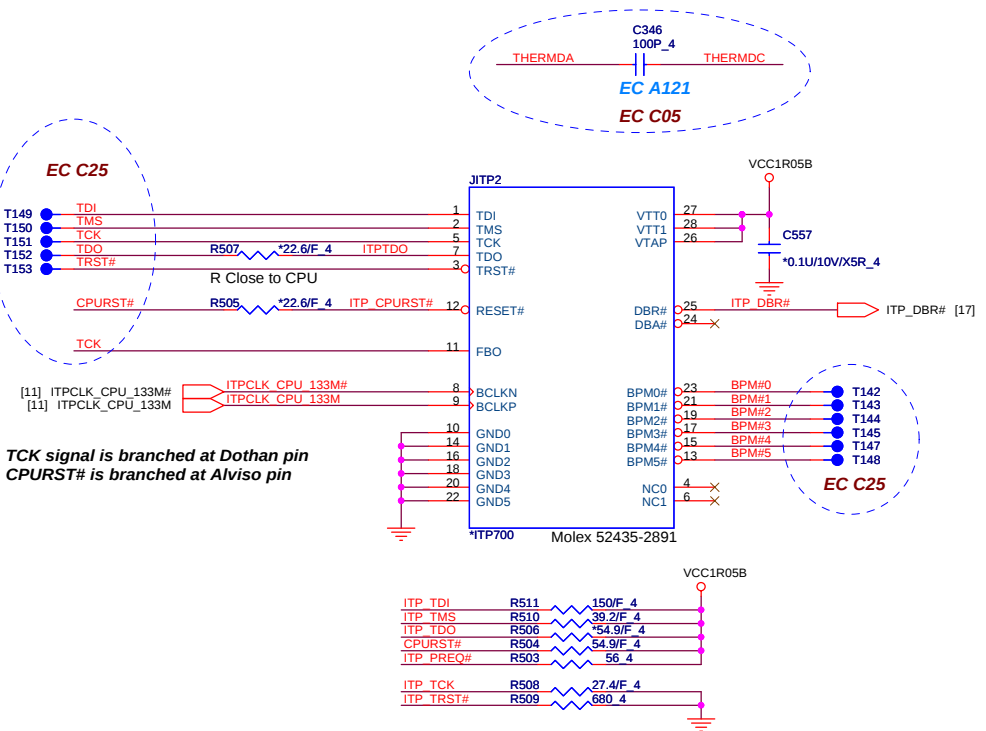
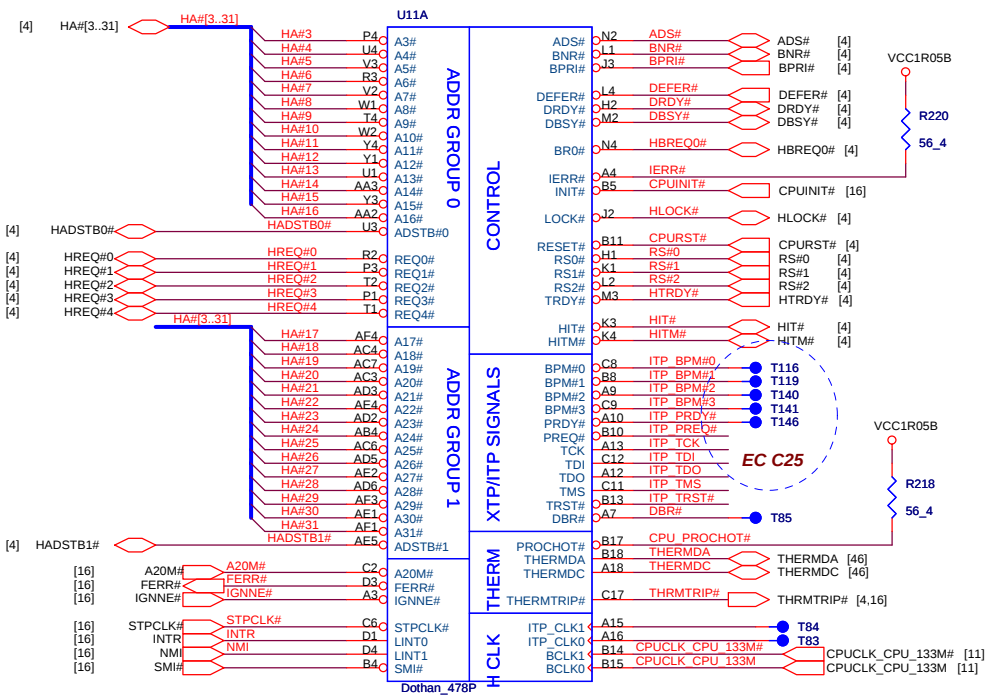


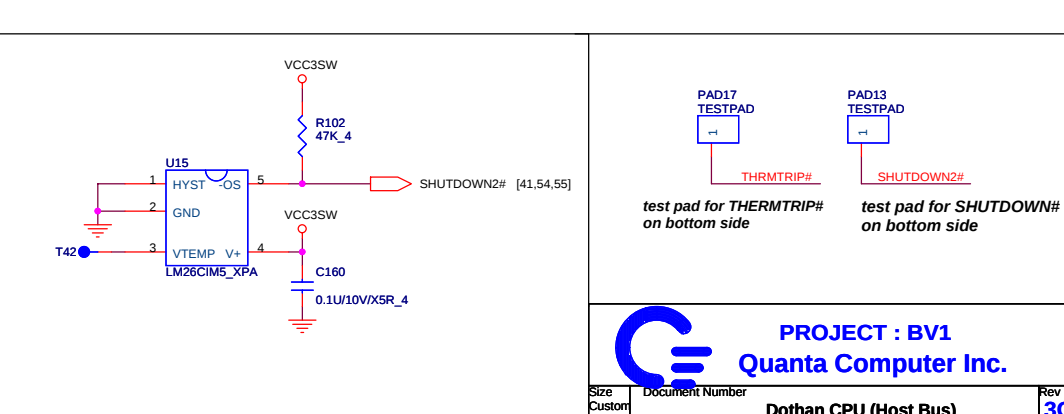
BV1 (M-Note) SYSTEM BLOCK DIAGRAM



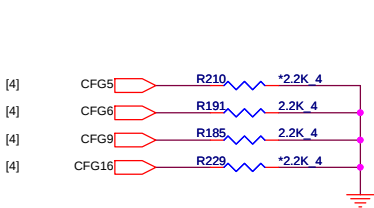
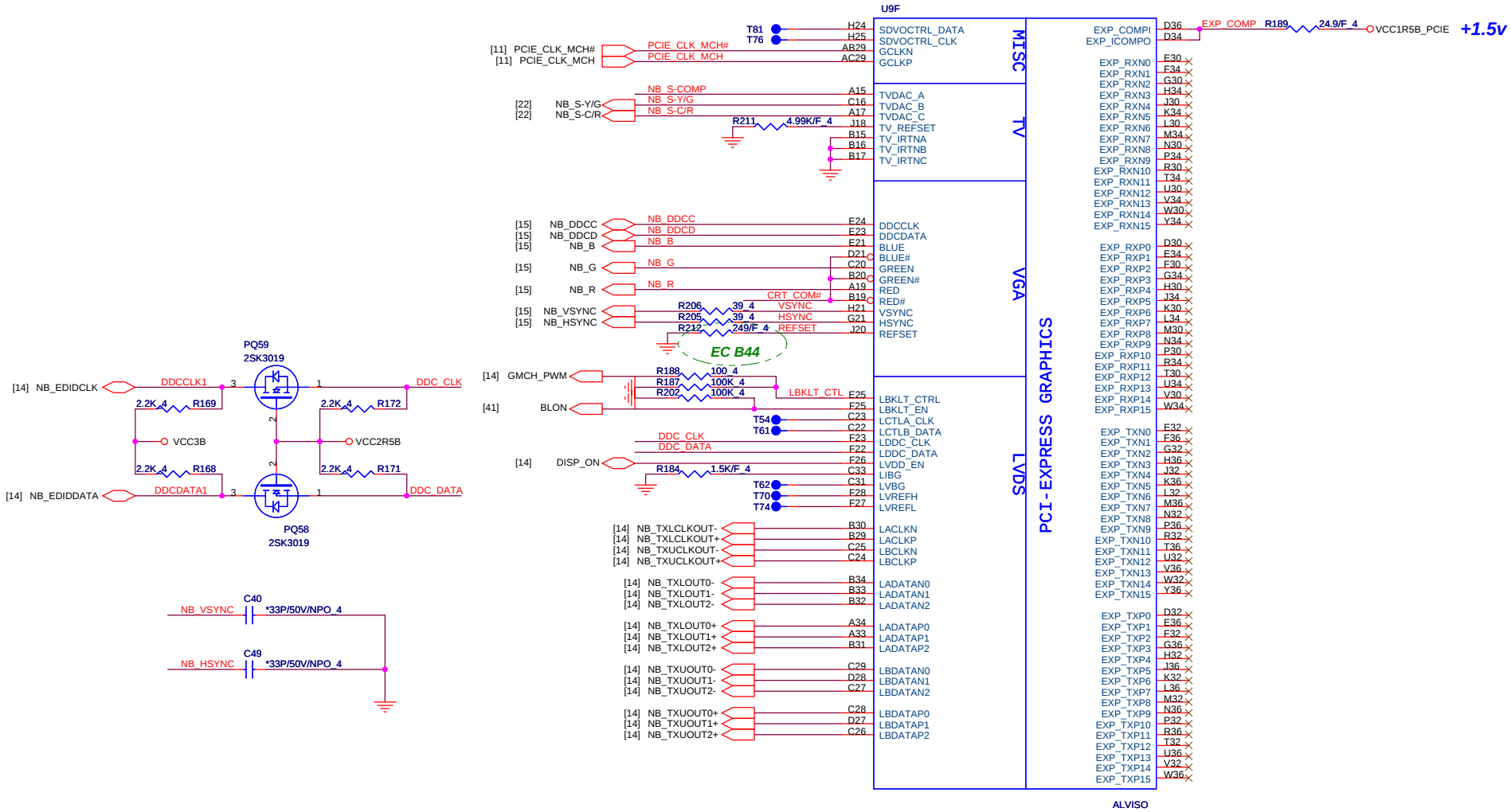


CPU front side bus select

	A Step	B Step
FSB	533	400
BSEL1	0	0
BSEL0	1	0



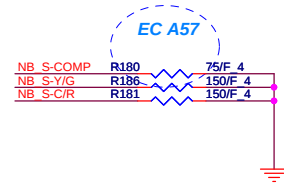
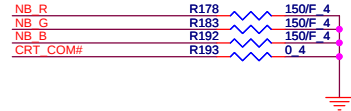


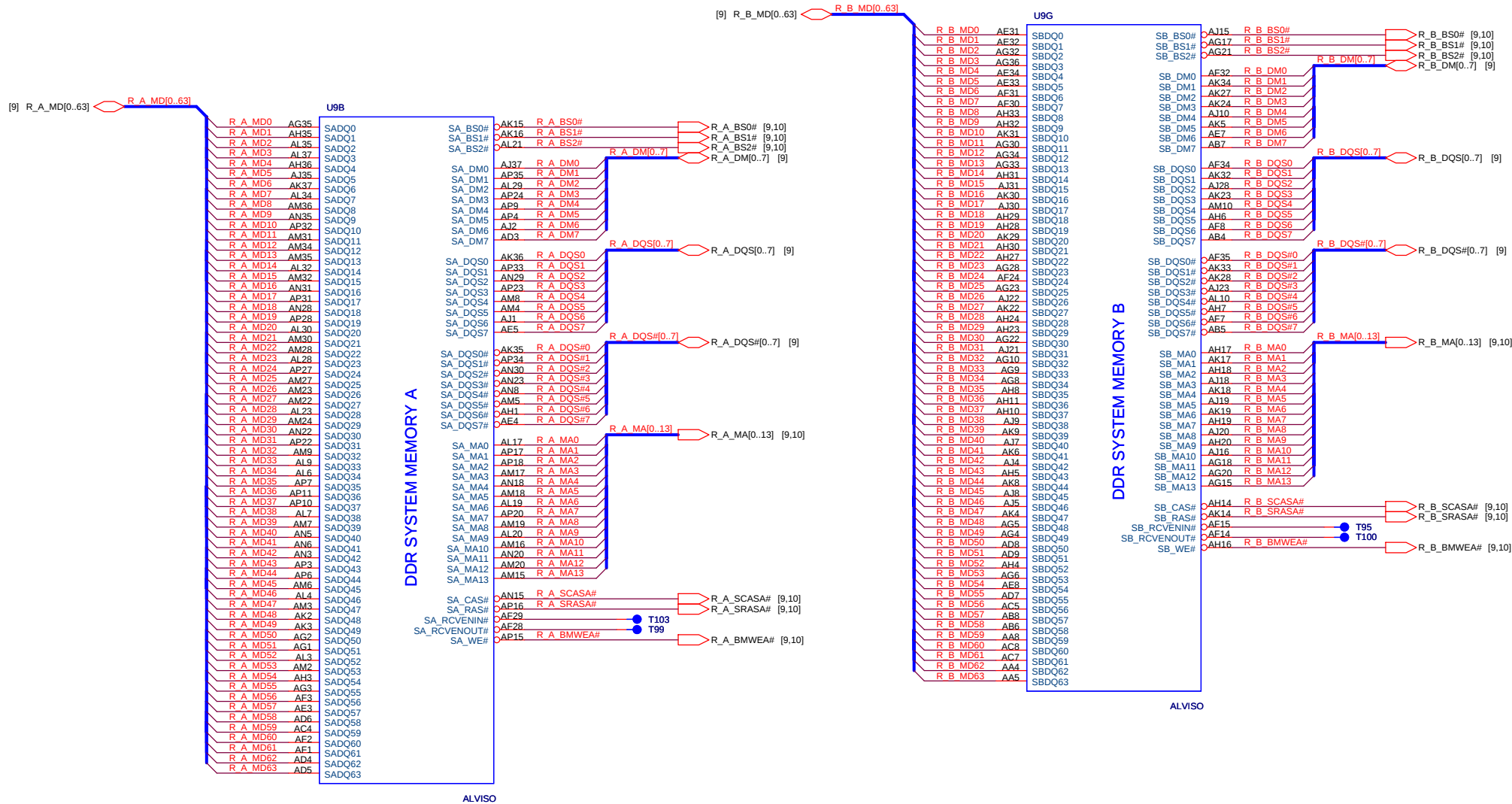


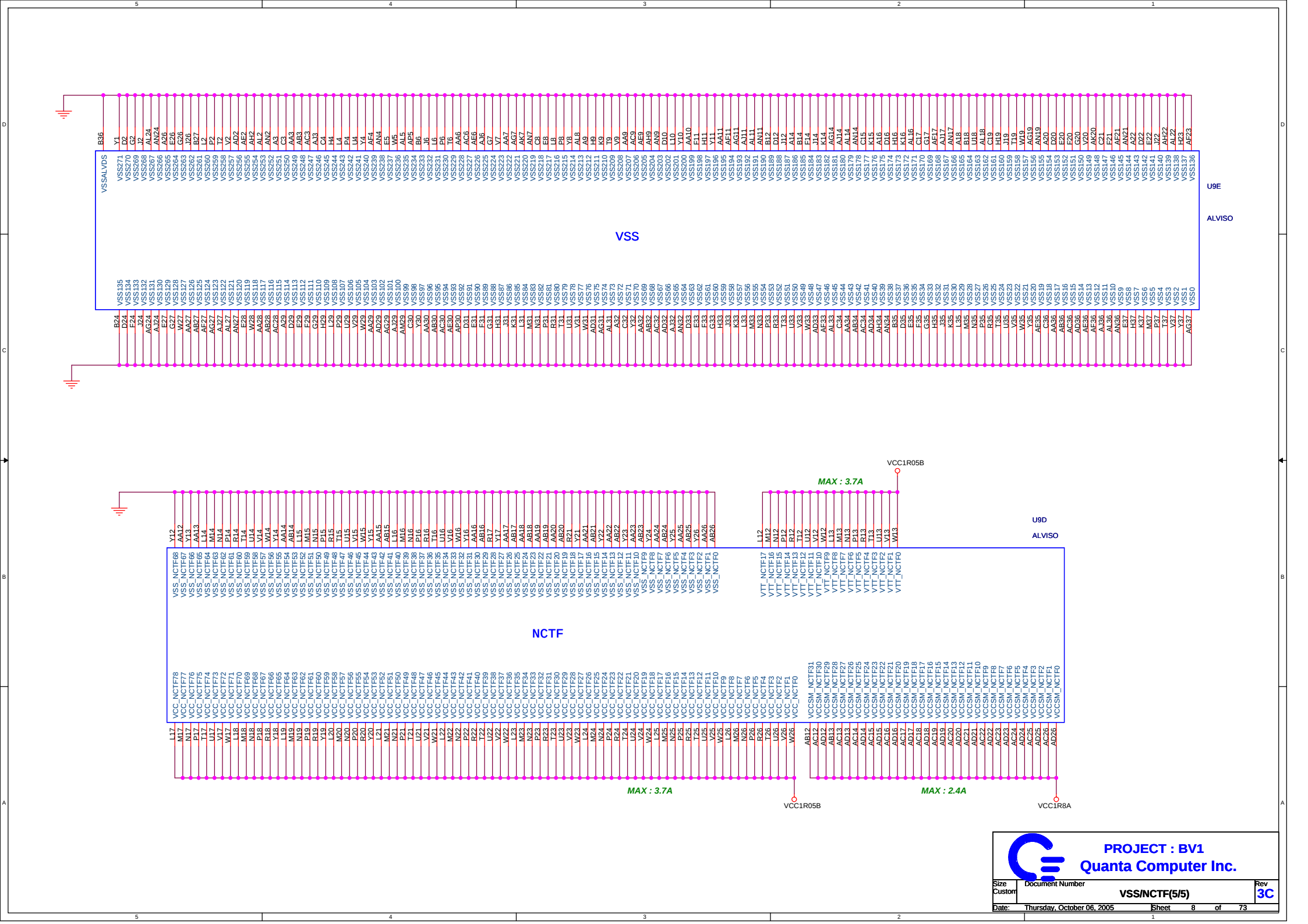
	Low	NO_ASM
CFG3	DDR533	
CFG5	DMix2	DMix4
CFG6	DDR II	DDR
CFG7	DT CPU	Mobile CPU
CFG9	Reverse Lane	Normal Operation
CFG11	FSB533	
CFG12	00 : Reserved	
CFG13	01 : XOR Mode Enabled 10 : All Z Mode Enabled 11 : Normal Operation	
CFG16	FSB Dynamic ODT Disabled	FSB Dynamic ODT Enabled

	NO_ASM	High
CFG3	CPUCORE=1.05V	CPUCORE=1.5V
CFG5	VTT=1.05V	VTT=1.2V

CFG9 : PCI-E Graphics Lane
Note : If in integrated GFX mode, need to use lane-reversal ADD2 add-in card since SDVO if it does not support lane reversal.









PLACE AT LEAST 1 CAP FOR EACH VDD PINS

BSEL0	BSEL1	CPU front side bus select			
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33

EC B38 EC C23

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

CG FS A 33.4

CG FS B 4.7K 4 R GC FS C

CG FS C 33.4

Clock Generator

	72 pin	64 pin
U38	954302	NO_ASM
U39	NO_ASM	954309
C152	10uF	NO_ASM
C151	0.047uF	NO_ASM
R80	2.2 ohm	NO_ASM
R362	NO_ASM	0 ohm
R365	0 ohm	NO_ASM
R385	NO_ASM	0 ohm
R361	NO_ASM	NO_ASM

PROJECT : BV1
Quanta Computer Inc.

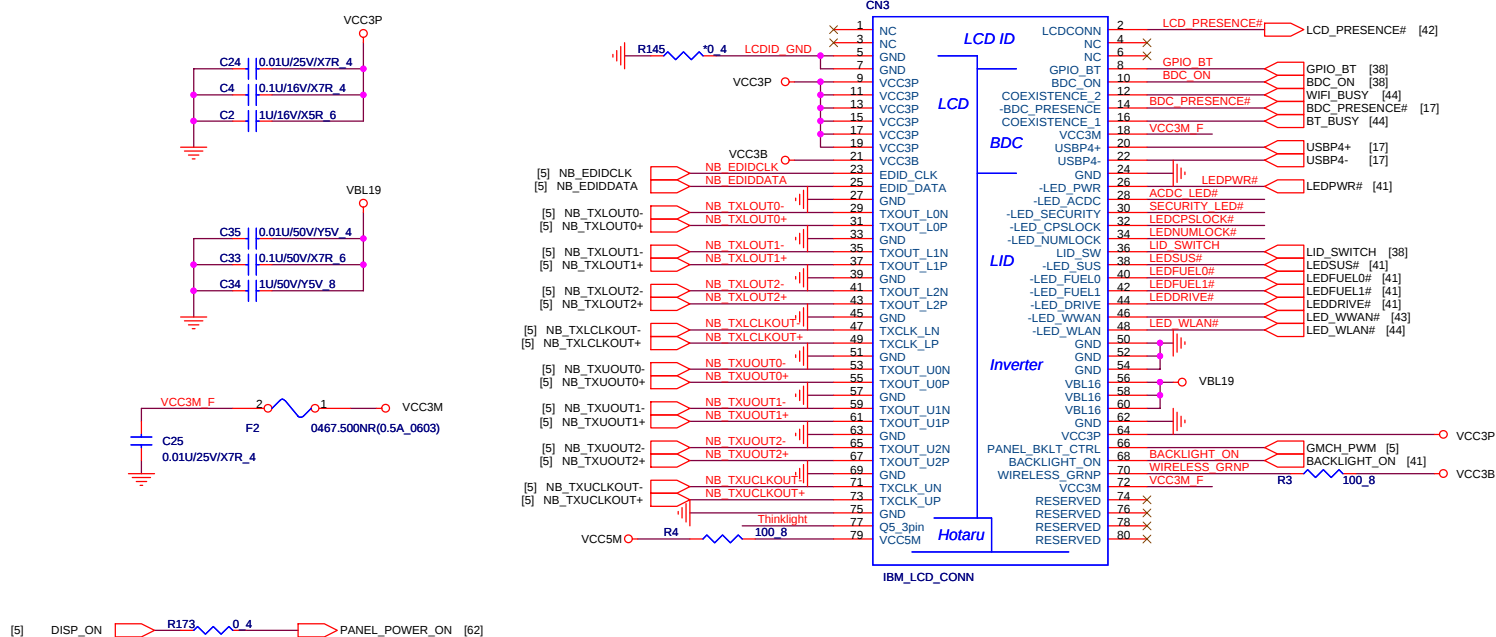
Size: Custom Document Number: CLOCK GENERATOR
Date: Thursday, October 06, 2005 Sheet 11 of 73

Rev 3C

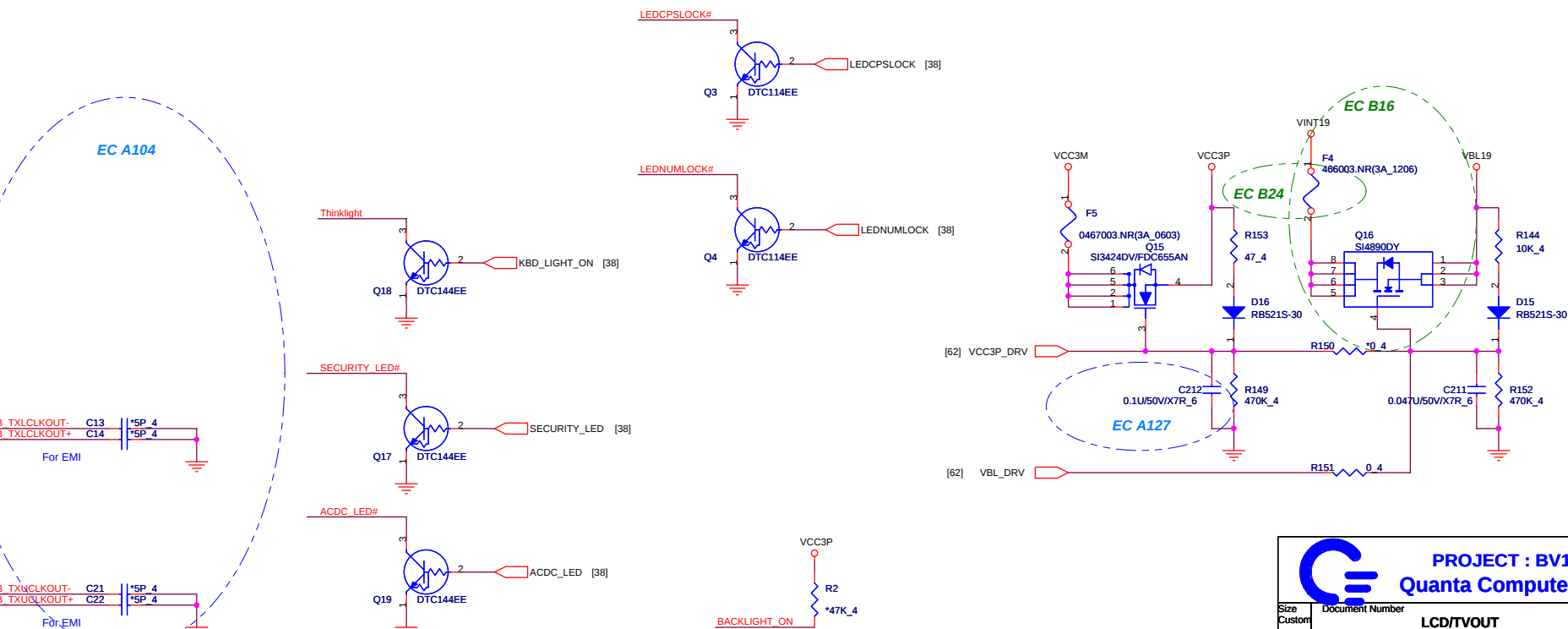
<BLANK PAGE>

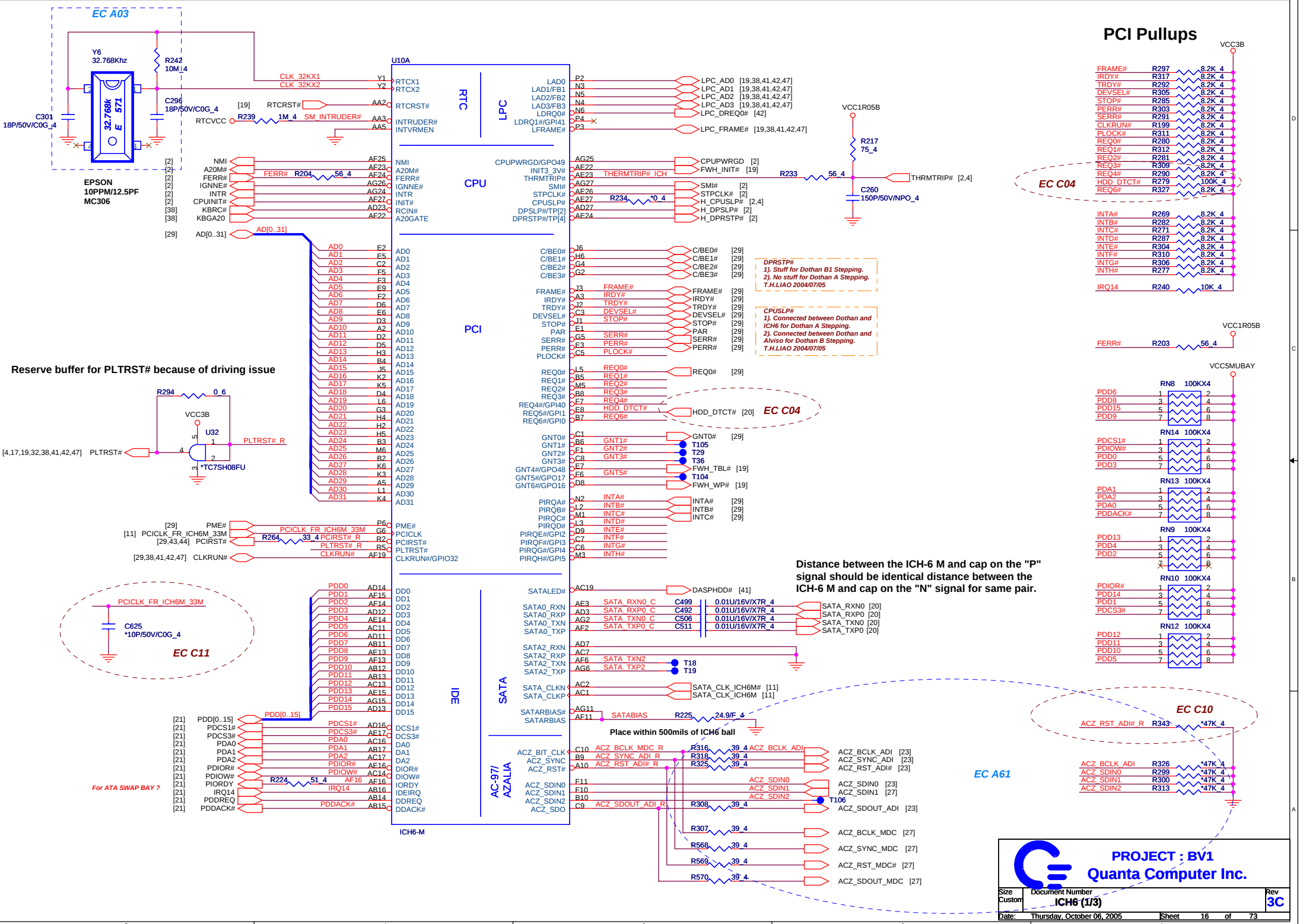
<BLANK PAGE>

LCD CONNECTOR



LCD PRESENCE#	C169	1000P 4
GPIO_BT	C176	1000P 4
BDC_ON	C197	1000P 4
BDC PRESENCE#	C199	1000P 4
LEDPOWER#	C26	1000P 4
LEDPCSLCK#	C27	1000P 4
LEDNUMLOCK#	C28	1000P 4
LID_SWITCH	C29	1000P 4
LEDSTATUS	C30	1000P 4
LEDPOWER#	C3	1000P 4
LEDPOWER#	C31	1000P 4
LEDPOWER#	C32	1000P 4
LED WLAN#	C634	1000P 4
NB_EIDIDCLK	C5	1000P 4
NB_EIDIDDATA	C6	1000P 4





PCI Pullups

FRAME#	R297	8.2K	4
IRDY#	R317	8.2K	4
TRDY#	R292	8.2K	4
DEVSEL#	R305	8.2K	4
STOP#	R285	8.2K	4
PERR#	R303	8.2K	4
SERR#	R291	8.2K	4
CLKRUN#	R199	8.2K	4
PLOCK#	R311	8.2K	4
REQ0#	R280	8.2K	4
REQ1#	R312	8.2K	4
REQ2#	R281	8.2K	4
REQ3#	R309	8.2K	4
REQ4#	R290	8.2K	4
HDD_DTCT#	R279	100K	4
REQ6#	R327	8.2K	4
INTA#	R269	8.2K	4
INTB#	R282	8.2K	4
INTC#	R271	8.2K	4
INTD#	R287	8.2K	4
INTE#	R304	8.2K	4
INTF#	R310	8.2K	4
INTG#	R306	8.2K	4
INTH#	R277	8.2K	4
IRQ14	R240	10K	4

PDD6	1	2
PDD8	3	4
PDD15	5	6
PDD9	7	8
PDCS1#	1	2
PDIOW#	3	4
PDD0	5	6
PDD3	7	8
PDA1	1	2
PDA2	3	4
PDA0	5	6
PDDACK#	7	8
PDD13	1	2
PDD4	3	4
PDD2	5	6
PDIOR#	7	8
PDD10	1	2
PDD1	3	4
PDCS3#	5	6
PDD12	7	8
PDD11	1	2
PDD14	3	4
PDD5	5	6
PDD7	7	8

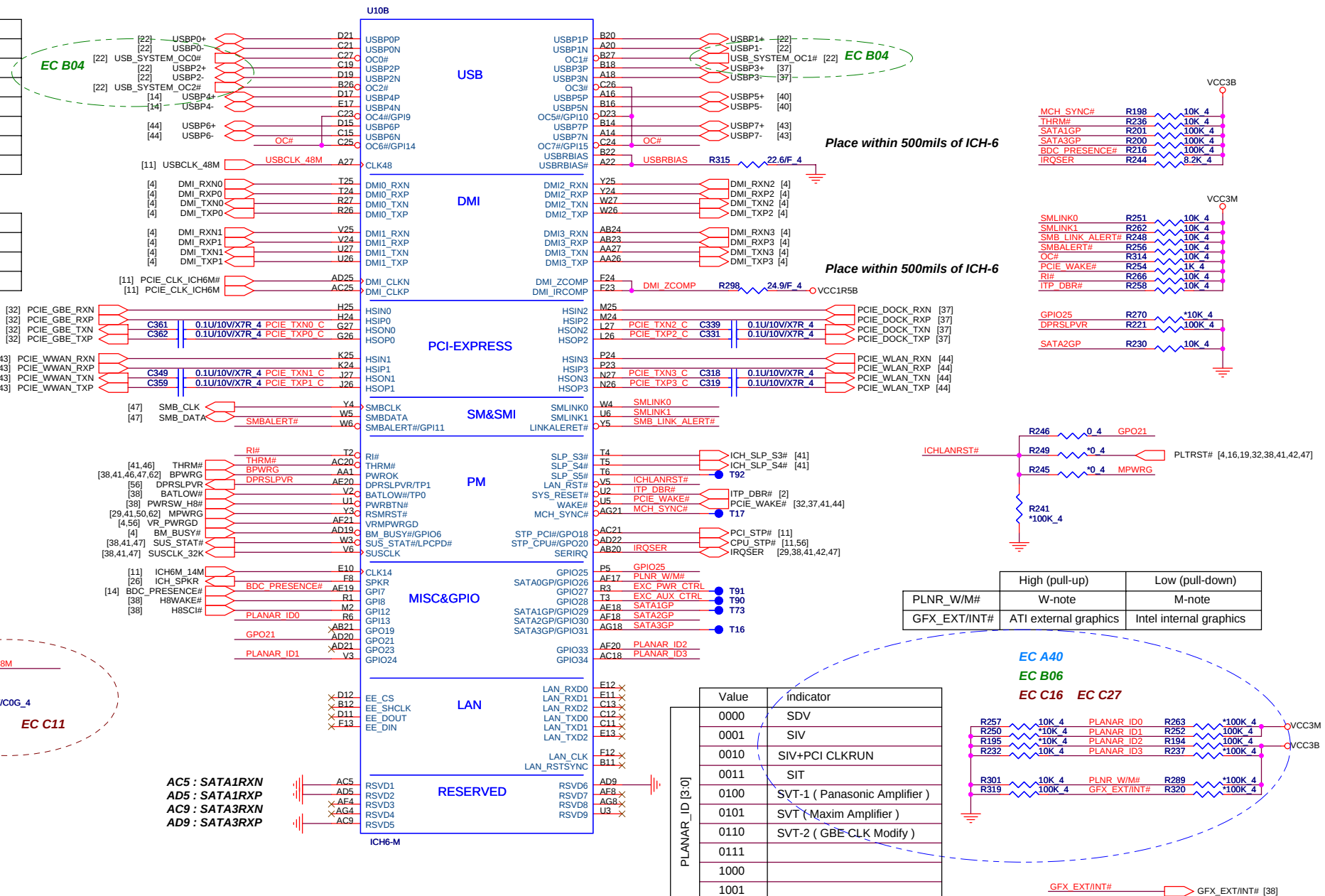
ACZ_RST_ADI#	R343	*47K	4
ACZ_BCLK_ADI	R326	*47K	4
ACZ_SDIN0	R299	*47K	4
ACZ_SDIN1	R300	*47K	4
ACZ_SDIN2	R313	*47K	4



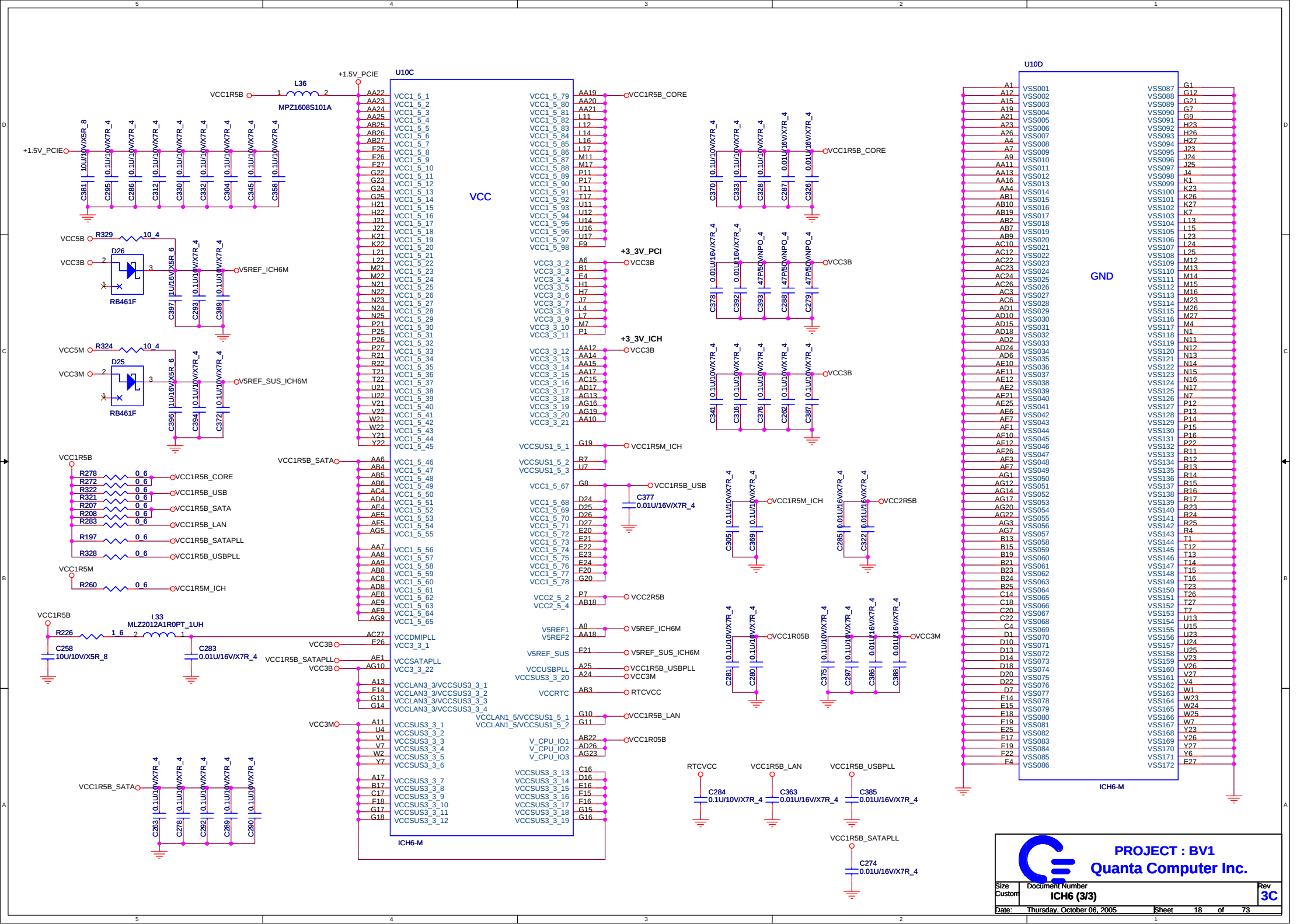
USB0	USB Port 1
USB1	USB Port 2
USB2	USB Port 3
USB3	Docking
USB4	Bluetooth
USB5	Finger print
USB6	N/A
USB7	MINI PCI-E Card

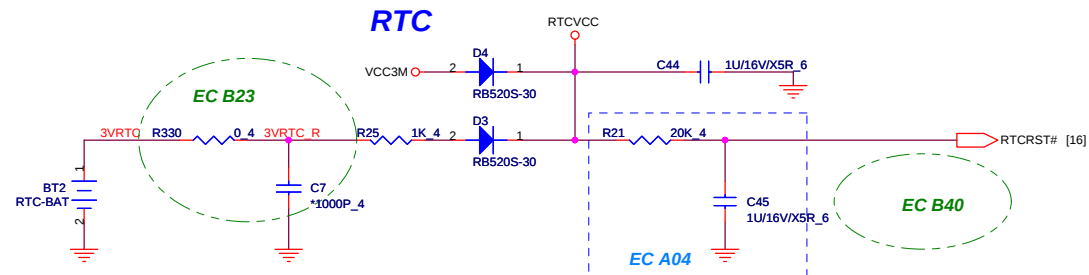
PCIe distribution

PCIE0	Enthernet Controller
PCIE1	N/A
PCIE2	Docking
PCIE3	MINI PCI-E Card

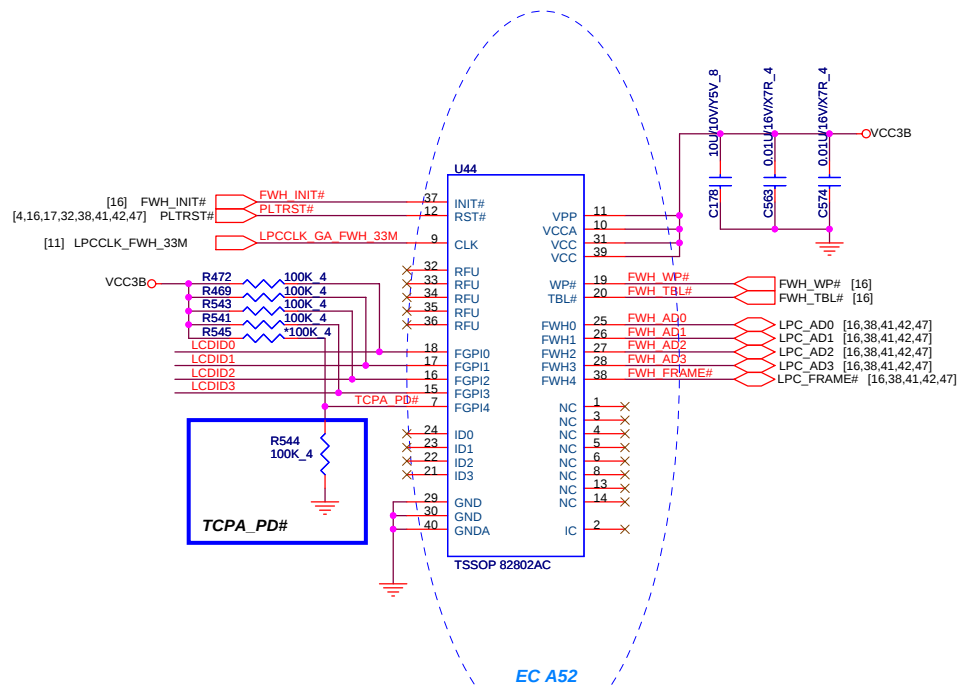


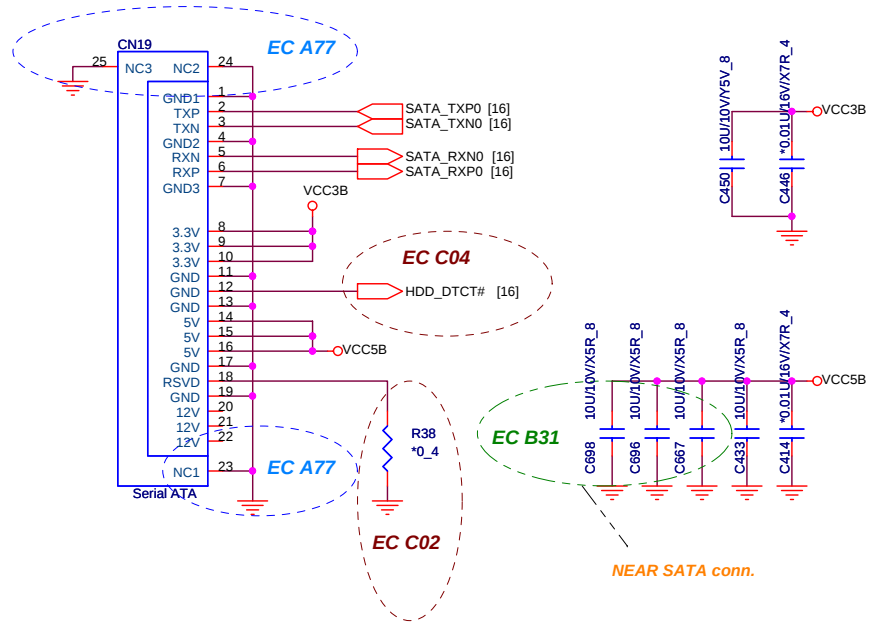
Value	indicator
0000	SDV
0001	SIV
0010	SIV+PCI CLKRUN
0011	SIT
0100	SVT-1 (Panasonic Amplifier)
0101	SVT (Maxim Amplifier)
0110	SVT-2 (GBE CLK Modify)
0111	
1000	
1001	
1010	
1011	
1100	
1101	
1110	
1111	



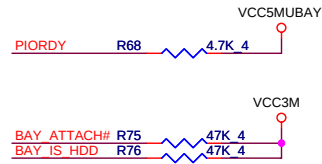
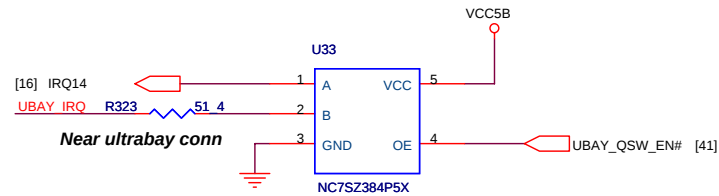
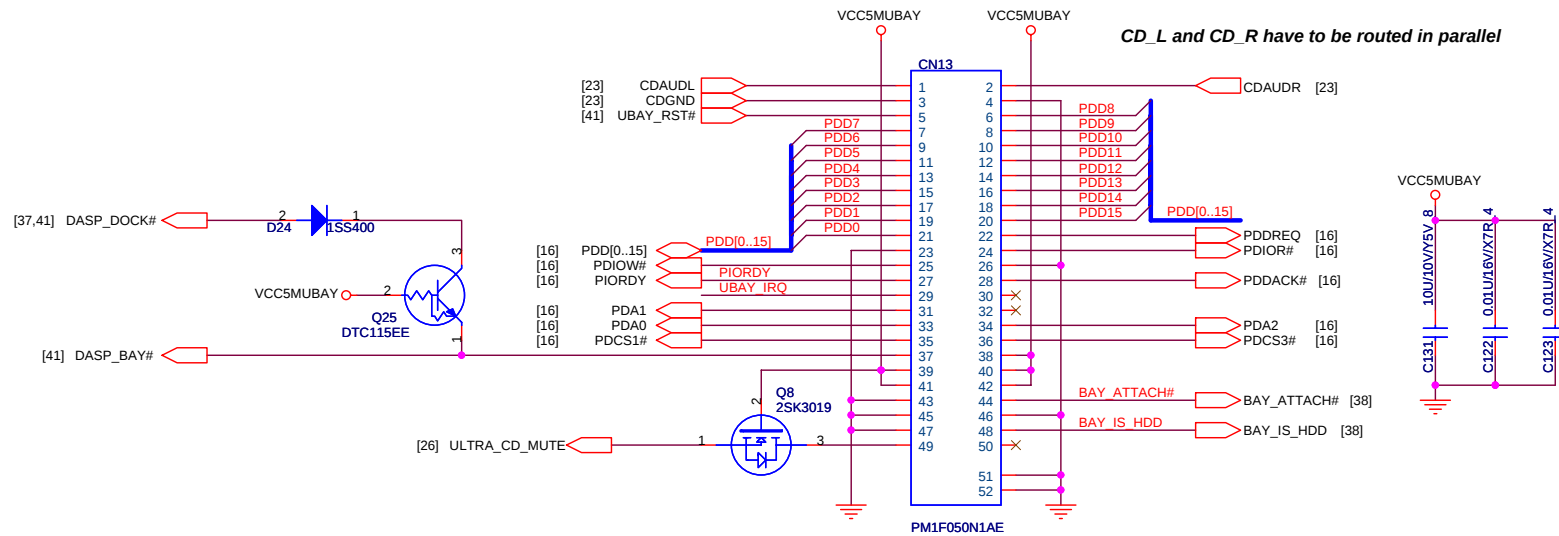


11/24 Change BT1 to be Tyco "1470366-2" and use cable-type coin battery





ODD CONNECTOR



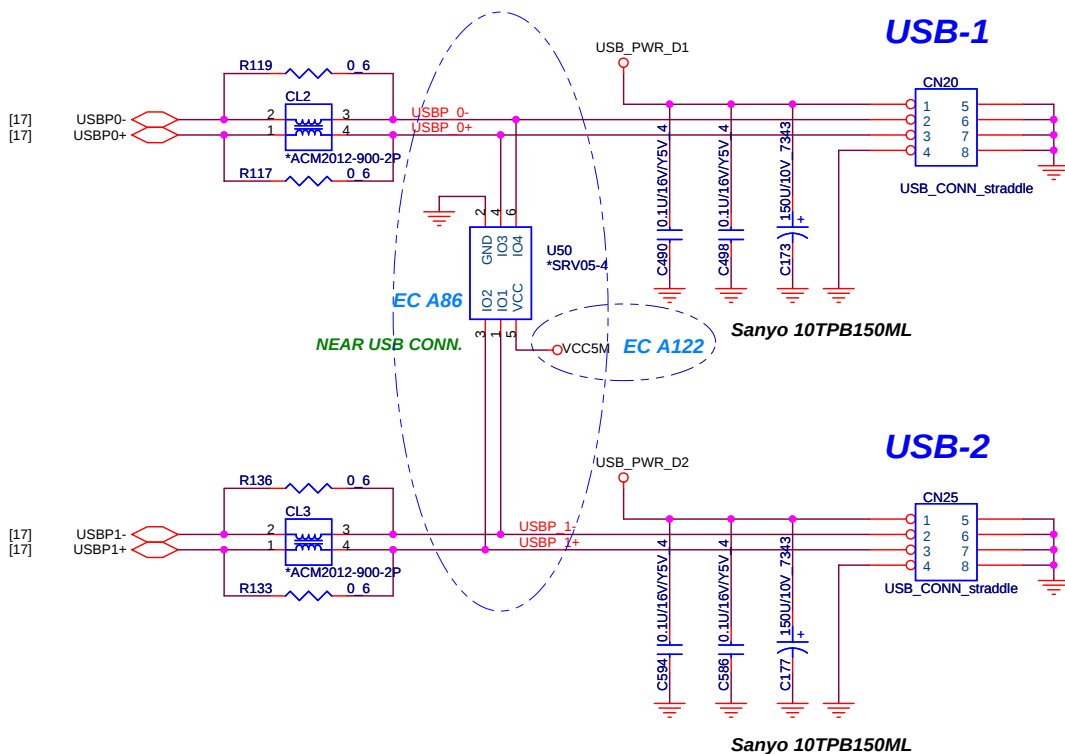
	BAY_ATTACH#	BAY_IS_HDD
NO_DEVICE	H	Don't care
OPTICAL	L	L
HDD	L	H

USB port(straddle type) *2

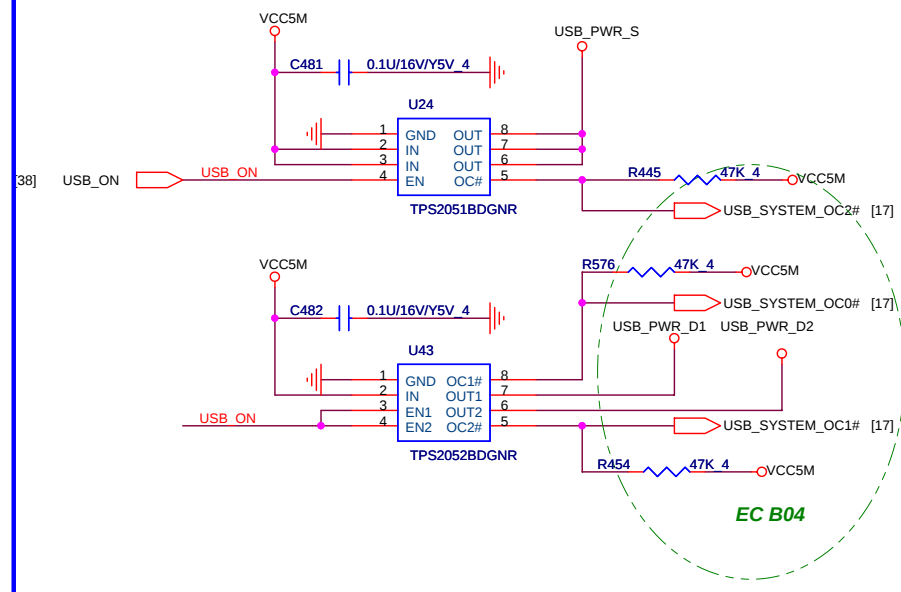
1. Common mode choke coil : TDK ACM2012-900 (reserved)

Place these resistors as the choke coil are also placed on the same pads of these two 0ohm resistors (0603size)

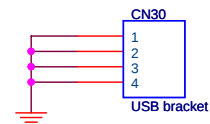
2. PGB0010603 ESD diode and 150U cap should close to USB conn



Change to TI solution (TPS2051 + TPS2052 power switch)
2004/12/09 Thunder

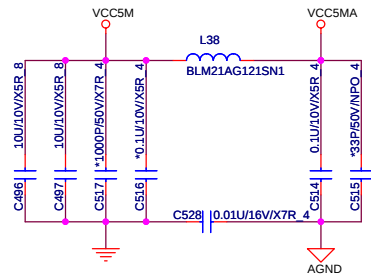
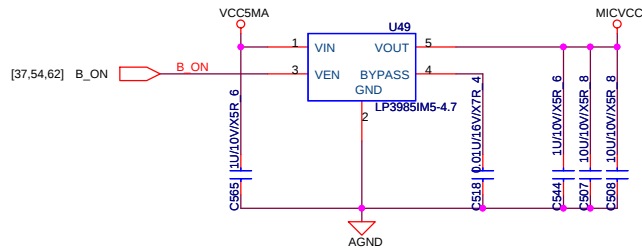


USB Bracket

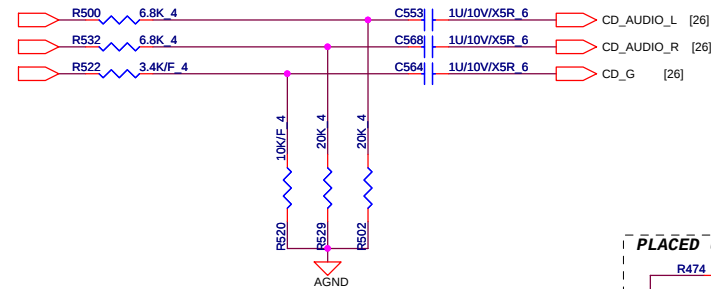


PROJECT : BV1
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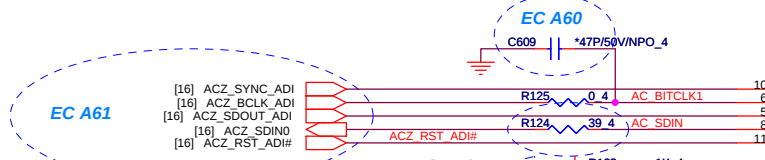
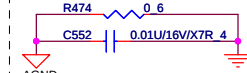
Size	Document Number	Rev
Custom	USB Ports	3C
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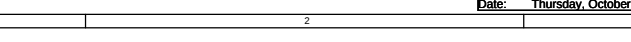
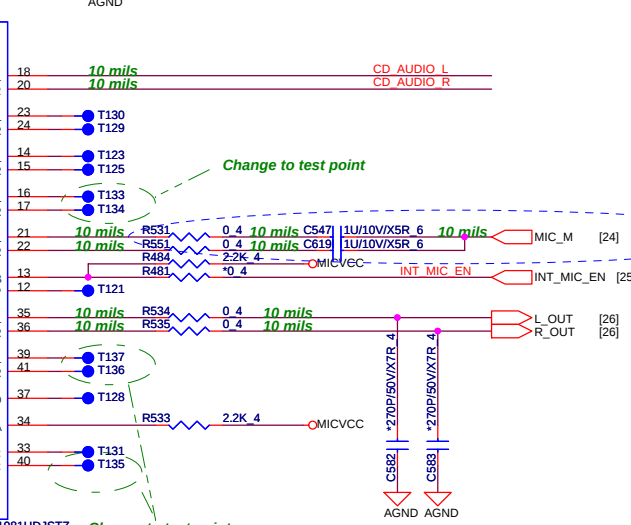
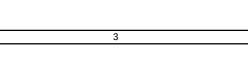
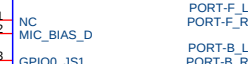
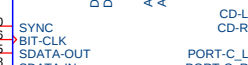
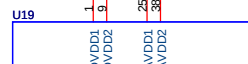
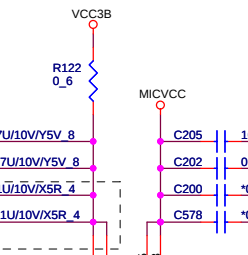
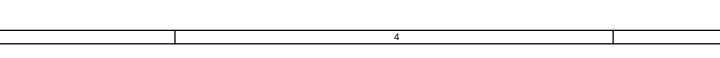
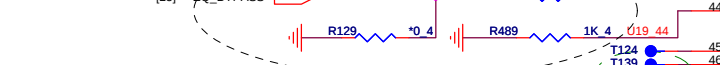
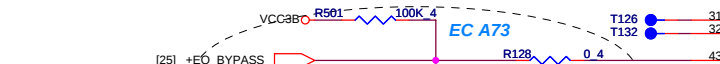
[21] CDAUDL
[21] CDAUDR
[21] CDGND

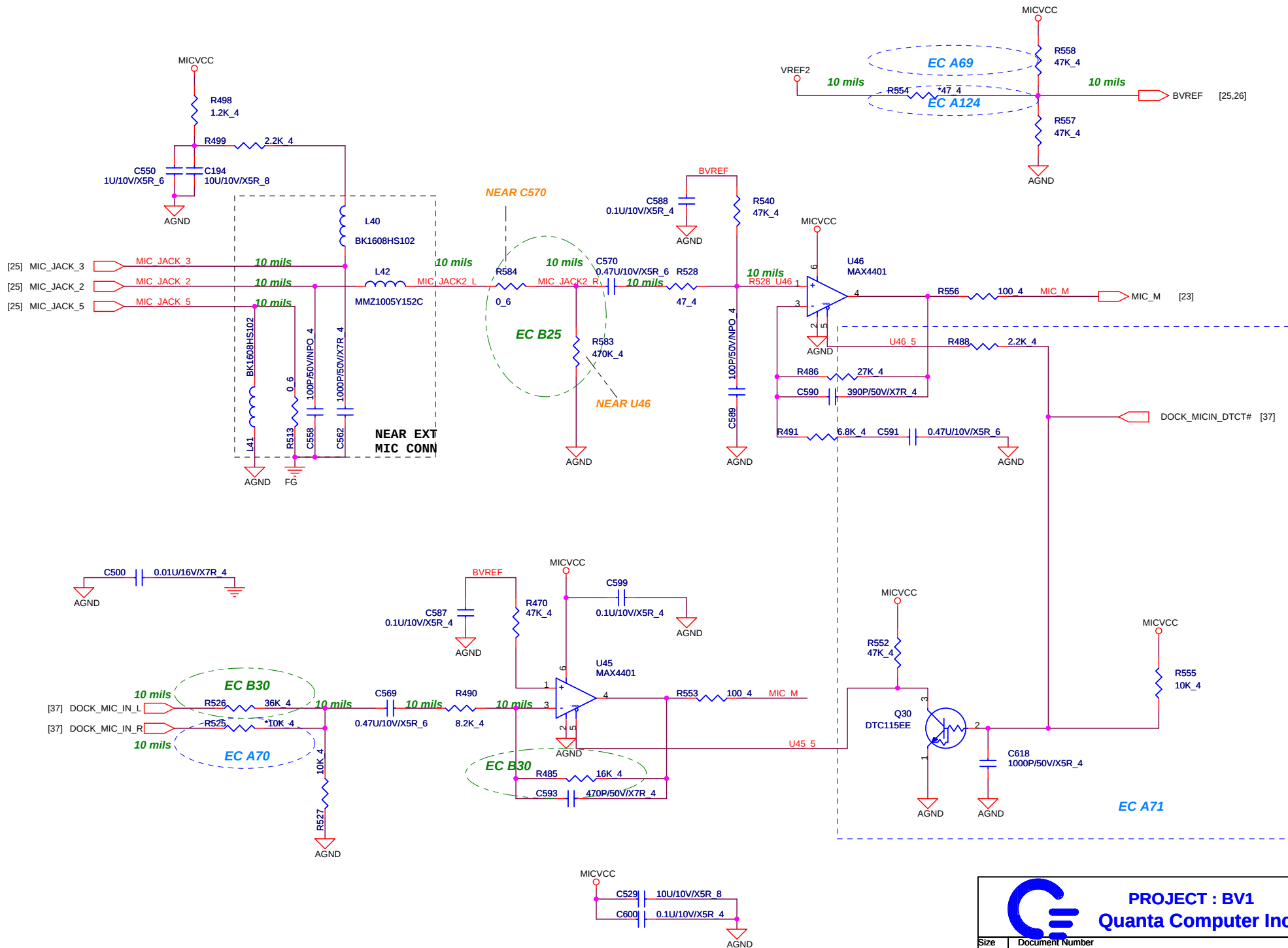


PLACED UNDER AD1981HD



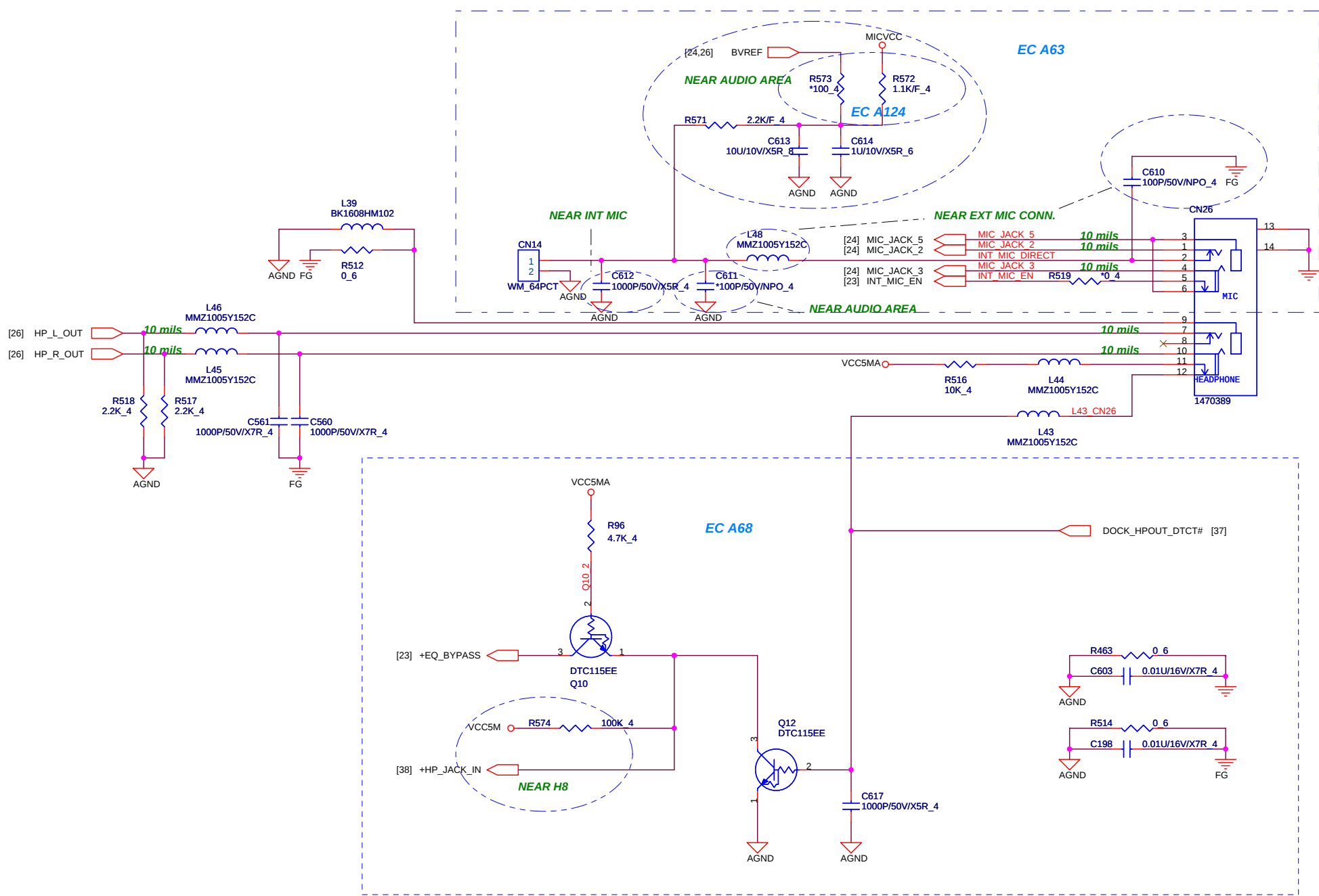
ACZ_RST_ADI#



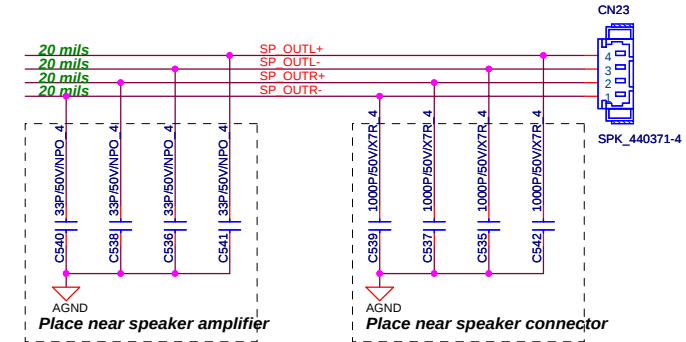
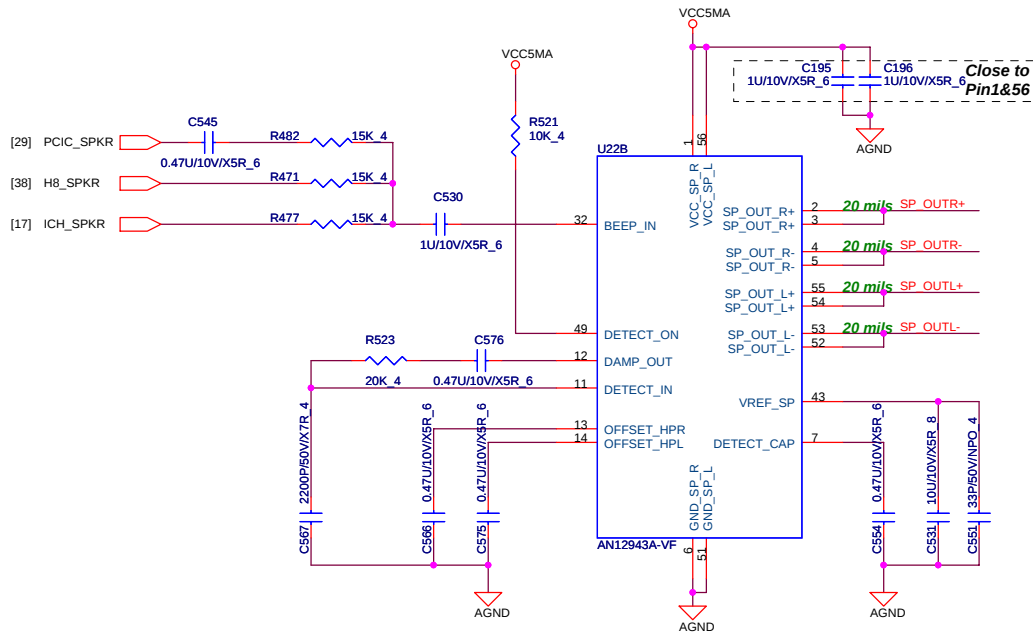
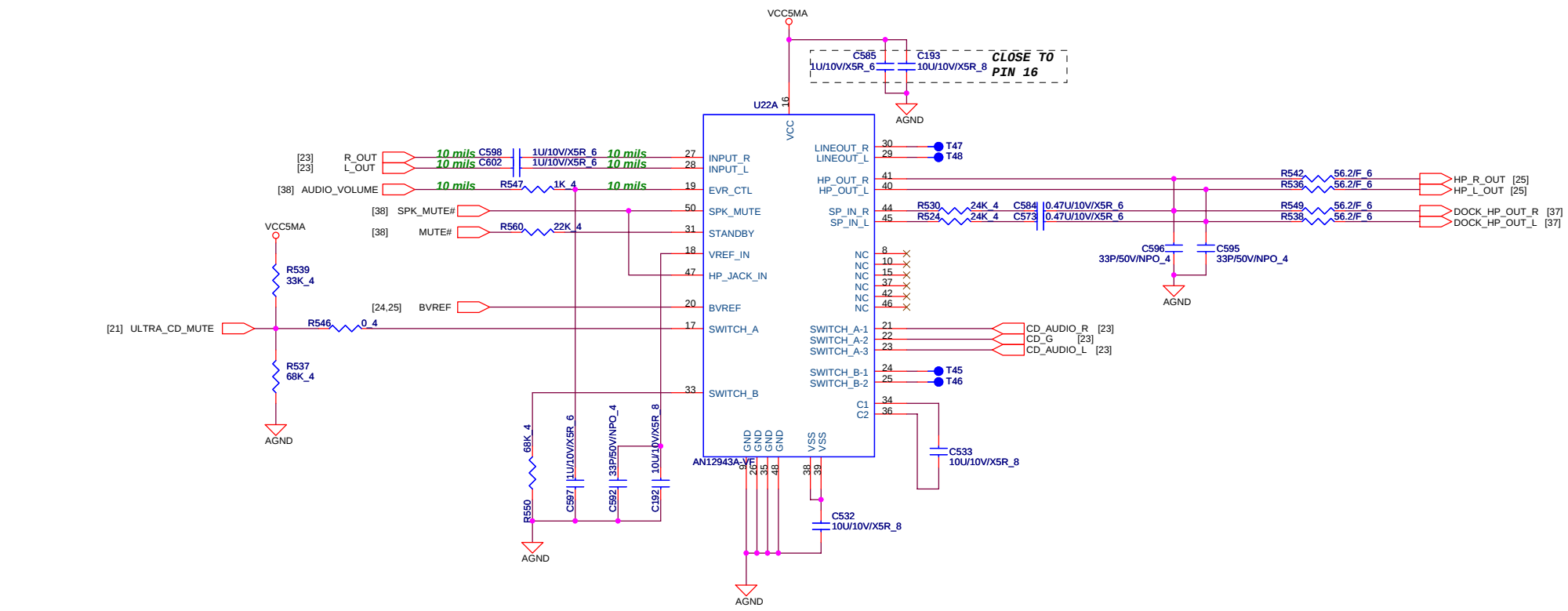


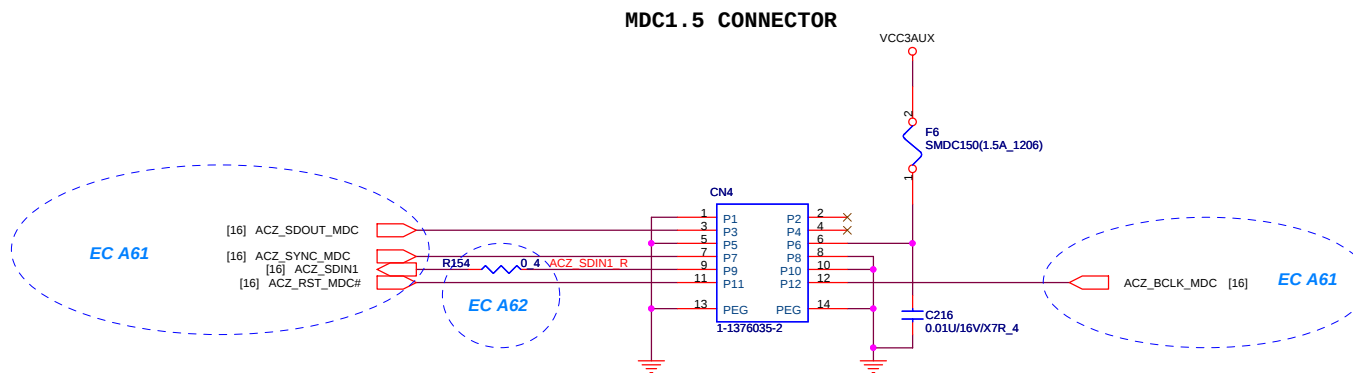
PROJECT : BV1
Quanta Computer Inc.

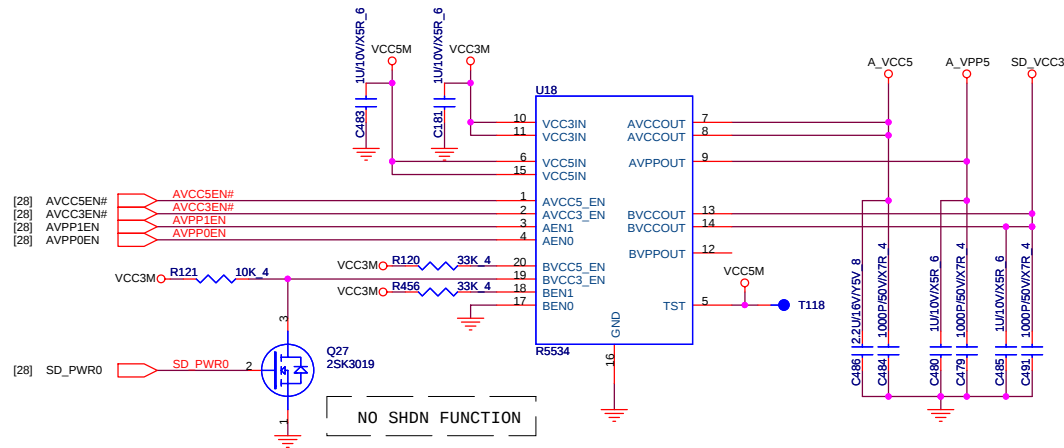
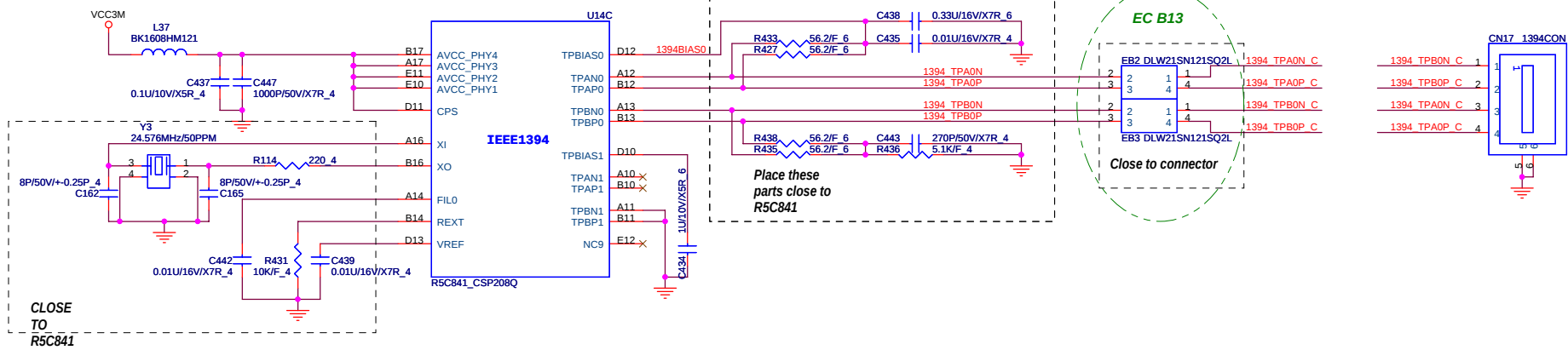
Size	Document Number	Rev
Custom	AUDIO_HP_MIC	3C
Date:	Thursday, October 06, 2005	Sheet 24 of 73

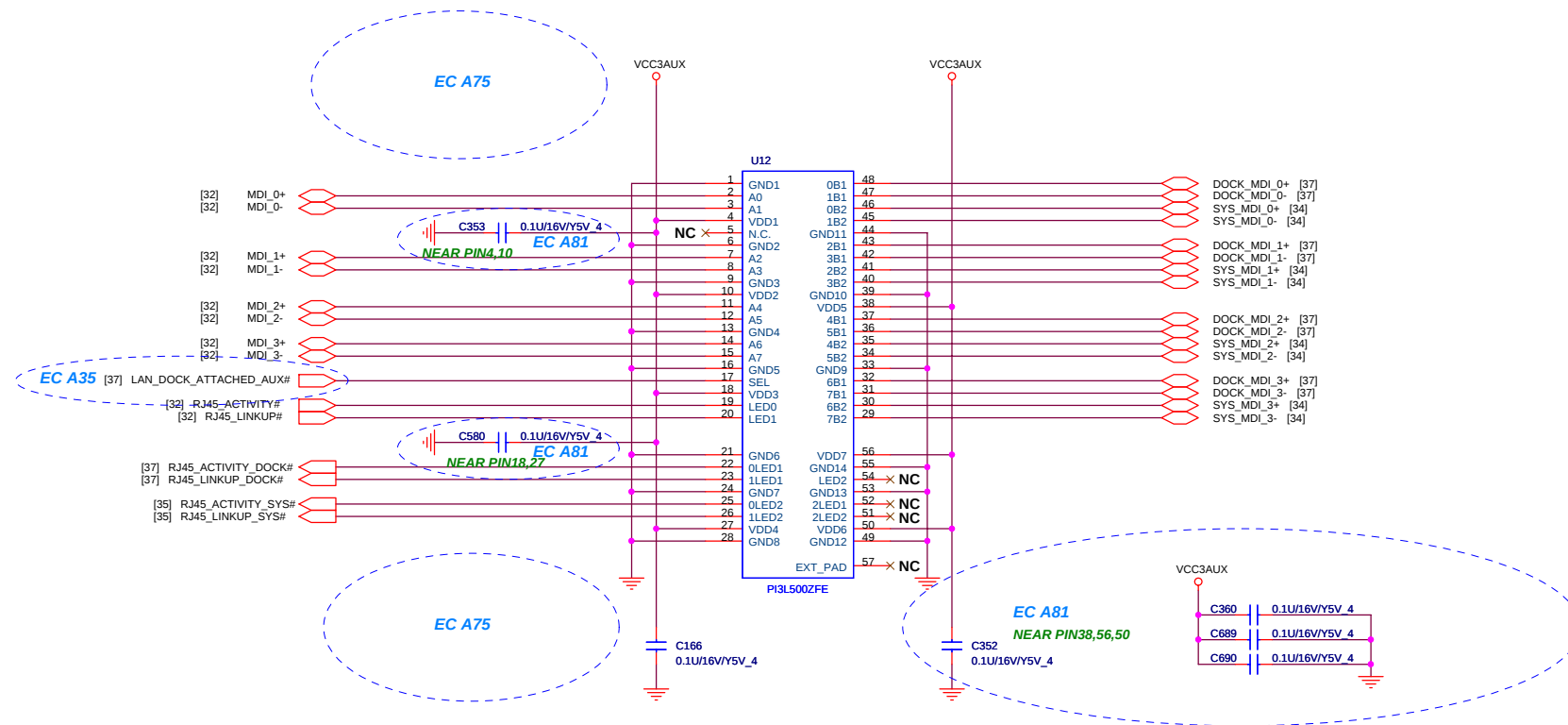


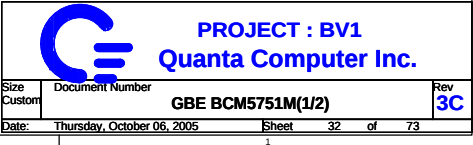
PROJECT : BV1
Quanta Computer Inc.



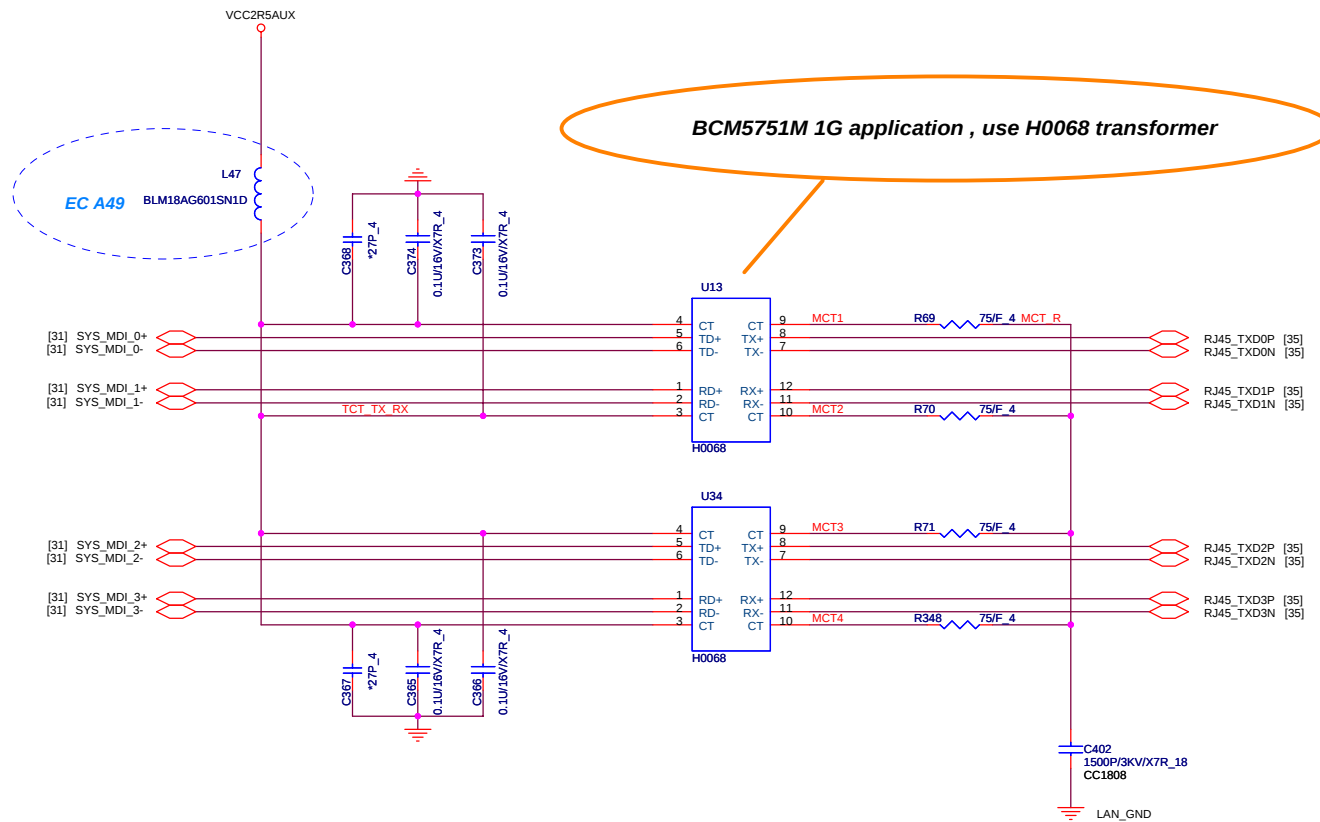






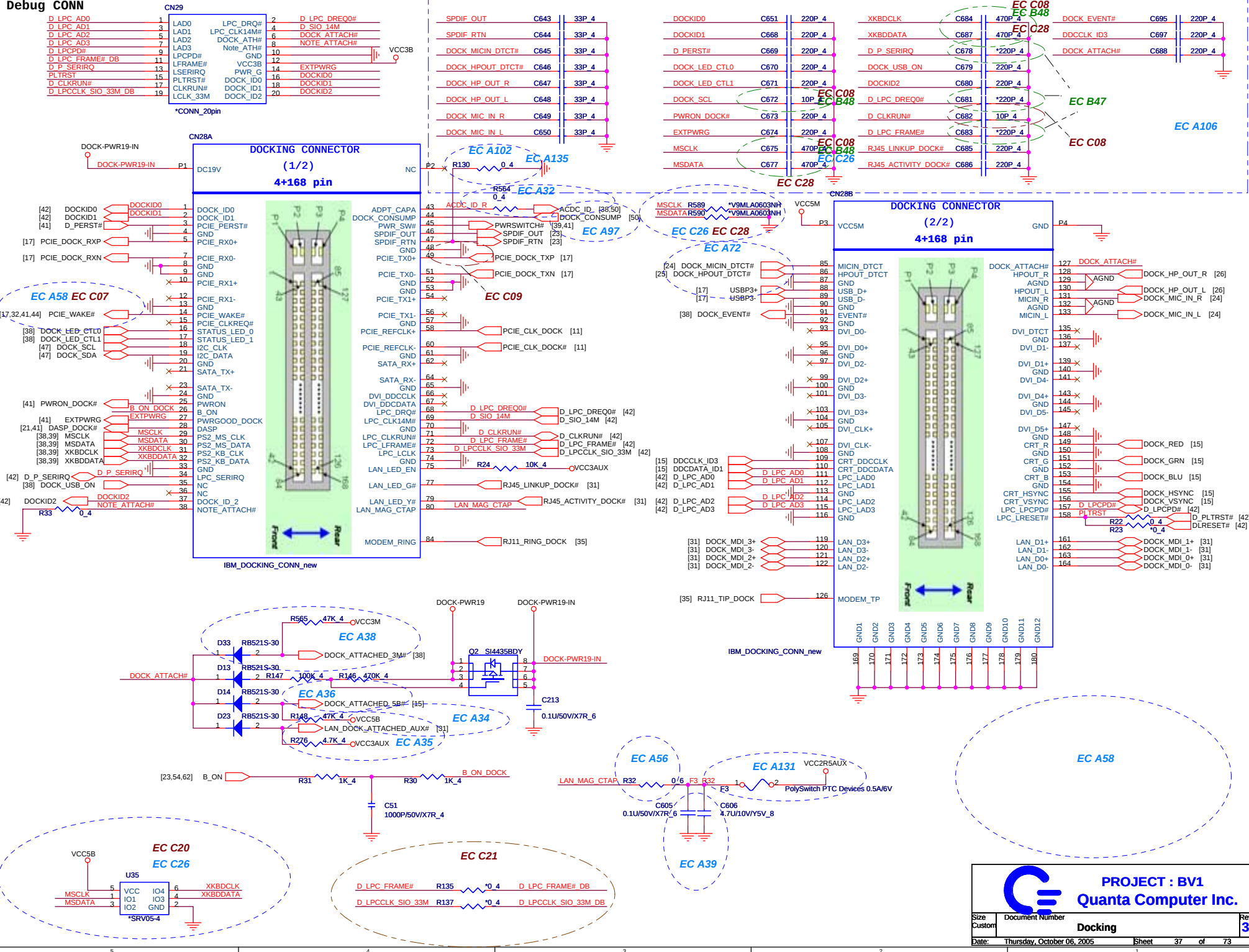


<BLANK PAGE>



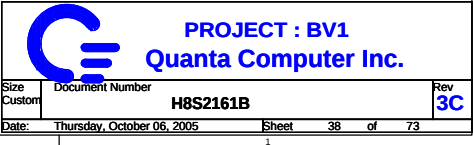
BLANK

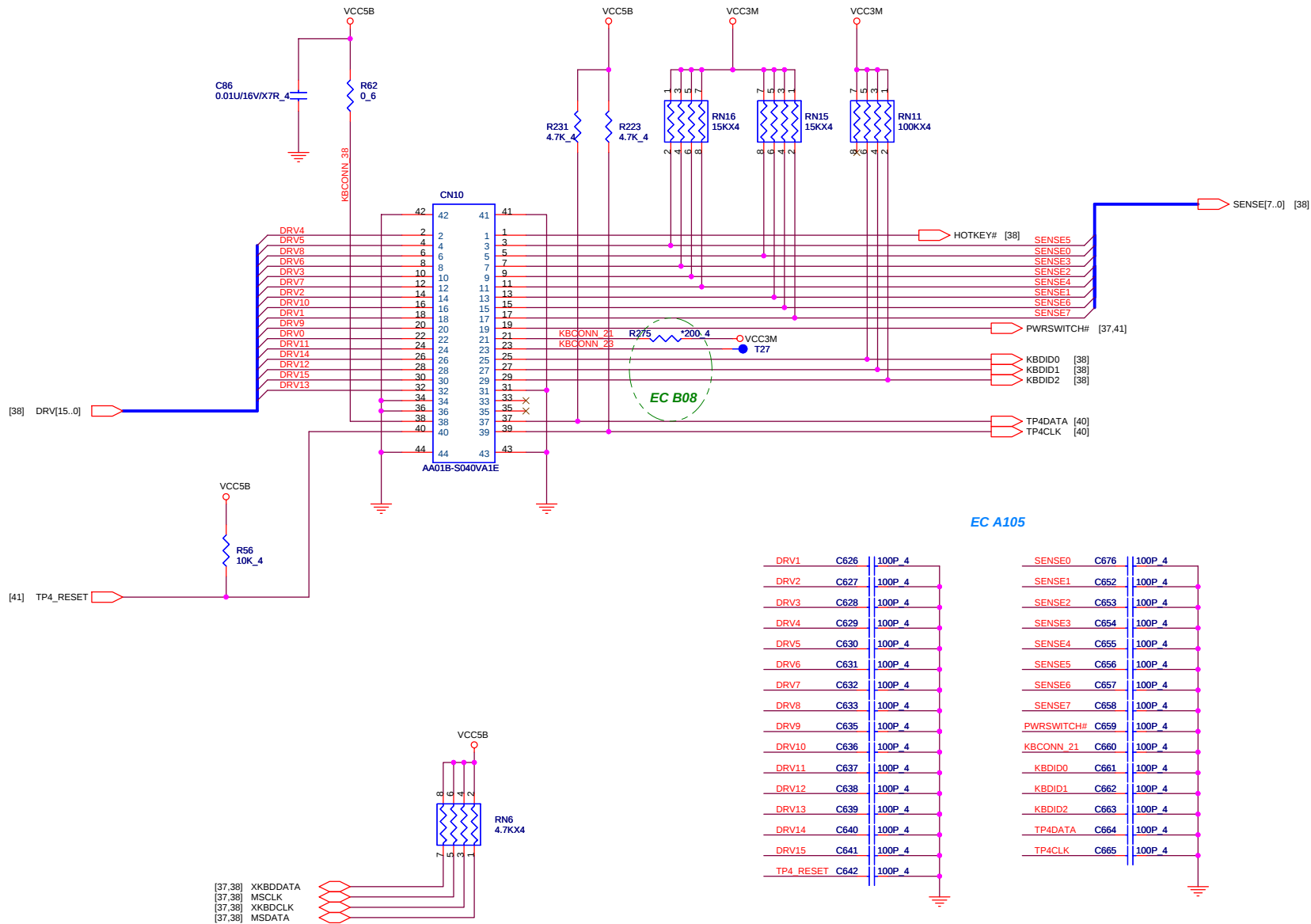
Debug CONN

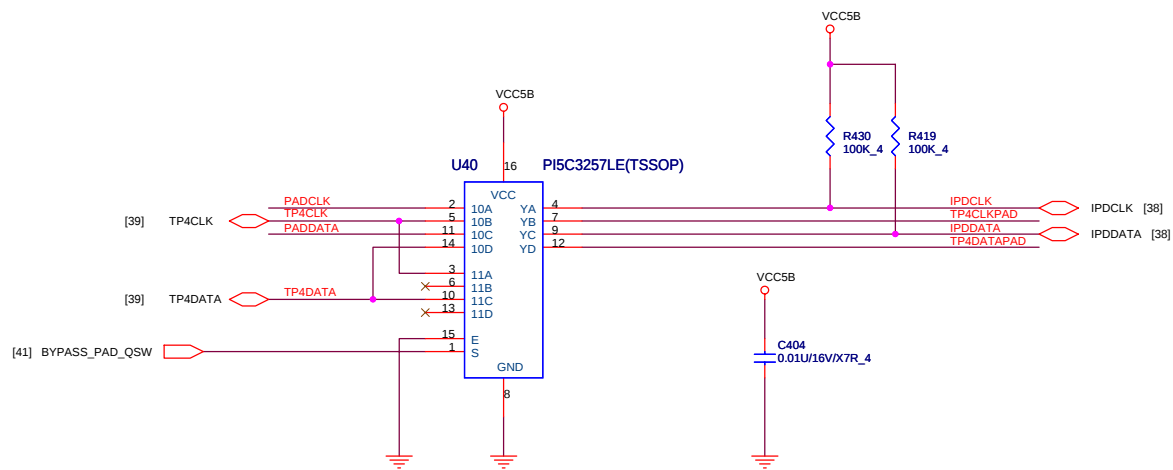
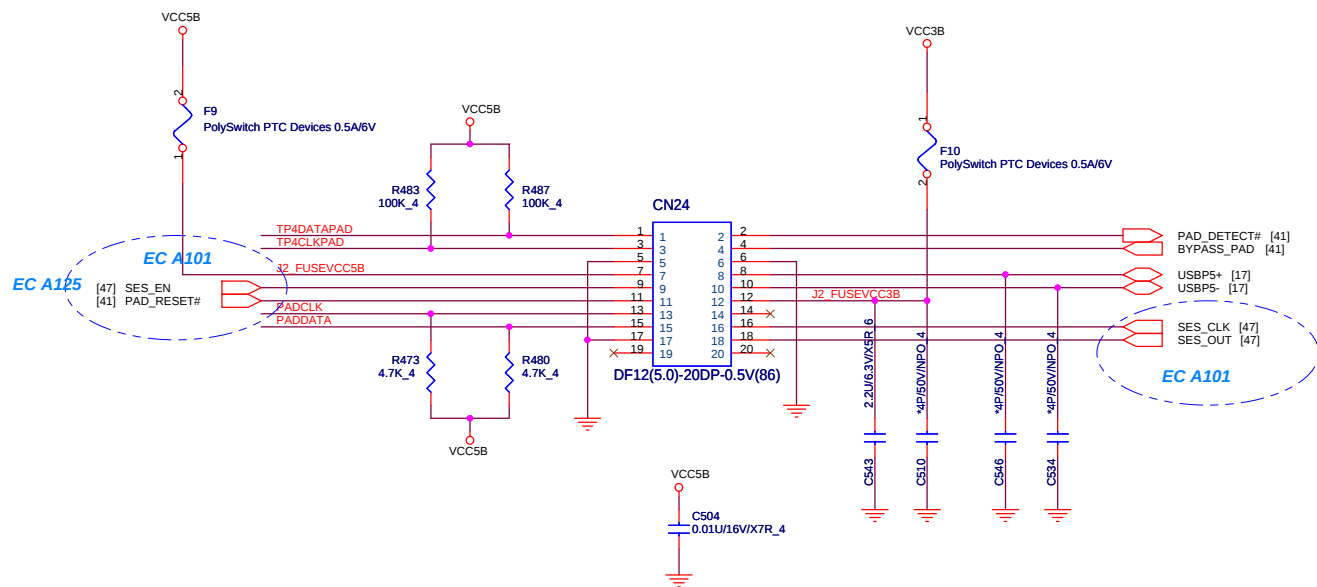


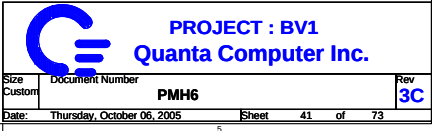
PROJECT : BV1
Quanta Computer Inc.

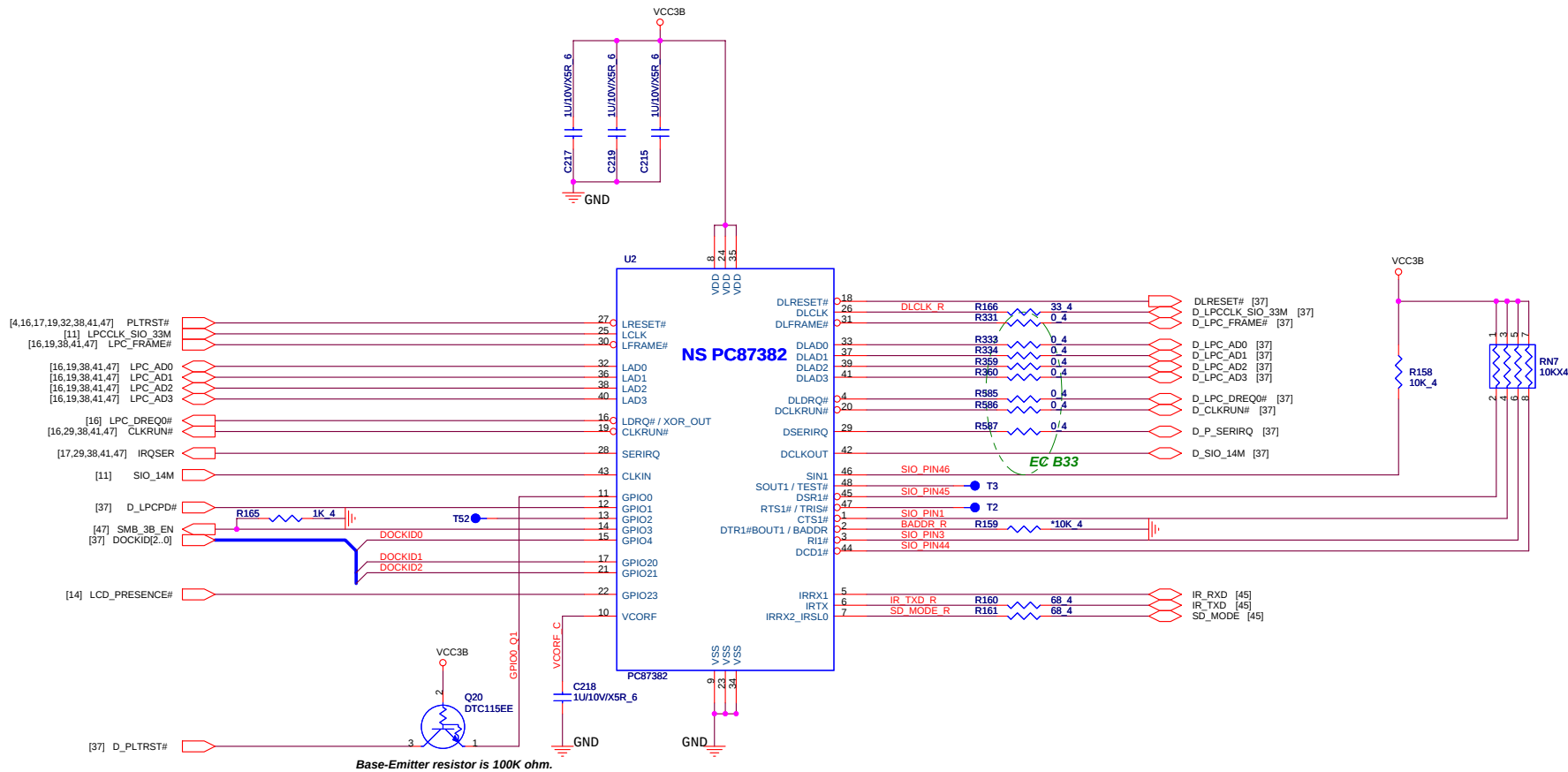
Size Custom	Document Number	Rev
	Docking	3C
Date:	Thursday, October 06, 2005	Sheet 37 of 73

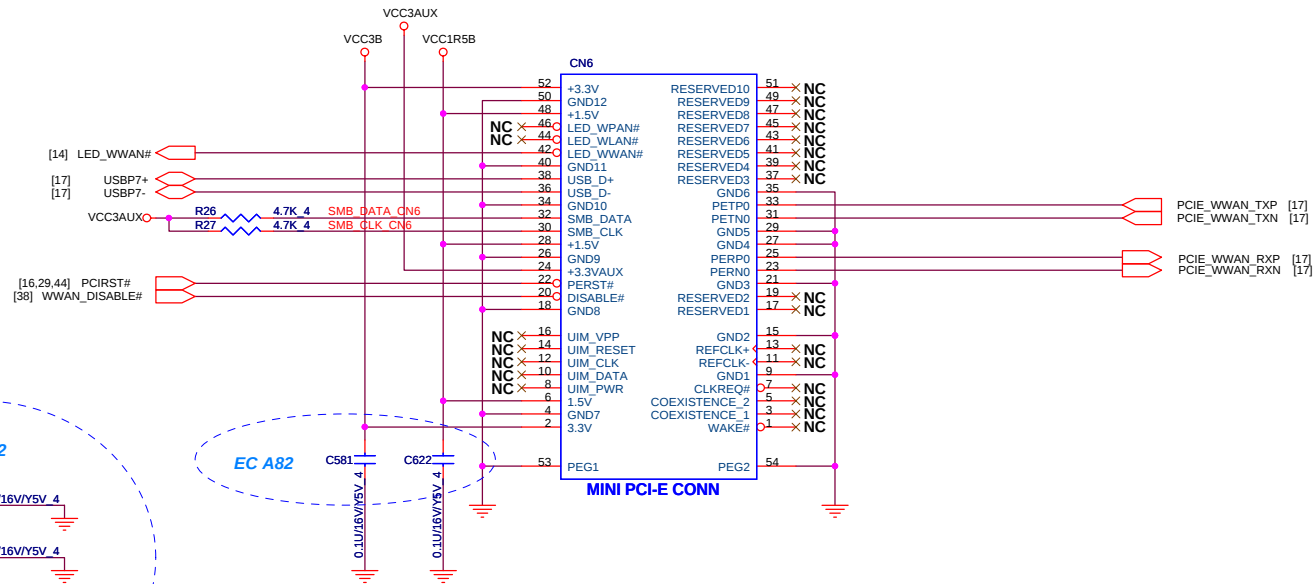




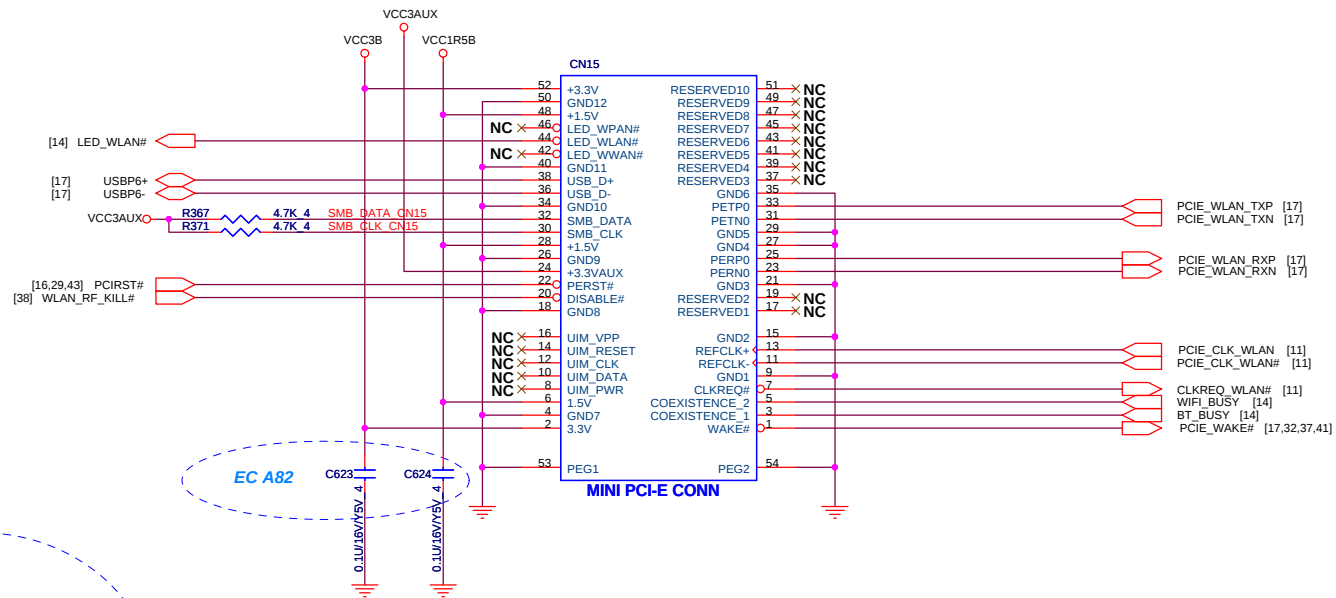




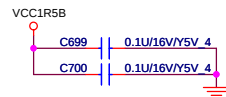




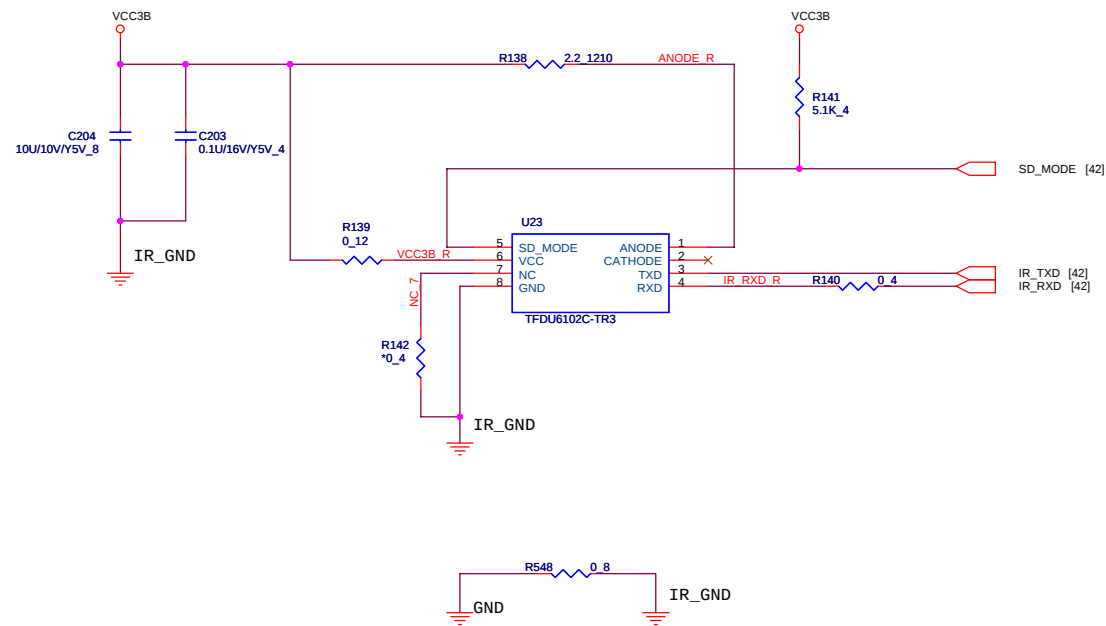
MINI PCIE CONN-2 FOR WLAN

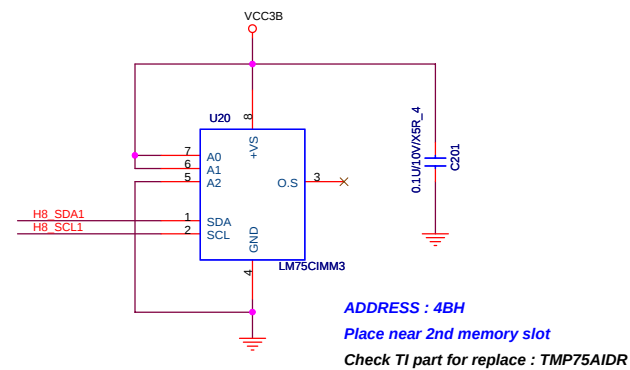
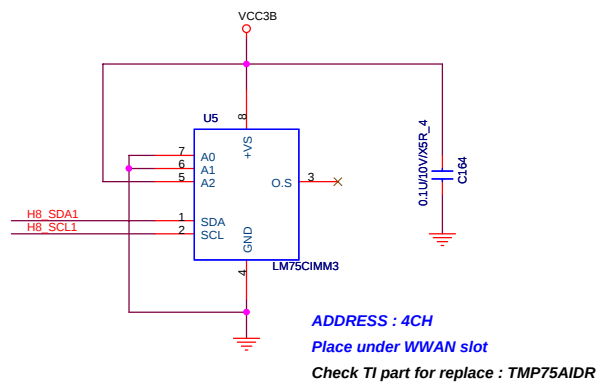
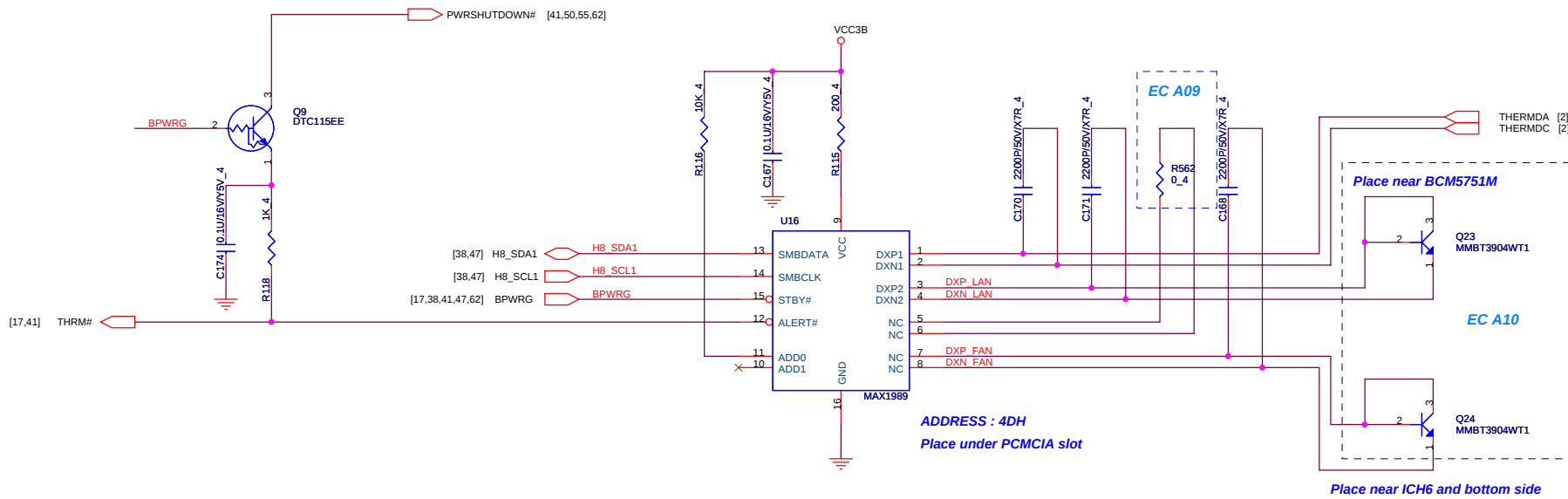


EC A82



EC B05



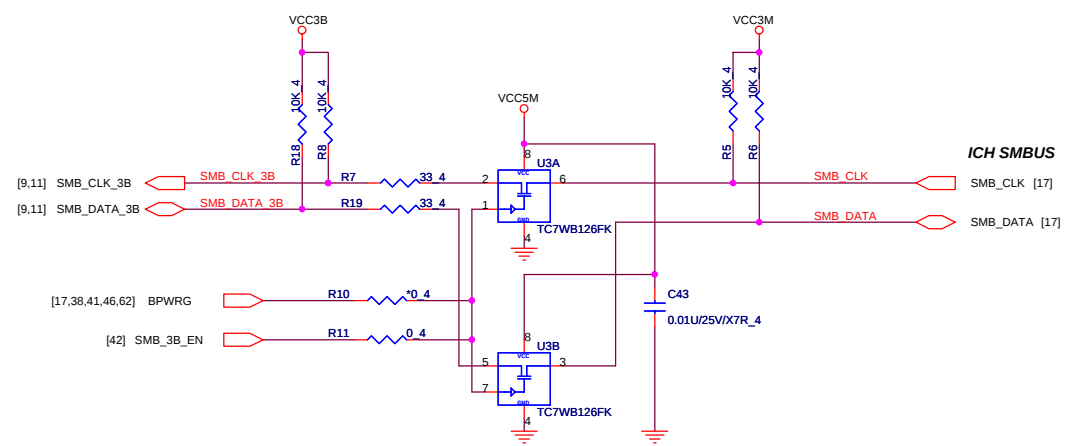
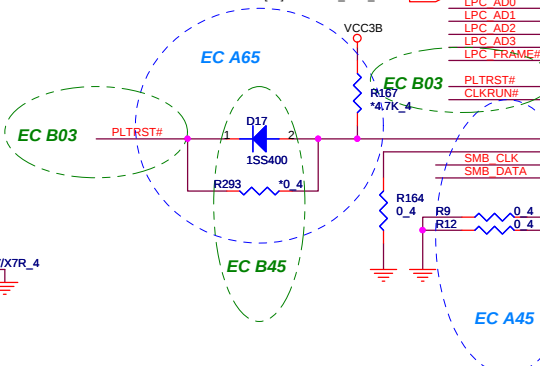
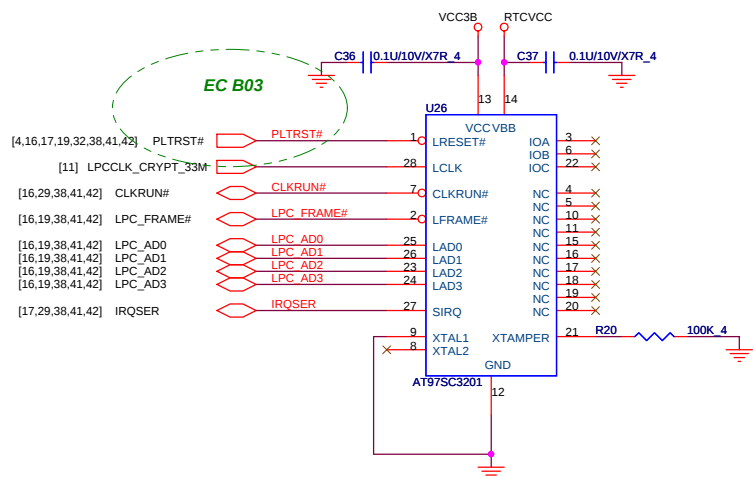
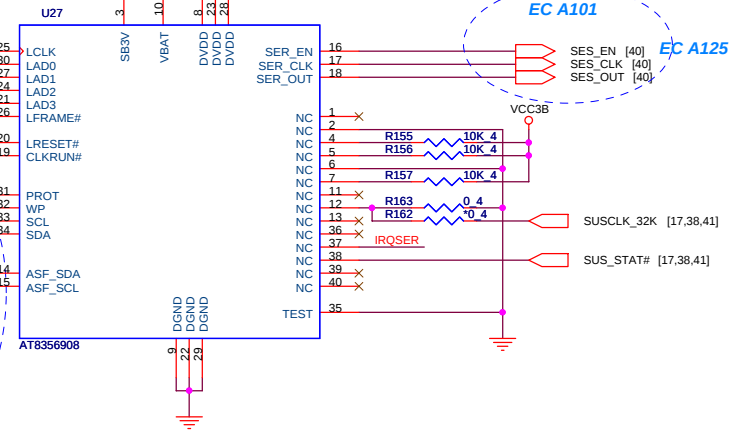
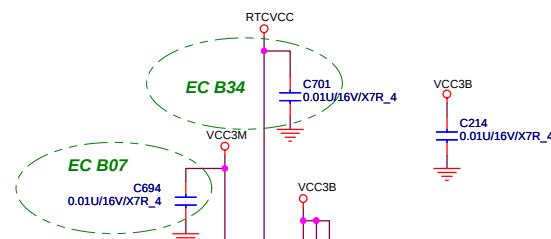
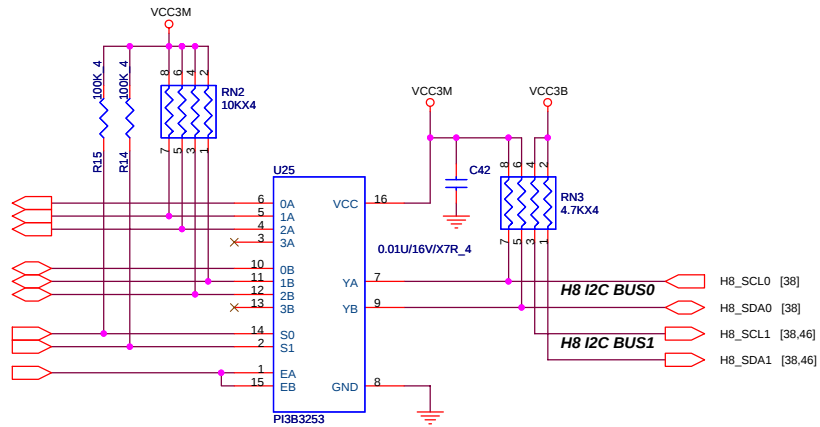


[37] DOCK_SCL
[52] I2C_CLK_BT0
[52] I2C_CLK_BT1

[37] DOCK_SDA
[52] I2C_DATA_BT0
[52] I2C_DATA_BT1

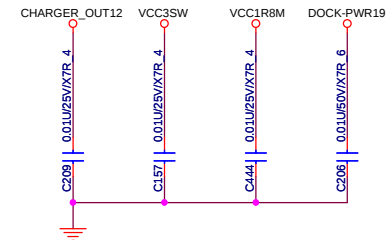
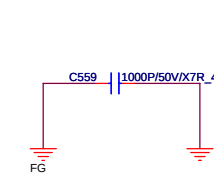
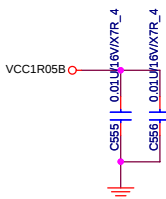
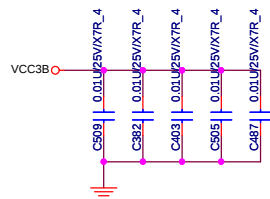
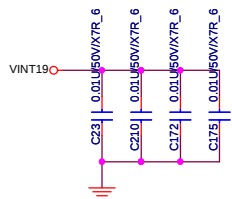
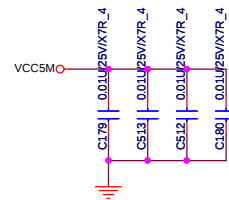
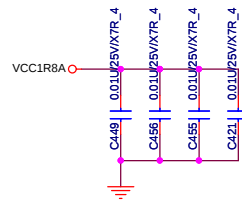
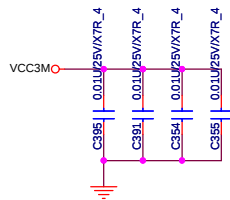
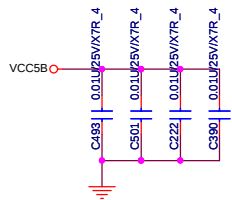
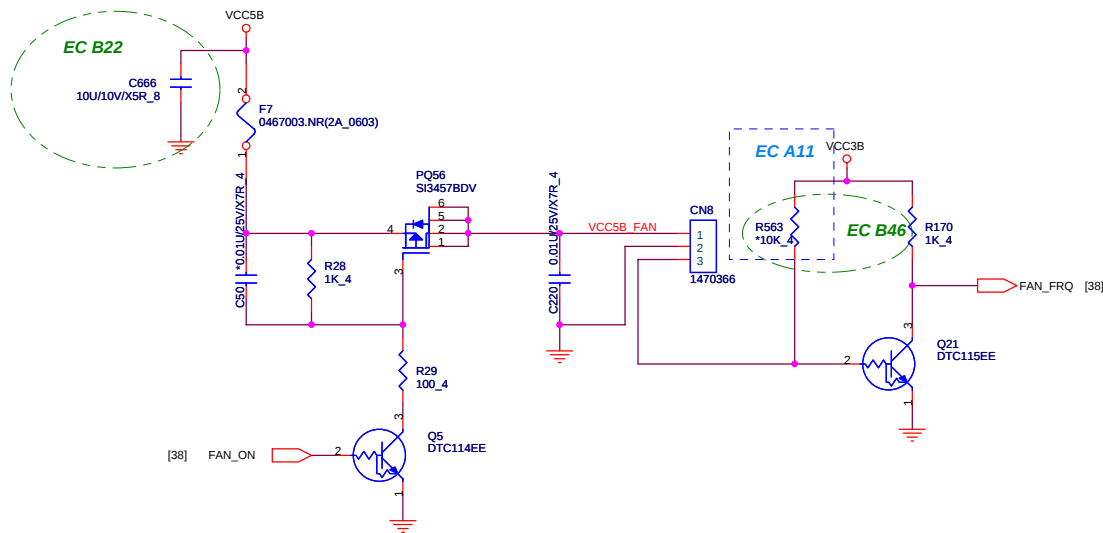
[38] I2C_SEL0
[38] I2C_SEL1

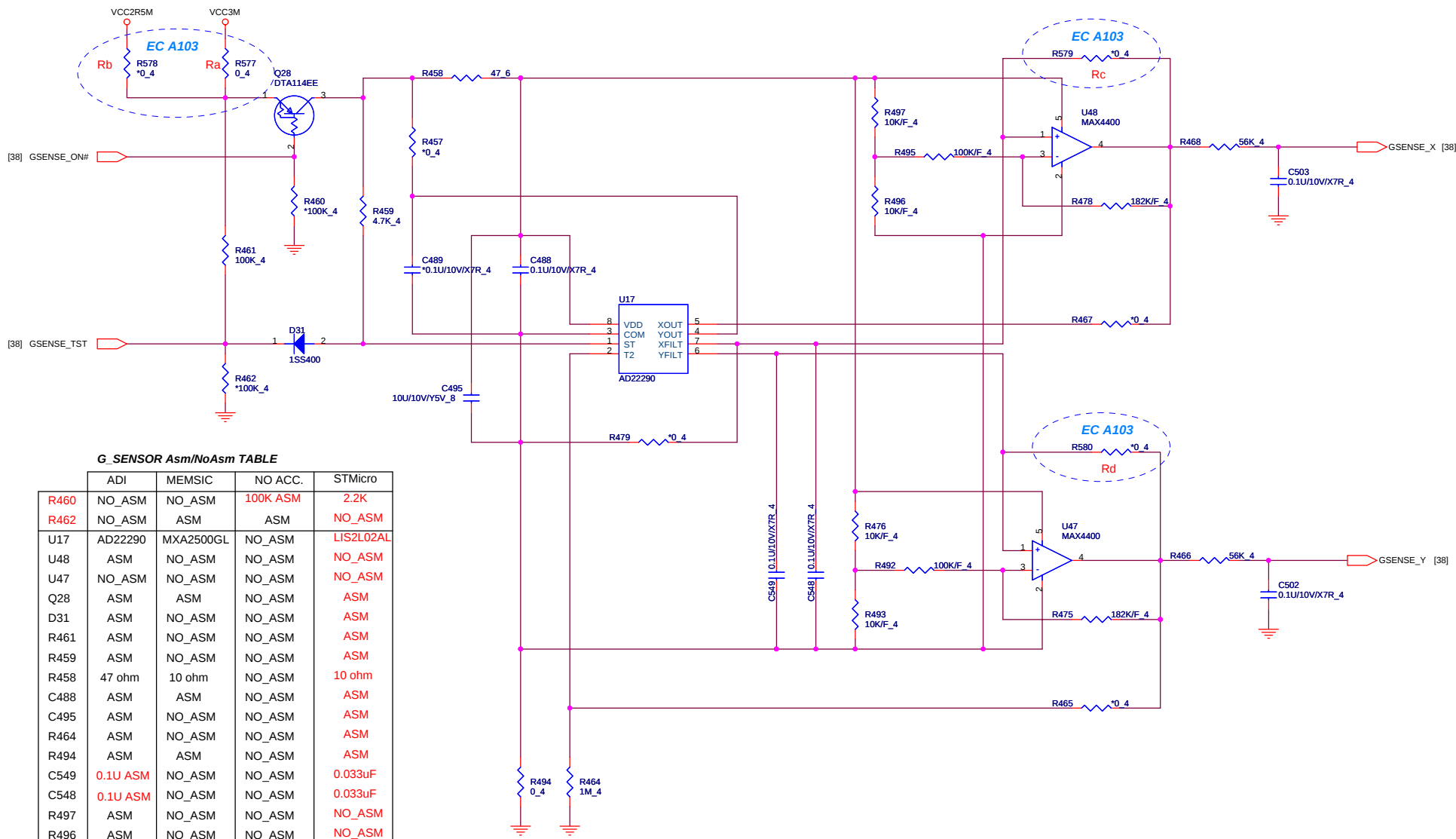
[38] I2C_ENABLE#



TCPA TABLE

	YES	NO
U26	ASM	NO_ASM
C36	ASM	NO_ASM
C37	ASM	NO_ASM
R544	ASM	NO_ASM
R545	NO_ASM	ASM

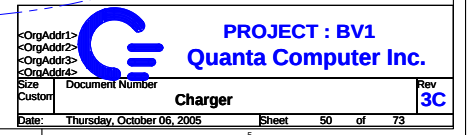


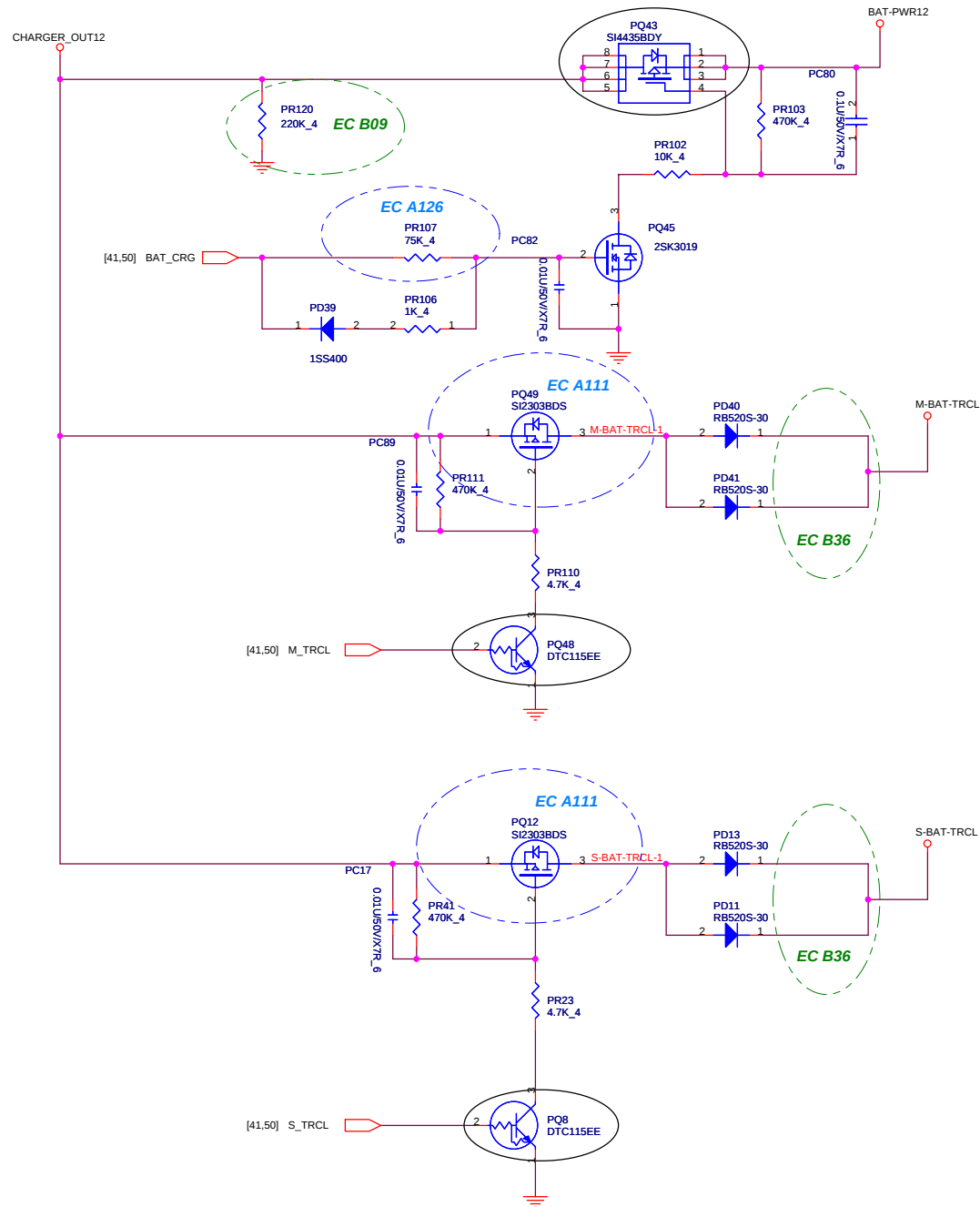


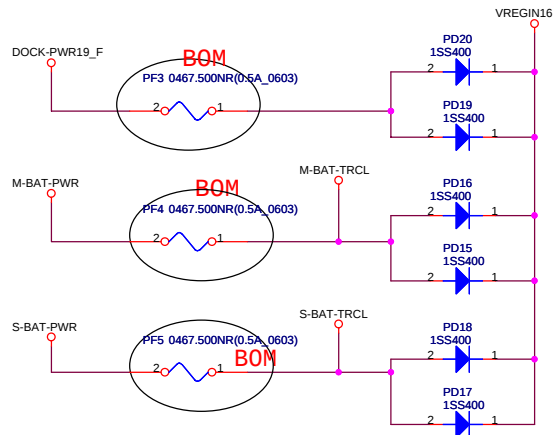
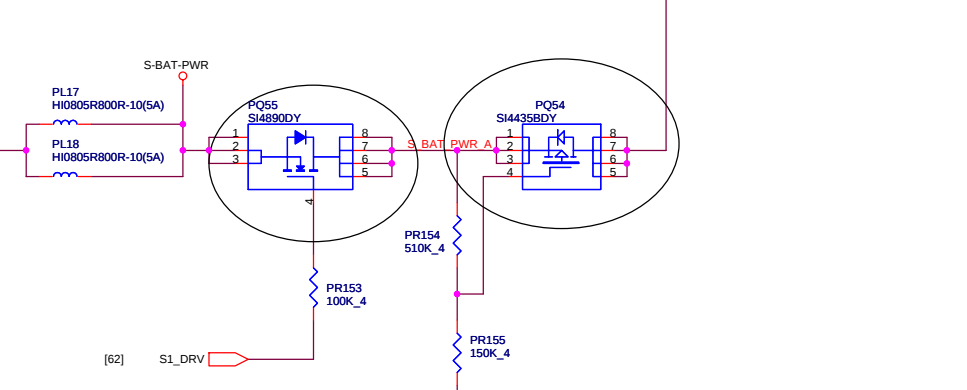
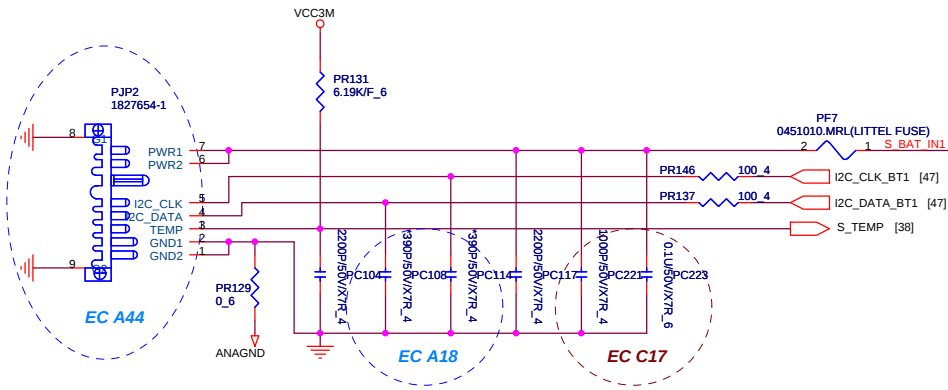
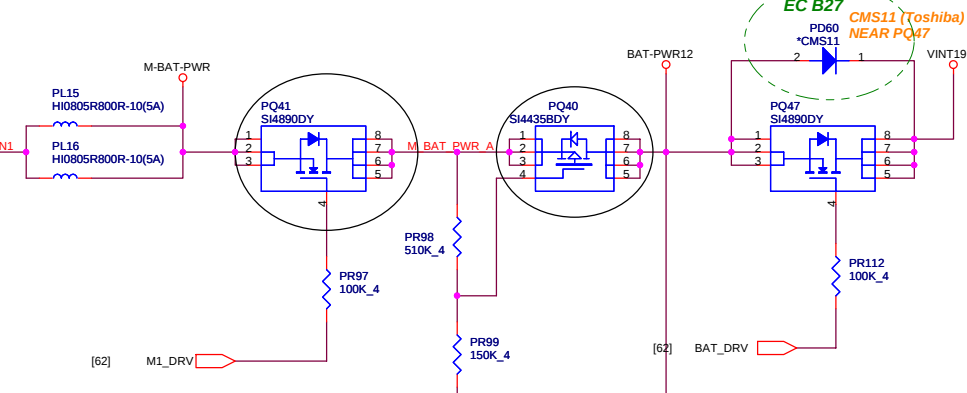
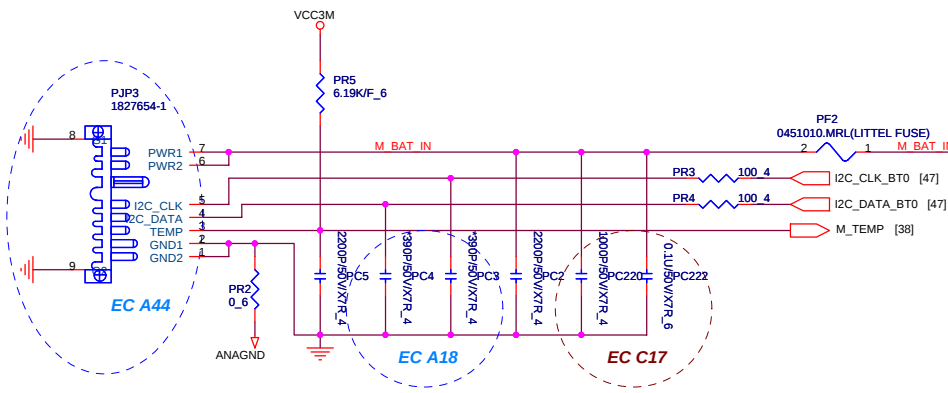
G_SENSOR Asm/NoAsm TABLE

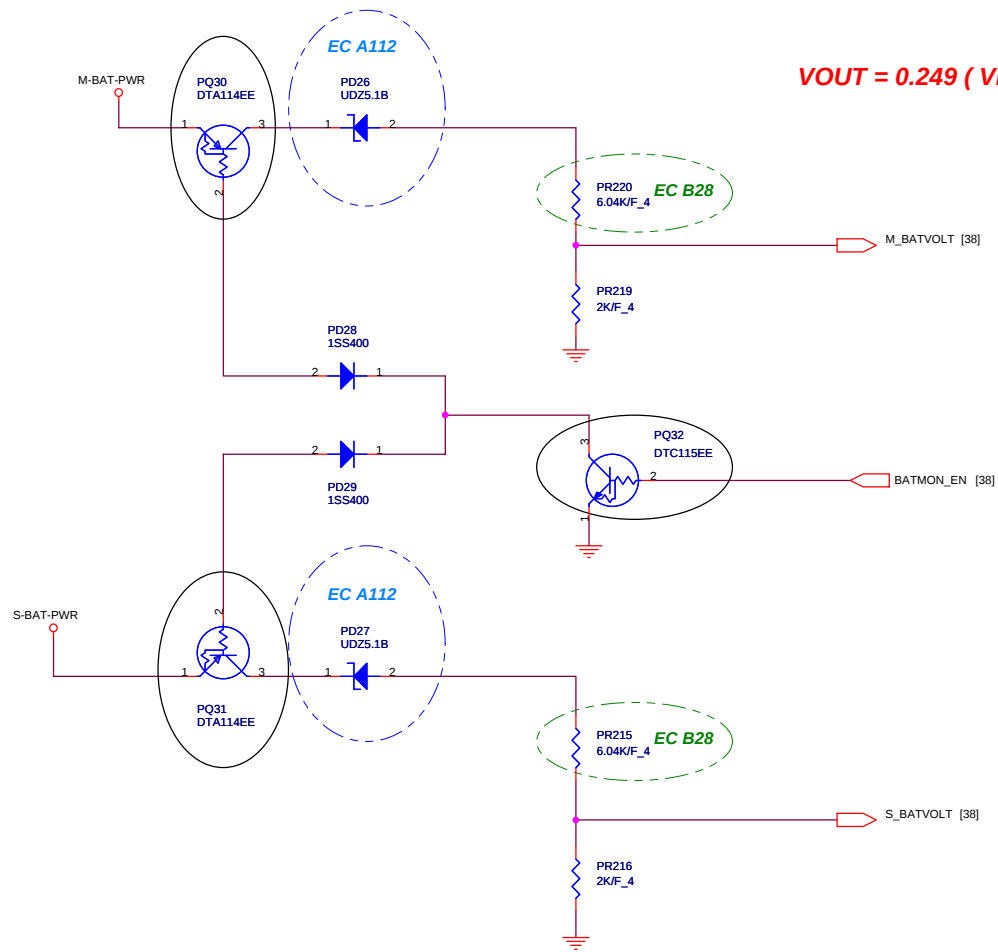
	ADI	MEMSIC	NO ACC.	STMicro
R460	NO_ASM	NO_ASM	100K ASM	2.2K
R462	NO_ASM	ASM	ASM	NO_ASM
U17	AD22290	MXA2500GL	NO_ASM	LIS2L02AL
U48	ASM	NO_ASM	NO_ASM	NO_ASM
U47	NO_ASM	NO_ASM	NO_ASM	NO_ASM
Q28	ASM	ASM	NO_ASM	ASM
D31	ASM	NO_ASM	NO_ASM	ASM
R461	ASM	NO_ASM	NO_ASM	ASM
R459	ASM	NO_ASM	NO_ASM	ASM
R458	47 ohm	10 ohm	NO_ASM	10 ohm
C488	ASM	ASM	NO_ASM	ASM
C495	ASM	NO_ASM	NO_ASM	ASM
R464	ASM	NO_ASM	NO_ASM	ASM
R494	ASM	ASM	NO_ASM	ASM
C549	0.1U ASM	NO_ASM	NO_ASM	0.033uF
C548	0.1U ASM	NO_ASM	NO_ASM	0.033uF
R497	ASM	NO_ASM	NO_ASM	NO_ASM
R496	ASM	NO_ASM	NO_ASM	NO_ASM
R495	ASM	NO_ASM	NO_ASM	NO_ASM
R478	ASM	NO_ASM	NO_ASM	NO_ASM
R476	ASM	NO_ASM	NO_ASM	NO_ASM
R493	ASM	NO_ASM	NO_ASM	NO_ASM
R492	ASM	NO_ASM	NO_ASM	NO_ASM
R475	ASM	NO_ASM	NO_ASM	NO_ASM
R468	56K	8.2K	NO_ASM	56K
C503	ASM	ASM	NO_ASM	ASM
R466	56K	8.2K	NO_ASM	56K
C502	ASM	ASM	NO_ASM	ASM
R457	NO_ASM	ASM	NO_ASM	NO_ASM
C489	NO_ASM	ASM	NO_ASM	NO_ASM
R479	NO_ASM	ASM	NO_ASM	NO_ASM
R467	NO_ASM	ASM	NO_ASM	NO_ASM
R465	NO_ASM	ASM	NO_ASM	NO_ASM

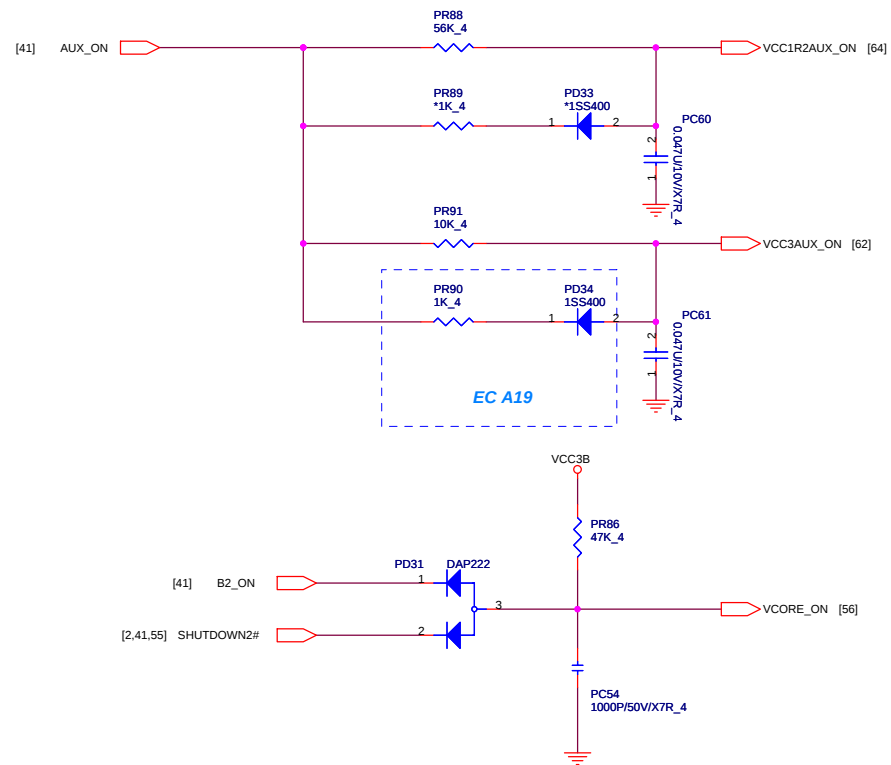
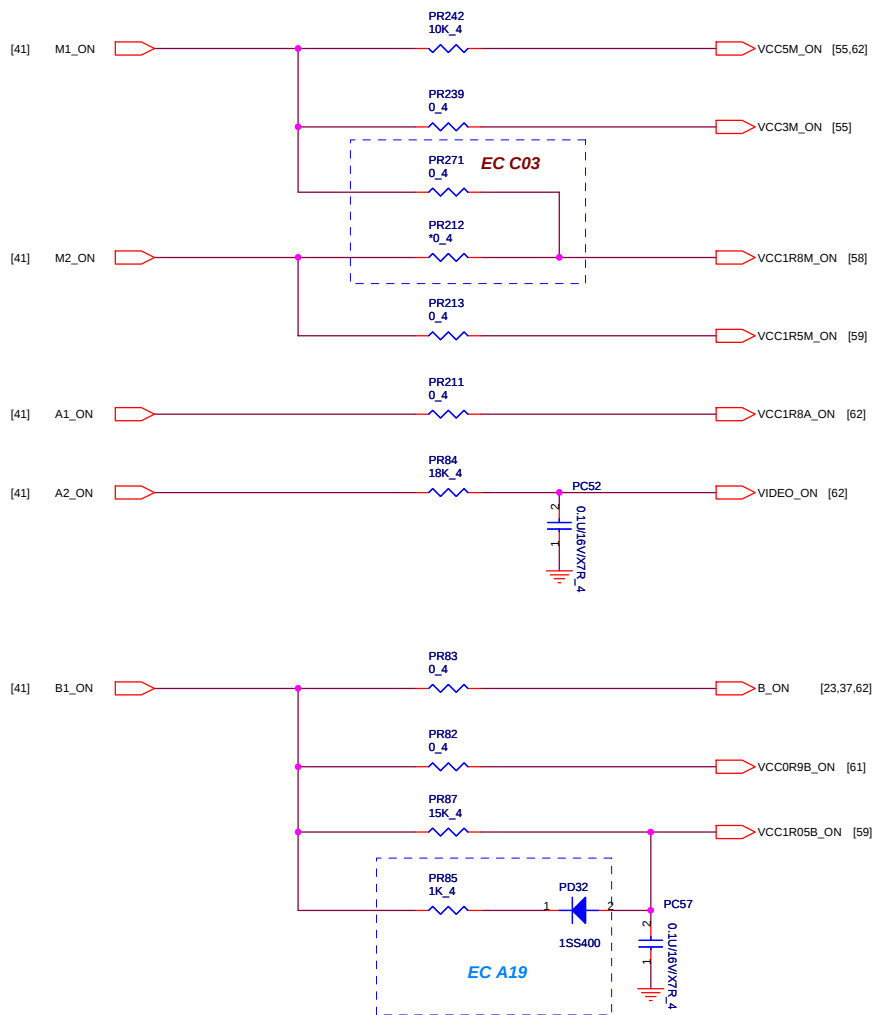
	ADI	MEMSIC	STMicro	NO ACC.
Ra	ASM	ASM	ASM	NO_ASM
Rb	NO_ASM	NO_ASM	NO_ASM	NO_ASM
Rc	NO_ASM	NO_ASM	ASM	NO_ASM
Rd	NO_ASM	NO_ASM	ASM	NO_ASM

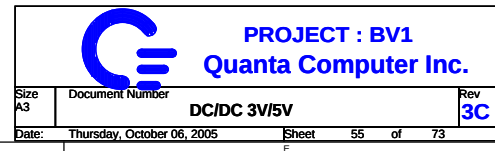








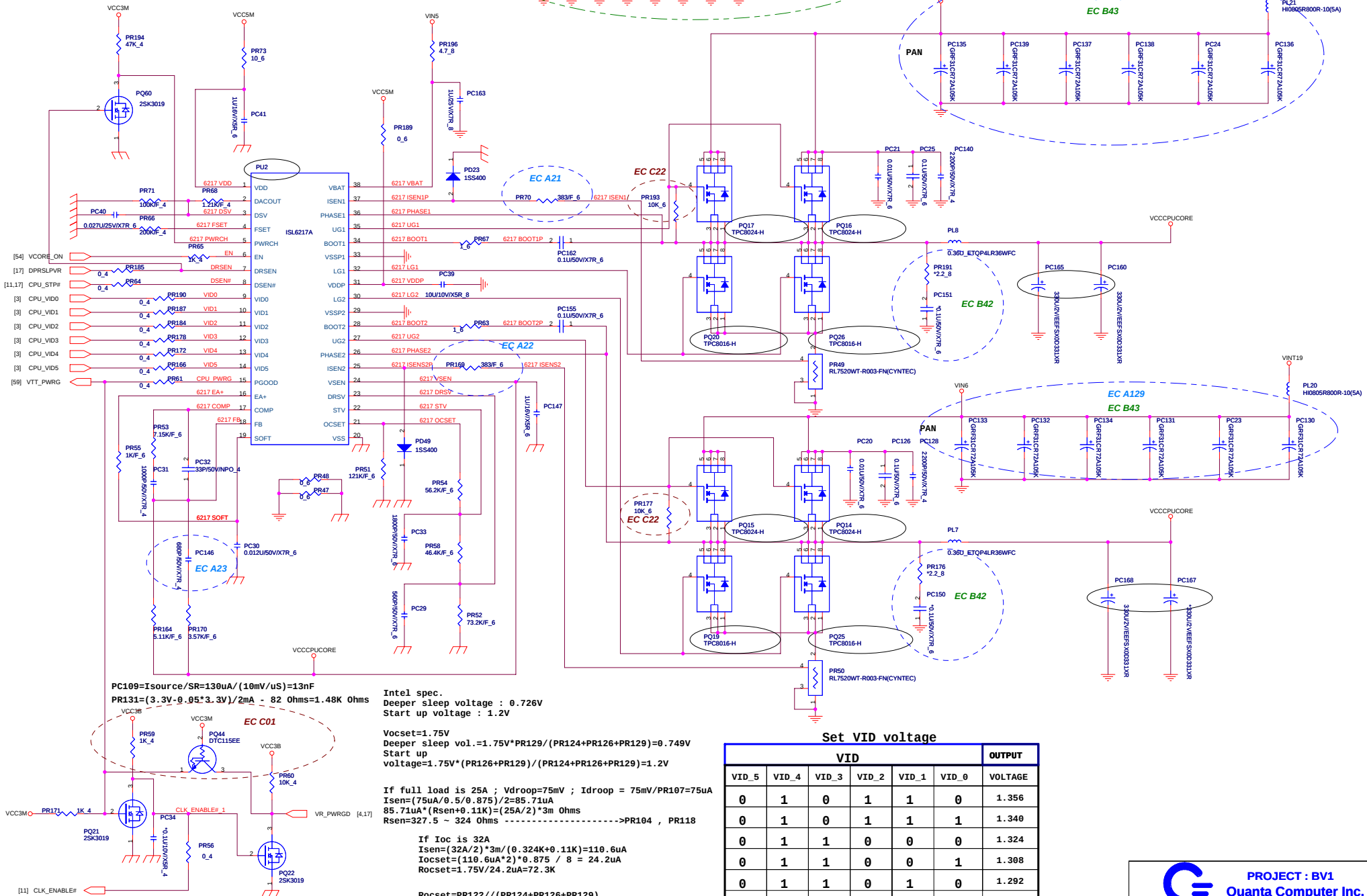


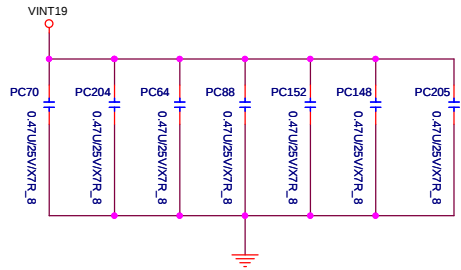


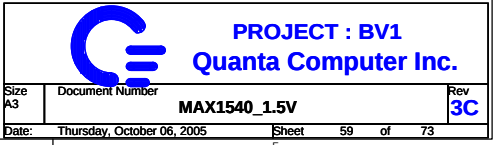
Intel spec. : Vos% = 1.2%
 $PR103/(PR102+PR103)=1.21K/(100K+1.21K) \sim 1.2\%$
 $PR105 : 200K \text{ Ohms} \rightarrow Fsw \sim 300K \text{ HZ}$

PWRCH : "H" -----> Two phase
 PWRCH : "L" -----> Single phase

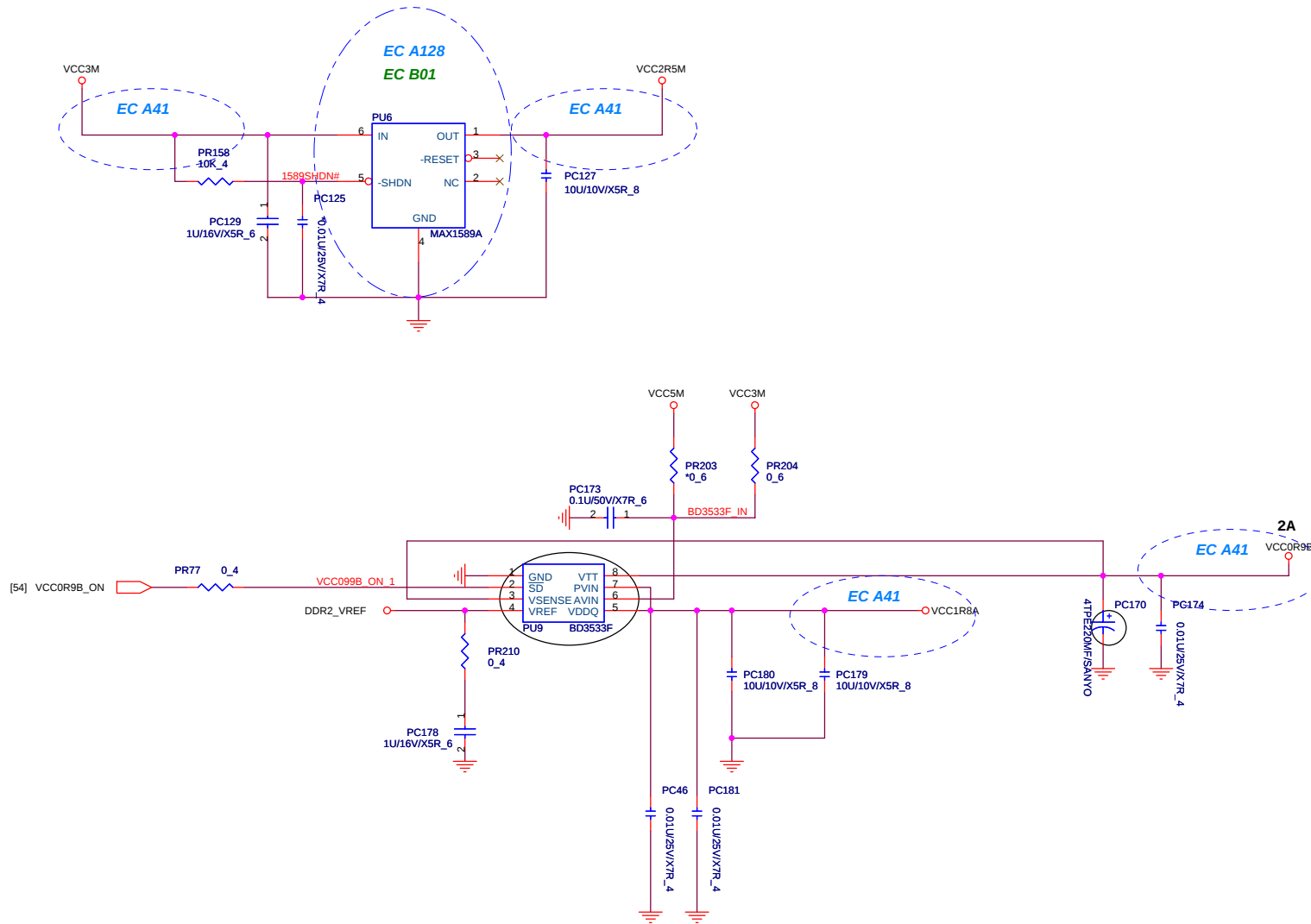
	DSEN#	DRSEN
Active	H	L
Deep	L	L
Deeper	L	H

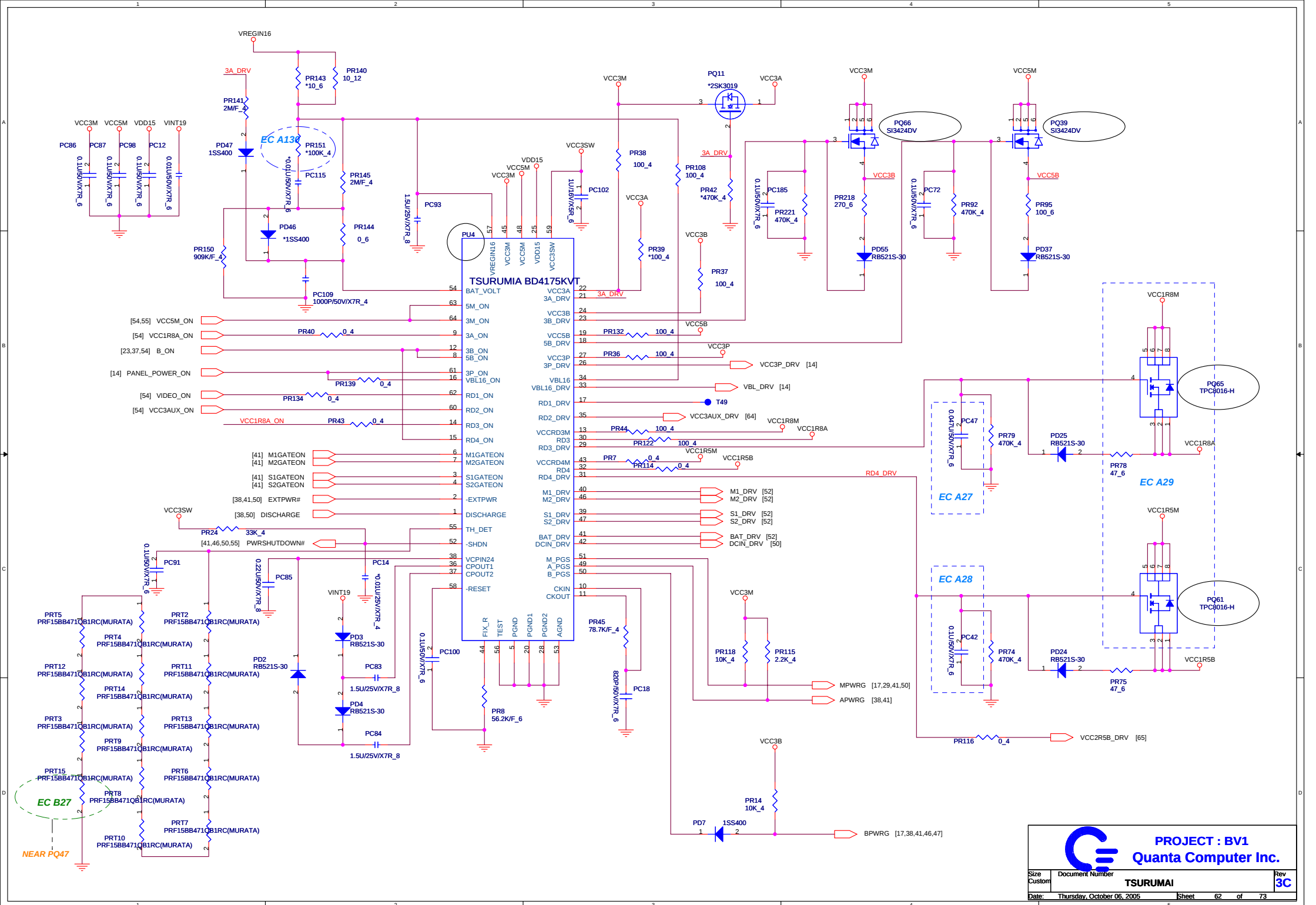






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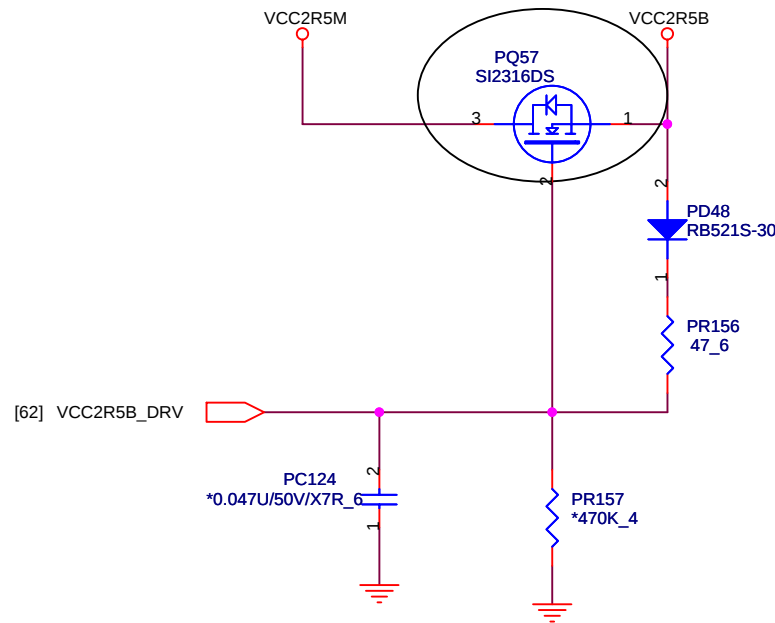
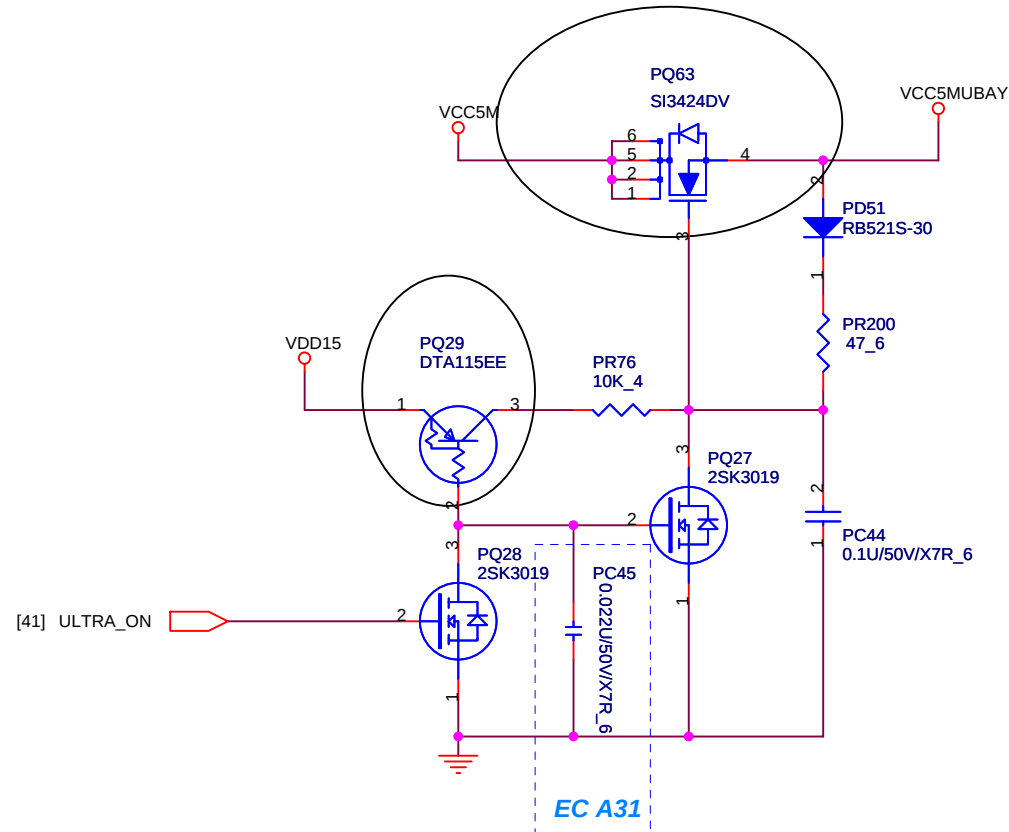


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[50]

ISYS

PR119
100K_4PR117
100K_4PU3B
LM392PR123
1K/F_4PR26
200K/F_4PC16
1U/16V/X5R_6PR27
10K/F_4PR10
10K_4

DCIN_AMP_19

PC90
0.1U/50V/X7R_6PR11
301K/F_4PU3A
LM392

VCC5M

PR17
51.1K/F_4

VCC3M

PR9
100K_4PQ3
2SK3019

LPMODE# [38]

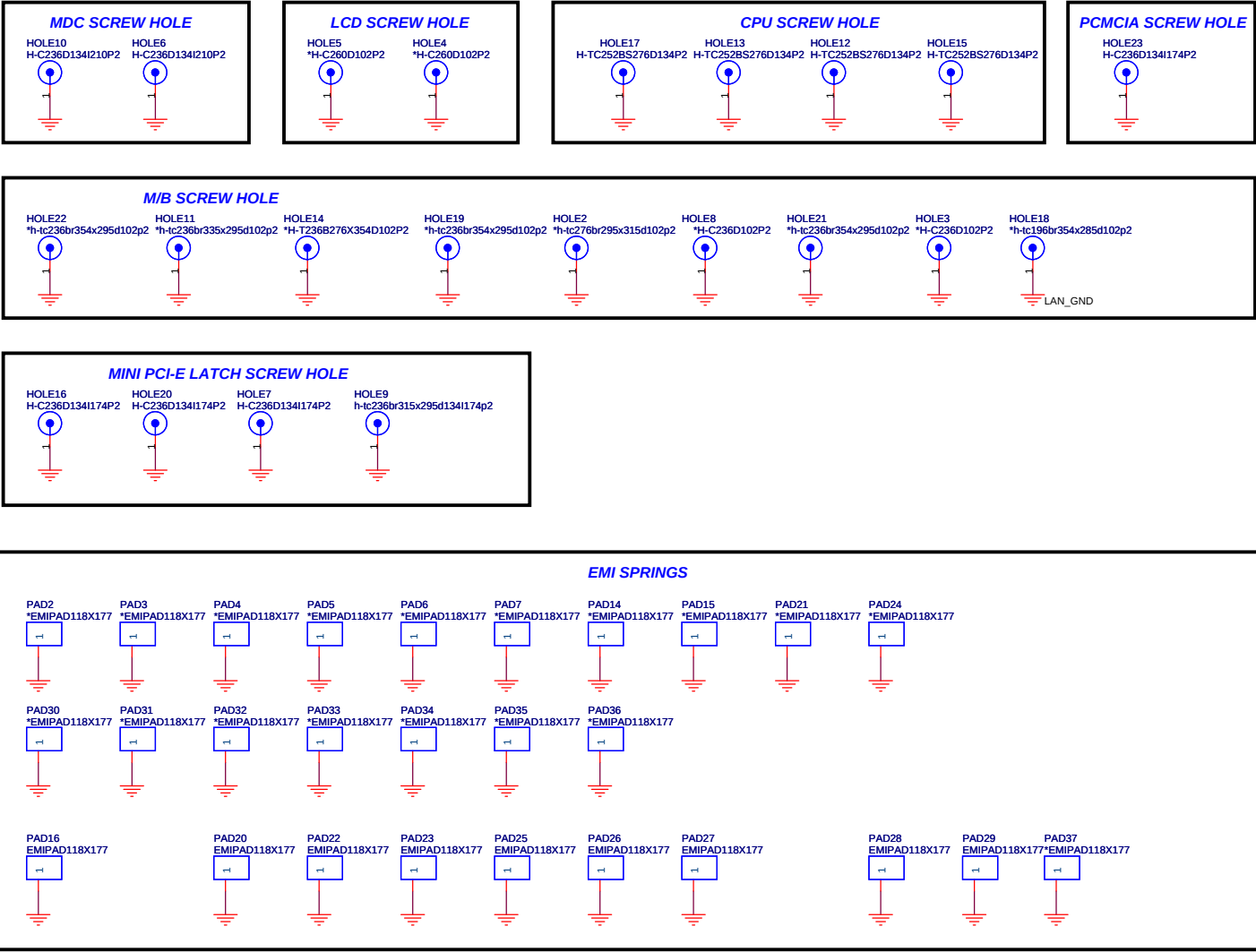
VCC3M

PR15
1K_4PQ7
LMV431PR16
10K/F_4PR25
10K/F_4PC13
0.1U/10V/X5R_4

PROJECT : BV1
Quanta Computer Inc.

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MAX POWER CTRL		
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SCREW HOLE



Revision History

Revision	Date	Phase	Change List	Release Schematic Date	Release Gerber File Date
1A	12/28 2004	DV	Initial release	12/28 2004	12/28 2004
2A	02/28 2005	SIV	See page 69 Record A-1 (EC A01 - EC A52) See page 70 Record A-2 (EC A53 - EC A106) See page 71 Record A-3 (EC A107 - EC A130)	03/07 2005	03/07 2005
3A	04/19 2005	SIT	See page 72 Record B-1 (EC B01 - EC B48)	05/09 2005	05/11 2005
3B	06/28 2005	SVT1	See page 73 Record C-1 (EC C01 - EC C23)		
3C	08/02 2005	SVT2	See page 73 Record C-1 (EC C24 - EC C27)	02/08 2005	03/08 2005

Schematic Value Explanation Description :

RESISTOR

Value	F	4	6	8	12	1210	*	Description
*1K/F_4	1%	0402 (1005)					NOASM	1K ohm 1% SMD 0402 package and NOASM
1K_6	5%		0603 (1608)				ASM	1K ohm 5% SMD 0603 package and ASM
1K_8	5%			0805 (2125)			ASM	1K ohm 5% SMD 0805 package and ASM
1K_12	5%				1206 (3216)		ASM	1K ohm 5% SMD 1206 package and ASM
1K_1210	5%					1210 (3225)	ASM	1K ohm 5% SMD 1210 package and ASM

CAPACITOR

Value	Voltage	Material	6				*	Description
*0.1U/10V/X5R_4	10V	X5R	0402 (1005)				NOASM	0.1UF 10V X5R SMD 0402 package NOASM
1U/25V/X7R_6	25V	X7R	0603 (1608)				ASM	0.1UF 25V X7R SMD 0603 package ASM

BV1 / M-Note Schematic EC Tracking Record A (for DV --> SIV) Dec. 28, 2004

EC #/Page/Description/Part Affected/CMVC #

EC A01/04/ Change R268,R267 from NO ASM to ASM 40.2 ohm 1% because the SMVREF[1:0] voltage will droop below specification 200uA (Intel plan fix in C1 stepping).

EC A02/11/ Add RP11 33ohm 4P2R resistor to ASM , the reason is for MINI-PCIE clock use.

EC A03/16/ Change Y6 32.768Khz crystal pin definition because it's different with layout footprint.

EC A04/19/ Change R21 from 180k to 20k ohm and C45 from 0.1uF to 1uF. This recommendation change is being made in order to reduce the likelihood of glitching on RTCRST#.

EC A05/28/ Change SD connector type to 2WX11101-1X-7F.

EC A06/29/ Change R404,R406 from ASM to NO ASM. Because pull high resistor was double used on Page 29 and Page 62,41.

EC A07/32/ Change Y5 25Mhz crystal pin definition because it's different with layout footprint.

EC A08/38/ Change R84,R95,R94,R454,R85 to NO ASM. Because these resistors was double side pull high.

EC A09/46/ Change U16 MAX1989 thermal sensor pin5,6 from capacitor to 0 ohm resistor between the two pins. Because if DXP,DXN not use must short of them.

EC A10/46/ Change Q23,Q24 transistor pin definition because it's different with layout footprint.

EC A11/48/ Add a 10k ohm pull high resistor to VCC3B for Q21 base driver and ASM.

EC A12/50/ Change PC77 from 0.1uF/50V/X7R to 0.01uF/50V/X7R.

EC A13/50/ Make "8724_LDO_3D3V" by using pin_LDO and change PU5 pin12 REFIN power plane from VCC3M to 8724_LDO_3D3V.

EC A14/50/ Change PC15 from NO ASM to ASM 1000P/50V/X7R.

EC A15/50/ Change PU5 pin17 CELLS pull high power plane from VCC3M to 8724_LDO_3D3V. (SIV-IBM-48)

EC A16/50/ Change PQ2 MOSFET from SI4804BDY to SI4978BDY.

EC A17/50/ Add a 0 ohm resistor between the 8724_AGND and digital GND. Because PU5 MAX8724ETI ground was floating.

EC A18/52/ Change PC3,PC4,PC108,PC114 from ASM to NO ASM. The reason is battery smbus signal rising time is out of spec. Because the smbus clock frequency is reduced to 1/2 so the total communication time is twice. Also, Sanyo battery has a PEC error problem. CMVC# 65982 (SIV-IBM-12)

EC A19/54/ Change PR85,PD32 from NO ASM to ASM.

EC A20/55/ Change PL10 pin definition because it's different with layout footprint.

EC A21/56/ Change PR70 from 324 ohm 1% to 383 ohm 1%.

EC A22/56/ Change PR169 from 324 ohm 1% to 383 ohm 1%.

EC A23/56/ Change PC146 from 330pF/50V/X7R to 680pF/50V/X7R.

EC A24/58/ Add a 0.22uF/10V/X7R.

EC A25/59/ Add a 0.22uF/10V/X7R.

EC A26/59/ Add a 0.22uF/10V/X7R.

EC A27/62/ Change PC47 derating from 0.047uF/16V/X7R to 0.047uF/50V/X7R.

EC A28/62/ Change PC42 from 0.047uF/16V/X7R to 0.1uF/50V/X7R. Because VCC3B sequence must be early than VCC2R5B 0.7V DC level.

EC A29/62/ Change PQ65,PQ61 MOSFET from SI4890DY to TPC 8016-H MOSFET.

EC A30/64/ Change PC169 derating from 0.047uF/16V/X7R to 0.047uF/50V/X7R.

EC A31/65/ Change PC45 derating from 0.022uF/16V/X7R to 0.022uF/25V/X7R.

EC A32/37/ Add ACDC_ID signal from docking. CMVC# 65894 (SIV-IBM-01)

EC A33/38/ Change USB_ON and DOCK_USB_ON signal from pull high to pull down. (SIV-IBM-02)

EC A34/37/ Change pull high signal for DOCK_ATTACHED# from VCC3B to VCC5B. (SIV-IBM-05)

EC A35/31,37/ Change signal name to clarify from LAN_DOCK_ATTACHED# to LAN_DOCK_ATTACHED_AUX#. (SIV-IBM-06)

EC A36/15,37/ Change signal name to clarify from DOCK_ATTACHED# to DOCK_ATTACHED_5B#. (SIV-IBM-06)

EC A37/15/ Change VCCCRT power source. (SIV-IBM-07)

EC A38/37/ Add docking attach signal with pull-up to VCC3M to H8. (SIV-IBM-08)

EC A39/37/ Add 2 caps 0.1U and 4.7U at LAN_MAG_CTAP near docking connector. (SIV-IBM-09)

EC A40/17/ Change PlanarID [3..0] from 0000 to 0001. (SIV-IBM-11)

EC A41/55,58,59,61,64/ Delete solder jumpers of power pads. (SIV-IBM-13)

EC A42/38/ Delete dock_attach# pull high resistor 100K R96 and charge_current0 pull high R454.

EC A43/38/ Change signal name from "DOCK_ATTACH#" to "DOCK_ATTACHED_3M#".

EC A44/52/ Change main & second battery connector from 5pin to 7pin. (SIV-IBM-15)

EC A45/47/ Change SES AT8356908 SMBUS switch path for SW request.

EC A46/35/ Change C360,C346,C133,C134 the signal filter of modem from 470pf to 1000pf for EMI request.

EC A47/35/ Add bypass cap on RJ45_ACTIVITY_SYS# and RJ45_LINKUP_SYS# signal ,the value is 1000pf for EMI request.

EC A48/35/ Add two resistor (0 ohm)to connect the Digital ground and LAN_GND for EMI request.

EC A49/34/ Change the power filter of LAN from 0 ohm to BLM18AG601SN1D for EMI request.

EC A50/55/ Change PL9 to GANSHIN(CDRH104RNP-5R2NC-EP)

EC A51/59/ Change PL6 to CDRH104R_1R5UH_10A.

EC A52/19/ Delete EEPROM PLCC32 socket package. (SIV-IBM-18)

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		Quanta Computer Inc.	
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BV1 / M-Note Schematic EC Tracking Record A (for DV --> SIV) Dec. 28, 2004

EC #/Page/Description/Part Affected/CMVC #

EC A53/04/ Change L35 filter bead from BLM18PG181SN1 to inductor 1uH.
EC A54/04/ Change L34 filter bead from BLM18PG181SN1 to inductor 91nH.
EC A55/04/ Change L19 inductor from 10uH to 1uH.
EC A56/37/ Change R32 from 1002 to 1608 size on LAN_MAG_TAP power trace. (SIV-IBM-19)
EC A57/05/ Change termination resister for Alviso composite TVout from 150ohm to 75ohm. (SIV-IBM-20)
EC A58/37/41/ Delete a gate logic from docking PCIE_WAKE# signal. (SIV-IBM-21)
EC A59/23/ Change R124 value from 33 ohm to 39 ohm. (SIV-IBM-22)
EC A60/23/ Add a cap 47P/50V/NPO for AC_BITCLK signal and NO ASM. (SIV-IBM-22)
EC A61/16/23/27/ Change signal name on Azalia interface. (SIV-IBM-22)
EC A62/27/ Change R154 from 33 ohm to 0 ohm.
EC A63/25/ Add internal MIC Bias logic circuit. CMVC# 66010/66014 (SIV-IBM-23)
EC A64/64/ Add filter HI0805R800R-10(5A) between VCC1R5M and VCC1R2AUX for EMI request.
EC A65/47/ Change R167,D17 to NO ASM and add series resistor between U27 pin31 and PCIRST# for ATMEL FAE recommend.
EC A66/32/ Change value of R247,R50,C299,C302 for IEEE lan measurement.
EC A67/08/ Add decouping capacitor of alviso power pin.
EC A68/25/ Change headphone selection circuit. (SIV-IBM-29)
EC A69/24/ Delete R555 pad. (SIV-IBM-23)
EC A70/24/ Change R525 to NO ASM. (SIV-IBM-23)
EC A71/24/ Change micphone selection circuit. (SIV-IBM-23)
EC A72/37/ Change net name from DOCK_MICIN_DTCT to DOCK_MICIN_DTCT# , DOCK_HPOUT_DTCT to DOCK_HPOUT_DTCT#. (SIV-IBM-29)
EC A73/23/ Change +EQ_BYPASS circuit. (SIV-IBM-23)
EC A74/23/ Add stereo external microphone circuit. (SIV-IBM-23)
EC A75/31/ Delete ESD lan switch diode.
EC A76/23/ Delete R130 pad.
EC A77/20/ Alignment guide pin of SATA HDD connector should be soldered to GND pad (SIV-IBM-30)
EC A78/35/ Add ESD TVD diode (SRV05-4) NO ASM near RJ45 connector side. (SIV-IBM-28)
EC A79/38/ Change H8 charge circuit. (SIV-IBM-31)
EC A80/38/ Keep CHARGE_CELL0 , CHARGE_CELL1 pull high resistor.
EC A81/31/ Add capacitors on lan switch power line. (SIV-IBM-32)
EC A82/43,44/ Add capacitors on MINI PCI-E power line. (SIV-IBM-32)
EC A83/15/ Change bead/capacitors value for CRT waveform measurement. (SIV-IBM-34)
EC A84/23/ Add a series resistor 33 ohm on SPDIF_OUT signal. (SIV-IBM-35)
EC A85/11/ Change R366,R378 from 100k to 10k ohm. (SIV-IBM-36)
EC A86/22/ Delete Clamp Diode and Add TVS diode (SEMTECH SRV05-4) on USB port 1,2. (SIV-IBM-38)
EC A87/22/ Change USB conn for SIV.
EC A88/58/ Change PR214,PR217 to from 1.5k to 732 ohm 1%.
EC A89/58/ Change PR250 from 49.9k 1% to 69.8k 1% ohm.
EC A90/59/ Change PR168,PR181 from 1.5k 1% to 732 1% ohm.
EC A91/59/ Change PR182,PR183 from 1.5k to 576 ohm 1%.
EC A92/59/ Change PR174 from 49.9K 1% to 48.7K 1% , PR188 from 49.9K 1% to 69.8K 1%.
EC A93/50/ Change PC11,PC10,PC7,PC79,PC8,PC58,PC63,PC65,PC66 from TMK325BJ475KN-T to TMK325BJ475KN-H.
EC A94/55/ Add a POSCAP capacitor "6TPE220Ml (220uF/6.3V/ESR=18mohm, 7343size, H=2.0mm_max) on VCC3M power line and NOASM
EC A95/50/ For the accuracy of Charge Voltage (Cell voltage), pin_REFIN and "CHARGE_VOLT" signal should use same Power Net, and it was VCC3M.
EC A96/50/ Modify pin_ICTL circuit.Pin_ICTL should also use 8724_LDO_3D3V.
EC A97/50/ Modify pin_CSSN to support Docking System.
EC A98/11/ Modify clock gen driven buffer.
EC A99/28/ Change PCMCIA connector vendor.
EC A100/38/ Delete R84 resistor pad,it's un-necessary pull-up.
EC A101/40,47/ Connect TATER signals from ATMEL to TouchPad connector. (SIV-IBM-40)
EC A102/23/ Add 0ohm pad (NOASM) on SPDIF_RTN signal. (SIV-IBM-41)
EC A103/49/ Prepare to support new STMicro G-sensor. (SIV-IBM-43)
EC A104/14/ Add bypass cap and delete some caps on LCD signal for EMI request.
EC A105/39/ Add bypass cap on KB signal for EMI request.
EC A106/37/ Add bypass cap on docking signal for EMI request.

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EC A107/50/ Change PR152 from 12.7k 1% 11.3k 1% ohm. (SIV-IBM-44)
EC A108/50/ Change PR130 from 18.2k 1% 19.1k 1% ohm. (SIV-IBM-44)
EC A109/50/ Change PR29 from 100k 1% 100k 0.5% ohm ; PR135 from 13k 1% 12.7k 0.5% ohm. (SIV-IBM-44)
EC A110/50/ Change PC112 from 0.01U/25V/X7R to 1000p/50V/X7R.
EC A111/51/ Change PQ49,PQ12 from SI2301BDS to SI2303BDS.
EC A112/53/ Update foorprint and change PD26,PD27 footprint to DSM.
EC A113/64/ Delete PL22,PL23 for IBM power request.
EC A114/41/ Change R400 from 6.8k to 100k ohm.
EC A115/50/ Change PR260 from 10K 1% to 10 ohm 1%. (SIV-IBM-48)
EC A116/50/ Delete PR120,PR121 pad.
EC A117/50/ Change PC94 from 0.1U/16V/X7R to 0.01U/25V/X7R.
EC A118/59/ Improve power function for QCI power team request.
EC A119/58/ Improve power function for QCI power team request.
EC A120/35/ Change RJ11+RJ45 connector vendor and footprint.
EC A121/02/ Add a 100pF cap between cpu THERMDA , THERMDC signal and NOASM.
EC A122/22/ Change SRV05-4 pin5 to VCC5M. (SIV-IBM-49)
EC A123/50/ Change charge description.
EC A124/24,25/ Change R554,R573 to NOASM and R572 to ASM.
EC A125/40,47/ Change signal name SES_EN# to SES_EN.
EC A126/51/ Change PR107 from 470K to 75K ohm.
EC A127/14/ Change C212 from 0.047U/50V/X7R_6 to 0.1U/50V/X7R_6.
EC A128/61/ Change PU6 value from "MAX1589ETA-T to MAX1589ETT250+TW".
EC A129/55,56/ Change PC135,PC139,PC137,PC138,PC24,PC136,PC133,PC132,PC134,PC131,PC23,PC130,PC56 value from "ECPU1E105JBM" to "GRM31MR71E225KA93L".
EC A130/62/ Change PC115 to NOASM.
EC A131/37/ Change F3 to NANOSMDC050F.
EC A132/28/ Change R443 to 10 ohm.
EC A133/11/ Change R358, R84, R131, R132, R581 to 12.1 ohm 1%.
EC A134/35/ Change C666,C667 to NOASM for homologation.
EC A135/37/ Change R130 from NOASM to ASM.

EC B01/61/ Change PU6 from"MAX1589ETT250+TW" to "MAX1589A". (SIT-IBM-01)
EC B02/38/ Change Wireless Disable switch circuit. (SIT-IBM-03)
EC B03/47/ Change reset signal change for TPM/TATER chip. (SIT-IBM-04)
EC B04/17/22/ Change divide USB OC# signal. (SIT-IBM-05)
EC B05/43/44/ Remove MINI PCI-E latch pad. (SIT-IBM-09)
EC B06/17/ Planar ID change to SIT "3". (SIT-IBM-10)
EC B07/47/ 0.01uF capacitor add on VCC3M at U27(Atmel 8356908). (SIT-IBM-12)
EC B08/39/ Change R275 to NO ASM. (SIT-IBM-13)
EC B09/51/ Add pull down resistor for charger inrush. (SIT-IBM-14)
EC B10/38/ Change R104 to NO ASM. (SIT-IBM-15)
EC B11/29/ Change PCMACT pull high from VCC3M to VCC3B. (SIT-IBM-16)
EC B12/38/ Change R99 to NO ASM. (SIT-IBM-17)
EC B13/30/ Change IEE1394 common choke from DLW21SN261SQ2L (260ohm@100Mhz) to DLW21SN121SQ2L (120ohm@100Mhz). (SIT-IBM-18)
EC B14/50/ Change MAX8724 pin_SHDN# circuit . (SIT-IBM-20)
EC B15/50/ Change MAX8724 pin_IINP# circuit . (SIT-IBM-21)
EC B16/14/ Change Q16 FET from SI3424DV to SI4890DY. (SIT-IBM-22)
EC B17/50/ Change for AC Adapter polarity Test Fail. (SIT-IBM-26)
EC B18/38/ Change GPIO_BT Pull up to Pull down. (SIT-IBM-27)
EC B19/50/ Change PQ46 from SI4890DY to SI4840DY. (SIT-IBM-26)
EC B20/35/ Change CN7 connector of pin3,4 to non ground pin for safety Hi-port test.
EC B21/35/ Remove C666,C667 pad.
EC B22/48/ Add 10uF/10V capacitor for VCC5B voltage drop while FAN turn on.
EC B23/19/ Reserve R,C for EMI request.
EC B24/14/ Change F4 to 1206 footprint.
EC B26/56/ Add NO ASM capacitors " GRM21BR71E155K" in VINT19 power rail to prevent any issue may happen like Voltage Dip on VINT19 or LCD Flicker or Acoustic Noise etc... (SIT-IBM-32)
EC B27/50/38/62/52/ Support 65W AC adapter. (SIT-IBM-33)
EC B28/53/ Chagne PR220,PR215 from 5.62K/F to 6.04K/F. (SIT-IBM-34)
EC B29/50/ Chagne PR96 from 470K 5% to 220K 5%. (SIT-IBM-35)
EC B30/24/ Chagne R526 from 10K 5% to 36K 5% ; R485 from 8.2K 5% to 16K 5%. (SIT-IBM-36)
EC B31/20/ Add 10 10V X5R capacitors near SATA connector. (SIT-IBM-37)
EC B32/55/ Add 100k 5% resistor between MAX1901 pin VL and pin SKIP#. (SIT-IBM-39)
EC B33/42/ Add 0 ohm resistor between DOCKING and SIO. (SIT-IBM-40)
EC B34/47/ 0.01uF capacitor add on RTCVCC at U27(Atmel 8356908). (SIT-IBM-41)
EC B35/07/ Add 10uF/10V/X5R capacitor for TVDAC power noise.
EC B36/51/ Remove PD42,PD43,PD12,PD14.
EC B37/35/ Change C133,C134 from 470pF to 1000pF and C607,C608 to ASM for EMI request.
EC B38/11/ Change R352 from 33 ohm to 75 ohm for 48 Mhz slew rate fail issue.
EC B39/11/ Change PCICLK 33MHz circuit for signal quality.
EC B40/19/ Remove G2 short pad for IBM request.
EC B41/49/ Correct G-sensor STMicro table as same as ADI.
EC B42/56/ EMI request
EC B43/50/56/ Change PC56, PC135, PC139, PC137, PC138, PC24, PC136, PC133, PC132, PC134, PC131, PC23, PC130 from GRM31MR71E225KA93L to GRF31CR72A105K
EC B44/05/ Change R212 from 255/F to 249/F. (SIT-IBM-44)
EC B45/47/ Change R293 to NOASM , D17 to ASM. (SIT-IBM-43)
EC B46/48/ Change R563 to NOASM.
EC B47/37/ Change C678,C681,C683 to NOASM, because these EMI capacitor will effect PS2 KB fail while with docking.
EC B48/37/ Change C672,C675,C677,C684,C687 to NOASM for EMI request.



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BV1 / M-Note Schematic EC Tracking Record C (for SIT --> SVT-1) Jul. 13, 2005
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EC C01/56/ Change CLK_ENABLE# and VR_PWRGD pull-up power from VCC3M to VCC3B for leakage issue. Add PQ44. (SVT-IBM-01)
EC C02/20/ Reserved R38 0ohm for SATA RSVD pin but not ASM. (SVT-IBM-08/15)
EC C03/54/ VCC1R8M_ON source change from M2_ON to M1_ON. For Cardbus leakage current concern. (SVT-IBM-02)
EC C04/16/20/ HDD detect pin connection. (SVT-IBM-07)
EC C05/02/ C346 100pF ASM because RF test failed by thermal shutdown. (SVT-IBM-08)
EC C06/35/ U30/U31 ASM for CDE ESD fail. (SVT-IBM-09)
EC C07/37/ Docking pin 14 connect to PCIE_WAKE#
EC C08/37/ C672, C675, C677, C684, C687, C682 change to 10pF for EMI request
EC C09/37/ Docking connector 48pin connect to GND (SVT_IBM-13)
EC C10/16/ Pull down preparation (No ASM) on ACZ_RST# pin on ICH6 side(SVT_IBM-12)
EC C11/16/17/ Reserve Cap on PCICLK_FR_ICH6M_33M & USBCLK_48M and close to ICH6 (No ASM)
EC C12/50/ Change PQ46 from SI4840DY to SI4890DY (SVT_IBM-13)
EC C13/50/ Change PU5 from MAX8724 to MAX8724A (SVT_IBM-14)
EC C14/55/ To reduce VCC5M undershoot.
EC C15/55/ To reduce VCC3M undershoot.
EC C16/17/ Planar ID change to SVT "4". (SVT_IBM-16)
EC C17/50/52/ Add cap according to EMI request.
EC C18/11/ Change PCICLK 33MHz damping for signal quality.
EC C19/23/ Pull down preparation (No ASM) on ACZ_RST# pin on code side (SVT_IBM-17)
EC C20/37/ Add ESD solution on PS/2 I/F
EC C21/37/ Reserve 0ohm resistor on FRAME# & PCICLK on debug connector for security concern (SVT-IBM-18)
EC C22/56/ Change PR193 & PR177 to ASM and value change to 10K
EC C23/11/ Change R352 value from 75ohm to 33ohm for USB 48MHz

BV1 / M-Note Schematic EC Tracking Record C (for SIT --> SVT-2) Aug. 02, 2005
EC #/Page/Description/Part Affected/CMVC #

EC C24/11/32 Add 0ohm serial resistor between clock gen and LAN chip for Ethernet transfer fail problem.
EC C25/02 Disconnect ITP signals and reserve test pad for jump wire. The reason is for GBE CLK routing.
EC C26/37 Add two caps on MCLK,MSDATA signals for ESD solution.
EC C27/17 Change planar ID to "0110" SVT-2 for Ethernet transfer problem.
EC C28/37 Change R589,R590 to NOASM , and C675,C677,C684,C687 to 470pF/50V ASM for ESD solution.