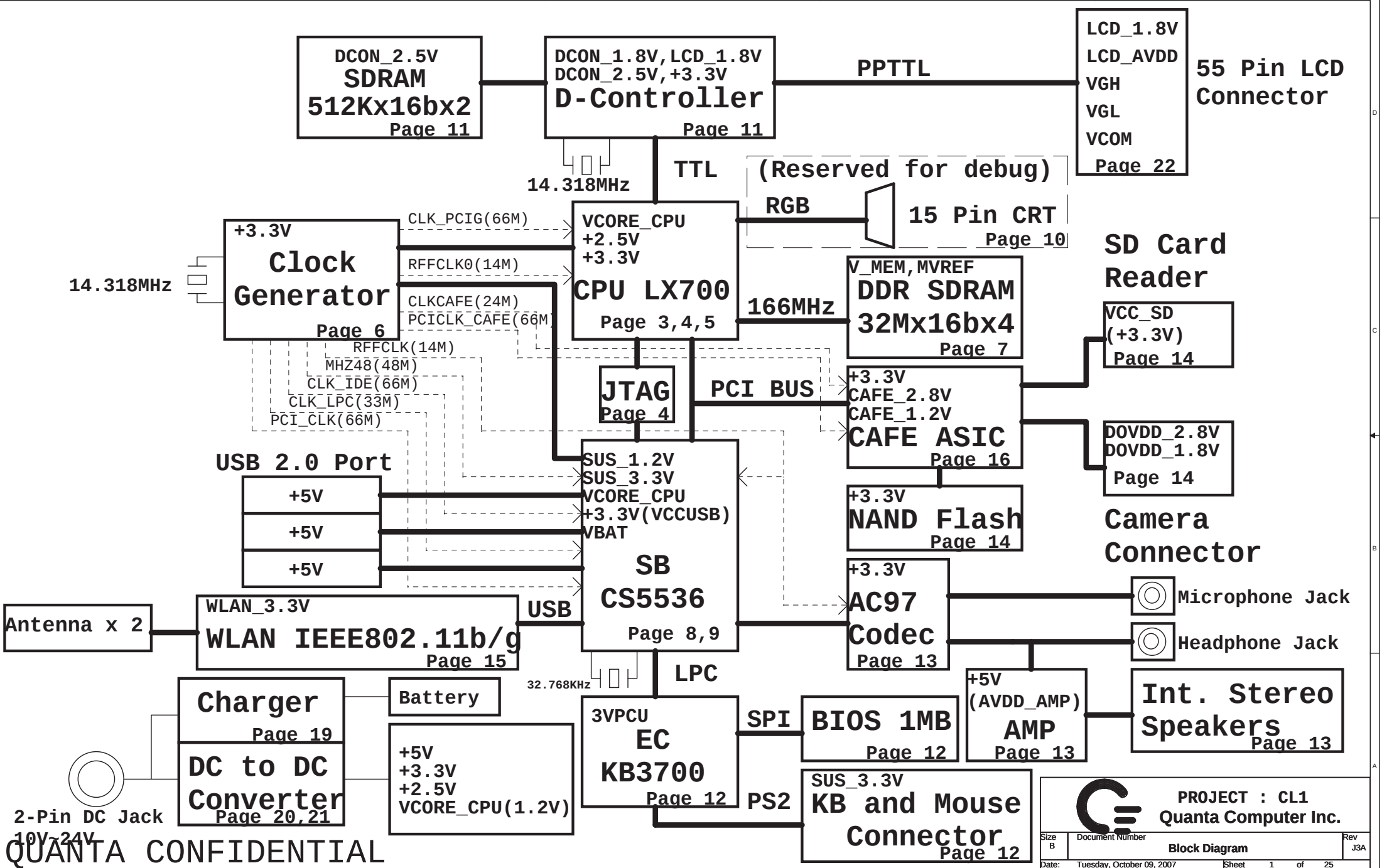


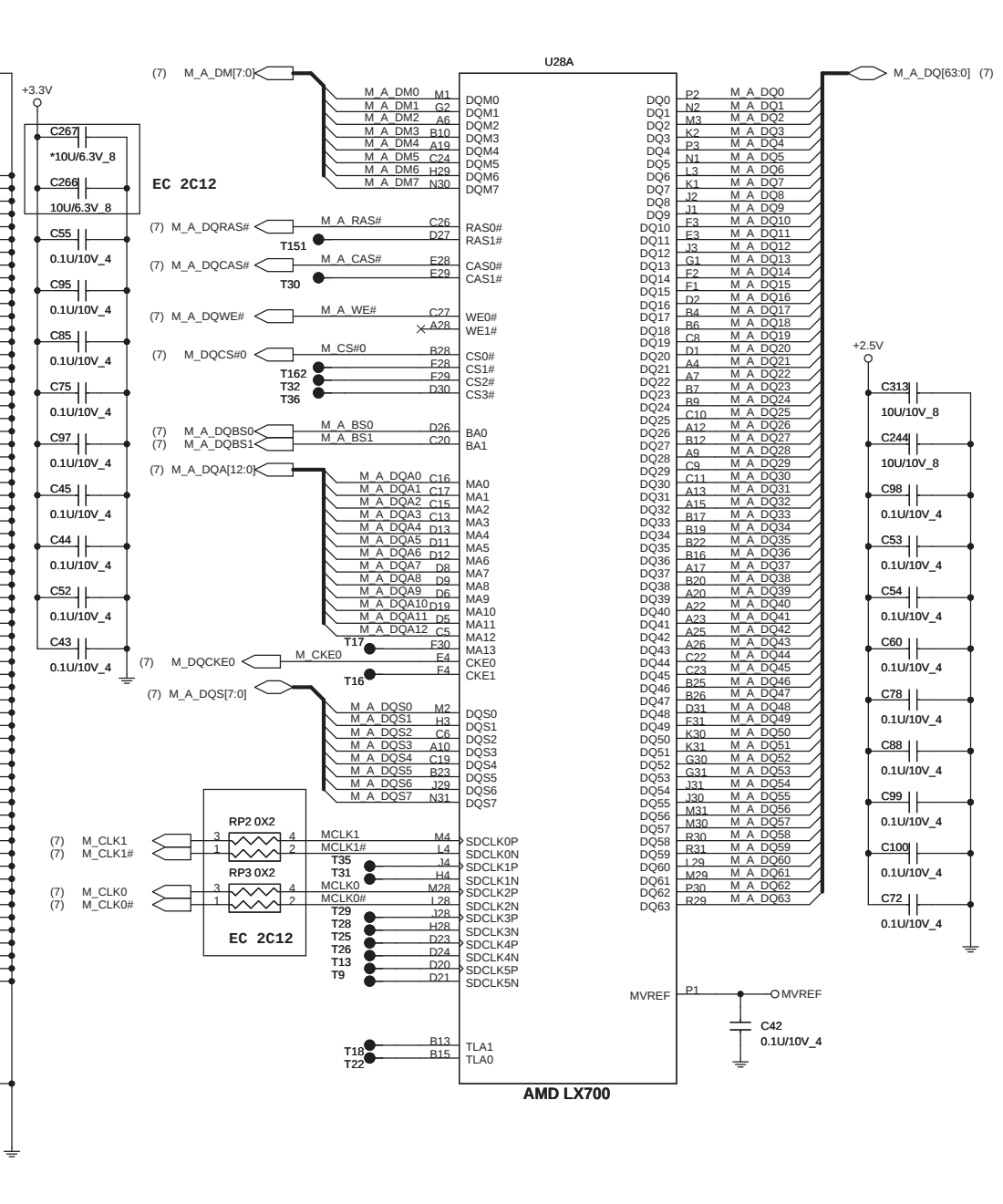
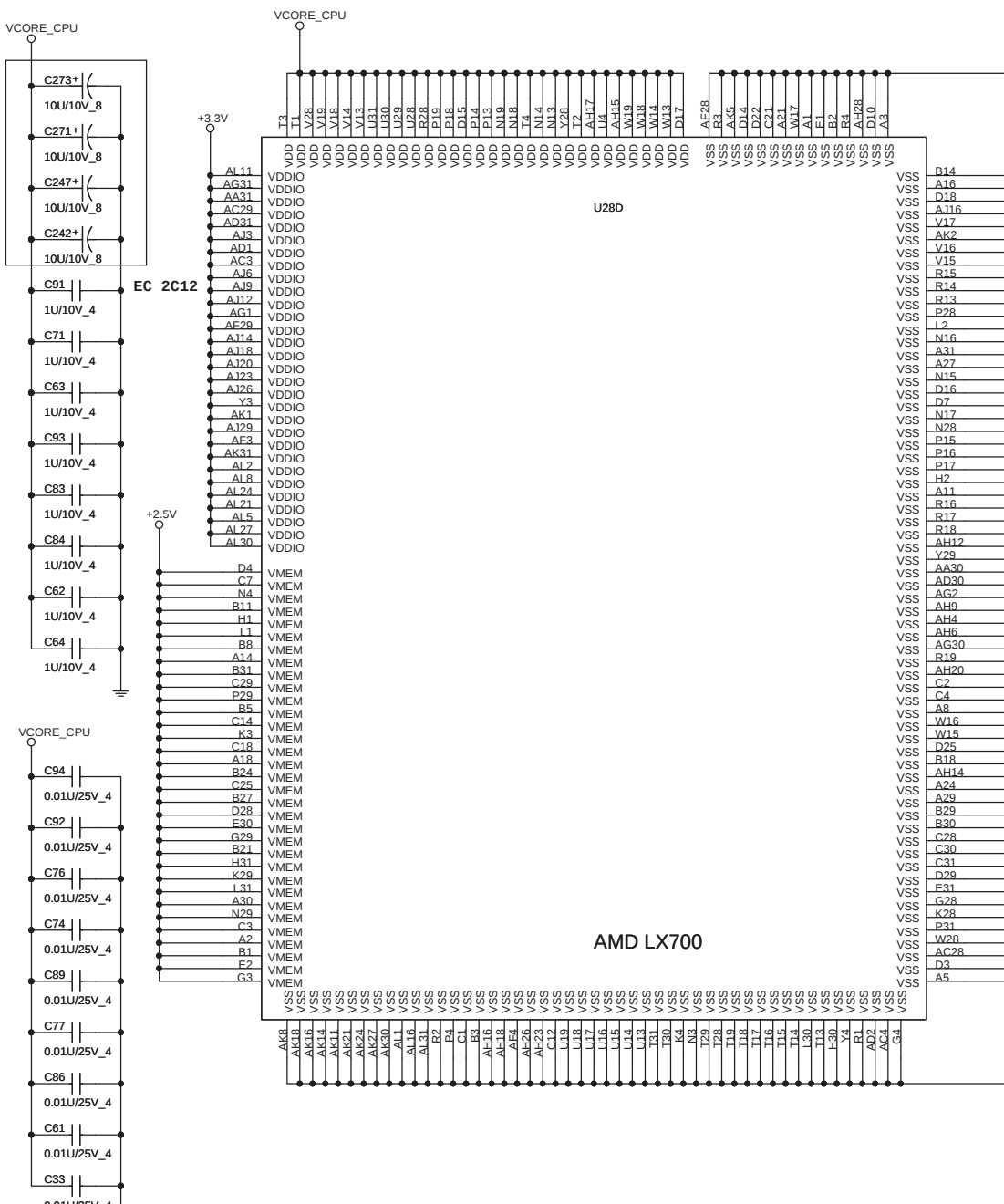


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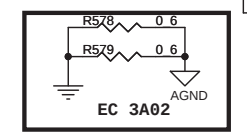
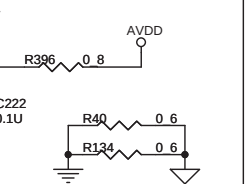
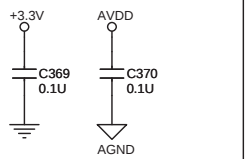
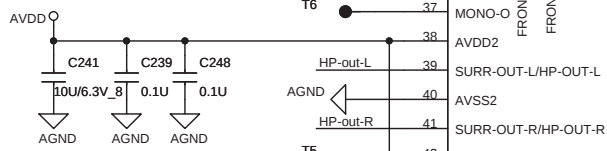
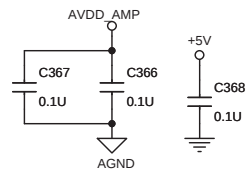
Voltage Rails	Voltage	S0	S3	S5	Power Source	Control Signal
VBAT	3V	ON	ON	ON	RTC-BATTERY	ALWAYS
3VPCU	3.3V	ON	ON	ON	VIN	ALWAYS
WLAN_3.3V	3.3V	ON	ON	ON	VIN	WLAN_EN
SUS_3.3V	3.3V	ON	ON	OFF	3VPCU	VR_ON#
SUS_1.2V	1.2V	ON	ON	OFF	SUS_3.3V	SUS_3.3V
V_MEM	2.5V	ON	ON	OFF	VIN	SUS_ON/MAIN_ON
MVREF	1.25V	ON	ON	OFF	V_MEM	V_MEM
+5V	5V	ON	OFF	OFF	VIN	PWR_ON
+3.3V	3.3V	ON	OFF	OFF	WLAN_3.3V	PWR_ON
+2.5V	2.5V	ON	OFF	OFF	V_MEM	PWR_ON
VCORE_CPU	1.2V	ON	OFF	OFF	VIN	MAIN_ON
VCORE_SB	1.2V	ON	OFF	OFF	VCORE_CPU	VCORE_CPU
VCCUSB	3.3V	ON	OFF	OFF	+3.3V	+3.3V
USBVDD	5V	ON	OFF	OFF	+5V	USB_PWR_EN1
LCD_1.8V	1.8V	ON	ON	OFF	DCON_1.8V	VDDEN
LCD_AVDD	9.6V	ON	ON	OFF	VIN	VDDEN
VGH	18V	ON	ON	OFF	VIN	VDDEN
VGL	-7V	ON	ON	OFF	VIN	VDDEN
VCOM	2.2V	ON	ON	OFF	LCD_AVDD	COLMODE
AVDD_AMP	5V	ON	OFF	OFF	+5V	+5V
VCC_SD	3.3V	ON	OFF	OFF	+3.3V	SD_ON
DCON_2.5V	2.5V	ON	ON	OFF	V_MEM	DCON_1.8V
DCON_1.8V	1.8V	ON	ON	OFF	DCON_2.5V	DCON_EN
LEDPWR	17.6V	ON	ON	OFF	VIN	BACKLIGHT
DVDD_1.8V	1.8V	ON	OFF	OFF	+3.3V	+3.3V
DOVDD_2.8V	2.8V	ON	OFF	OFF	+3.3V	Cam_GPI02
CAFE_1.2V	1.2V	ON	OFF	OFF	VCORE_CPU	VCORE_CPU



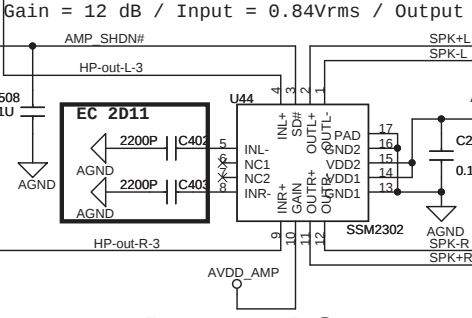
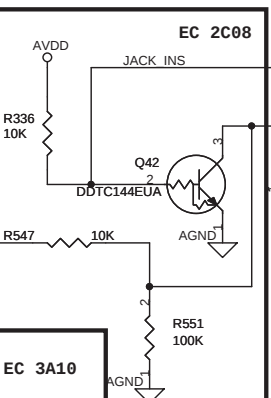
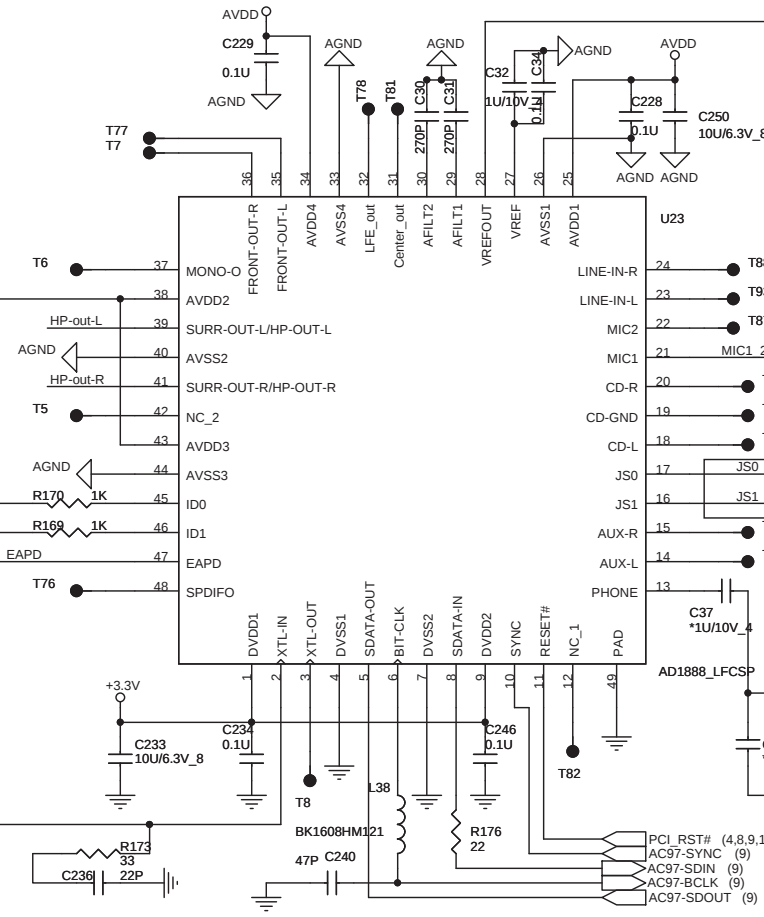
PROJECT : CL1
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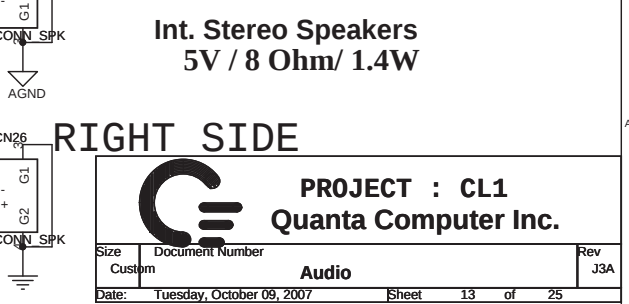
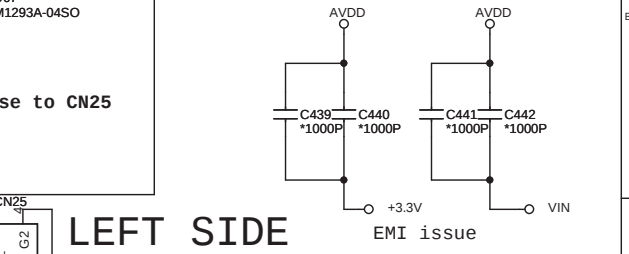
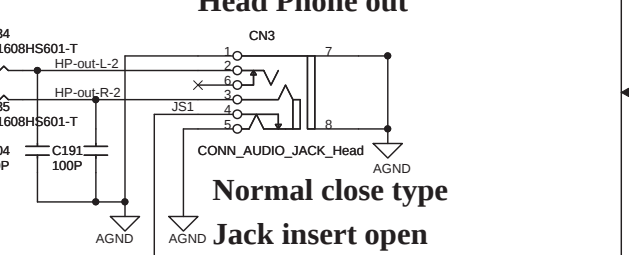
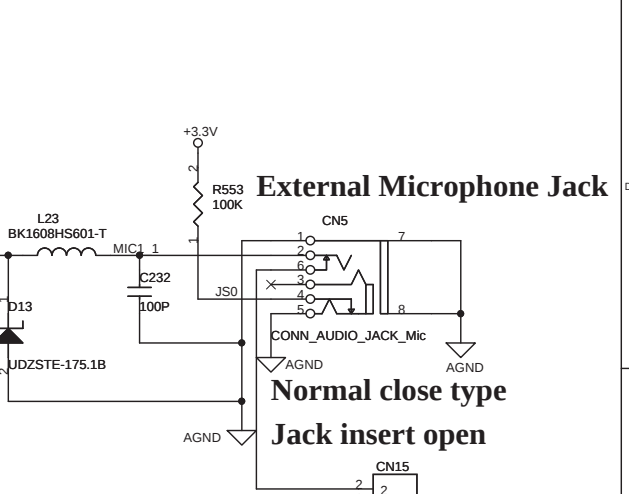
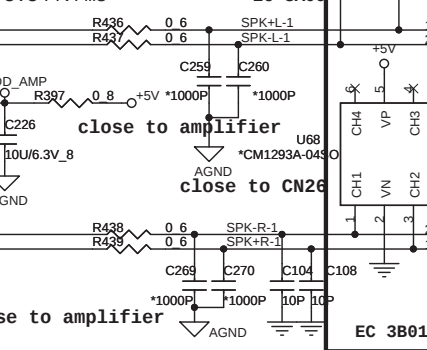
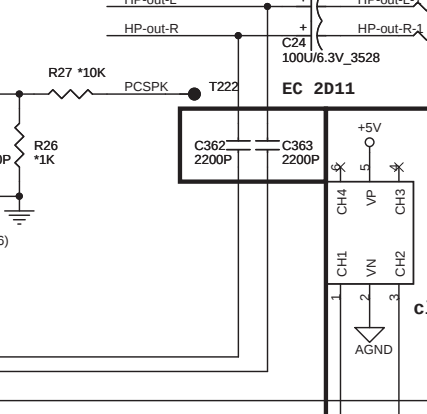
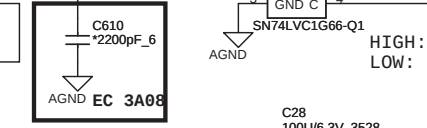
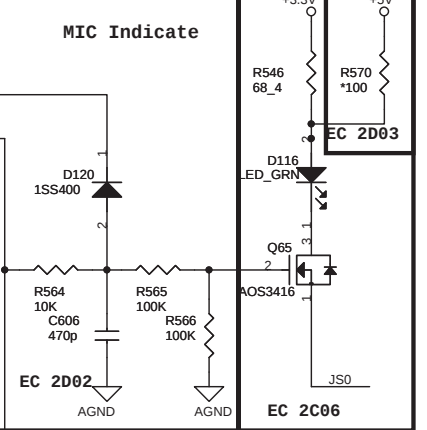




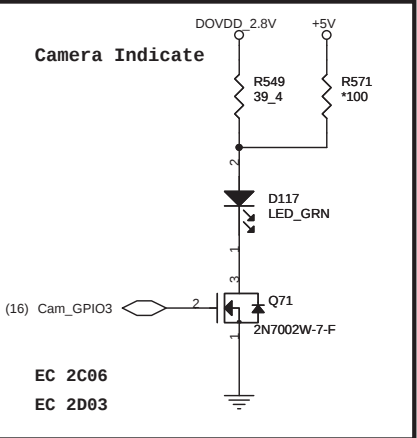
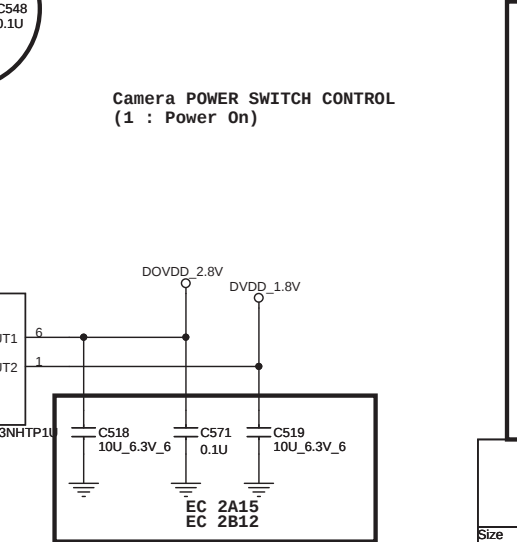
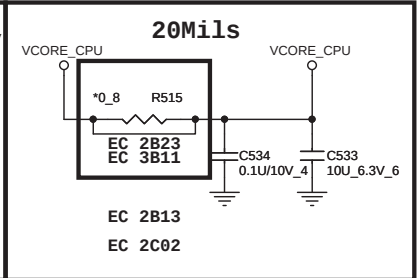
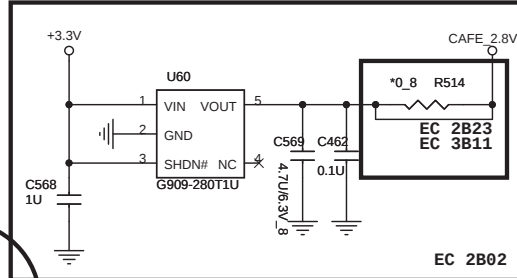
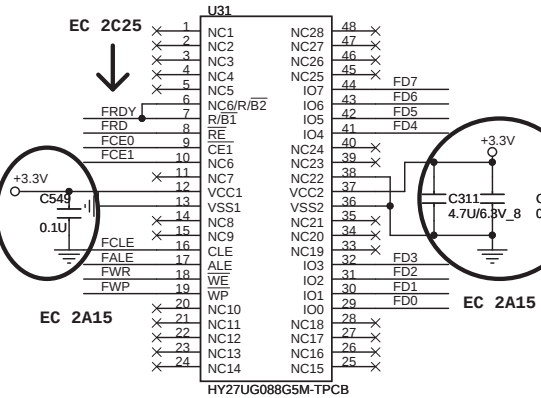
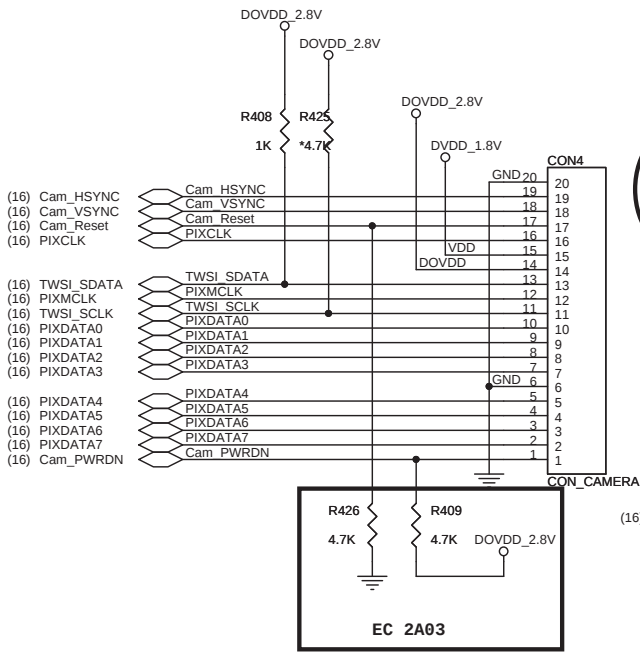
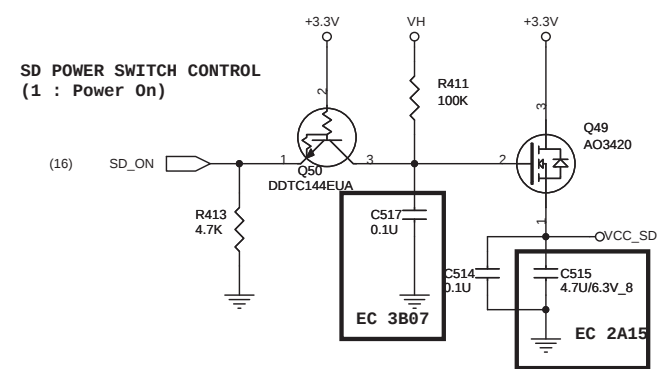
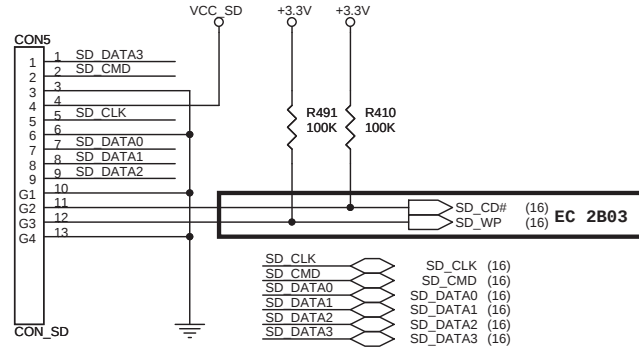
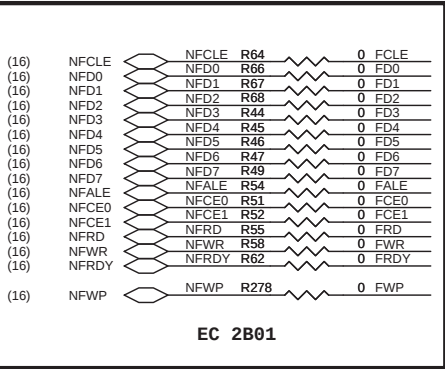
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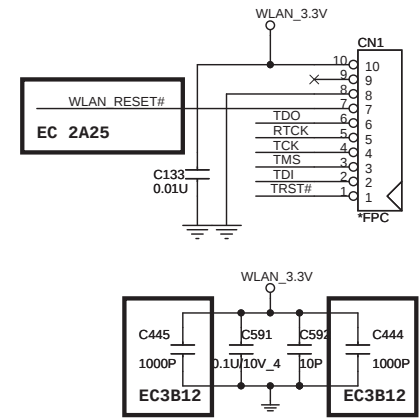
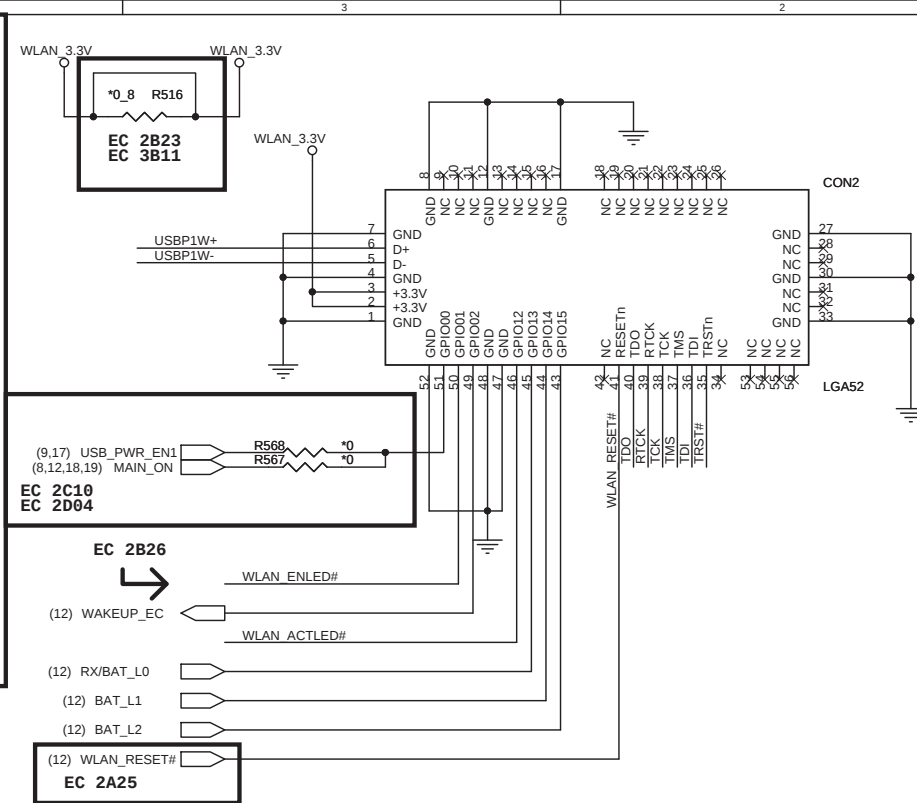
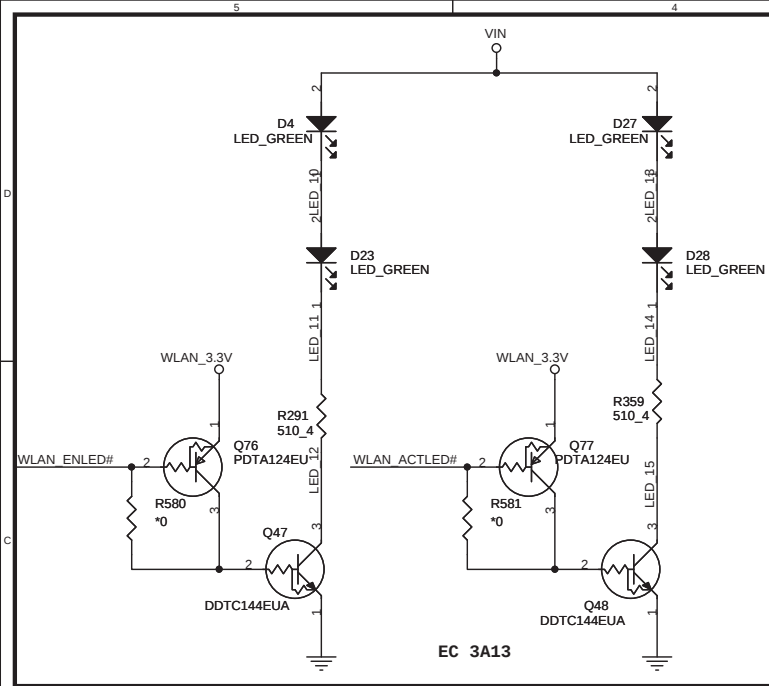
Audio amplifier



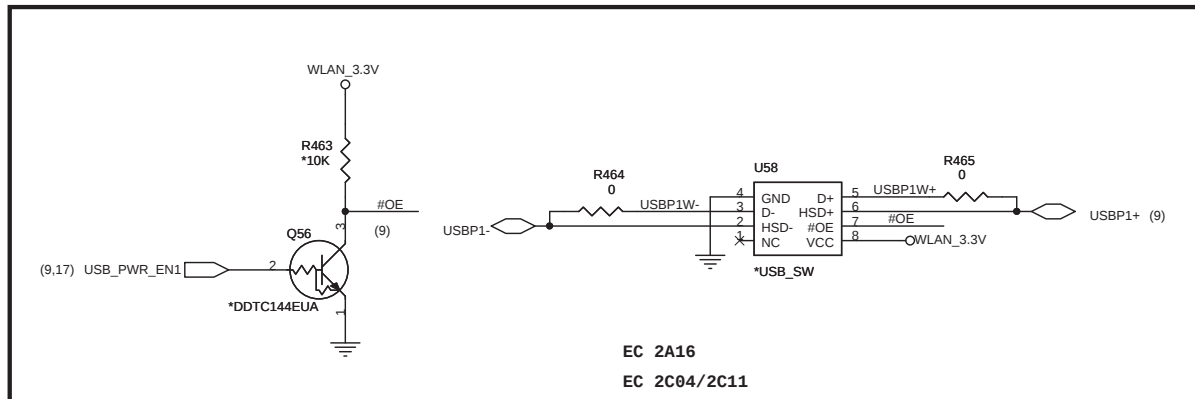
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EC 2A04
EC 2C10

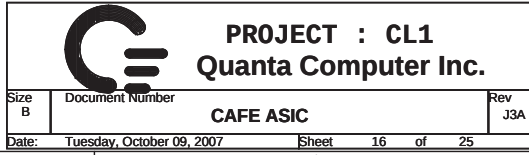


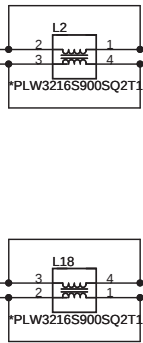
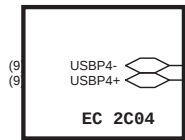
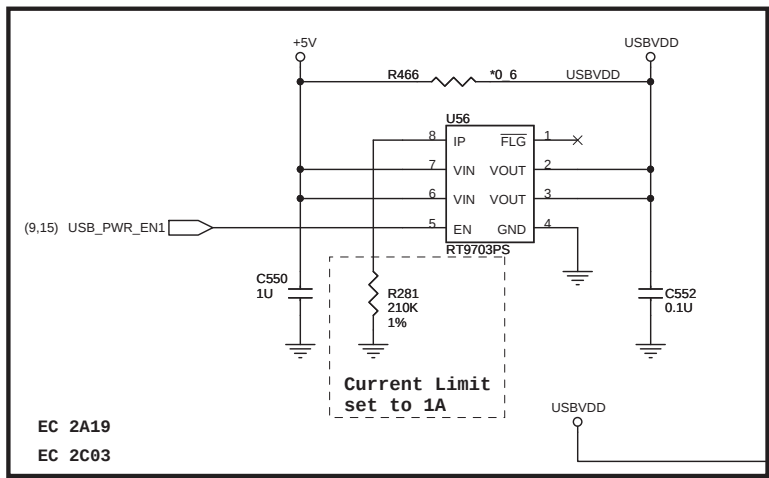
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Quanta Computer Inc.

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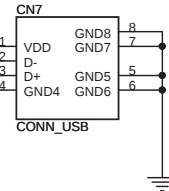
QUANTA CONFIDENTIAL



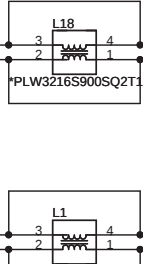


USBVDD

USBP 4-
USBP 4+

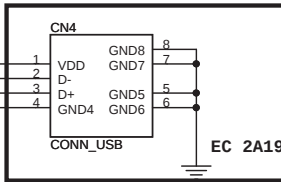
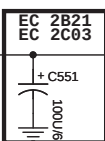


USBP2-
USBP2+

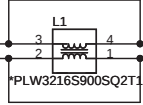


USBVDD

USBP 2-
USBP 2+

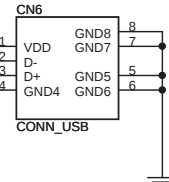


USBP3-
USBP3+



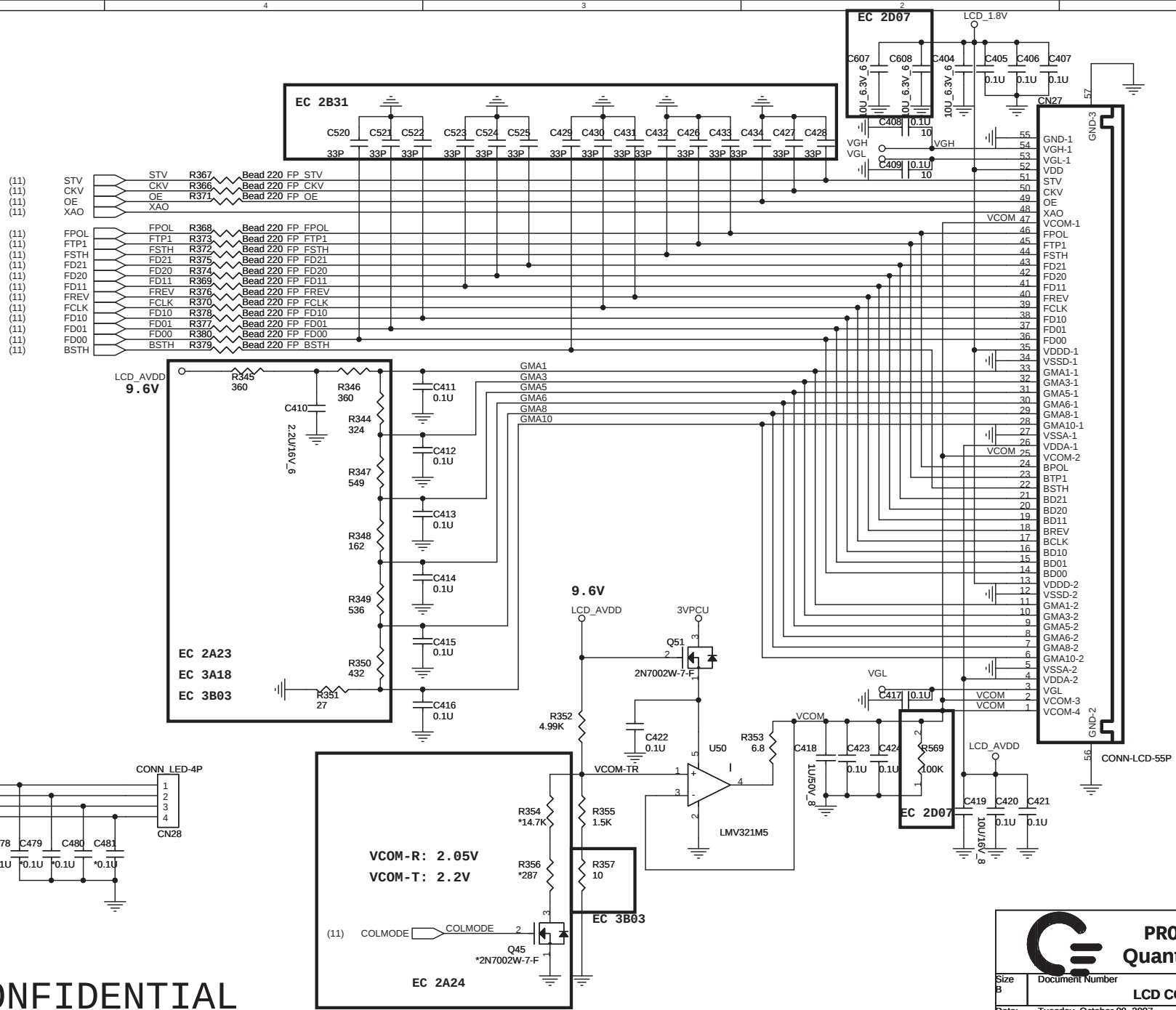
USBVDD

USBP 3-
USBP 3+



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		Quanta Computer Inc.	
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EC #/Page/Description

EC 2A01/Page 8,16/Add buffer logic IC U55 and Delete D32,R449 and R450 pop 0 Ohm ,De-pop R43,C56 for CAFE Reset circuit.
EC 2A02/Page 10/Change R115,R116 pull up to 5V_CRT for current leakage.
EC 2A03/Page 14/Modify Camera PWDN/RESET pull up/down resistor R409,R426 4.7KOhm.
EC 2A04/Page 15/Delete U54, Add R452 10K Ohm for wireless wake up control circuit.
EC 2A05/Page 17/Change R402 from 10K to 100K Ohm for CAFE DOWN.
EC 2A06/Page 06/Seperate CLK_GND and GND. Add R454,R455 00hm.
EC 2A07/Page 07/Add C547 0.1uF for EMI.
EC 2A08/Page 08,09/Add R460/R461/R462 10K,D37/D38/D39 1SS400, Q54/Q55 2N7002 for DCON leakage.
EC 2A09/Page 08,10,12,17/De-pop J1/CN2/CN31/CN24/CN34 for build.
EC 2A10/Page 9/POP R442 for DDR 128MB, POP R443 for DDR 256MB.
EC 2A11/Page 11/Add R470 1M0hm,R471 00hm. Change C379/C381 to 22pF for DCON XTAL circuit. Add R456(DE-POP)/R457(POP) 10K Ohm for DCON seting.
EC 2A12/Page 12,19/Modify charger circuit.
EC 2A13/Page 12/Left switch SW3/SW5/SW6/SW9 change to PAD.
EC 2A14/Page 12/Add D33,D34. Change R295/R407 to 1K Ohm. Delete CN29/C436/C437/C438 for Power/Charge LED.
EC 2A15/Page 14/Add C548/C549 0.1uF,Change C311/C515/C518/C519 to 4.7uF for backup capacitor.
EC 2A16/Page 15/Add U58,Q56 DTC144EU,R463 10K Ohm for wireless USB switch.
EC 2A17/Page 16/Change R391 to 0 Ohm,De-pop C483.
EC 2A18/Page 17/Delete C460/C462/C452/C486/C489/C501/C504/C505.
EC 2A19/Page 18/Add U56/U57/C550/C553/C554/C551 for separate USB power. CN4 change connector part.
EC 2A20/Page 20/Separate power plant capacitor .
EC 2A21/Page 21/Pop R459 0 Ohm, De-pop U1 for DCON1.8V connect to DCON_2.5V
EC 2A22/Page 21/Change circuit for panel leakage
EC 2A23/Page 23/Change R345-R351 value for display gamma.
EC 2A24/Page 23/De-pop R354,R356,Q45 for Disable Color Mode control circuit
EC 2A25/Page 12,15/EC pin51 connect to wireless module pin 41 RESET# for reset wireless.



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EC 2A to 2B History		
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EC #/Page/Description/Ticket#/(ECO#)

EC 2B01/Page 14,16/Change CAFE FPGA to ASIC Design and NAND Flash damping change to 00hm.
EC 2B02/Page 14/Add U60,C568,C569,C462 for CAFE ASIC 2.8V power plant
EC 2B03/Page 14/Change SD card CD# and WP# pin define.
EC 2B04/Page 8/Remove U55,R432,C545 for CAFE ASIC. Pop R43 1K 0hm, C56 2200pF.
EC 2B05/Page 6/Add R493 1K pull up for Clock GEN PWD#, control by +3.3V rail.
EC 2B06/Page 9/Change 48MHz circuit for AMD required.
EC 2B07/Page 9/Change R57 from 4.70hm to 00hm for AMD required.
EC 2B08/Page 6/Add R495,R496 and pop R495 for enable PCI CLK to 66MHz.
EC 2B09/Page 12/Add D40,D41,D42,D43,D44,D45,D46,D47,D48,AND pull high resister R497,R498,R499 to prevent gamepad ghost key.
EC 2B10/Page 8,12/Move MIC_AC#/DC signal from EC to SB ,add WAKE_PRECHG signal control battery switchfor fix battery low can't boot.
EC 2B11/Page 11/Remove U59,R472,R473,R474,C556 for DCON OSC circuit.
EC 2B12/Page 14/Change C518,C519 to 10uF. Add C570,C571 for improve camera power noise.
EC 2B13/Page 14/Change CAFE 1.2V LDO suport 300mA.
EC 2B14/Page 6,20/Change R381 to 1200hm bead, pop C435 15pF,C539,C543 0.1uF for EMI solution.
EC 2B15/Page 19,20,21/Change C546,C355,C339,C321,C201,C274,C210,C162,C130 voltage rating to 16V.
EC 2B16/Page 19/Modify Power/Charger circuit.
EC 2B17/Page 20/Change U13 source to A0Z1013 and add D112 for add CPU Core power power consumption.
EC 2B18/Page 20/Change SB 1.2V power circuit.
EC 2B19/Page 21/Pop U1 and De-pop R459 recover DCON_1.8V.
EC 2B20/Page 8,12/Add wakeup circuit for EC wakeup.
EC 2B21/Page 4/Add CPU thermal protect circuit
EC 2B22/Page 4/Add R522,R523 for M/B ID select.
EC 2B23/Page 11,12,14,15,16,21/Add R512 ~ R521 for current measure resistor.
EC 2B24/Page 11/Add L48,L49,L50,C577,C578,C579 for EMI solution.
EC 2B25/Page 12/Add R534,R535 and C582 for go to pin AD1 of EC for EC ID Pin .(ECO-CL1-HW-00001)
EC 2B26/Page 12/Add Q64,R533 to avoid EC can't entry stop mode when system in power off and WLAN no power.(ECO-CL1-HW-00001)
EC 2B27/Page 12/Change EC Reset solution to Reset IC U64 from RC avoid EC Hang up when battery voltage too low even AC plug in .(ECO-CL1-HW-00001)
EC 2B28/Page 19/Add D115 between U39 pin8 and pin15,change C320 from 680pF to 820p to reduce VIN overshoot
when battery charging and remove the battery.(ECO-CL1-HW-00001)
EC 2B29/Page 20/Change U61 VBIAS source from VH for +5V unstable when turn on theUSB Power switch .(ECO-CL1-HW-00001)
EC 2B30/Page 20/Reserved U65 when Battery Voltage below 4.8V will turn off the 3VPCU Power output.
Avoid EC Hang up when battery voltage below 3.0V (EC Operating Voltage 3.0 ~ 3.6v).(ECO-CL1-HW-00001)
EC 2B31/Page 22/Change LCD interface for EMI apacitor C426 ~ C434,C520 ~ C525 from 47pF to 22pF for fix display garbage on B2 new panel.(ECO-CL1-HW-00002)
EC 2B32/Page 20/Remove U64 EC reset IC for avoid assert ECRST# when 3VPCU unstable.(ECO-CL1-HW-00003)

CL1 Schematic EC Tracking Record C (for [B2] --> [B3])April. 27, 2007)

EC #/Page/Description/#Ticket/(ECO#)

EC 2C00/Page 20/Remove U38 for improve 3VPCU voltage and add OVR for EC reset# / #543/(ECO-CL1-HW-00005)
EC 2C01/Page 21/Remove Q12 for avoid ESD damage./(ECO-CL1-HW-00005)
EC 2C02/Page 9,14,20/VCORE_CPU change to 1.2V for LX700, delete VCORE_SB power plant and connect to VCORE_CPU, delete CAFE_1.2V power plant and connect to VCORE_CPU./(ECO-CL1-HW-00005)
EC 2C03/Page 18/Change U56 USB PWR SW for reduce inrush current./(ECO-CL1-HW-00005)
EC 2C04/Page 15,18/Swap USB1 and USB4 signal./ #1095/(ECO-CL1-HW-00005)
EC 2C05/Page 12/Change PS/2 power plant to 3VSUS for KB/TP S3 wakeup function./ #1094/(ECO-CL1-HW-00005)
EC 2C06/Page 13,14/Add Mic/Camera indicate LED/(ECO-CL1-HW-00005)
EC 2C07/Page 8/Change R188 to pull down for DCON S3 function/ #947/(ECO-CL1-HW-00005)
EC 2C08/Page 13/Add a mosfet control AMP_SD# for pop noise/ #640,#977/(ECO-CL1-HW-00005)
EC 2C09/Page 12/Remove Q46,R395,R358,CN30 for delete keyboard light function/(ECO-CL1-HW-00005)
EC 2C10/Page 15/Remove CN33,R452,R365,C544 for delete WLAN wakeup enable switch function/(ECO-CL1-HW-00005)
EC 2C11/Page 15/Remove U58 USB analog switch for cost down./ #1085/(ECO-CL1-HW-00005)
EC 2C12/Page 3,4,5/change CPU from GX500 to LX700/(ECO-CL1-HW-00005)
EC 2C13/Page 4/Add R526,R525,R522,R540,R541 for CPU and Memory frequency hardware boot strap/(ECO-CL1-HW-00005)
EC 2C14/Page 4/Add R184,R538 for PCI bus frequency hardware boot strap/(ECO-CL1-HW-00005)
EC 2C15/Page 5/Rewire RGB TTL signal use MSB-aligned instead of LSB-aligned./ #336/(ECO-CL1-HW-00005)
EC 2C16/Page 19/Add 4A fuse and zener diode for DC input high surge voltage protect/(ECO-CL1-HW-00005)
EC 2C17/Page 20/Add D16/D17 for modify V_MEM power sequency same as 3.3V/(ECO-CL1-HW-00006)
EC 2C18/Page 20/Change AOZ1010 to AOZ1016 and add pull down resistor on pin6 EN pin/(ECO-CL1-HW-00006)
EC 2C19/Page 20/Add 100K/0.1uF RC for fix WLAN_3.3V voltage dip/(ECO-CL1-HW-00006)
EC 2C20/Page 21/Add LCD_AVDD short protect circuit/(ECO-CL1-HW-00006)
EC 2C21/Page 21/Add RC for analog control LCD backlight for fix choice noise/(ECO-CL1-HW-00006)
EC 2C22/Page 12/Setting EC_ID to 0 for B3 version/(ECO-CL1-HW-00006)
EC 2C23/Page 4/Add Q69 for voltage leakage from SB THERM_ALRM# in S3 mode/(ECO-CL1-HW-00006)
EC 2C24/Page 20/Add C601,C602,C603,C604 for reduce power plant voltage translation noise/(ECO-CL1-HW-00006)
EC 2C25/Page 14/Hynix/Micron is 2 die chip and Samsung/ST is 1 die chip, pin 6 define is diffeent. Hynix/Micron need to connect pin 6 to pin 7/(ECO-CL1-HW-00007)

CL1 Schematic EC Tracking Record D (for [B3] --> [B4])April. 27, 2007)

EC #/Page/Description/#Ticket/(ECO#)

EC 2D00/Page 11/LCD_1.8V have power noise in DCON power pin.Relocate C375,C376,C377 for each DCON power pin./(ECO-CL1-HW-00008)
EC 2D01/Page 12/Change EC_ID 1/8 X 3VPCU for B4 M/B ID. /(ECO-CL1-HW-00008)
EC 2D02/Page 13/Add control circuit in VREFOUT to control Microphone LED turn On/Off for Microphone LED indicate blink when suspen/resume./(ECO-CL1-HW-00008)
EC 2D03/Page 13,14/Reserve option for 5V higher luminous LED for Microphone/Camera LED indicate light./(ECO-CL1-HW-00008)
EC 2D04/Page 15/Reserve MAIN_ON and USB_PWR_EN1 signals to WLAN GPIO00 for isolate USB interface when S3 for voltage leakage./(ECO-CL1-HW-00008)
EC 2D05/Page 19/Modfiy battery charger current limit to dynamic mode for power consumption limit circuit from constant current to constant watt./(ECO-CL1-HW-00008)
EC 2D06/Page 21/Modify LCD_AVDD voltage short protect circuit, remove current limit circuit and add turn off LCD_AVDD control circuit./(ECO-CL1-HW-00008)
EC 2D07/Page 22/Power sequence modify add C607/C608 10uF for keep LCD_1.8V longer and add R569 100K to GND in VCOM power plant
for discharge quickly /(ECO-CL1-HW-00008)
EC 2D08/Page 20/Modify charger IC CV setting circuit for VIN have overshoot(around 15V) with 19V Adapter IN./(ECO-CL1-HW-00008)
EC 2D09/Page 12/Move PS2 interface pull up resritors to the other side of the bead for RFI issue./(ECO-CL1-HW-00008)
EC 2D10/Page 20/Change U33,U26,U15,U13 PWM IC COMP R C value for improve power plant transient response./(ECO-CL1-HW-00008)
EC 2D11/Page 13/Change Audio Amplify Cin(C362,C363,C402,C403) value to 2200pF for setting high-pass frequency to 480Hz.
/(ECO-CL1-HW-00008)
EC 2D12/Page 20/U13 AOZ1017 latch up function error, it'll change back to AOZ1013 and mount C602./(ECO-CL1-HW-00008)

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CL1 Schematic EC Tracking Record E (for [B4] --> [C1])July 16, 2007)

EC #/Page/Description/#Ticket/(ECO#)

EC 3A00/Page 18,19/Populate R268/R160 2.20hm and C356/C203 2200pF for EMI solution./(ECO-CL1-HW-00009)
EC 3A01/Page 5,11/Move R328 near U28 and change to bead BK1608LL680 and add C609/22pF. Populate R332 330hm/C390 22pF for EMI solution./(ECO-CL1-HW-00009)
EC 3A02/Page 13/Add R578,R579 00hm resistors between AGND and GND for EMI solution./(ECO-CL1-HW-00009)
EC 3A03/Page 20/Modify DCON power sequence to DCON_1.8V lead DCON_2.5V power plant./(ECO-CL1-HW-00009)
EC 3A04/Page 12/Change C371 from 1uF to 2.2uF for EC Reset timing worst case issue./(ECO-CL1-HW-00009)
EC 3A05/Page 4,8,10/Depopulate R190,J1,SW11 and all of CRT page 10 parts for cost down./(ECO-CL1-HW-00009)
EC 3A06/Page 12/Change R534 to 30K_1% for change M/B ID to reversion C1./(ECO-CL1-HW-00009)
EC 3A07/Page 18/Add D123 serial with D118 39V zenor diode for adapter IN high voltage range protect./(ECO-CL1-HW-00009)
EC 3A08/Page 13/Add C610 6800pF between R25 and C35 to AGND for providing an antialiasing filter at the input to the A/D./(ECO-CL1-HW-00009)
EC 3A09/Page 13/Depopulate C510,C511,C512,C513 for the click in the speakers upon suspend/resume issue/ #1735/(ECO-CL1-HW-00009)
EC 3A10/Page 12,13/Change ACIN signal to EC GPIO8 and delete EC_EAPD signal and depopulate D113 for EC wake up function./(ECO-CL1-HW-00009)
EC 3A11/Page 4/Remove CPU JTAG Port connector CN16 for EMI solution./(ECO-CL1-HW-00009)
EC 3A12/Page 18/Depopulate D36,R468 and delete R252 and short VIN1 and VIN for eliminate the ability to measure the system current./(ECO-CL1-HW-00009)
EC 3A13/Page 12,15/Rewire the indicator LEDs for conserve power./(ECO-CL1-HW-00009)
EC 3A14/Page 12,18/Eliminating ISYS signal, move SUS_ON to EC GPAD3(pin34) and wire PWR_BUT_in to EC GPIO 1B(pin57) and populate R584,R586 for EC lid open/closed wake up function./(ECO-CL1-HW-00009)
EC 3A15/Page 19/Change U13 from AOZ1013 to AOZ1017 for disable latch function./(ECO-CL1-HW-00009)
EC 3A16/Page 18/Wire R269 to VA2 and change R269 and R270 to 1M and 158K, remove D1 and reserve C612/0.1uF for plugging in an XO with a low battery causes immediate poweroff and EC lockup/#2182/(ECO-CL1-HW-00009)
EC 3A17/Page 20/Reserved D125,D126,D127 for U33,U26,U15 AOZ1016 PWM 2nd source/(ECO-CL1-HW-00009).
EC 3A18/Page 22/Change R344 ~ R351 LCD gamma resistors value for reduce power consumption/(ECO-CL1-HW-00009).

CL1 Schematic EC Tracking Record F (for [C1] --> [C2])Aug 21, 2007)

EC #/Page/Description/#Ticket/(ECO#)

EC 3B00/Page 12/Reserved ESD protect diode on PS/2 interface.(ECO-CL1-HW-00011).
EC 3B01/Page 13/Reserved ESD protect diode on Speaker Out.(ECO-CL1-HW-00011).
EC 3B02/Page 18/Connect U39 charger IC pin 1&24 to REF5V for supplier suggest for unused pin.(ECO-CL1-HW-00011).
EC 3B03/Page 21/Change R345,R346,R347,R349,R350,R351,R357 for new LCD gamma value. (ECO-CL1-HW-00011).
EC 3B04/Page 8/Pop back J1 console port connector. (ECO-CL1-HW-00011).
EC 3B05/Page 12/Change R534 to 16.5K_1% for change M/B ID to reversion C2(ECO-CL1-HW-00011).
EC 3B06/Page 8 /Remove R43 and C56, Add U69 74LVC1G17GW,C96 4.7u ,R61 7.4K, C613 0.1u, D128 CH501H-40PT ,for South Bridge reset delay.
EC 3B07/Page 14/Change Q49 to A03420 Add C517 0.1u for SD card power soft start ,Add R444 4.7K ,C572 0.1u for separate Camera 1.8V and 2.8V power-on sequence,to reduce inrush current .
EC 3B08/Page 19/Change Q1 to A03420 for reduce +3.3V RDSon ,change R22 to 32.4K 1% for adjust +3.3V to 3.36V, Change R20 and R231 to 33K 1% for improve the transience response.
EC 3B09/Page 20/Add R145 10K,C483 0.1u for DCON power soft start.(ECO-CL1-HW-00011).
EC 3B10/Page 16/Reserved C614 150u for improve +3.3V quality.
EC 3B11/Page 11,12,14,15,16,21 /remove Q25 and ,R512,R513,R514,R515,R516,R517,R520,R521 (EC 2B23) current measure resister
EC 3B12/Page 4,7,9,11,15/Add 1000P cap on DCON_2.5V: C281,C286,C292,C297 LCD_1.8V: C298 DCON_1.8V :C309,C310,C314 VMEM: C315,C336,C337,C338,C339 +3.3V C344,C353,C354,C361,C425,C436,C437,C438,C443 WLAN_3.3V: C444,C445,VCORE_SB: C446 VCCUSB:C447.
EC 3B13/Page 20 /Add R587,588 2.2K resister for discharge DCON 1.8V,2.5V power.



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