

HAINAN2

CPU : Intel Merom
Chip Set : RS600M & SB600
Remarks : Mobility Platform

Model Name : HAINAN2
PBA Name : MAIN
PCB Code : BA41-XXXXXA
Dev. Step : PR
Revision : 1.0
T.R. Date : 2006.11.16

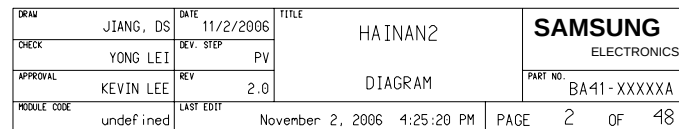
DRAW	CHECK	APPROVAL

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DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	COVER		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	1	OF 48

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Power Diagram

KBC3_SUSPWRON

KBC3_PWRON

KBC3_VRON

VCCP3_PWRGD

AC Adapter

Battery DC

VDC

P12.0V_ALWS

P5.0V_ALWS

P1.2V_ALWS

P3.3V_MICOM

P1.8V_AUX

P1.5V

P1.8V

P1.2V

P0.9V

P5.0V_AUX

P5.0V

P3.3V_AUX

P3.3V

P2.5V_LAN

P1.05V
MEROM
NB
SB
(VCCP_CORE)

CPU_CORE
MEROM

Rail State	+V*Always	+V*AUX	+V	SUSPWR	PWRON	VRON
Full On	ON	ON	ON	H	H	H
S3	ON	ON	OFF	H	L	L
S4	ON	ON	OFF	H	L	L
S5	ON	OFF	OFF	L	L	L

S5 / S4

S3

S0

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	POWER DIAGRAM	PART NO.	BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	3	OF 48

POWER SEQUENCE

VDC
ADT3_SEL
P3.3V_MICOM
KBC3_RST*
P5.0V_ALWS
P1.2V_ALWS
PRTC_BAT

KBC3_PWRSW*

KBC3_SUSPWON

P5V_AUX/P3.3V_AUX/P1.8V_AUX/1.2V_AUX

KBC3_RSMRST*

MEM1_VREF

KBC3_PWRBTN*

CHP3_SLPS5*

CHP3_SLPS3*

KBC3_PWRON

KBC3_CLKPWRGD#

P5.0V

PWRON

P1.8V/P1.5V/P0.9V/P1.2V

KBC3_RUNSCI#

KBC3_VRON

VCCP_CORE

VCCP3_PWRGD

CPU_CORE

VRM3_CPU_PWRGD

KBC3_NBPWRGD

KBC3_SBPWRGD

CPU1_PWRGD CPU

CHP3_ALINK_RST*

PCI3_RST*

CPU1_CPURST*

T1

T2=10mS

T3=40mS

T5=100mS

T16=170mS

T4=120mS

T8=3mS

T14=50mS

T15=30mS

T6=100mS

T8=50mS

T7=15mS

T9=110mS

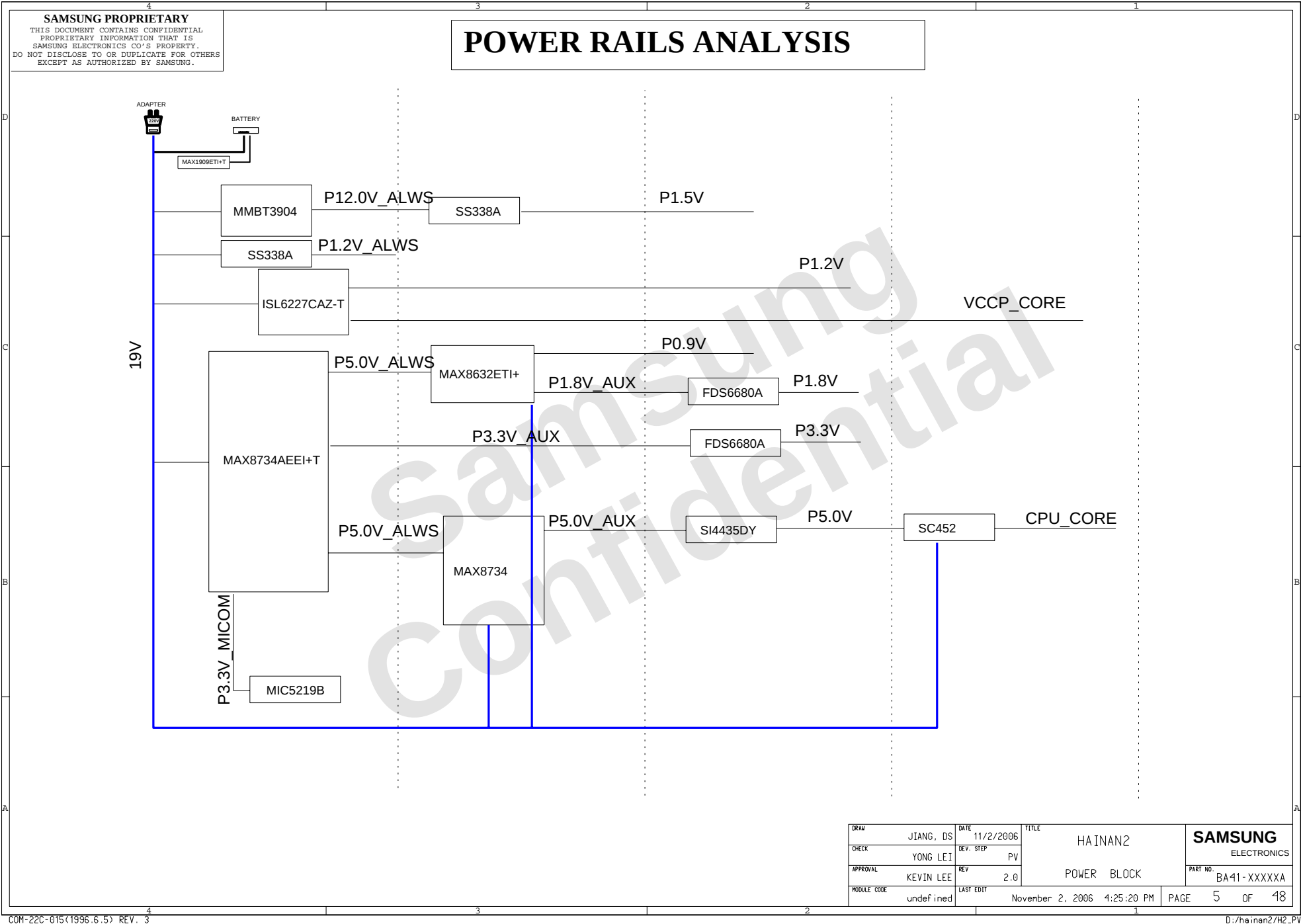
T10=47.40mS

T11=80mS

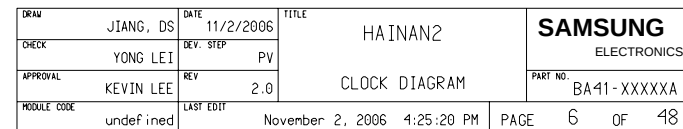
T12=72mS

T13=73mS

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SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
LAN	AD21	1	G
USB	AD30(internal)	-	-
Hub to PCI	AD31(internal)	-	-
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	-
Internal MAC	AD31(internal)	-	-
AC Link	-	-	-

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	SB600	Real Time Clock
Crystal	10MHz	MICOM	HD64F2110B
Crystal	14.318MHz	CLOCK-Generator	ICS95461
Crystal	12MHz	AU6366	4 IN 1 CARD
Crystal	25MHz	LAN	LOM

Voltage Rails

CPU_CORE	CPU
VCCP_CORE	RS600 SB600
P1.2V	RS600 SB600
P1.5V	CPU EXPCARD
P1.8V	RS600
P0.9V	DDR2
P5.0V	FAN THERMAL CRT SB600 AU6366 MINIPCI ALC262 HDD ODD MICOM USB TOUCH-PAD CPU_CORE
P3.3V	SYSTEM POWER
P2.5V_LAN	RTL8100
P1.8V_AUX	DDR2 P1.8V RS600
P5.0V_AUX	THERMAL LCD P1.2V P5.0V
P3.3V_AUX	MDC MICOM MINIPCI EXP-CARD SB600 THERMAL
P12.0V_ALWS	P1.5V P3.3V P1.8V
P5.0V_ALWS	P3.3V DDR2-PWR
P3.3V_MICOM	THERMAL SB600 MICOM LED P1.8V_ALWS
P1.2V_ALWS	SB600,P1.2V
VDC	Primary DC system power supply (7 to 21V)

2 I C / SMB Address

Devices	Address	Hex	Bus
SB600	Master	-	SMBUS Master
EMCN300(CPU Thermal Sensor)	1001 110X	9Ch	Thermal Sensor
SODIMM0	1010 0000	A0h	-
SODIMM1	1010 001X	A2h	-
ICS95411 (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable

USB PORT Assign

PORT NUMBER	ASSIGNED TO
0	SYSTEM PORT A
1,2	SYSTEM PORT B
3	DMB
4	EXPRESS CARD
5	4 IN 1 CARD
6,8	MINI PCIE
7	BT

System Power States

- CHP3_SLPS1* S1, Powered-On-Suspend(POS) : In this state, all clocks(except the 32.768KHz clock) are stopped.
The system context is maintained in system DRAM. Power is maintained to PCI, the CPU, memory controller, memory, and all other critical subsystems.
Note that this state does not preclude power being removed from non-essential devices, such as disk drives. During this state, CPU can be selected for either Deep Sleep or Deeper Sleep.
- CHP3_SLPS3* S3, Suspend-To-RAM(STR) : The system context is maintained in system DRAM, but power is shut off to non-critical circuits.
Memory is retained, and refreshes continue. All clocks stop except RTC clock.
- CHP3_SLP4S* S4, Suspend-To-Disk(STD) : The Context of the system is maintained on the disk. All power is then shut off to the system except for the logic required to resume.
Externally appears same as S5, but may have different wake events.
- CHP3_SLPS5* S5, Soft Off(SOFF) : System context is not maintained. All power is shut off except for the logic required to restart. A full boot is required when waking.

CPU Core Voltage Table IMVP-6

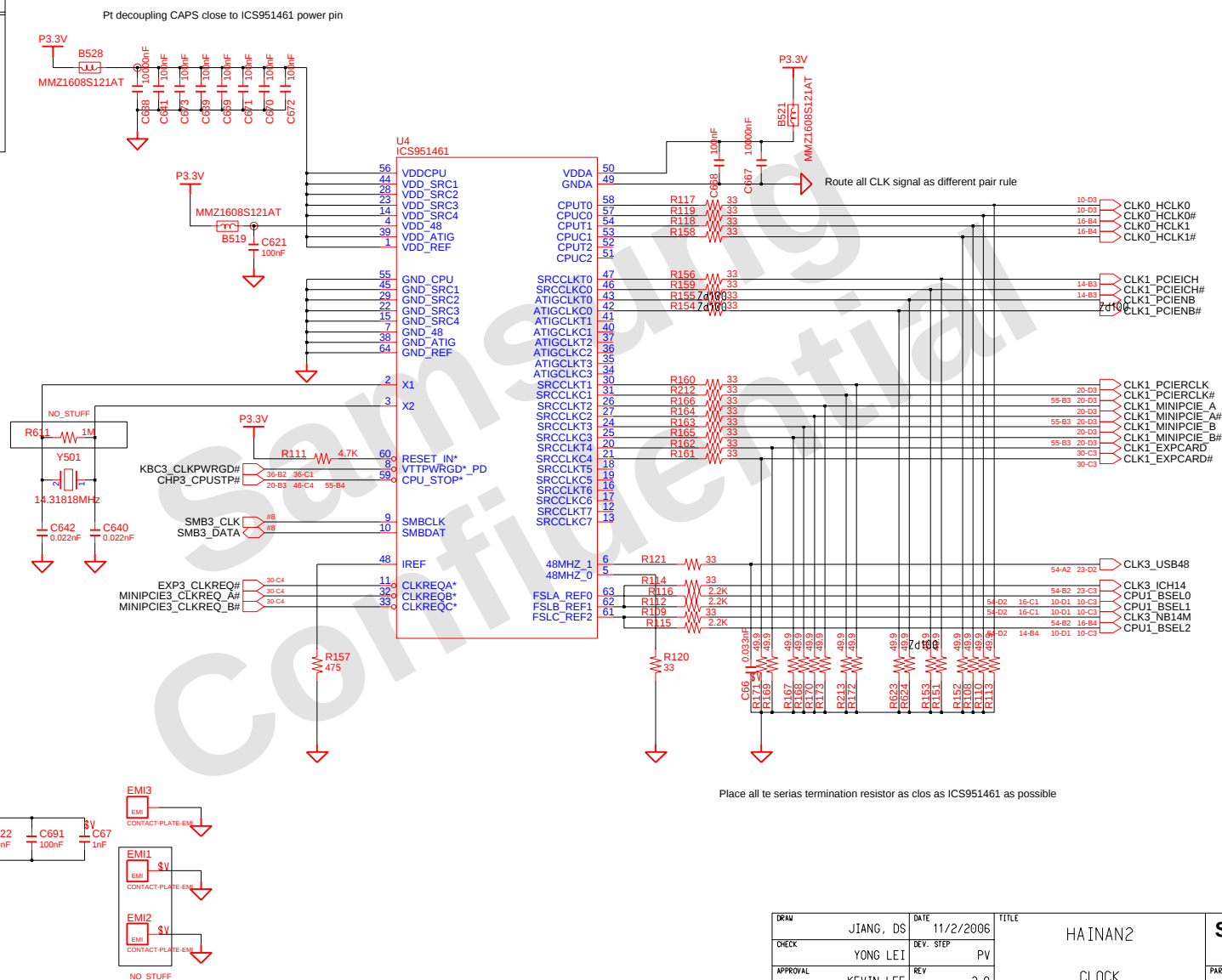
Active Mode		Active/Deeper Sleep Dual Mode Region		Deeper Sleep/Extended Deeper Sleep Dual Mode Region	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0 0	1.0000 V	1 0 1 0 0 0 0 1	0.4875 V
0 0 0 0 0 0 0 1	1.4875 V	0 1 0 1 0 0 0 1	0.9875 V	1 0 1 0 0 0 1 0	0.4750 V
0 0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 0 1 0	0.9750 V	1 0 1 0 0 0 1 1	0.4625 V
0 0 0 0 0 0 1 1	1.4625 V	0 1 0 1 0 1 0 0	0.9625 V	1 0 1 0 0 1 0 0	0.4500 V
0 0 0 0 0 1 0 0	1.4500 V	0 1 0 1 0 1 0 1	0.9500 V	1 0 1 0 0 1 0 1	0.4375 V
0 0 0 0 0 1 0 1	1.4375 V	0 1 0 1 0 1 0 1	0.9375 V	1 0 1 0 0 1 1 0	0.4250 V
0 0 0 0 0 1 1 0	1.4250 V	0 1 0 1 0 1 1 0	0.9250 V	1 0 1 0 0 1 1 1	0.4125 V
0 0 0 0 0 1 1 1	1.4125 V	0 1 0 1 0 1 1 1	0.9125 V	1 0 1 0 0 1 0 0	0.4000 V
0 0 0 0 1 0 0 0	1.4000 V	0 1 0 1 0 0 0 0	0.9000 V	1 0 1 0 1 0 0 0	0.3875 V
0 0 0 0 1 0 0 1	1.3875 V	0 1 0 1 0 0 0 1	0.8875 V	1 0 1 0 1 0 0 1	0.3750 V
0 0 0 0 1 0 1 0	1.3750 V	0 1 0 1 0 0 1 0	0.8750 V	1 0 1 0 1 0 1 0	0.3625 V
0 0 0 0 1 0 1 1	1.3625 V	0 1 0 1 0 0 1 1	0.8625 V	1 0 1 0 1 0 1 1	0.3500 V
0 0 0 0 1 1 0 0	1.3500 V	0 1 0 1 0 1 0 0	0.8500 V	1 0 1 0 1 1 0 0	0.3375 V
0 0 0 0 1 1 0 1	1.3375 V	0 1 0 1 0 1 0 1	0.8375 V	1 0 1 0 1 1 0 1	0.3250 V
0 0 0 0 1 1 1 0	1.3250 V	0 1 0 1 0 1 1 0	0.8250 V	1 0 1 0 1 1 1 0	0.3125 V
0 0 0 0 1 1 1 1	1.3125 V	0 1 0 1 0 1 1 1	0.8125 V	1 0 1 0 0 0 0 0	0.3000 V
0 0 0 1 0 0 0 0	1.3000 V	0 1 0 1 1 0 0 0	0.8000 V	1 0 1 0 0 0 0 1	0.2875 V
0 0 0 1 0 0 0 1	1.2875 V	0 1 0 1 1 0 0 1	0.7875 V	1 0 1 0 0 0 1 0	0.2750 V
0 0 0 1 0 0 1 0	1.2750 V	0 1 0 1 1 0 1 0	0.7750 V	1 0 1 0 0 0 1 1	0.2625 V
0 0 0 1 0 0 1 1	1.2625 V	0 1 0 1 1 0 1 1	0.7625 V	1 0 1 0 0 1 0 0	0.2500 V
0 0 0 1 0 1 0 0	1.2500 V	0 1 0 1 1 1 0 0	0.7500 V	1 0 1 0 0 1 0 1	0.2375 V
0 0 0 1 0 1 0 1	1.2375 V	0 1 0 1 1 1 0 1	0.7375 V	1 0 1 0 0 1 1 0	0.2250 V
0 0 0 1 0 1 1 0	1.2250 V	0 1 0 1 1 1 1 0	0.7250 V	1 0 1 0 0 1 1 1	0.2125 V
0 0 0 1 0 1 1 1	1.2125 V	0 1 0 1 1 1 1 1	0.7125 V	1 0 1 0 1 0 0 0	0.2000 V
0 0 0 1 1 0 0 0	1.2000 V	1 0 0 0 0 0 0 0	0.7000 V	1 0 1 0 1 0 0 1	0.1875 V
0 0 0 1 1 0 0 1	1.1875 V	1 0 0 0 0 0 0 1	0.6875 V	1 0 1 0 1 0 1 0	0.1750 V
0 0 0 1 1 0 1 0	1.1750 V	1 0 0 0 0 0 1 0	0.6750 V	1 0 1 0 1 0 1 1	0.1625 V
0 0 0 1 1 0 1 1	1.1625 V	1 0 0 0 0 0 1 1	0.6625 V	1 0 1 0 1 1 0 0	0.1500 V
0 0 0 1 1 1 0 0	1.1500 V	1 0 0 0 0 1 0 0	0.6500 V	1 0 1 0 1 1 0 1	0.1375 V
0 0 0 1 1 1 0 1	1.1375 V	1 0 0 0 0 1 0 1	0.6375 V	1 0 1 0 1 1 1 0	0.1250 V
0 0 0 1 1 1 1 0	1.1250 V	1 0 0 0 0 1 1 0	0.6250 V	1 0 1 0 1 1 1 1	0.1125 V
0 0 0 1 1 1 1 1	1.1125 V	1 0 0 0 0 1 1 1	0.6125 V	1 0 1 1 0 0 0 0	0.1000 V
0 0 1 0 0 0 0 0	1.1000 V	1 0 0 0 1 0 0 0	0.6000 V	1 0 1 1 0 0 0 1	0.0875 V
0 0 1 0 0 0 0 1	1.0875 V	1 0 0 0 1 0 0 1	0.5875 V	1 0 1 1 0 0 1 0	0.0750 V
0 0 1 0 0 0 1 0	1.0750 V	1 0 0 0 1 0 1 0	0.5750 V	1 0 1 1 0 0 1 1	0.0625 V
0 0 1 0 0 0 1 1	1.0625 V	1 0 0 0 1 0 1 1	0.5625 V	1 0 1 1 0 1 0 0	0.0500 V
0 0 1 0 0 1 0 0	1.0500 V	1 0 0 0 1 1 0 0	0.5500 V	1 0 1 1 0 1 0 1	0.0375 V
0 0 1 0 0 1 0 1	1.0375 V	1 0 0 0 1 1 0 1	0.5375 V	1 0 1 1 0 1 1 0	0.0250 V
0 0 1 0 0 1 1 0	1.0250 V	1 0 0 0 1 1 1 0	0.5250 V	1 0 1 1 0 1 1 1	0.0125 V
0 0 1 0 0 1 1 1	1.0125 V	1 0 0 0 1 1 1 1	0.5125 V	1 0 1 1 1 0 0 0	0.0000 V
		1 0 0 0 1 0 0 0	0.5000 V	1 0 1 1 1 0 0 1	0.0000 V
				1 0 1 1 1 0 1 0	0.0000 V
				1 0 1 1 1 0 1 1	0.0000 V
				1 0 1 1 1 1 0 0	0.0000 V
				1 0 1 1 1 1 0 1	0.0000 V
				1 0 1 1 1 1 1 0	0.0000 V
				1 0 1 1 1 1 1 1	0.0000 V
				1 1 1 1 1 1 1 1	0.0000 V
				**"11111111" : 0V power good asserted.	

*Yonah Processor (2.33 GHz / 800 MHz : TBD)

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEE	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	ANNOTATIONS		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	7	OF 48

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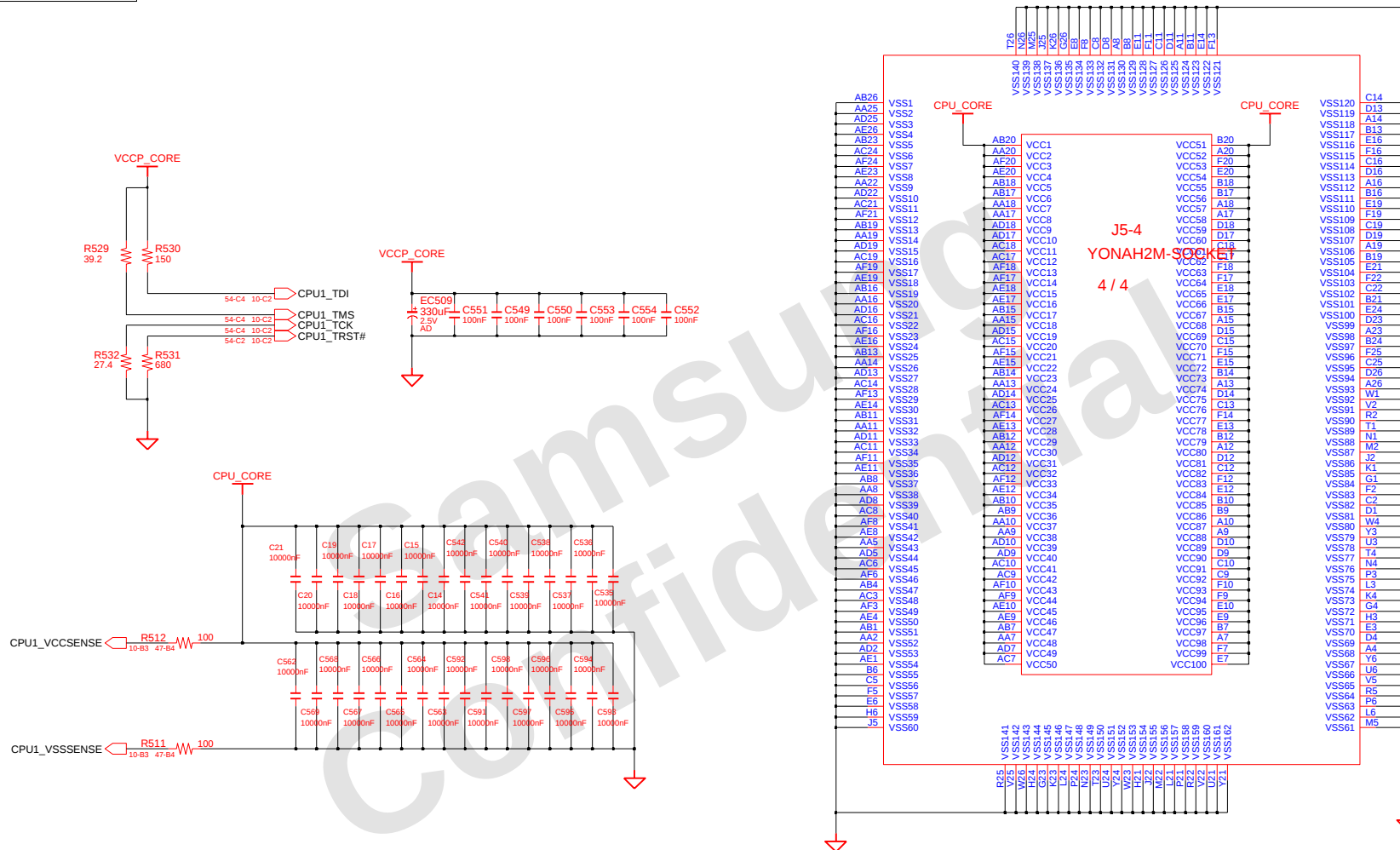
CPU	FSA	FSB	FSC	HOST CLK
	BSEL0	BSEL1	BSEL2	
	0	0	0	266 MHz
	0	0	1	333 MHz
	0	1	0	200 MHz
	0	1	1	400 MHz
	1	0	0	133 MHz
	1	0	1	100 MHz
	1	1	0	166 MHz
	1	1	1	RSVD



DATE	11/2/2006	TITLE		HAINAN2			
DRAW	JIANG, DS						
CHECK	YONG LEI	DEV. STEP	PV	CLOCK		PART NO. BA41-XXXXXA	
APPROVAL	KEVIN LEE	REV	2.0				
MODULE CODE		LAST EDIT		November 2, 2006	4:25:20 PM	PAGE	8 OF 48

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DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		MEROM(3/3)	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	11 OF 48	

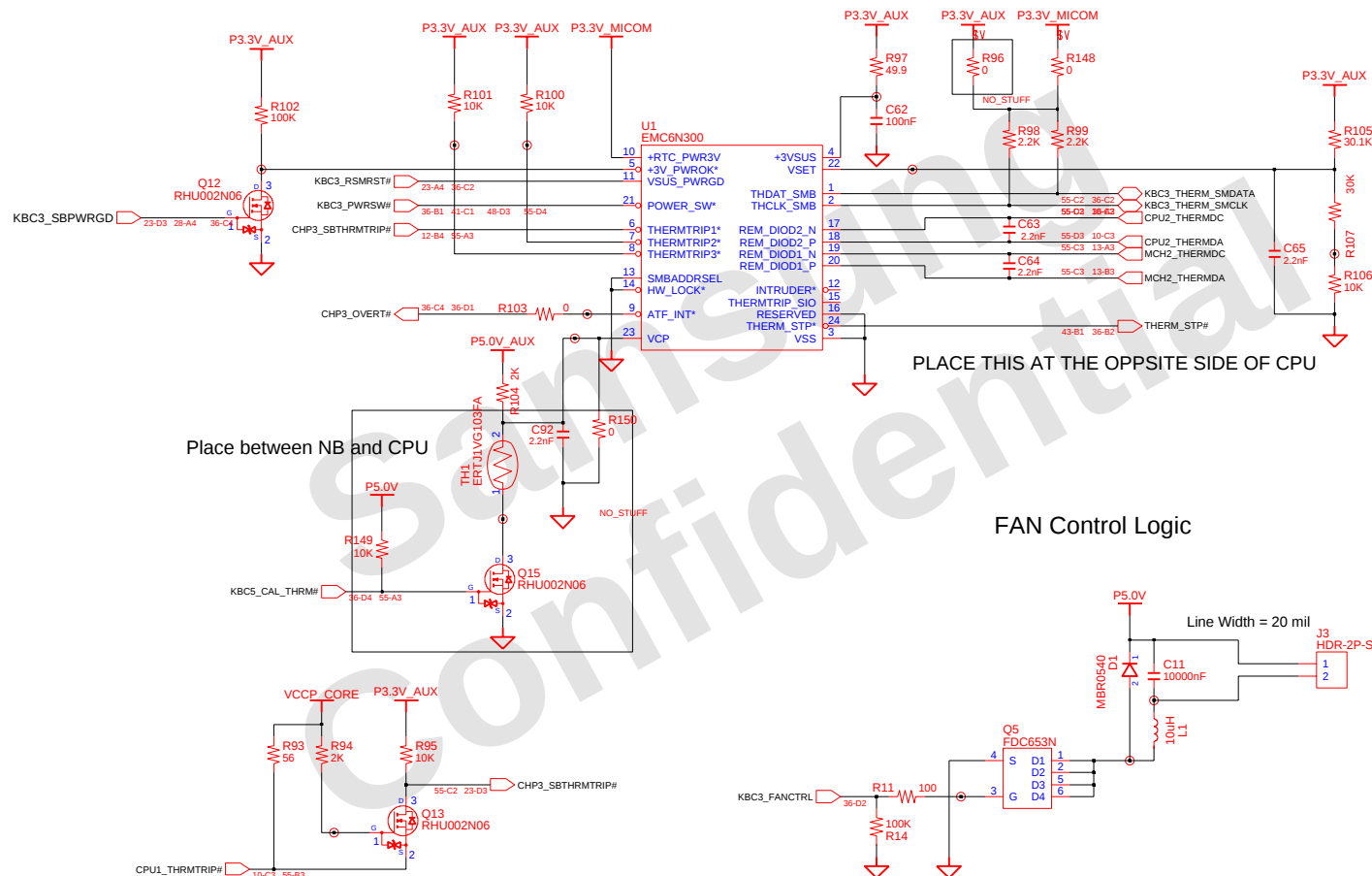
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CPU / RS600 Thermal Sensor

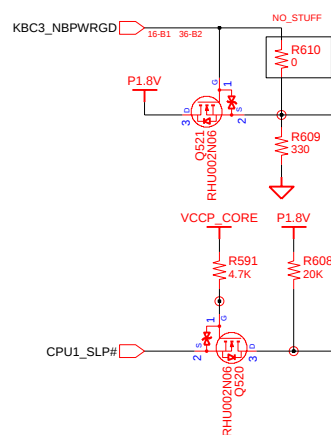
Refer To Thermal Sensor Layout Guidelines.

- Place the Thermal Sensor close to a remote diode.
- Keep traces away from high voltage (+12V bus)
- Keep traces away from fast data buses and CRT signal.
- Use recommended trace widths and spacings (10mil)
- Place a ground plane under the traces.
- Use guard traces flanking DXP and DXN and connecting to GND

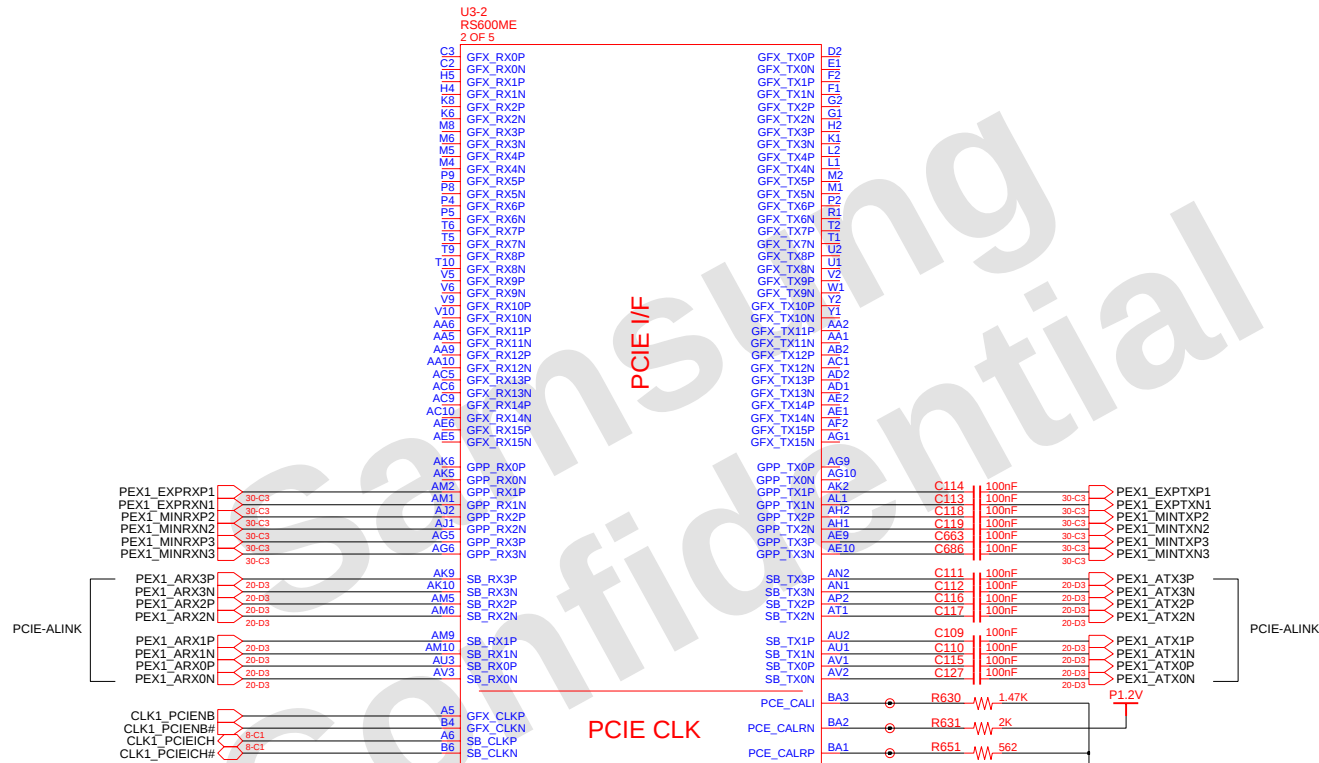


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		THERMAL & FAN	PART NO. BA41-XXXXXA
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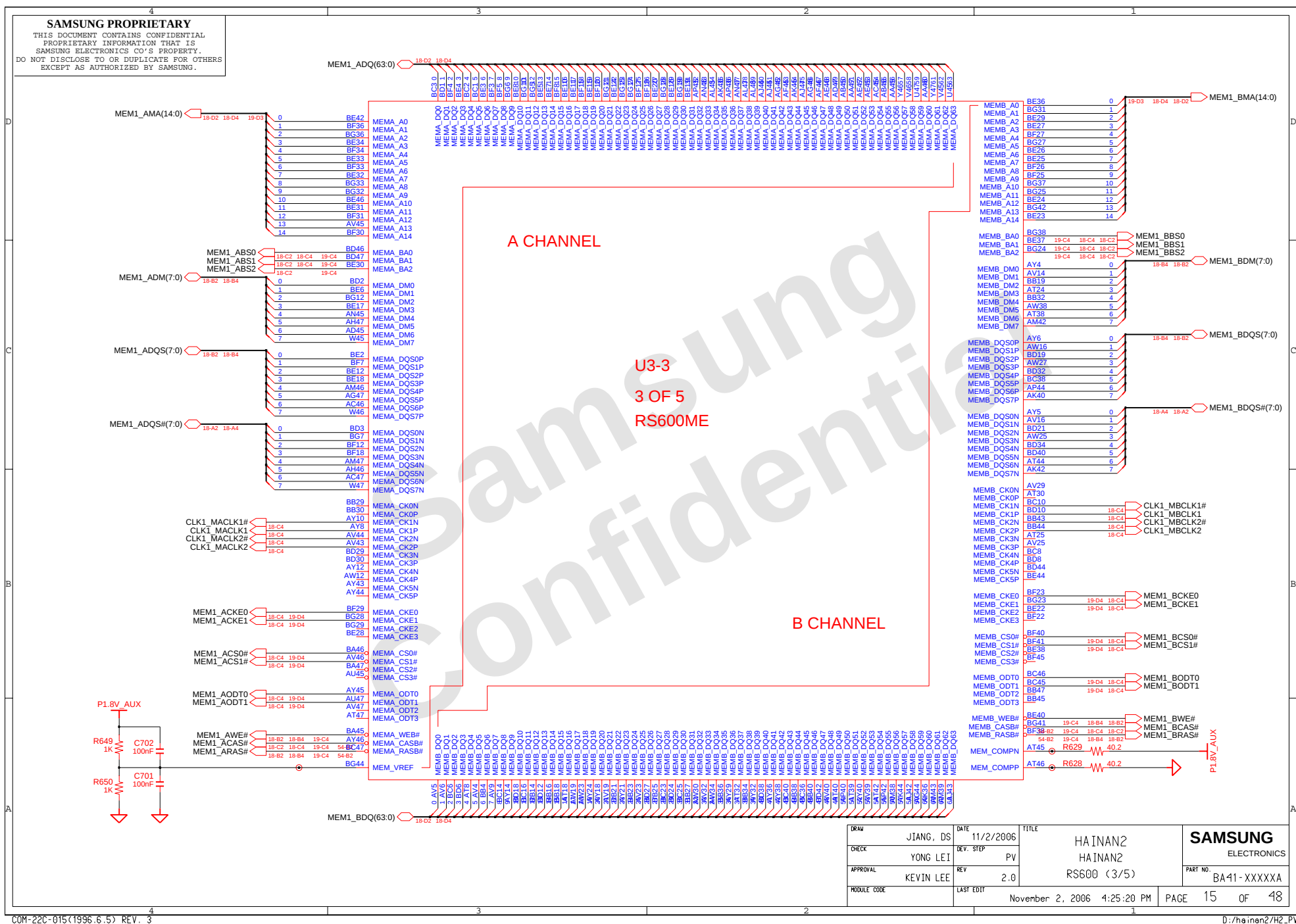


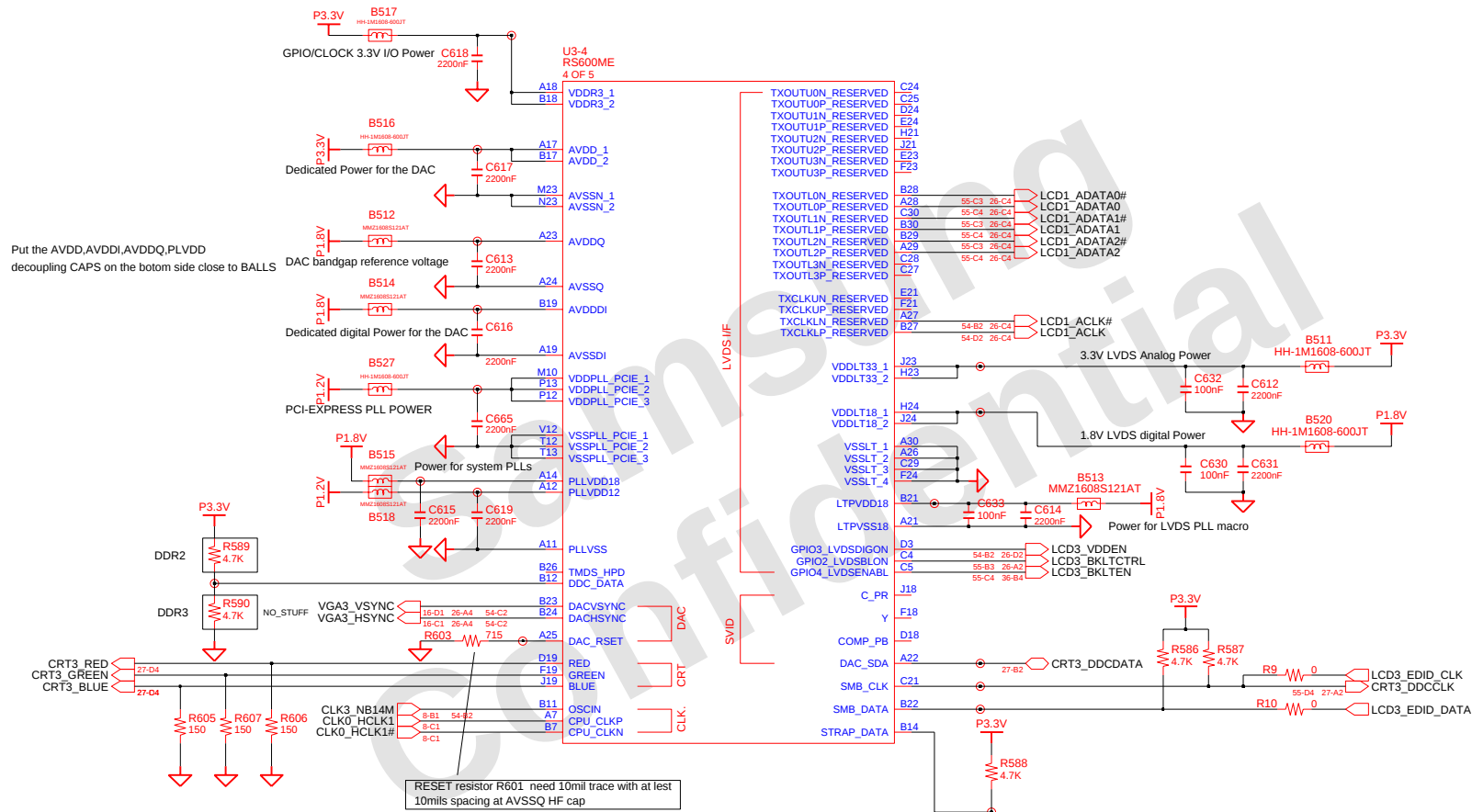
DRAM	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2 HAINAN2 RS600 (1/5)	SAMSUNG ELECTRONICS		
CHECK	YONG LEI	DEV. STEP	PV					
APPROVAL	KEVIN LEE	REV	2.0					
MODULE CODE	LAST EDIT		November 2, 2006 4:25:20 PM				PAGE	13
						PART NO.	BA41-XXXXXA	



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CHECK	YONG LEI	DEV. STEP	PV		HAINAN2	
APPROVAL	KEVIN LEE	REV	2.0		RS600 (2/5)	
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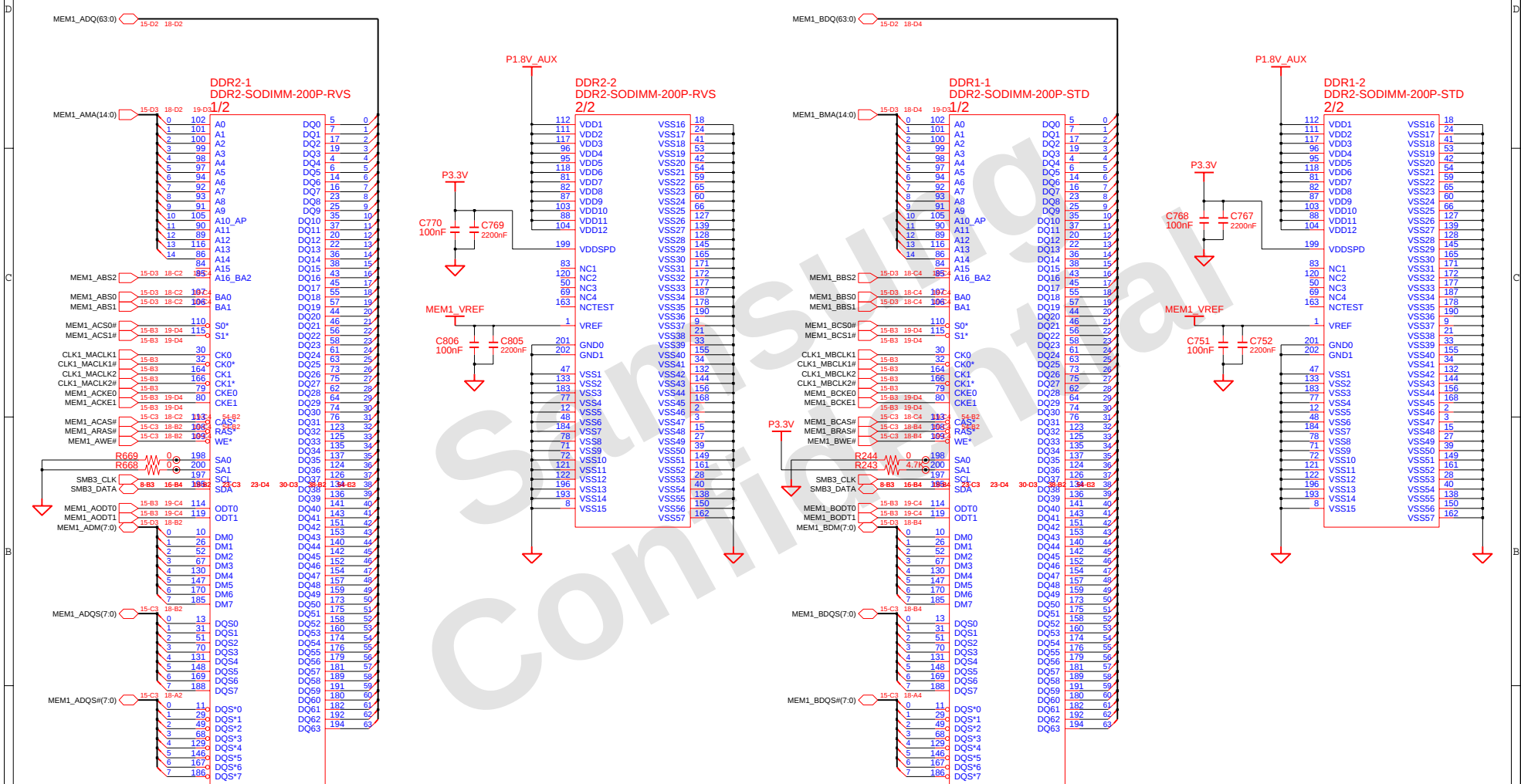




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CHECK	YONG LEI	DEV. STEP	PV		HAINAN2	
APPROVAL	KEVIN LEE	REV	2.0		RS600 (4/5)	
MODULE CODE		LAST EDIT	November 2, 2006 4:25:20 PM			
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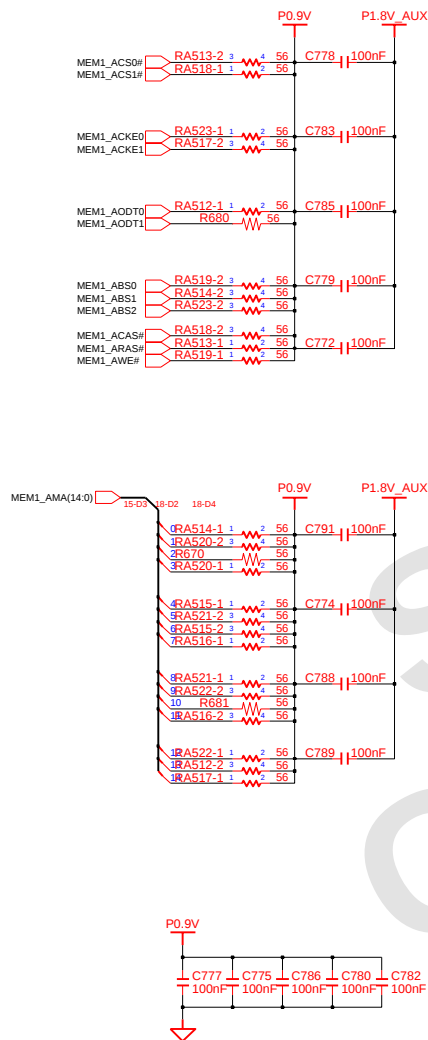


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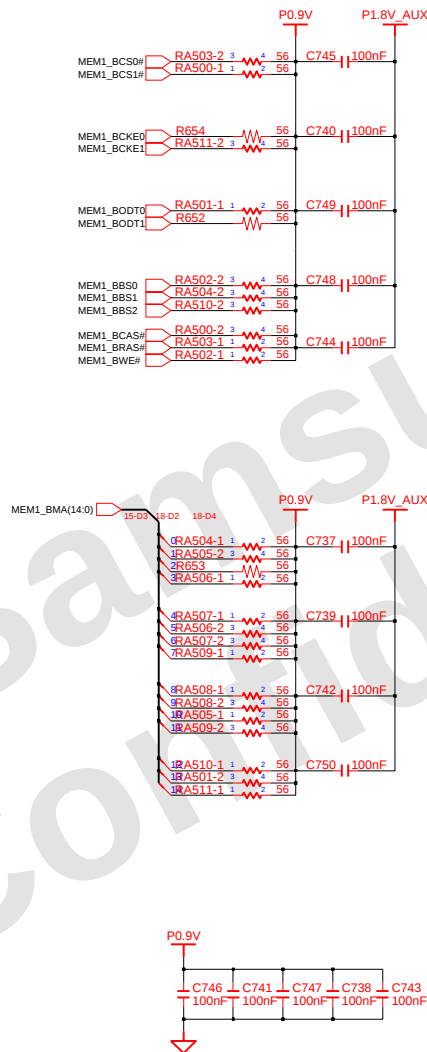


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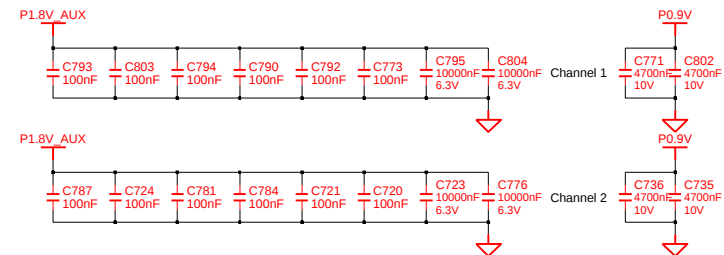
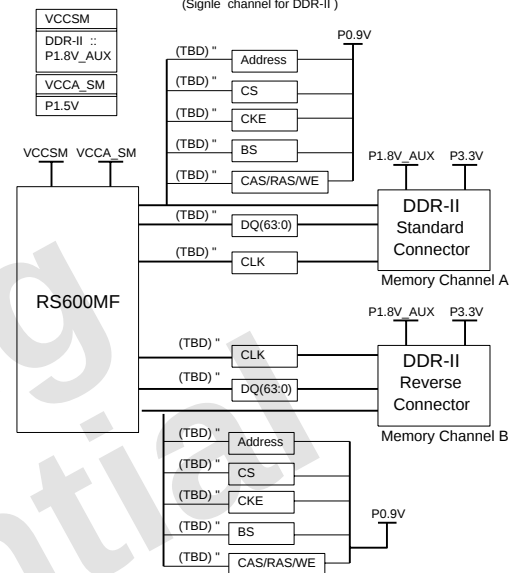
Channel 1



Channel 2

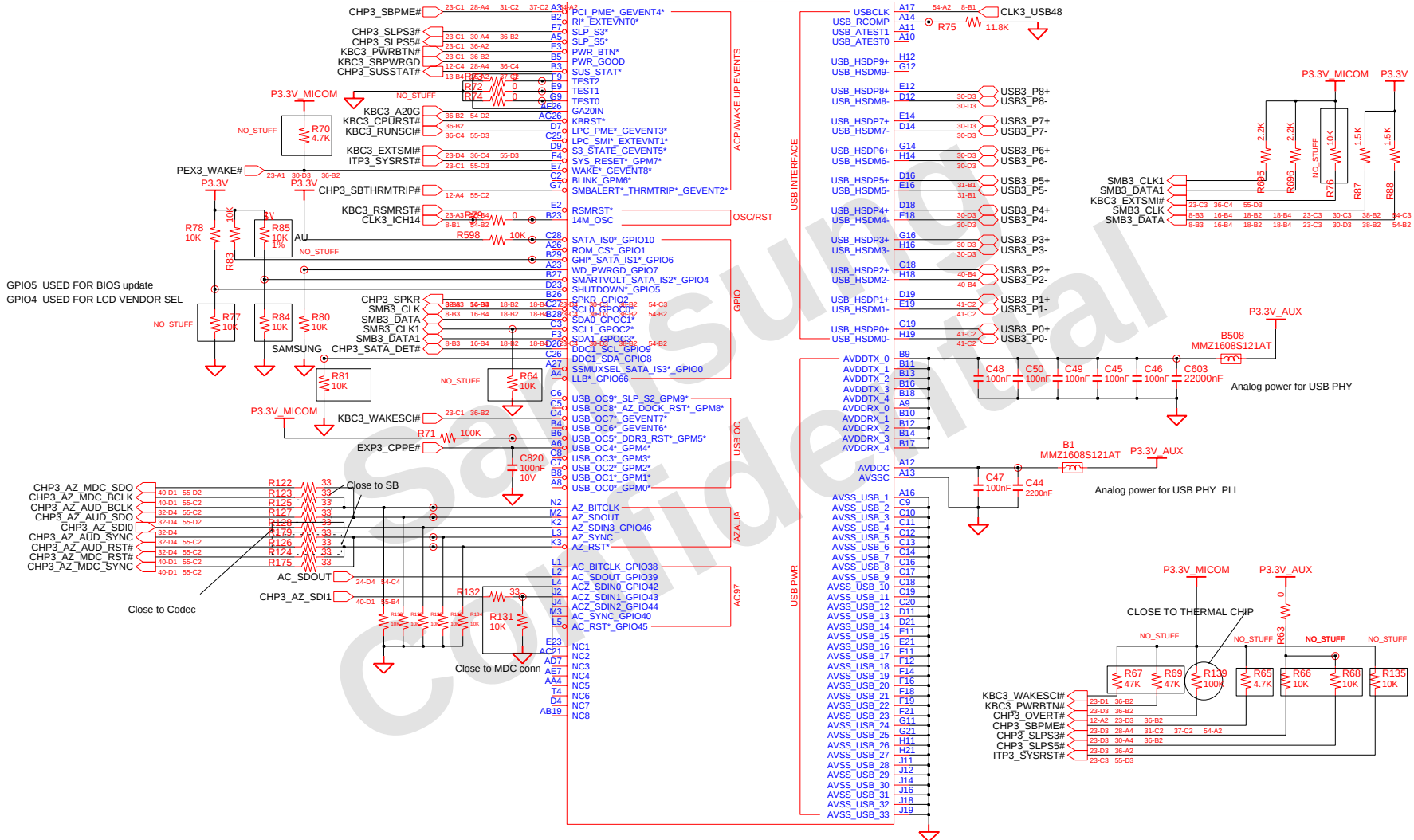


Memory Topology



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		DDR2 TERMINATION	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	19 OF 48	

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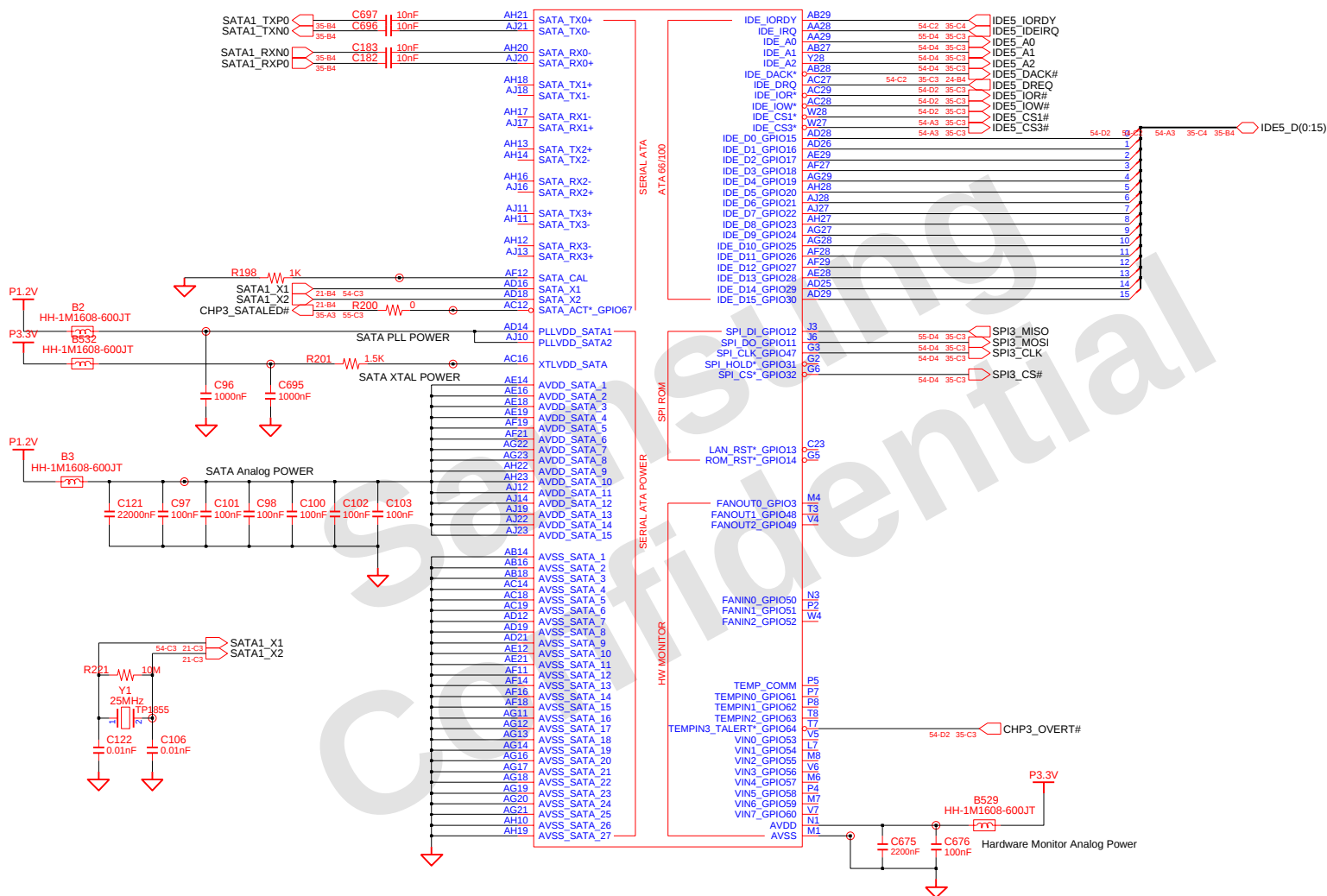


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CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SB600(2/4)		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	21 OF 48	

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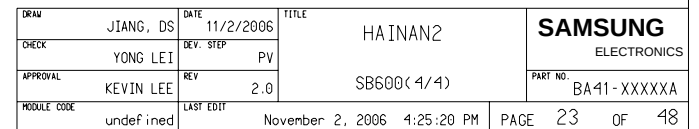
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U510-3
218S6ECLA21FG
3 / 4

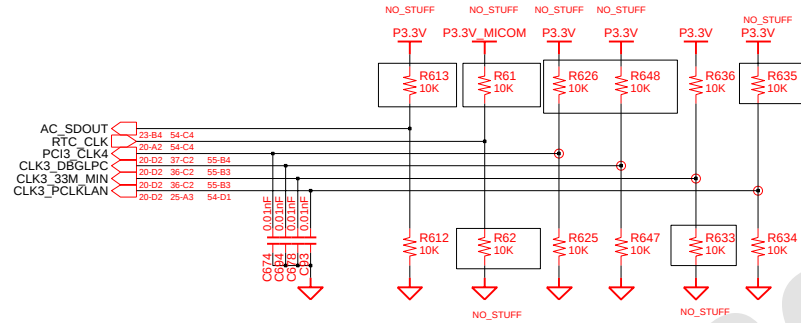


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SB600(3/4)		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	22	OF 48

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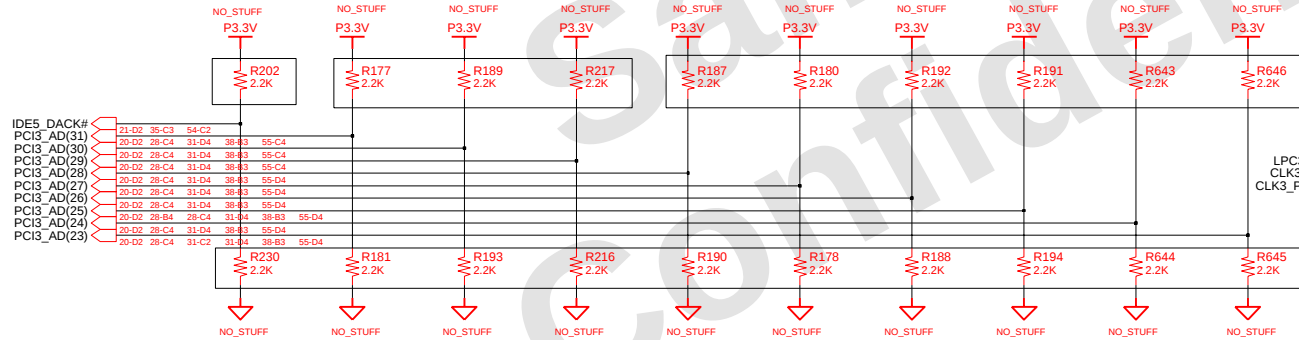


REQUIRED SYSTEM STRAPS



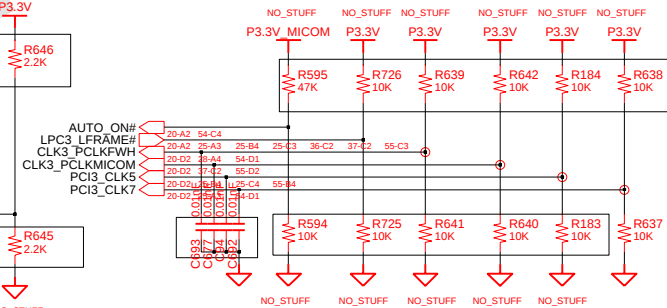
	AC_SDOUT	RTC_CLK	PCI3_CLK4	CLK3_DBG LPC	CLK3_33M_MIN, CLK3_PCLKLAN
STRAP HIGH	USE DEBUG STRAPS	INTERNAL RTC DEFAULT	USE INT PLL 48	CPU I/F = K8	ROM TYPE H.H = PCI ROM H.L = SPI ROM L.H = LPC ROM L.L = FWH ROM
STRAP LOW	IGNORE DEBUG STRAPS DEFAULT	EXRERNAL RTC	USE EXT 48MHZ DEFAULT	CPU I/F = P4 DEFAULT	

DEBUG STRAPS



	IDE5_DACK*	PCI3_AD(31)	PCI3_AD(30)	PCI3_AD(29)	PCI3_AD(28)	PCI3_AD(27)	PCI3_AD(26)	PCI3_AD(25)	PCI3_AD(24)	PCI3_AD(23)
STRAP HIGH	RESERVED	RESERVED	RESERVED	RESERVED	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	BOOTF ALL TIMER DISABLED DEFAULT
STRAP LOW					USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	BYPASS EEPROM PCIE STRAPS	BOOTF ALL TIMER ENABLED

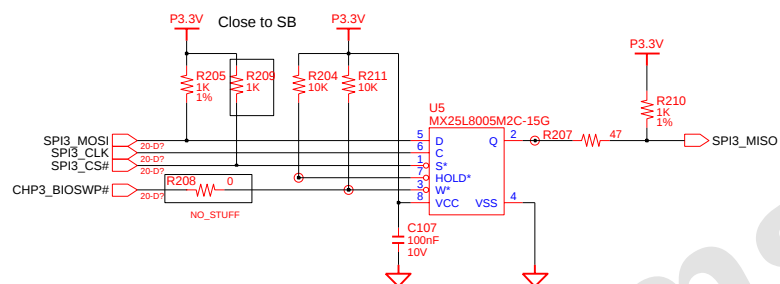
RESERVED PORT DEBUG



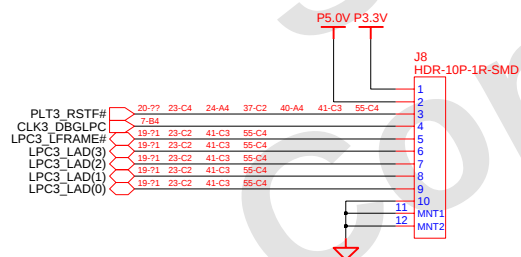
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	SB600 STRAPS		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	24 OF 48	

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DEBUG CARD CONN

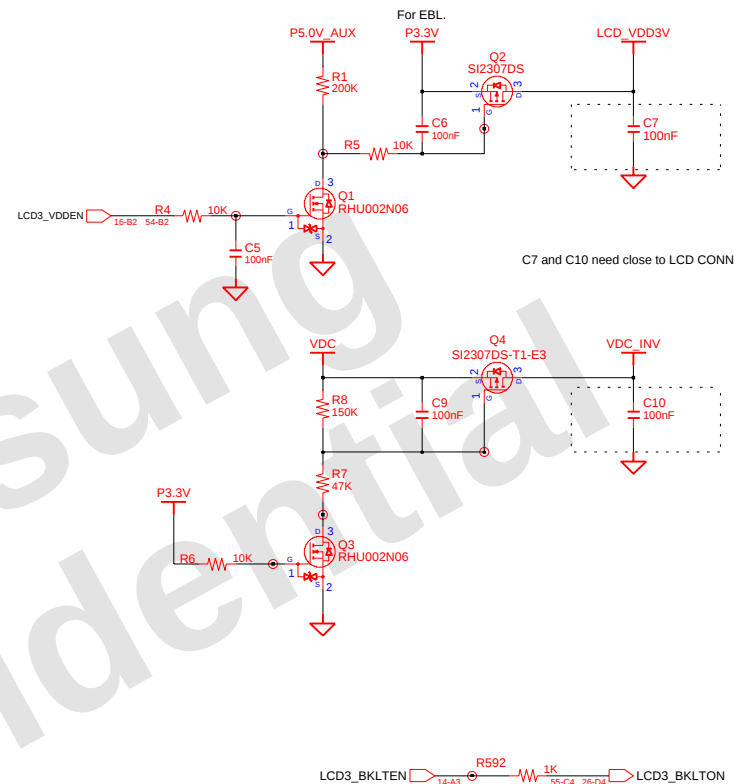
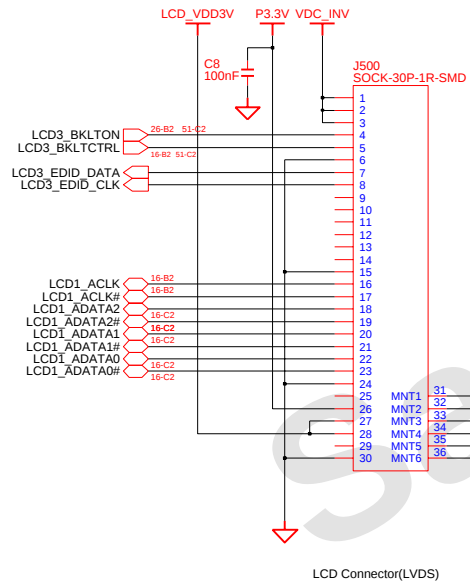


- | | | | |
|----|---|----|---------------------------------|
| 02 | VERIFY REAL MODE | 66 | CONFIGURE ADVANCE CACHE REG. |
| 03 | DISABLE NMI | 6A | DISPLAY EXTERNAL CACHE SIZE |
| 04 | GET CPU TYPE | 6C | DISPLAY SHADOW MESSAGE |
| 06 | INIT. SYSTEM H/W | 6E | DISPLAY NON-DISPOSABLE SEGMENT |
| 08 | INIT. CHIPSET REG. | 70 | DISPLAY ERROR MESSAGE |
| 09 | SET IN POST FLAG | 72 | CHECK FOR CONFIGURATION ERROR |
| 0A | INIT CPU.REG | 74 | TEST REAL-TIME CLOCK |
| 0B | CPU CACHE ON | 76 | CHECK FOR KEYBOARD ERROR |
| 0C | INIT.CACHE TO POST | 7C | SETUP HARDWARE INTERRUPT VECTOR |
| 0E | INIT. I/O VALUE | 7E | TEST COPROCESSER IF PRESENT |
| 0F | ENABLE THE L-BUS IDE | 80 | DISABLE ON-BOARD I/O PORT |
| 10 | INIT. POWER MANAGER | 82 | DETECT AND INSTALL EXT.RS232C |
| 11 | LOAD ALTERNATE REG. | 84 | DETECT AND INSTALL EXT.PARALLEL |
| 13 | PCI BUS MASTER RESET
WITH INITIAL POST VALUE | 86 | RE-INIT. ON-BOARD I/O PORT |
| 14 | INIT. KEYBOARD CONTROLLER | 88 | INIT. BIOS DATA ROM |
| 16 | CHECK CHECKSUM | 8A | INIT.EXTENDED BIOS DATA AREA |
| 18 | 8254 TIMER INIT. | 8C | INIT. FDD CONTROLLER |
| 1A | 8237 DMA CONTROLLER INIT. | 9A | SHADOW OPTION ROMS |
| 1C | RESET INTERRUPT CONTROLLER | 9C | SETUP POWER MANAGEMENT |
| 20 | TEST DRAM REFRESH | 9E | ENABLE H/W INTERRUPT |
| 22 | TEST 8742 KEYBOARD CONTROLLER | A0 | SET TIME OF DAY |
| 24 | SET ES SEGMENT REG. TO 4GB | A4 | INIT. TYPEMATIC RATE |
| 26 | ENABLE A20 | A8 | ERASE F2 PROMPT |
| 28 | AUTO SIZING DRAM | AA | SCAN FOR F2 KEY STROKE |
| 32 | COMPUTE THE CPU SPEED | AC | ENTER SETUP |
| 34 | TESET CMOS RAM | AE | CLEAR IN POST FLAG |
| 38 | SHADOW SYSTEM BIOS ROM | B0 | CHECK FOR ERRORS |
| 3A | AUTO SIZING CACHE | B2 | POST DONE-PREPARE TO BOOT O/S |
| 3C | CONFIGURE ADVANCED CHIPSET REG. | B4 | ONE BEEP |
| 3D | LOAD ALTER REG. WITH CMOS VALUE | B6 | CHECK PASSWORD (OPTION) |
| 42 | INIT. INTERRUPT VECTOR | B7 | ACPI INIT |
| 44 | INIT. BIOS INTERRUPT | BA | DMI INIT |
| 46 | CHECK ROM COPYRIGHT NOTICE | BE | CLEAR SCREEN |
| 47 | INIT. I20 SUPPORT IF INSTALLED | C0 | TRY BOOT WITH INT19 |
| 48 | CHECK VIDEO CONFIGURE AGAINST CMOS | D0 | INTERRUPT HANDLER ERROR |
| 49 | INIT. PCI BUS AND DEVICE | D2 | UNKNOWN INTERRUPT ERROR |
| 4A | INIT. ALL VIDEO BIOS ROM | D4 | PENDING INTERRUPT ERROR |
| 4C | SHADOW VIDEO BIOS ROM | D6 | SHUTDOWN 5 |
| 50 | DISPLAY CPU TYPE AND SPEED | D8 | SHUTDOWN ERROR |
| 52 | TEST KEYBOARD | DA | EXTENDED BLOCK MOVE |
| 54 | SET KEYCLICK IF ENABLED | DC | SHUTDOWN 10 |
| 56 | ENABLE KEYBOARD | 89 | ENABLE NMI |
| 58 | TEST FOR UNEXPECTED INTERRUPTS | 90 | INIT. HDD CONTROLLER |
| 5A | DISPLAY "PRESS SETUP" | 91 | INIT. LOCAL BUS HDD CONTROLLER |
| 5C | TEST RAM BETWEEN 512K AND 640K | 92 | JUMP TO USER PATCH 2 |
| 60 | TEST EXTENDED MEMORY | 94 | DISABLE A20 ADDRESS LINE |
| 62 | TEST EXTENDED MEMORY ADDRESS LINE | 96 | CLEAR HUGE ES SEGMENT REG. |
| 64 | JUMP TO USER PATCH 1 | 98 | SEARCH FOR OPTION ROMS |

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	FWH		PART NO.
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM			BA41-XXXXXA
				PAGE	25	OF 48

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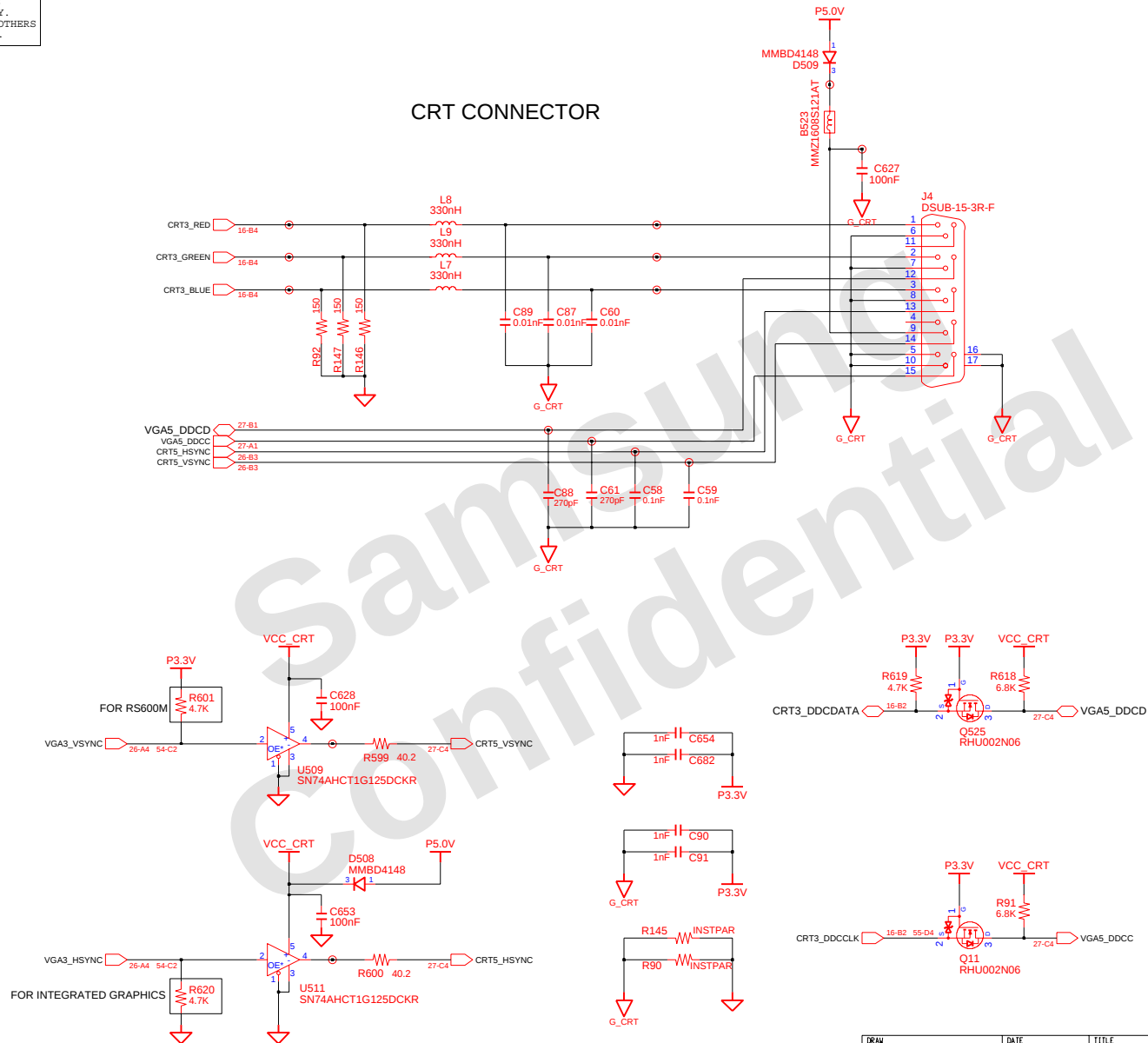
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DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		LCD	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	26	OF 48

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CRT CONNECTOR

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		CRT	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	27	OF 48

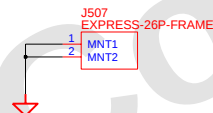
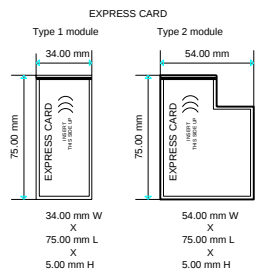
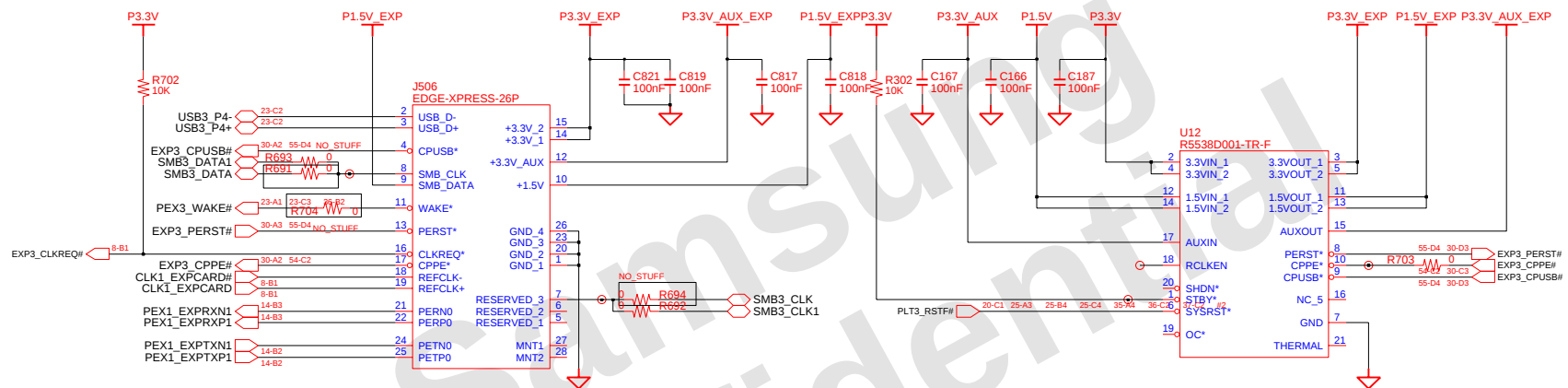
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[illegible]

DRAW	JIANG, DS	DATE	11/2/2006	TITLE		HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV				
APPROVAL	KEVIN LEE	REV	2.0	4 IN 1 CARD		PART NO.	BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM				

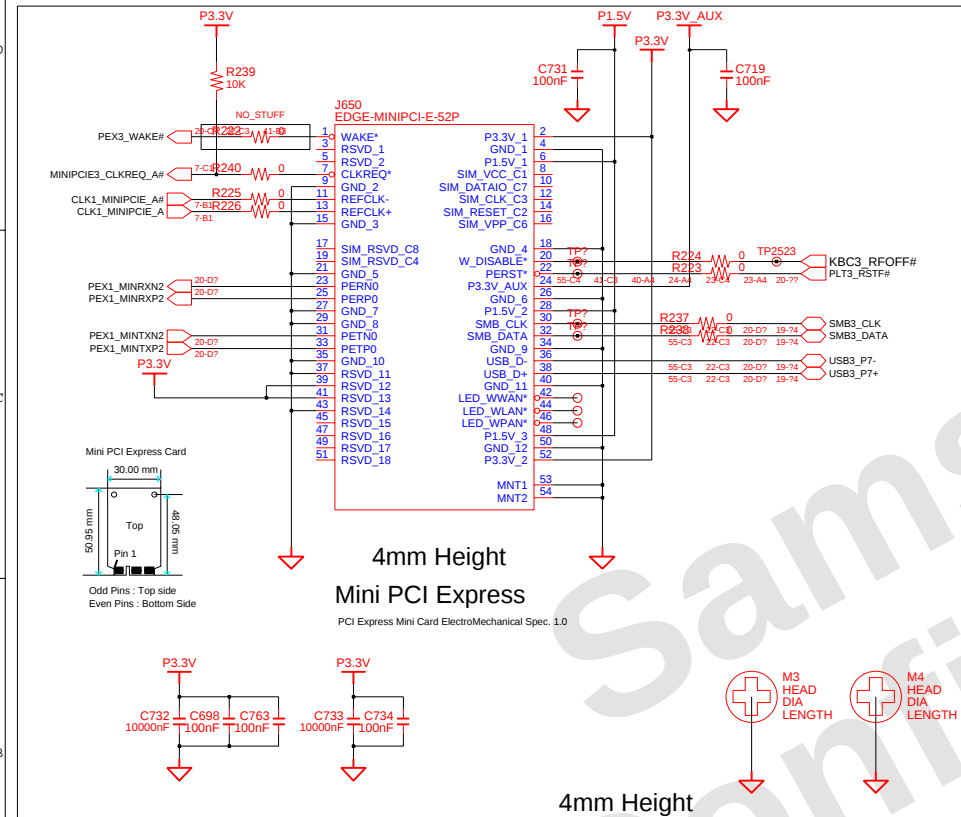
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EXPCARD

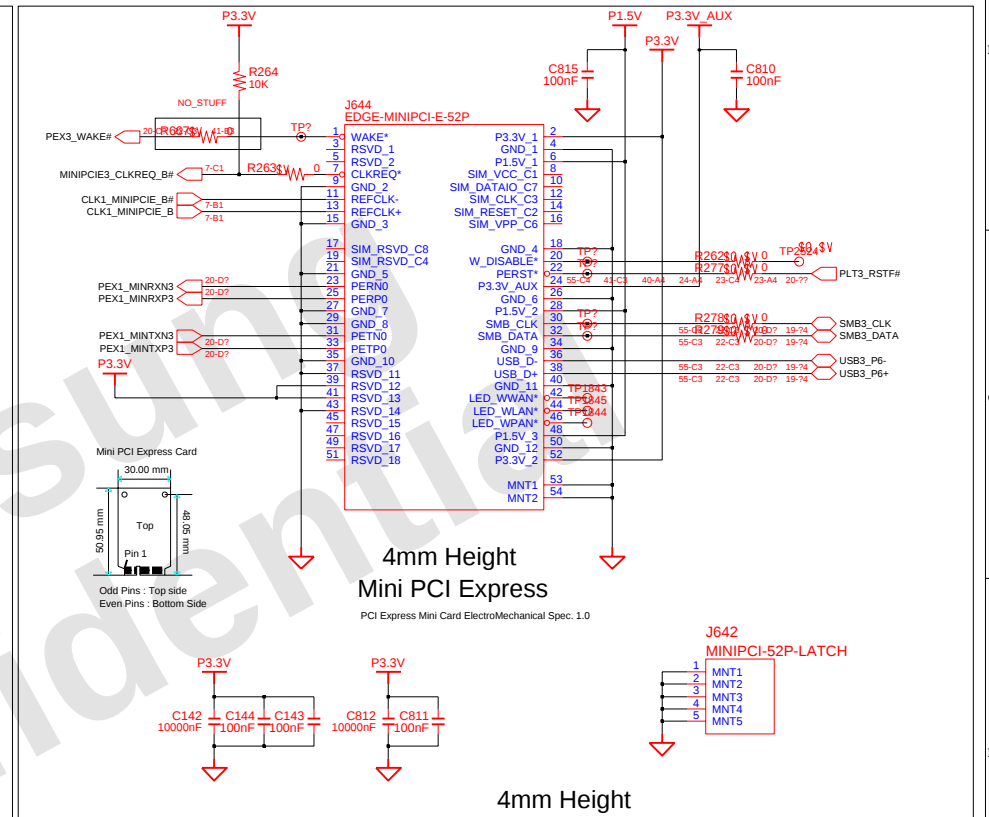


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	EXPRESS CARD	PART NO.	BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	29	OF 48

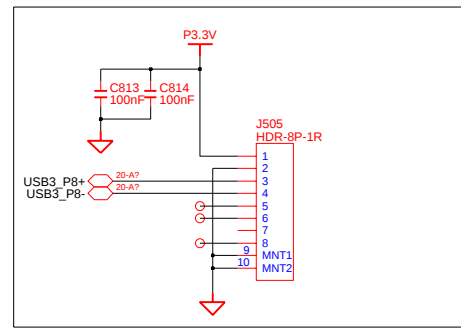
MINICARD



DVB-T

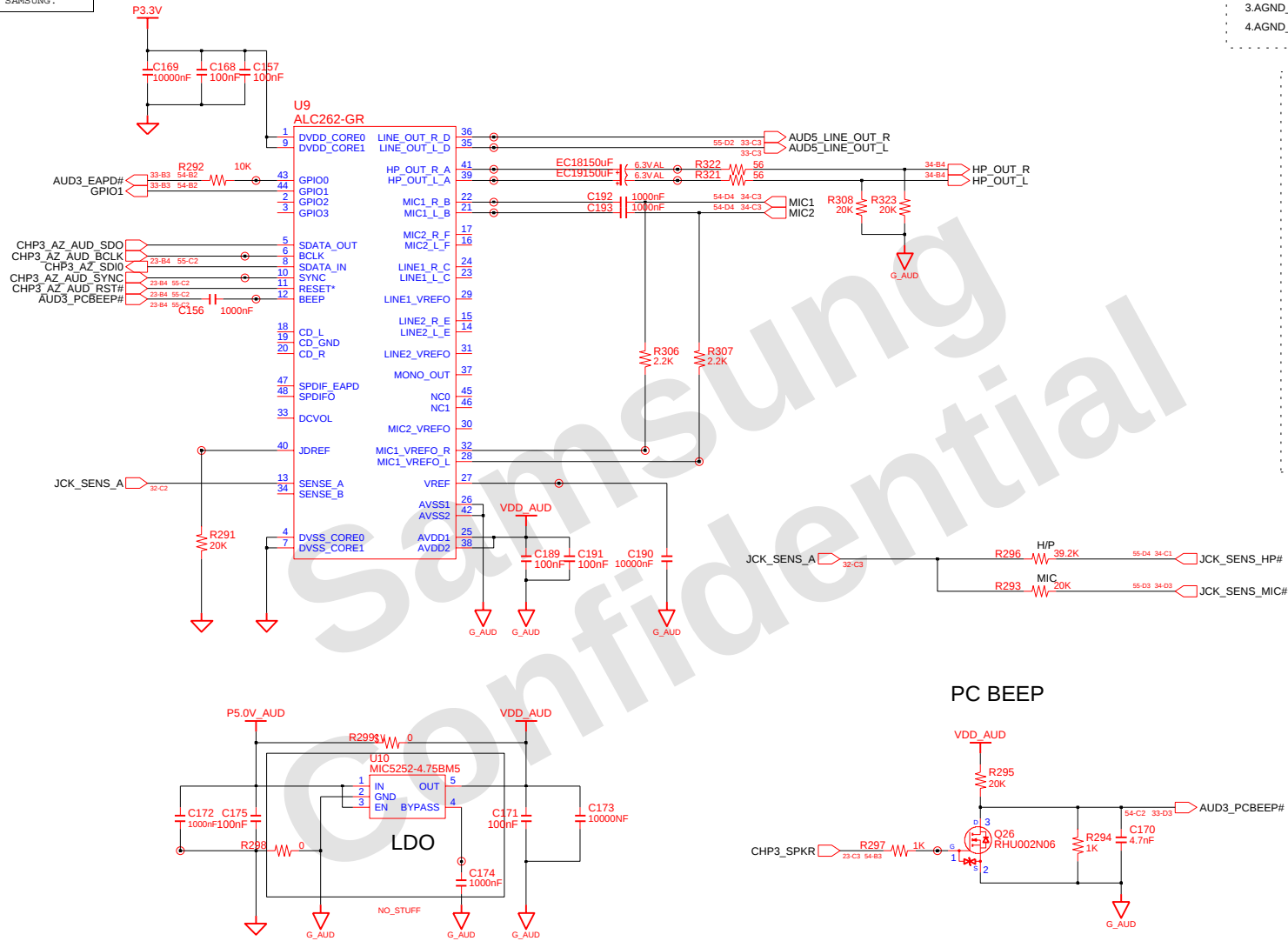


Bluetooth Interface



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2 HAINAN2 MINI CARD	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			PART NO. BA41-XXXXXA
APPROVAL	KEVIN LEE	REV	2.0			
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	30	OF 48

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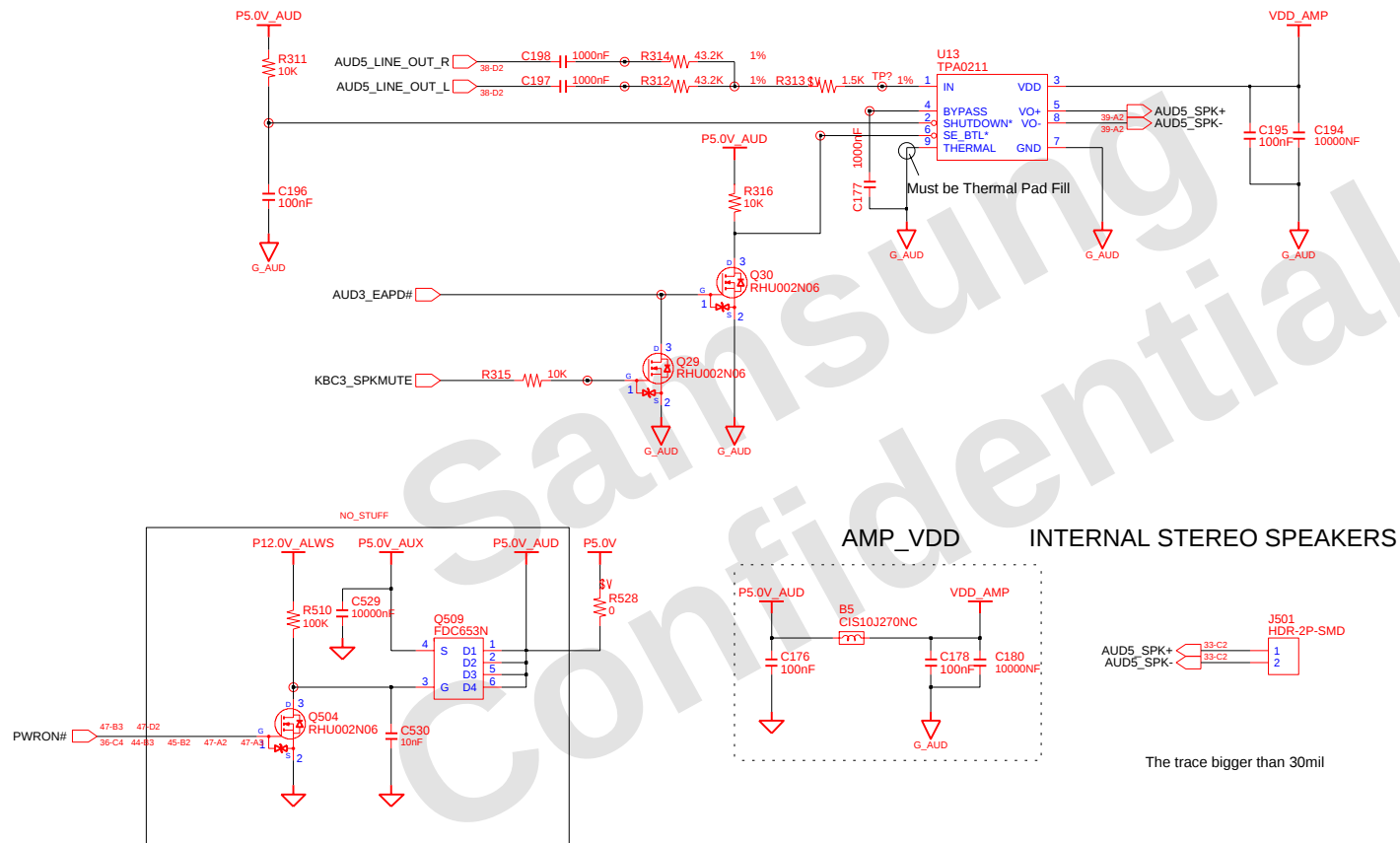
- 1.AGND_AUD IS AUDIO GROUND
2. GND IS DIGITAL GROUND
- 3.AGND_MIC IS MIC GROUND
- 4.AGND_CHS IS CHASS GROUND

ALL TYPE IS 1608

PC BEEP

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	AUDIO(1/3)		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	31 OF 48	

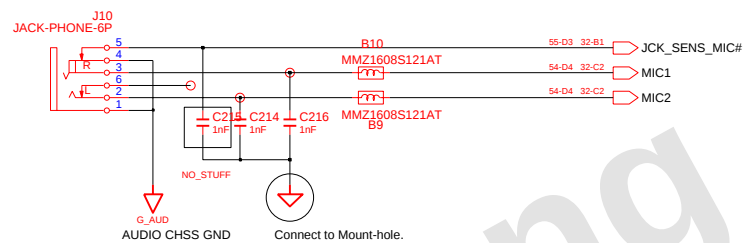
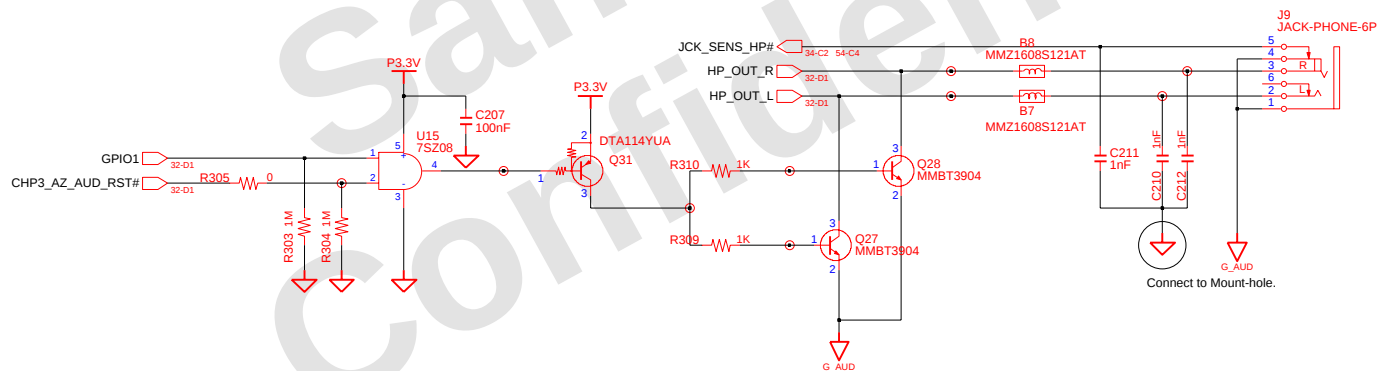
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DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		AUDIO<2/3>	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	32	OF 48

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MIC JACK**HEADPHONE**

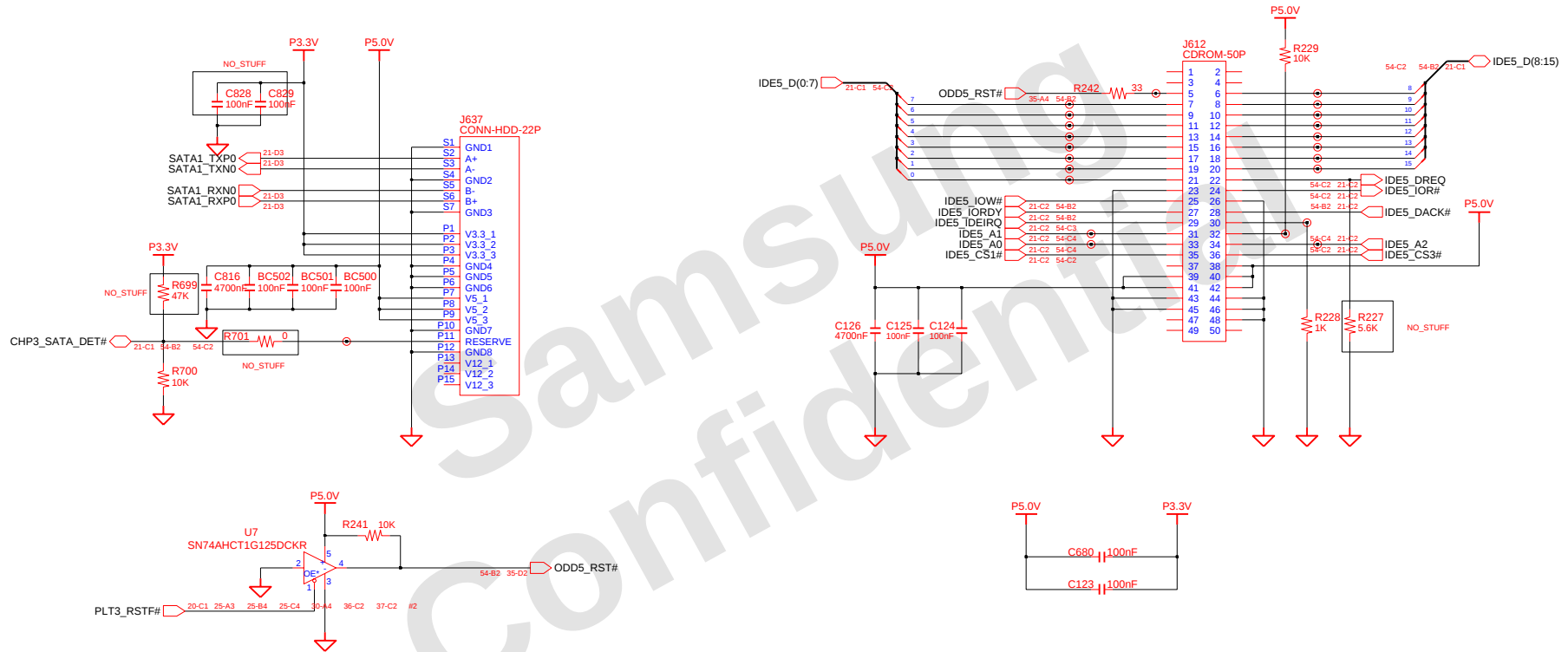
The traces led to Audio Jacks have the width over 10mil

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		AUDIO<3/3>	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	33	OF 48

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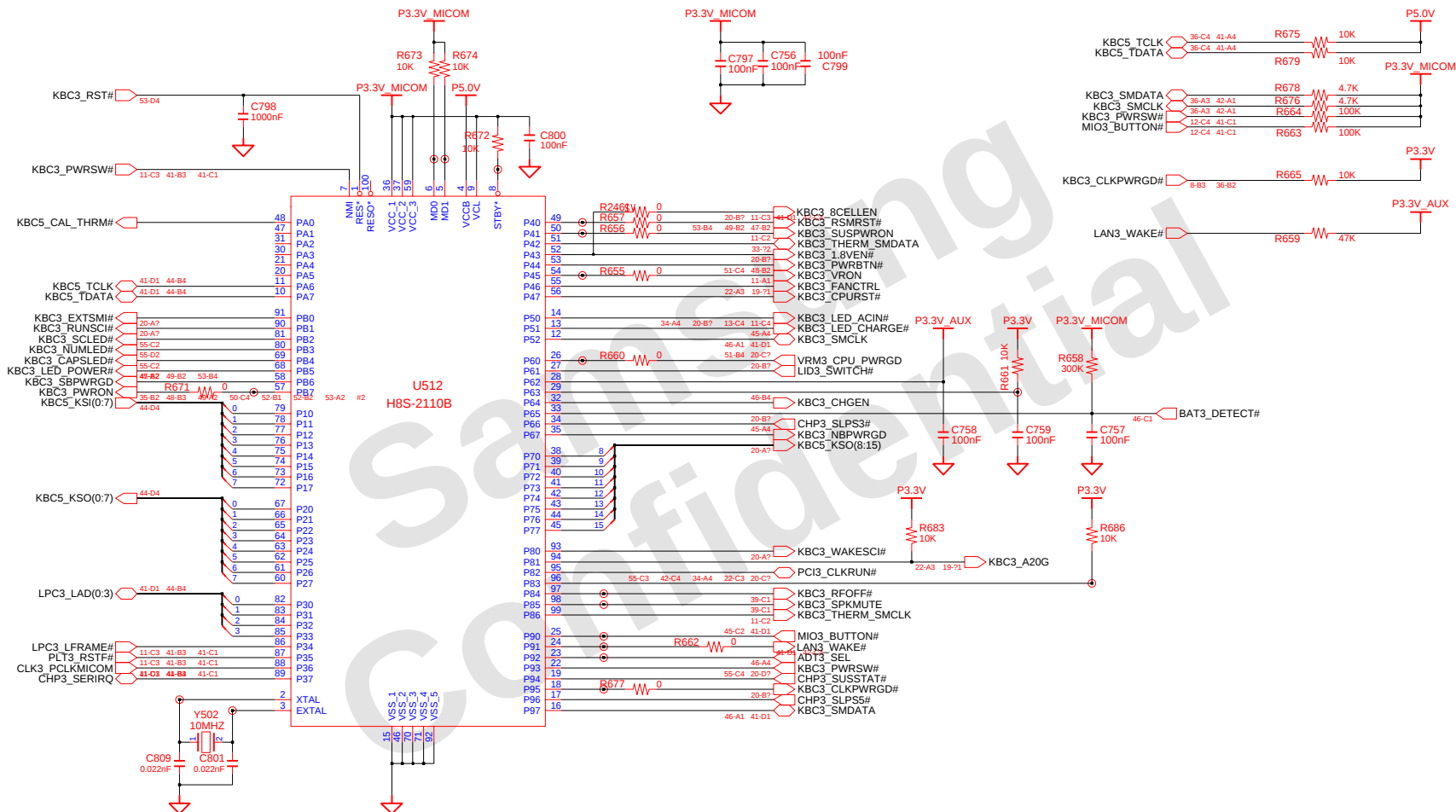
Main to HDD

Main to ODD



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	HDD & ODD		
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	34 OF 48	

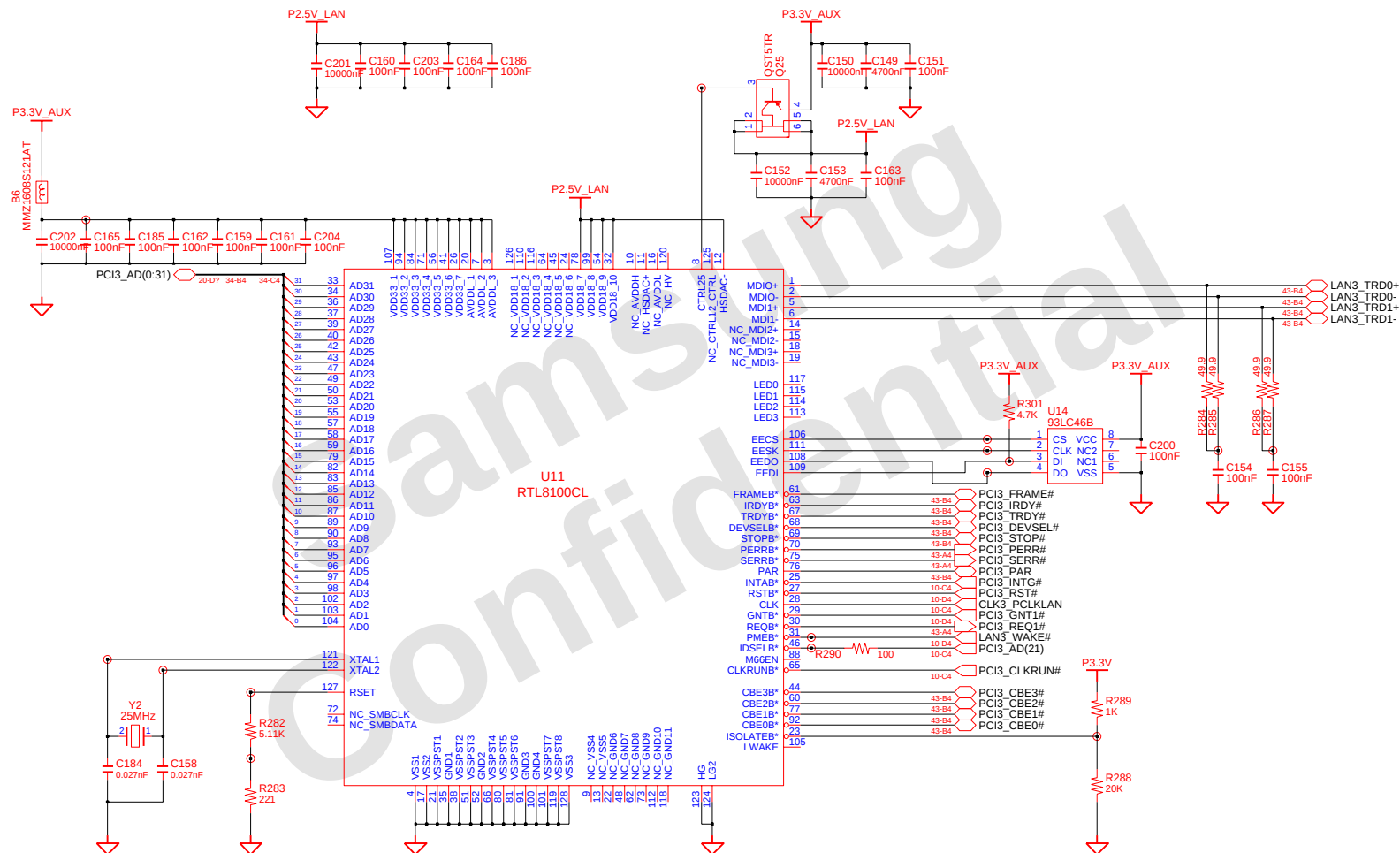
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TP FOR UPDATE MICOM:
P84=>TXD;P85=>RXD
MD0 and MD1 =>GND
P90,P91,P92=>VCC

DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	MICOM		PART NO.
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM			BA41-XXXXXA
				PAGE	35	OF 48

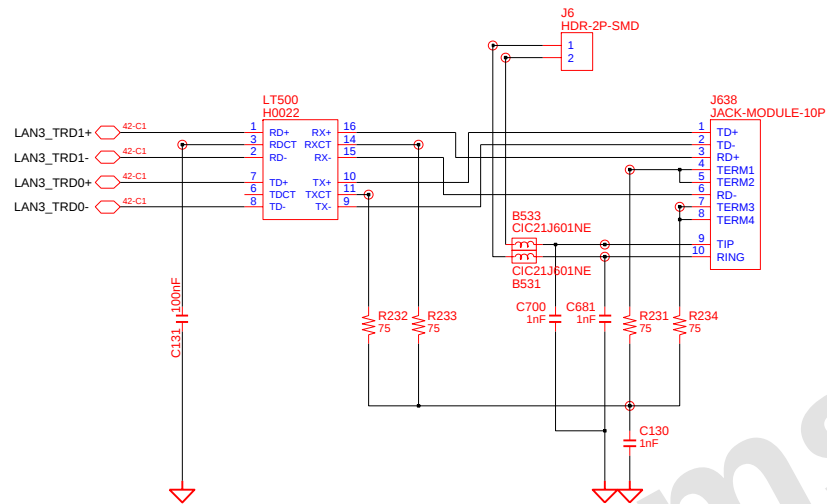
LAN Controller (Only 10/100M)



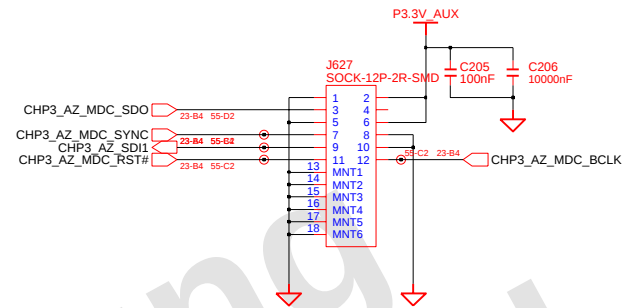
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0	LAN		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	36	OF 48

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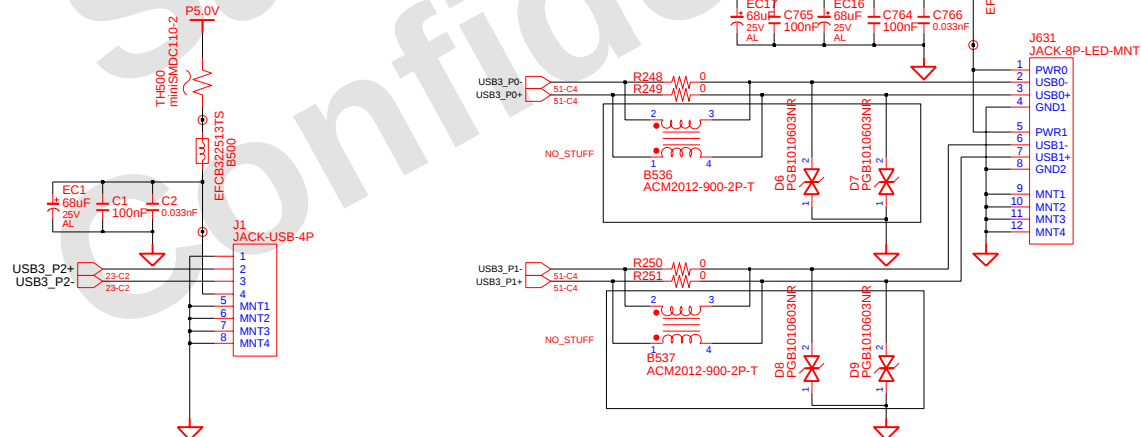
LAN Connector



MDC Connector



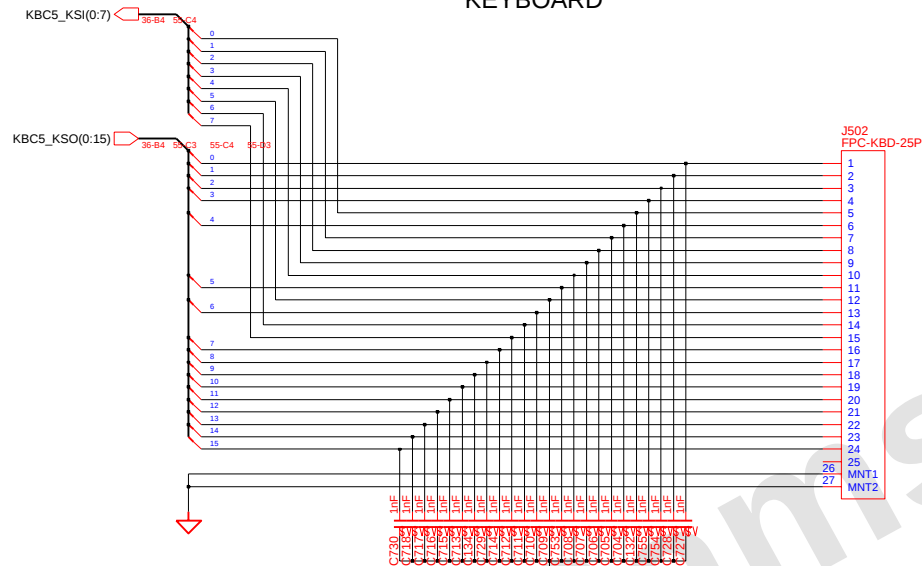
USB Connector



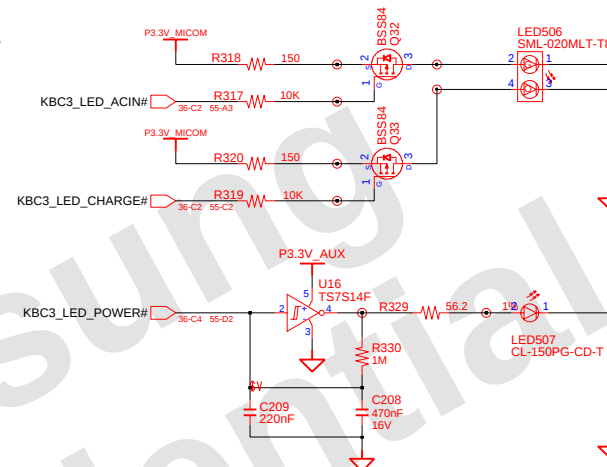
DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	LAN & MDC CONN		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	37	OF 48

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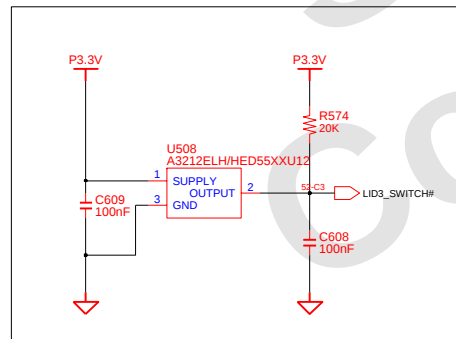
KEYBOARD



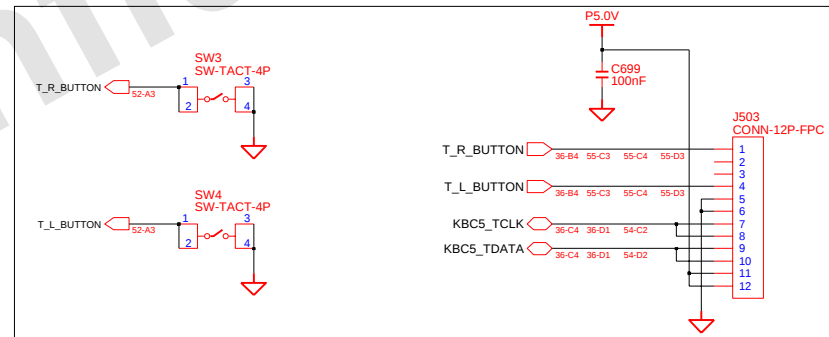
ADAPTERIN/CHARGING LED



LID SWITCH



TOUCHPAD



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		BOARD CONN	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	38 OF 48	

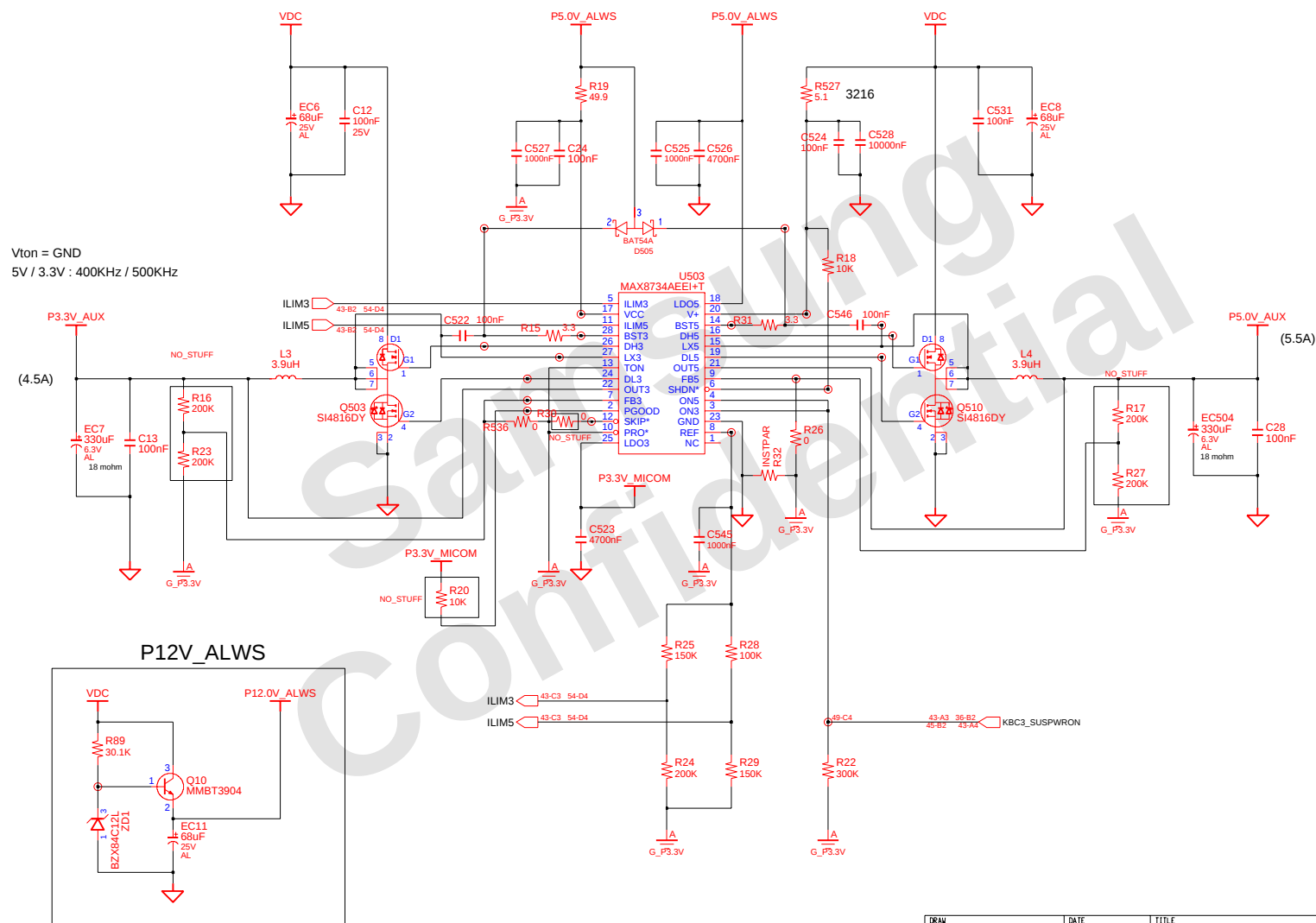
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[illegible]

DATE	11/2/2006	TITLE		HAINAN2	SAMSUNG ELECTRONICS
DRAW	JIANG, DS				
CHECK	YONG LEI	DEV. STEP	PV	CHARGE	PART NO. BA41-XXXXXA
APPROVAL	KEVIN LEE	REV	2.0		
MODULE CODE	undefined	LAST EDIT		November 2, 2006 4:25:20 PM	PAGE 39 OF 48

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Vton = GND
5V / 3.3V : 400KHz / 500KHz

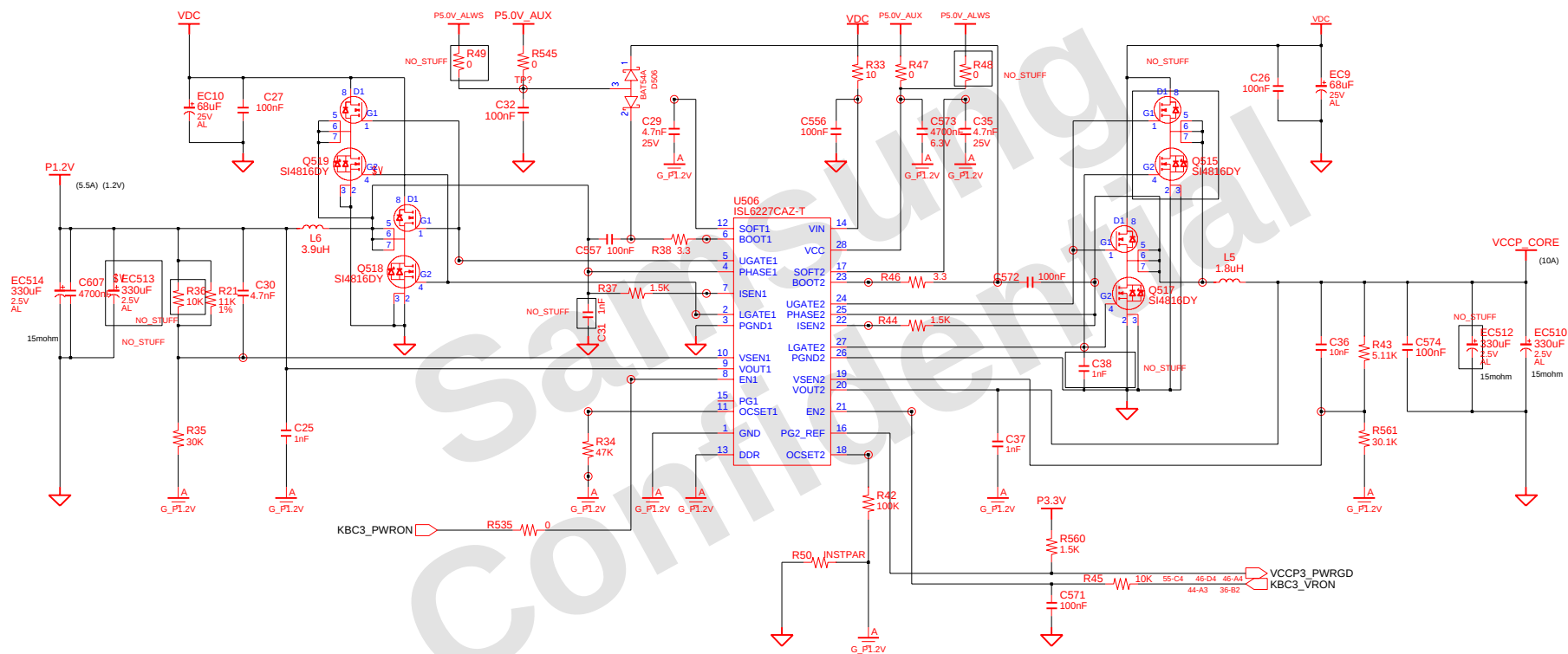


DATE	11/2/2006	TITLE		HAINAN2		SAMSUNG ELECTRONICS	
DRAW	JIANG, DS						
CHECK	YONG LEI	DEV. STEP	PV	P3.3V_AUX & P5V_AUX		PART NO. BA41-XXXXXA	
APPROVAL	KEVIN LEE	REV	2.0				
MODULE CODE		LAST EDIT		November 2, 2006	4:25:20 PM	PAGE	40 OF 48

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P1.2V & VCCP_CORE(1.05V)



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS PART NO. BA41-XXXXXA
CHECK	YONG LEI	DEV. STEP	PV			
APPROVAL	KEVIN LEE	REV	2.0		P1.2V & VCCP	
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	41 OF 48	

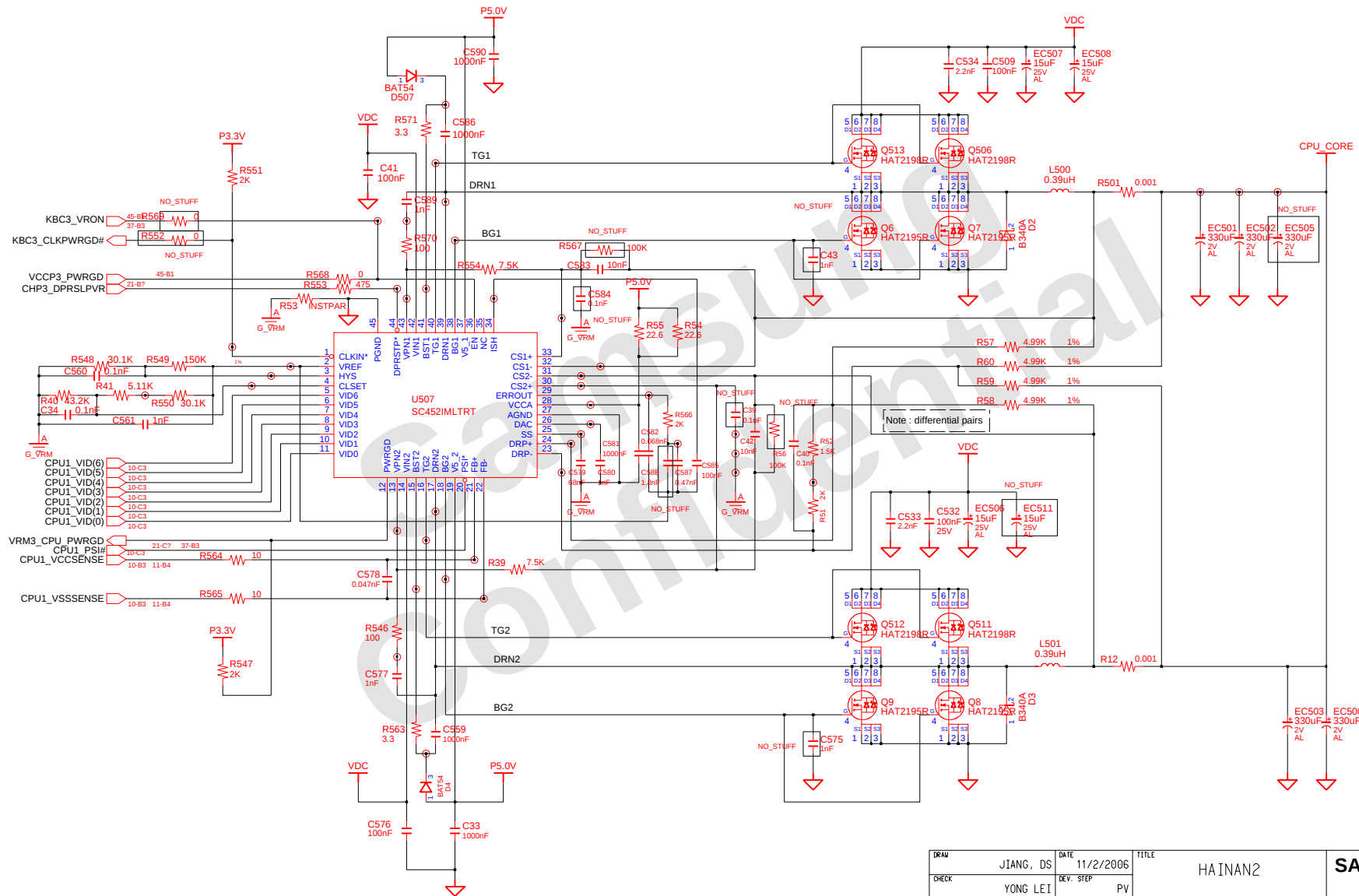
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DRAM	JIANG, DS	DATE	11/2/2006	TITLE		SAMSUNG ELECTRONICS	
CHECK	YONG LEI	DEV. STEP	PV				
APPROVAL	KEVIN LEE	REV	2.0				
				DDR2 POWER		PART NO.	BA14-XXXXXA
MODULE CODE		undef ined		LAST EDIT		November 2, 2006 4:25:20 PM	PAGE 42 OF 48

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CPU VRM [SEMTECH]

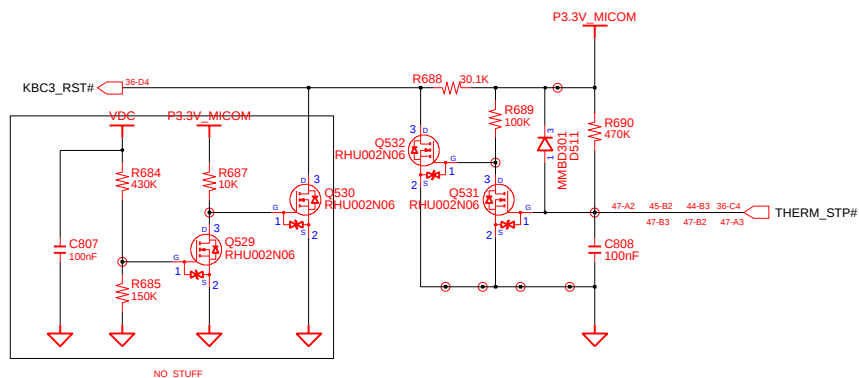


DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG ELECTRONICS
CHECK	YONG LEI	DEV. STEP	PV	
APPROVAL	KEVIN LEE	REV	2.0	PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	
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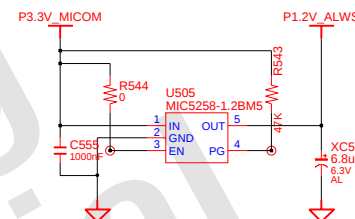
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MICOM RESET

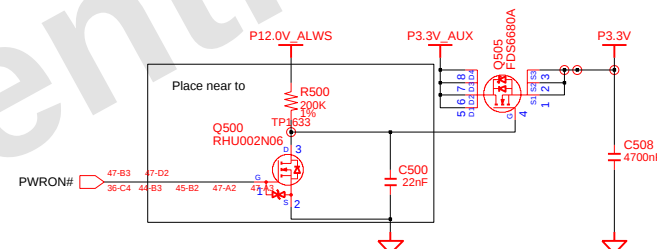
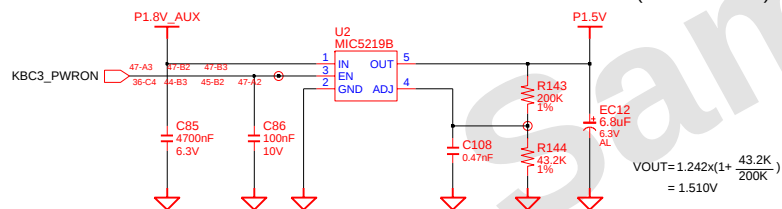


Switched Power On (P3.3V & 1.8V&1.2V_ALWS)

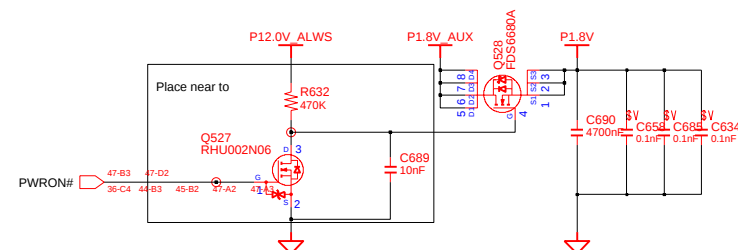
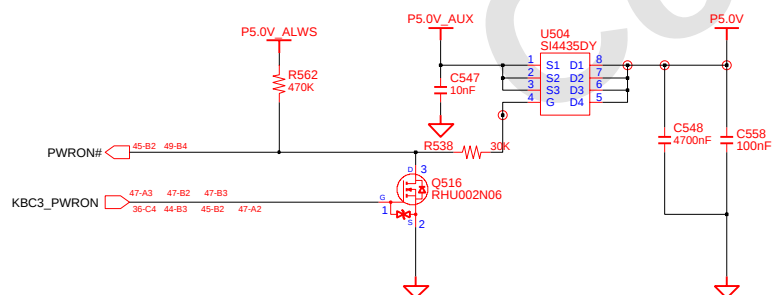


P1.5V POWER

(EBL)
(AVE MAX : 2A)



Switched Power On (P5V)

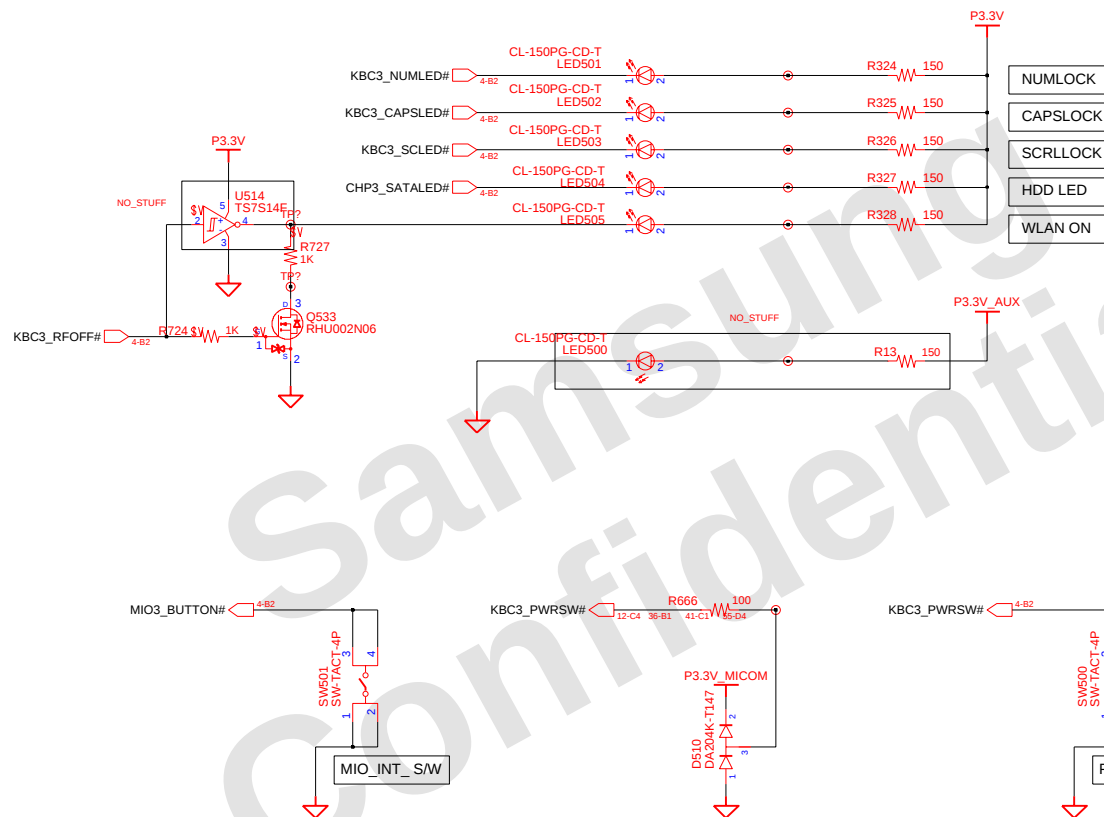


DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	SWITCH POWER		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	44	OF 48

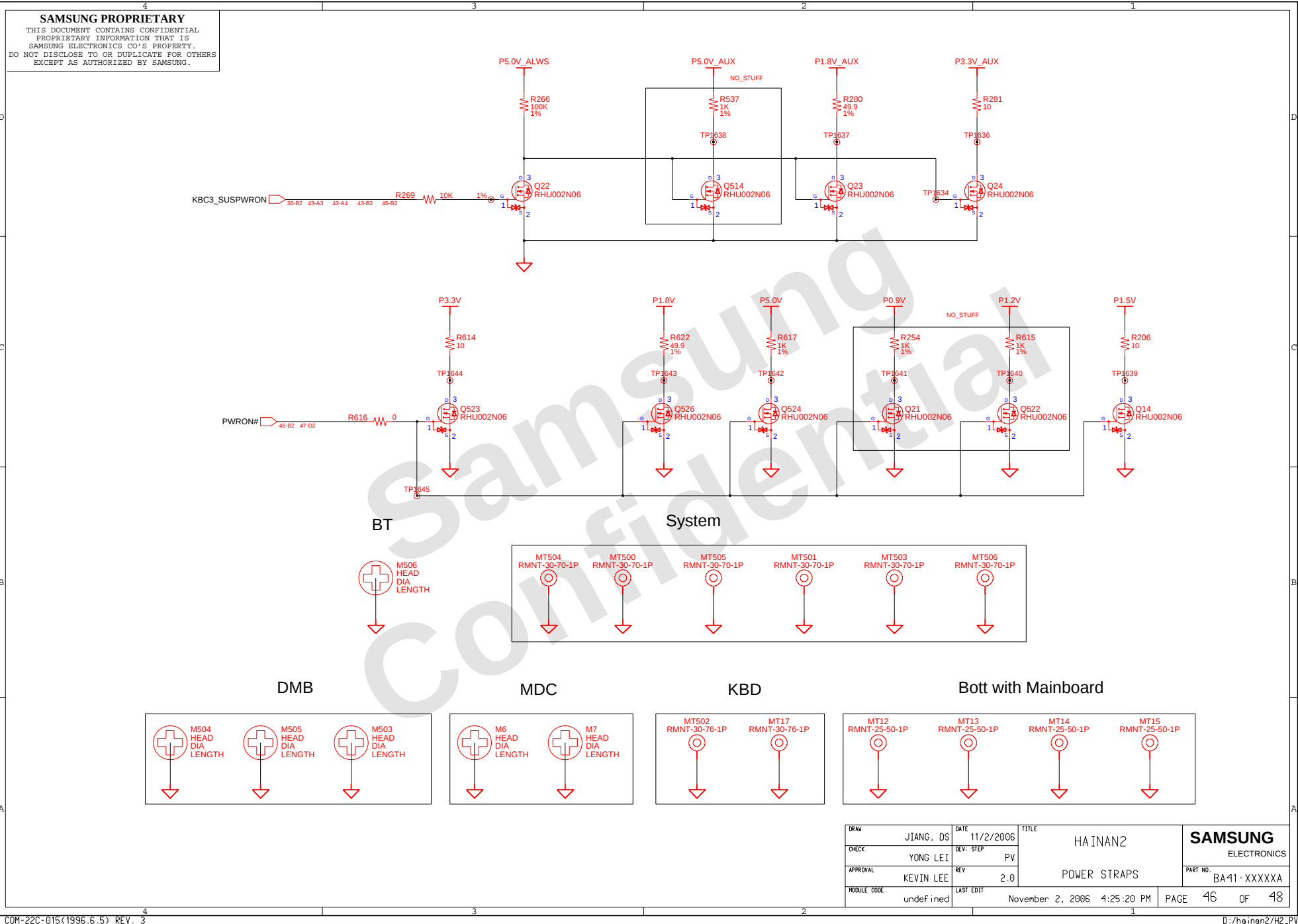
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LED 0801-002195



DRAW	JIANG, DS	DATE	11/2/2006	TITLE	HAINAN2	SAMSUNG
CHECK	YONG LEI	DEV. STEP	PV			ELECTRONICS
APPROVAL	KEVIN LEE	REV	2.0	POWER SWITCH		PART NO. BA41-XXXXXA
MODULE CODE	undefined	LAST EDIT	November 2, 2006 4:25:20 PM	PAGE	45	OF 48



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TP2027MIC1
TP2028MIC2
TP2029GPIO1
TP2030ILIM3
TP2031ILIM5

TP2020CPU1_TDI
TP2021CPU1_TMS
TP2022PCI3_PAR
TP2023SATA1_X1
TP2024SMB3_CLK
TP2025SPI3_CLK
TP2026SPI3_CS#

TP1968CPU1_A20M#

TP1971CPU1_BSEL0
TP1972CPU1_BSEL1
TP1973CPU1_BSEL2

TP1939PCI3_CBE0#
TP1940PCI3_CBE1#
TP1941PCI3_CBE2#
TP1942PCI3_CBE3#
TP1943PCI3_GNT1#

TP1945PCI3_IRDY#
TP1946PCI3_PERR#
TP1947PCI3_REQ1#
TP1914PCI3_RST#
TP1915PCI3_SERR#
TP1916PCI3_STOP#
TP1917PCI3_TRDY#

TP1888KBC5_KSO(0)
TP1889KBC5_KSO(1)
TP1890KBC5_KSO(2)
TP1891KBC5_KSO(3)
TP1892KBC5_KSO(4)
TP1893KBC5_KSO(5)
TP1894KBC5_KSO(6)
TP1895KBC5_KSO(7)
TP1896KBC5_KSO(8)
TP1897KBC5_KSO(9)

TP1898LCD3_BKLTON
TP1899LPC3_LAD(0)
TP1900LPC3_LAD(1)
TP1869LPC3_LAD(2)
TP1870LPC3_LAD(3)
TP1871MCD3_MSIN#

TP2002CHP3_SPKR

TP1977CPU1_FERR#
TP1979CPU1_INIT#

TP1950CPU1_TRST#
TP1951EXP3_CPPE#

TP1918PEX3_WAKE#
TP1919PLT3_RSTF#

TP2006CPU1_INTR
TP2007CPU1_PSI#

TP1952IDES_DACK#
TP1953IDES_IORDY
TP1954JCK_SENS_A
TP1955KBC3_CHGEN
TP1956KBC3_SMCLK
TP1957KBC3_TDATA

TP1920OT_L_BUTTON
TP1921OT_R_BUTTON
TP1922VGA3_HSYNC
TP1923VGA3_VSYNC
TP1924BAT3_SMCLK#
TP1925CHP3_OVERT#
TP1910CHP3_SBPME#
TP1911CHP3_SERIRQ
TP1912CHP3_SLPS3#
TP1913CHP3_SLP5#

TP2011CPU1_SLP#
TP2012CPU1_SM#
TP2013IDES_CS1#
TP2014IDES_CS3#

TP1980IDES_DREQ
TP1981IDES_IOR#
TP1982IDES_IOW#
TP1983KBC3_A20G
TP1984KBC3_RST#
TP1985KBC3_VRON
TP1986KBC5_TCLK

TP1958LCD3_VDDEN
TP1959MCD3_SDCH#
TP1960MCD3_XDALE
TP1961MCD3_XDCD#
TP1962MCD3_XDCE#
TP1963MCD3_XDRE#
TP1964MCD3_XDWE#

TP1996ODDS_RST#

TP1997SMB3_DATA
TP1998SPI3_MISO
TP1999SPI3_MOSI
TP2000AUD3_EAPD#

TP2001CLK3_NB14M

TP2015RTC_CLK
TP2016AC_SDOUT
TP2017AUTO_ON#
TP2018CPU1_NMI
TP2019CPU1_TCK

DRAW
CHECK
APPROVAL
MODULE CODE

JIANG, DS
YONG LEI
KEVIN LEE

DATE
DEV. STEP
REV
LAST EDIT

11/2/2006
PV
2.0
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TITLE
HAINAN2
TP

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ELECTRONICS
PART NO.
BA41-XXXXXA

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