

GENEVA

CPU : MEROM
Chip Set : SANTAROSA CHIPSET
Remarks : Mobility Platform

Model Name : GENEVA
PBA Name : MAIN
PCB Code : BA41-XXXXX
Dev. Step : PV2
Revision : 1.0
T.R. Date :

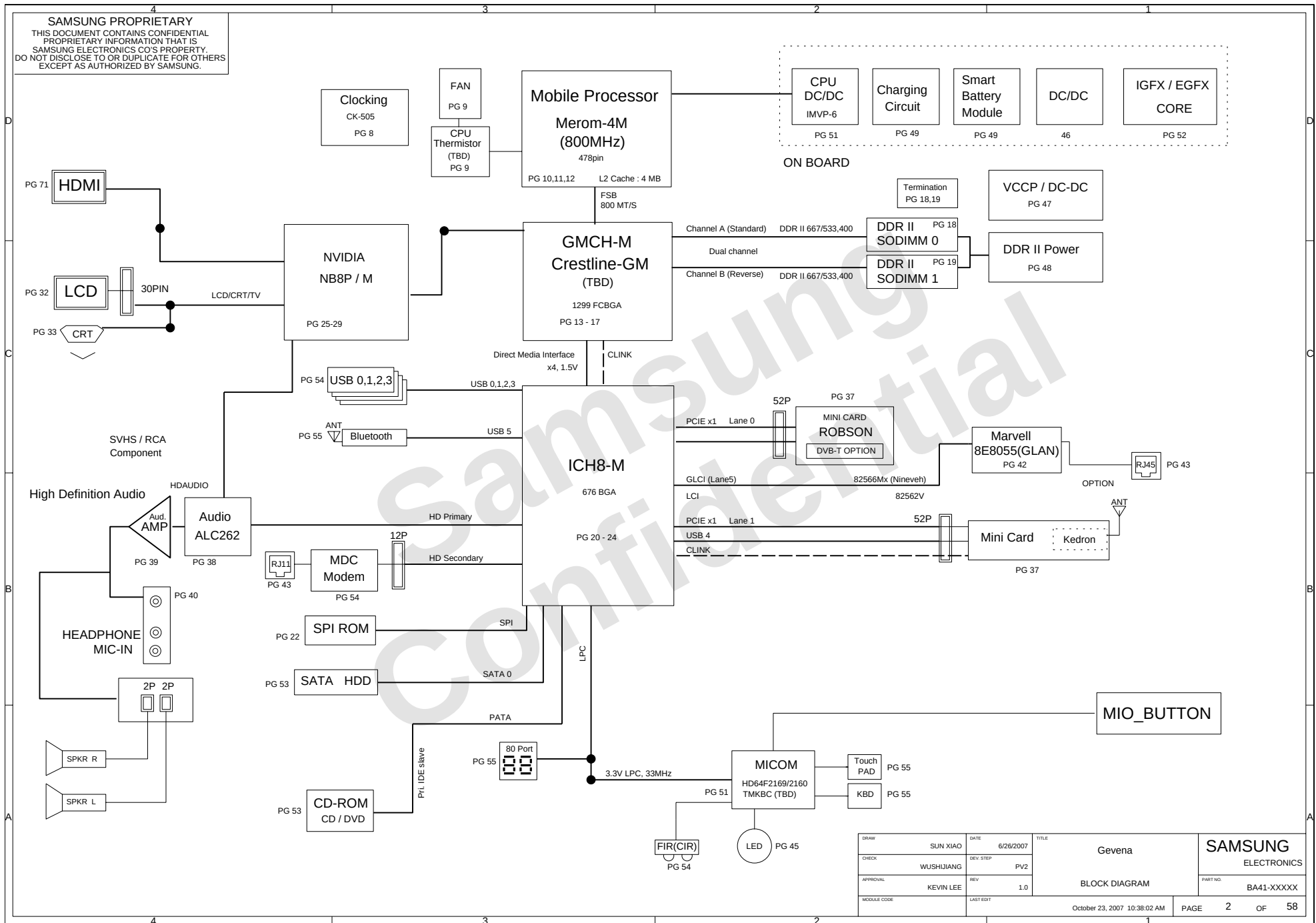
DRAW	CHECK	APPROVAL

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DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Geneva	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2	MILAN		
APPROVAL	KEVIN LEE	REV	1.0	COVER	PART NO. BA41-XXXXX	
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	1	OF 58

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CHECK	WUSHUJIANG	DEV STEP	PV2			ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	BLOCK DIAGRAM	PART NO.	BA41-XXXXX
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BOARD INFORMATION

SCHEMATIC ANNOTATIONS AND BOARD INFORMATION

PCI Devices

Devices	IDSEL#	REQ/GNT#	Interrupts
Cardbus	AD25	3	B,C,D
DT PCI SLOT	AD22	1	B,C,D,C
MiniPCI SLOT	AD23	2	A,B
USB	AD29(internal)	-	USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHC1) : H
Hub to PCI	AD30(internal)	-	-
LPC bridge/IDE/AC97/SMBUS	AD31(internal)	-	B
Internal MAC	AD24(internal)	-	E
AC Link	-	-	B
GLAN	-	-	F

Voltage Rails

VDC	Primary DC system power supply (7 to 21V)
VCC_CORE	Core Voltage for CPU
MCH_CORE	Core Voltage for CPU
ICH_CORE	Core Voltage for CPU
P1.05V (VCCP)	VTT for CPU, Crestline & ICH8-M
P1.05V_M	1.05V switched power rail for ME (on in M0/M1)
P1.25V_M	1.25V switched power rail for ME (on in M0/M1)
P3.3V_M	3.3V switched power rail for ME (on in M0/M1)
P3.3V_A	3.3V always power rail (by KBC3_ALWS_ON)
MICOM_P3V	3.3V always power rail (for Micom)
LAN_3.3V	3.3V always power rail (for LAN)
P1.5V	1.5V switched off power rail (off in S3-S5)
P1.5V_AUX	1.5V switched on power rail
P2.5V	2.5V switched off power rail (off in S3-S5)
P1.8V	1.8V switched off power rail (off in S3-S5)
P1.8V_AUX	1.8V power rail for DDR
P0.9V	0.9V power rail for DDR
P3.3V	3.3V switched off power rail (off in S3-S5)
P3.3V_AUX	3.3V switched on power rail
PSV	5.0V switched off power rail (off in S3-S5)
PSV_AUX	5.0V switched on power rail

USB PORT Assign

PORT #	ASSIGNED TO
0	SYSTEM PORT 0
1	SYSTEM PORT 1
2	DMB
3	Expresas Card
4	DOCKING PORT 0
5	Bluetooth
6	Mini PCI Express
7	Aux Display
8	Test Port (RSVD)
9	WWAN/WIBRO (TBD)

PCI Express Assign

PORT #	ASSIGNED TO
0	PCIE x1 Slot
1	Mini Card (Golan)
2	Mini Card (WIBRO : RSVD)
3	Expresas Card
4	DOCKING PORT 0
5	GLAN

Crystal / Oscillator

TYPE	FREQUENCY	DEVICE	USAGE
Crystal	32.768KHz	ICH8-M	Real Time Clock
Crystal	10MHz	MICOM	HD64F2169/2160
Crystal	14.318MHz	CLOCK-Generator	CK-505
Crystal	24.576MHz	Cardbus Controller	1394
Crystal	25MHz	LAN	Intel LAN
Crystal	27MHz (TBD)	VIDEO	PEG
Crystal	24.576MHz (TBD)	HD Audio	Audio

LCD Pannel Detect (TBD)

Devices	Resolution	PANNEL_DETECT_0

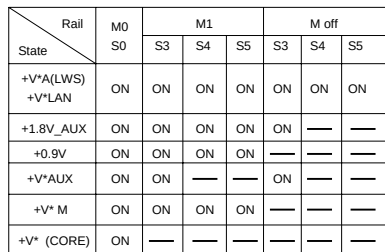
I²C / SMB Address

Devices	Address	Hex	Bus
ICH8-m	Master	-	SMBUS Master
CPU Thermal Sensor	1001 110x	9Ch	Thermal Sensor
SODIMM0	1010 000x	A0h	-
SODIMM1	1010 010x	A4h	-
Thermal Sensor on SODIMM0	0011 000x	30h	-
Thermal Sensor on SODIMM1	0011 010x	34h	-
CK-505M (Clock Generator)	1101 001x	D2h	Clock, Unused Clock Output Disable
DB400 (PCI Express Clocks)	1101 110x	DCh	Clock Buffer for PCI Express
TBD	0100 1100	4Ch	Thermal Sensor
TBD : (LVDS BackLight Inverter)	0101 1000	58h	-
TBD : (ALS)	0111 0010	72h	Ambient Light Sensor
TBD : (AUX DISPLAY)	0011 110x	3Ch	EMA
LAN	1100 1000	C8h	82566
PCI Express Docking	TBD	TBD	TBD
TPM	TBD	TBD	TBD

REVISION HISTORY

See rev notes for more information.

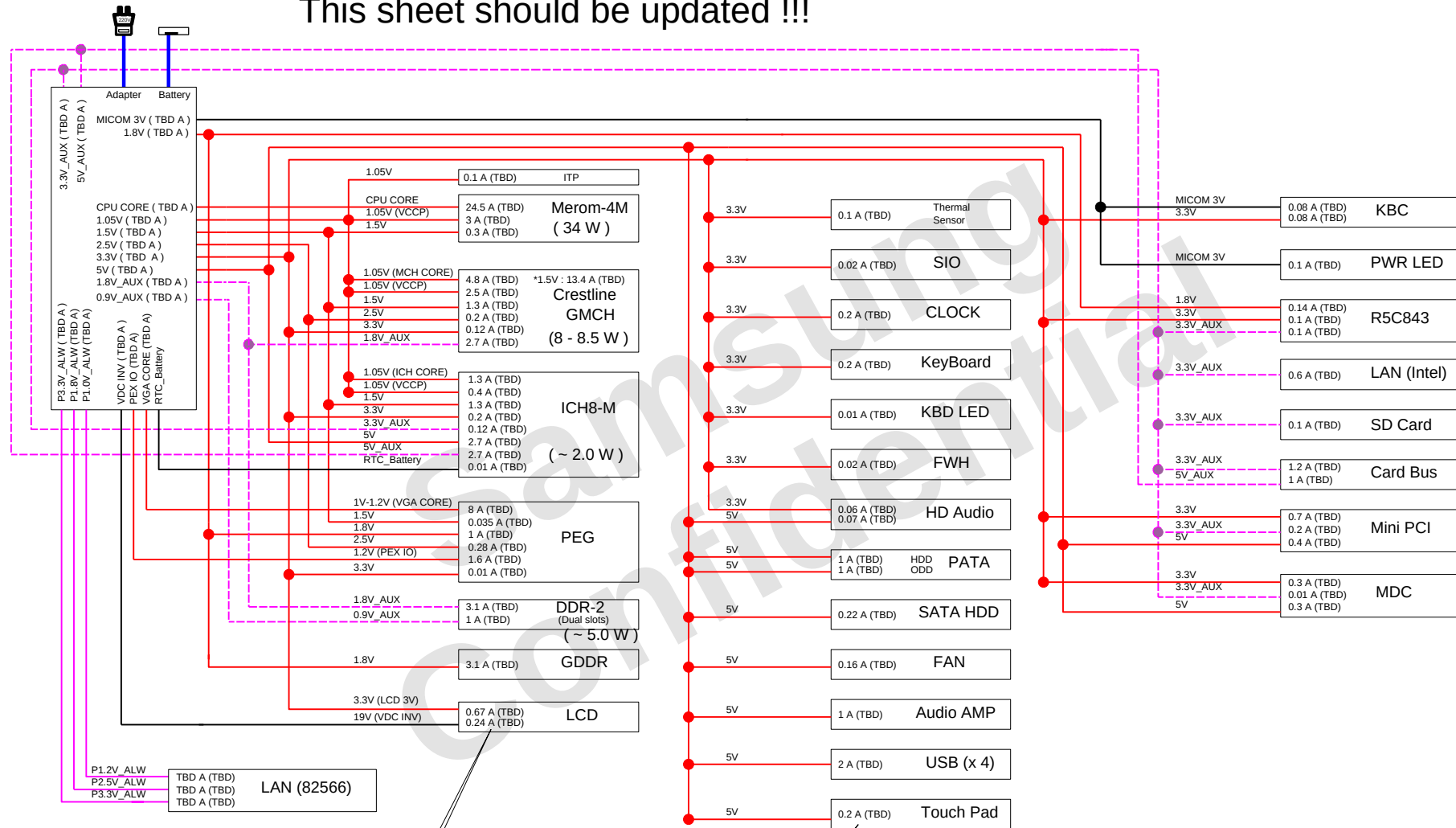
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CHECK	WUSHIJIANG	DEV. STEP	PV2			ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	BOARD INFO		PART NO. BA41-XXXXX
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POWER RAILS ANALYSIS

Rev. 0.5

This sheet should be updated !!!



Value by Datasheet/Application notes (Value by measurement)

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CHECK	WUSHIJIANG	DEV. STEP	PV2			ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	POWER RAILS	PART NO.	BA41-XXXXX
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Host Boot / ME Off

(SLPS4* = S4_STATE*) > (SLPM* = SLPS3*)

M-1) KBC3_DDR_PWRON (TBD) = 8) KBC3_SUSPWR

M-2) KBC3_ME_PWRON = 15) KBC3_PWRON

Host / ME Boot

Host S5 / ME Boot
(SLPS4* = SLPM*) > S4 STATE* > SLPS3*

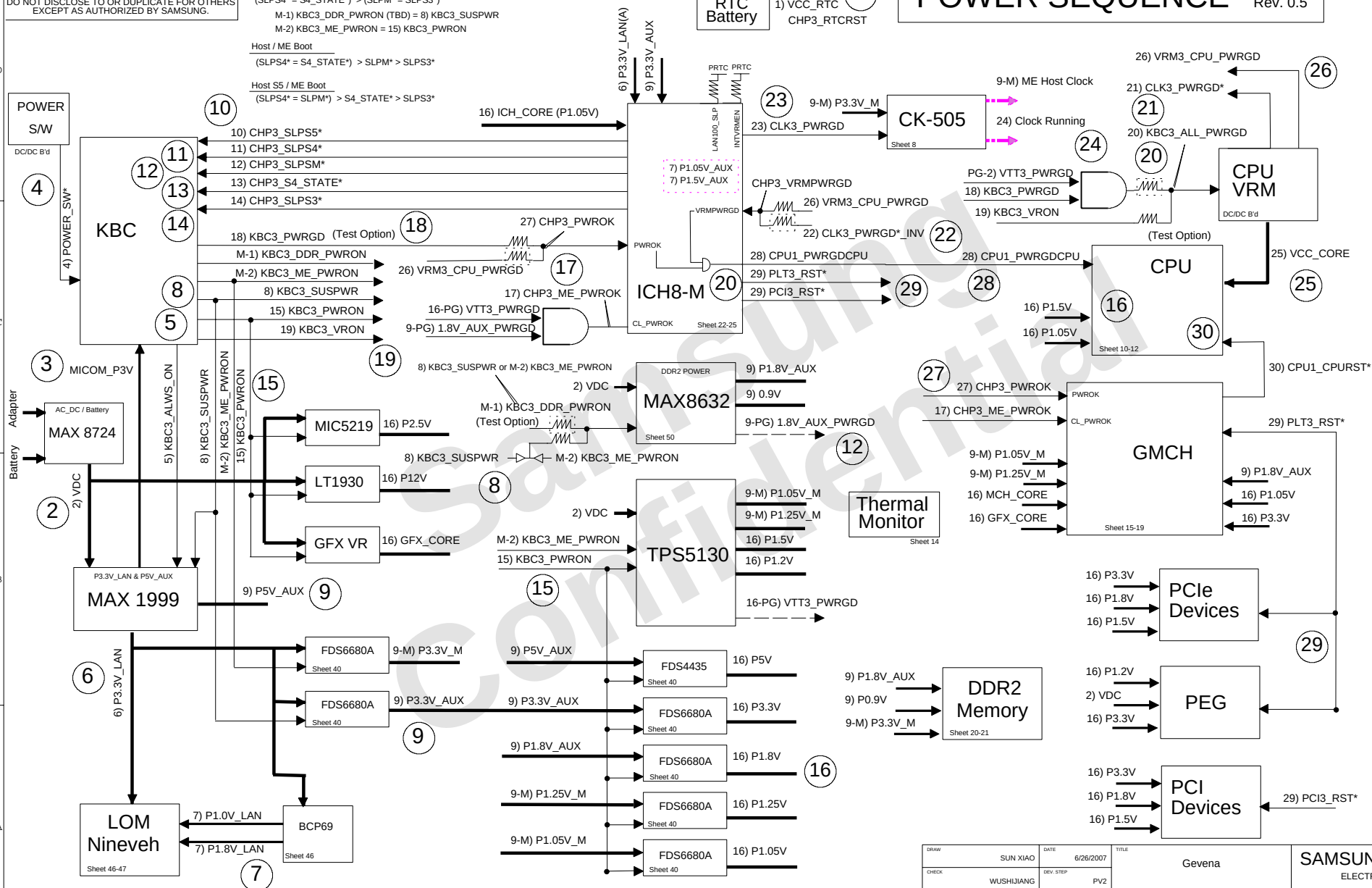
TO BE UPDATED!!

RTC
Battery

1) VCC_RTC (1)
CHP3_RTCRST

POWER SEQUENCE

Rev. 0.5



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CHECK	WUSHUJIANG	DEV. STEP	PV2				
APPROVAL	KEVIN LEE	REV	1.0	POWER SEQUENCE		PART NO.	
						BA41-XXXXX	
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D:/t ingt ing/geneva/Geneva_pr_1023

CLK3_USB48 

CPU1_BSEL0 

CPU1_BSEL1 

CPU1_BSEL2 

CLK3_ICH14 

CLK3_SIO14 

CHP3_CPUSTP# 

CHP3_PCISTP# 

CLK3_PWRGD 

CLK3_PCLKICH 

CLK3_PCLKSIO_DS 

CLK3_DBG LPC 

CLK3_PCLKSIO 

CLK3_PCLKMICOM 

MCH3_CLKREQ# 

CHP3_SATACLKREQ# 

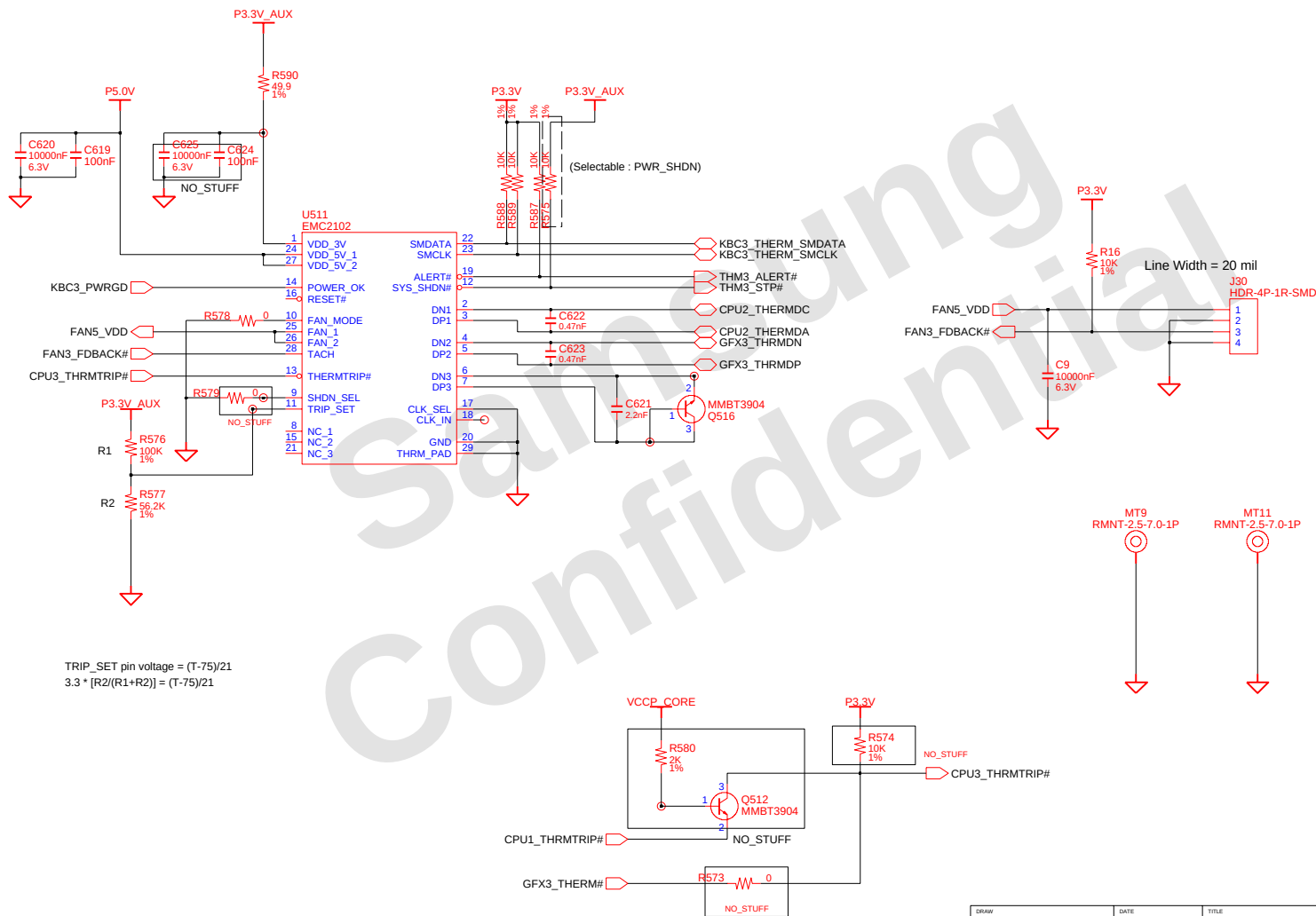
SMB3_CLK 

SMB3_DATA 

CLK REQ	DEVICE	SRC PORT
CLK REQ A	SATA	SRC2
CLK REQ B	GMCH	SRC4
CLK REQ E	MINI CARD	SRC6
CLK REQ F	EXPRESS CARD	SRC8

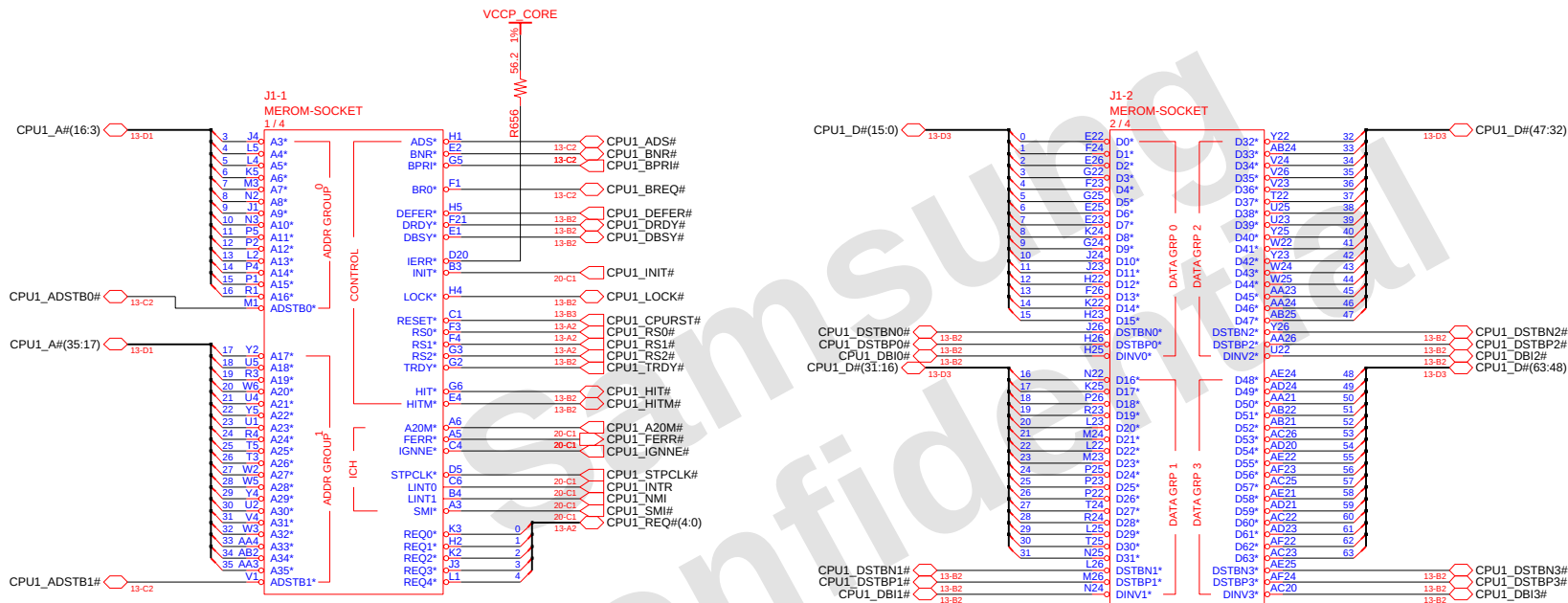
DRAW	SUN XIAO	DATE	6/26/2007	Gevena CLOCK GENERATOR CK-505	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2		
APPROVAL	KEVIN LEE	REV	1.0		
			PART NO.		
MODULE CODE	LAST EDIT			DATE	8-05-08

THERMAL SENSOR & FAN CONTROL

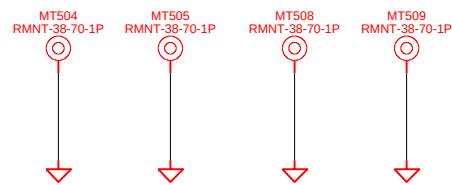


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena THERMAL BLOCK	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0		THERMAL & FAN CONTROL	PART NO. BA41-XXXXX
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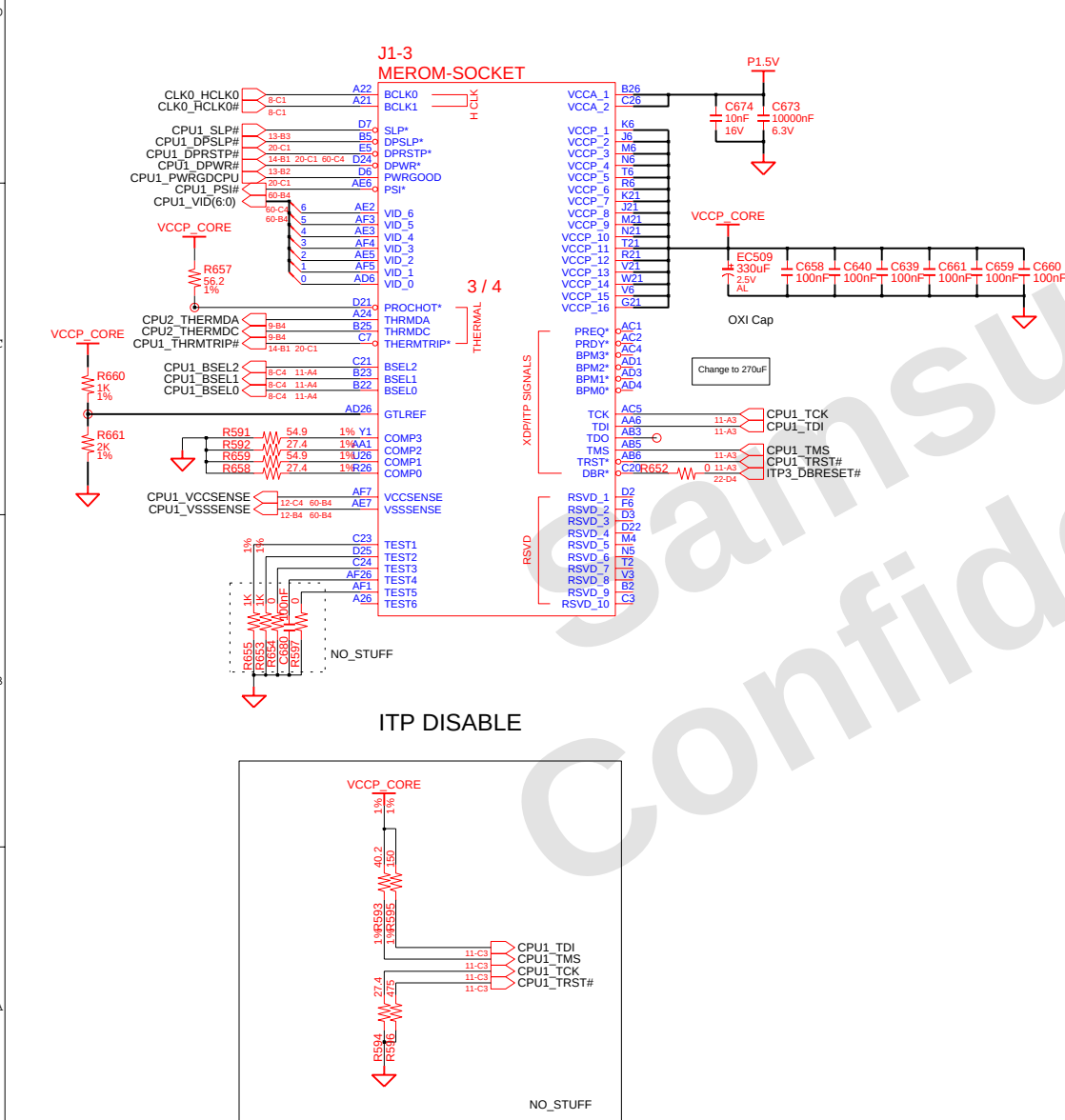


CPU CONNECTOR PART NUMBER 3704-001153



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CPU Core Voltage Table IMVP-6

Active Mode		Active/Deeper Sleep Dual Mode Region		Deeper Sleep/Extended Deeper Sleep Dual Mode Region	
VID(6:0)	Voltage	VID(6:0)	Voltage	VID(6:0)	Voltage
0 0 0 0 0 0 0	1.5000 V	0 1 0 1 0 0 0	1.0000 V	1 0 1 0 0 0 1	0.4875 V
0 0 0 0 0 0 1	1.4875 V	0 1 0 1 0 0 1	0.9875 V	1 0 1 0 0 1 0	0.4750 V
0 0 0 0 0 1 0	1.4750 V	0 1 0 1 0 1 0	0.9750 V	1 0 1 0 0 1 1	0.4625 V
0 0 0 0 0 1 1	1.4625 V	0 1 0 1 1 0 1	0.9625 V	1 0 1 0 1 0 0	0.4500 V
0 0 0 0 1 0 0	1.4500 V	0 1 0 1 1 0 0	0.9500 V	1 0 1 0 1 0 1	0.4375 V
0 0 0 0 1 0 1	1.4375 V	0 1 0 1 1 1 0	0.9375 V	1 0 1 0 1 1 0	0.4250 V
0 0 0 0 1 1 0	1.4250 V	0 1 0 1 1 1 1	0.9250 V	1 0 1 0 1 1 1	0.4125 V
0 0 0 0 1 1 1	1.4125 V	0 1 0 1 1 1 1	0.9125 V	1 0 1 1 0 0 0	0.4000 V
0 0 0 1 0 0 0	1.4000 V	0 1 1 0 0 0 0	0.9000 V	1 0 1 1 0 0 1	0.3875 V
0 0 0 1 0 0 1	1.3875 V	0 1 1 0 0 0 1	0.8875 V	1 0 1 1 0 1 0	0.3750 V
0 0 0 1 0 1 0	1.3750 V	0 1 1 0 0 1 0	0.8750 V	1 0 1 1 0 1 1	0.3625 V
0 0 0 1 0 1 1	1.3625 V	0 1 1 0 1 0 1	0.8625 V	1 0 1 1 1 0 0	0.3500 V
0 0 0 1 1 0 0	1.3500 V	0 1 1 0 1 0 0	0.8500 V	1 0 1 1 1 0 1	0.3375 V
0 0 0 1 1 0 1	1.3375 V	0 1 1 0 1 0 1	0.8375 V	1 0 1 1 1 1 0	0.3250 V
0 0 0 1 1 1 0	1.3250 V	0 1 1 0 1 1 0	0.8250 V	1 0 1 1 1 1 1	0.3125 V
0 0 0 1 1 1 1	1.3125 V	0 1 1 1 0 1 1	0.8125 V	1 1 0 0 0 0 0	0.3000 V
0 0 1 0 0 0 0	1.3000 V	0 1 1 1 0 1 0	0.8000 V	1 1 0 0 0 0 1	0.2875 V
0 0 1 0 0 0 1	1.2875 V	0 1 1 1 0 1 1	0.7875 V	1 1 0 0 0 1 0	0.2750 V
0 0 1 0 0 1 0	1.2750 V	0 1 1 1 1 0 1	0.7750 V	1 1 0 0 0 1 1	0.2625 V
0 0 1 0 0 1 1	1.2625 V	0 1 1 1 1 1 0	0.7625 V	1 1 0 0 1 0 0	0.2500 V
0 0 1 0 1 0 0	1.2500 V	0 1 1 1 1 1 1	0.7500 V	1 1 0 0 1 0 1	0.2375 V
0 0 1 0 1 0 1	1.2375 V	0 1 1 1 1 1 1	0.7375 V	1 1 0 0 1 1 0	0.2250 V
0 0 1 0 1 1 0	1.2250 V	0 1 1 1 1 1 1	0.7250 V	1 1 0 0 1 1 1	0.2125 V
0 0 1 0 1 1 1	1.2125 V	0 1 1 1 1 1 1	0.7125 V	1 1 0 1 0 0 0	0.2000 V
0 0 1 1 0 0 0	1.2000 V	1 0 0 0 0 0 0	0.7000 V	1 1 0 1 0 0 1	0.1875 V
0 0 1 1 0 0 1	1.1875 V	1 0 0 0 0 0 1	0.6875 V	1 1 0 1 0 1 0	0.1750 V
0 0 1 1 0 1 0	1.1750 V	1 0 0 0 0 1 0	0.6750 V	1 1 0 1 0 1 1	0.1625 V
0 0 1 1 0 1 1	1.1625 V	1 0 0 0 1 0 0	0.6625 V	1 1 0 1 1 0 0	0.1500 V
0 0 1 1 1 0 0	1.1500 V	1 0 0 0 1 0 1	0.6500 V	1 1 0 1 1 0 1	0.1375 V
0 0 1 1 1 0 1	1.1375 V	1 0 0 0 1 0 1	0.6375 V	1 1 0 1 1 1 0	0.1250 V
0 0 1 1 1 1 0	1.1250 V	1 0 0 0 1 1 0	0.6250 V	1 1 0 1 1 1 1	0.1125 V
0 0 1 1 1 1 1	1.1125 V	1 0 0 1 0 1 1	0.6125 V	1 1 1 0 0 0 0	0.1000 V
0 1 0 0 0 0 0	1.1000 V	1 0 0 1 0 1 0	0.6000 V	1 1 1 0 0 0 1	0.0875 V
0 1 0 0 0 0 1	1.0875 V	1 0 0 1 0 1 1	0.5875 V	1 1 1 0 0 1 0	0.0750 V
0 1 0 0 0 1 0	1.0750 V	1 0 0 1 0 1 1	0.5750 V	1 1 1 0 0 1 1	0.0625 V
0 1 0 0 0 1 1	1.0625 V	1 0 0 1 1 0 1	0.5625 V	1 1 1 0 1 0 0	0.0500 V
0 1 0 0 1 0 0	1.0500 V	1 0 0 1 1 0 0	0.5500 V	1 1 1 0 1 0 1	0.0375 V
0 1 0 0 1 0 1	1.0375 V	1 0 0 1 1 1 0	0.5375 V	1 1 1 0 1 1 0	0.0250 V
0 1 0 0 1 1 0	1.0250 V	1 0 0 1 1 1 1	0.5250 V	1 1 1 0 1 1 1	0.0125 V
0 1 0 0 1 1 1	1.0125 V	1 0 0 1 1 1 1	0.5125 V	1 1 1 1 0 0 0	0.0000 V
		1 0 0 1 1 1 1	0.5000 V	1 1 1 1 0 0 1	0.0000 V
				1 1 1 1 0 1 0	0.0000 V
				1 1 1 1 0 1 1	0.0000 V
				1 1 1 1 1 0 0	0.0000 V
				1 1 1 1 1 0 1	0.0000 V
				1 1 1 1 1 1 0	0.0000 V
				1 1 1 1 1 1 1	0.0000 V
Active		Deeper Slp			
DPRSLPVR	0	DPRSLPVR	1		
DPRSTP*	1	DPRSTP*	0		
PSI2*	0 or 1	PSI2*	0 or 1		

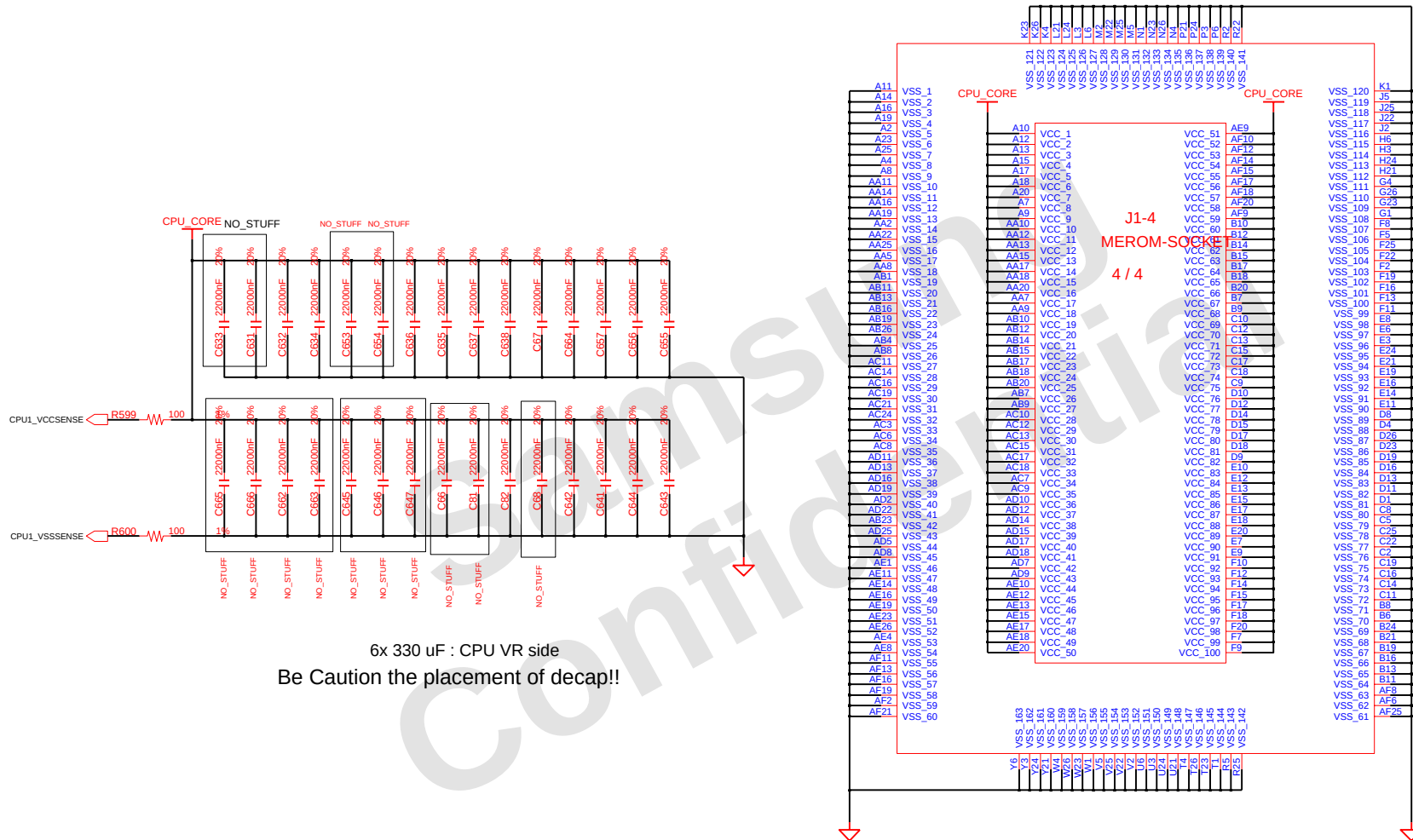
*Yonah Processor (2.33 GHz / 800 MHz : TBD)

GTLREF : Keep the Voltage divider within 0.5" of the first GTLREF0 pin with Zo=55ohm trace. Minimize coupling of any switching signals to this net.

COMP0,2(COMP1,3) should be connected with Zo=27.4ohm(55ohm) trace shorter than 1/2" to their respective Banias socket pins.

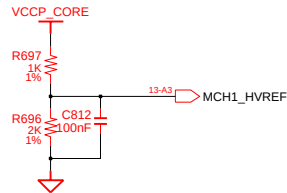
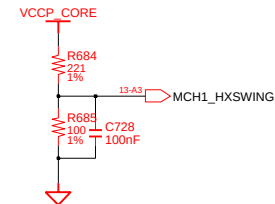
GND test points within 100mil of the VCC/VSSsense at the end of the line. Route the VCC/VSSsense as a Zo=55ohm traces with equal length. Observe 3:1 spacing b/w VCC/VSSsense lines and 25mil away (preferred 50mil) from any other signal. And GND via 100mil away from each of the VCC/VSS test point vias.

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CHECK	WUSHIJIANG	DEV. STEP	PV2		CPU	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		MEROM(3/3)	PART NO.
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CPU1_D#(63:0)

10-C1 10-C2 10-D1 10-D2

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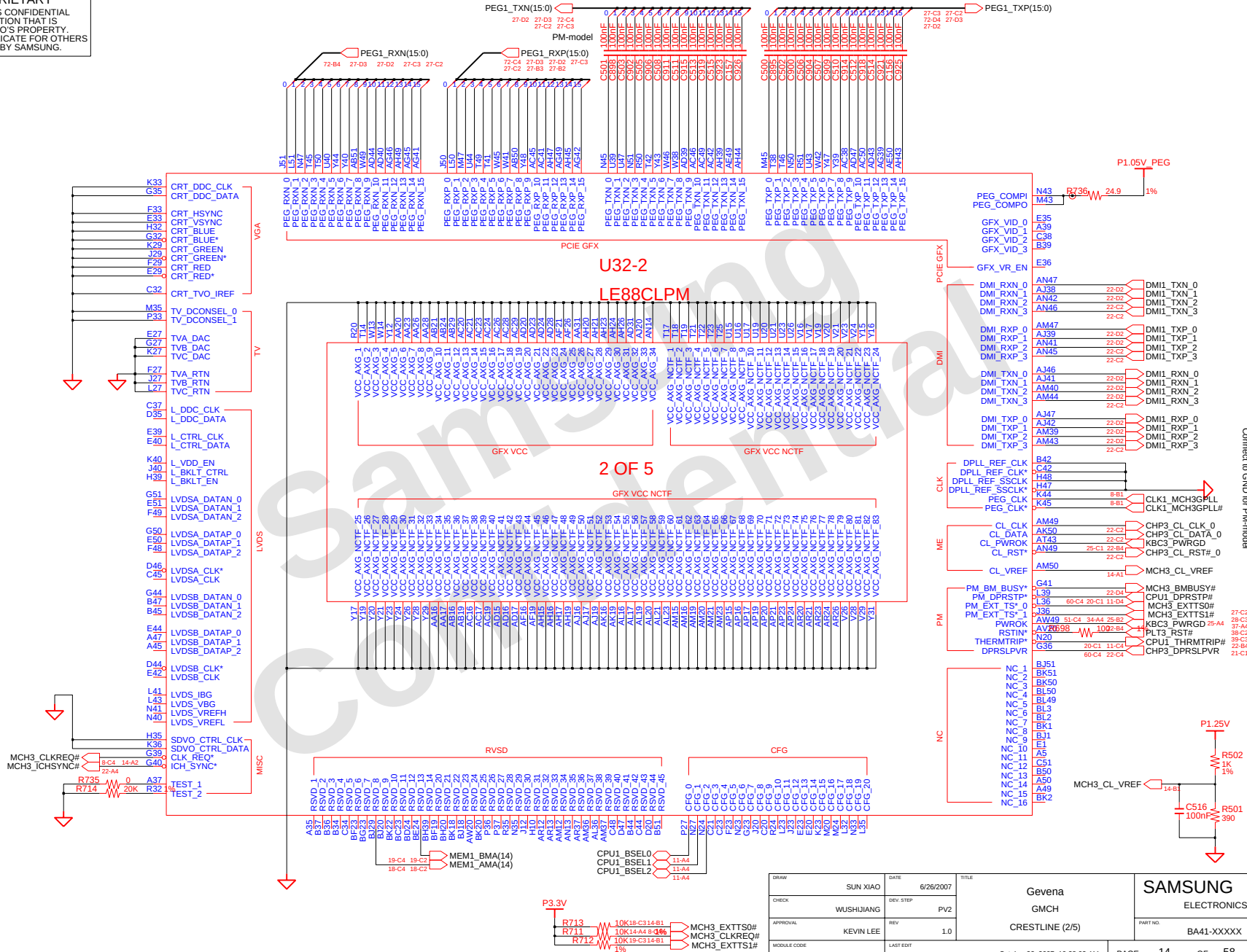
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321

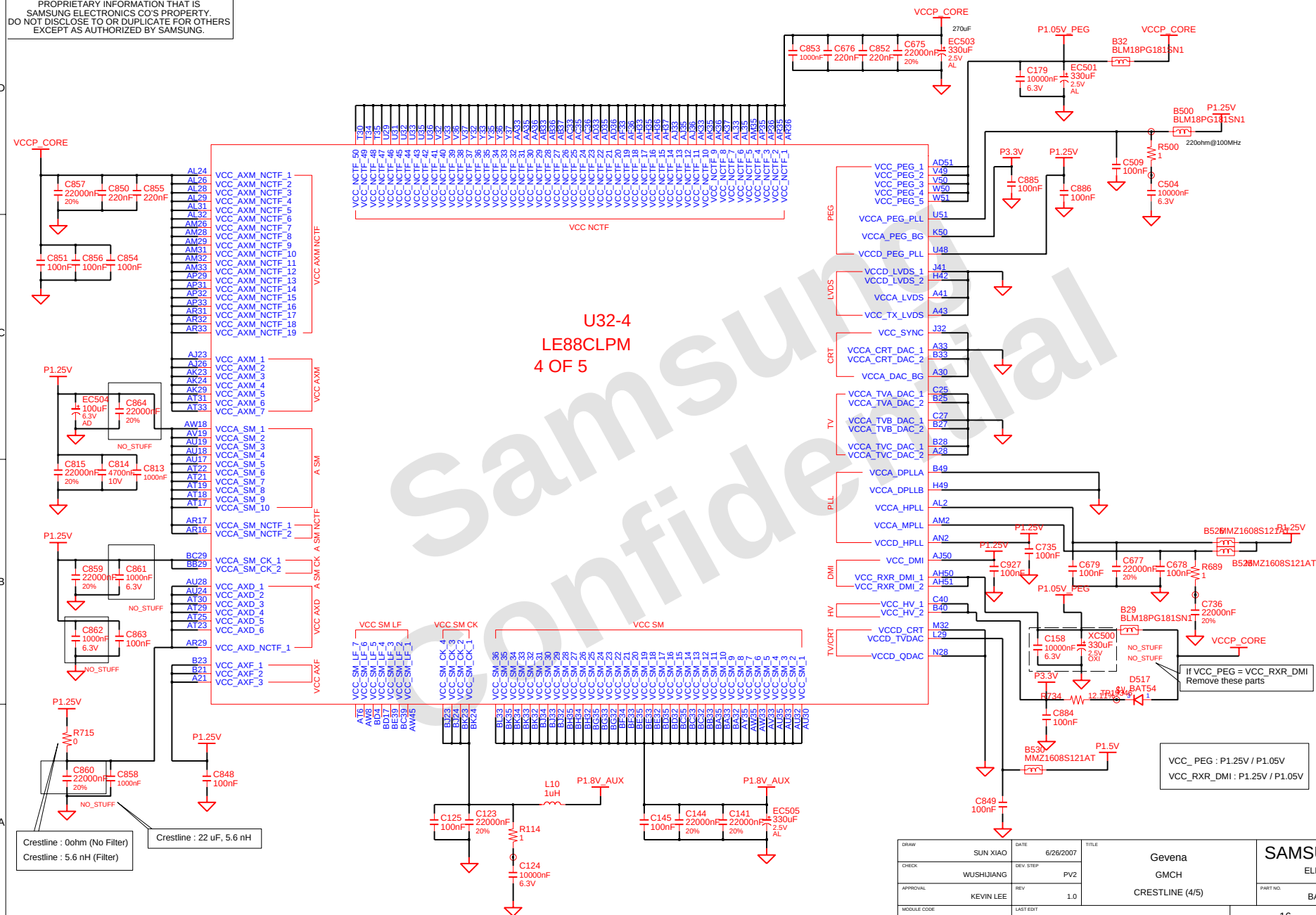
322

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[illegible]

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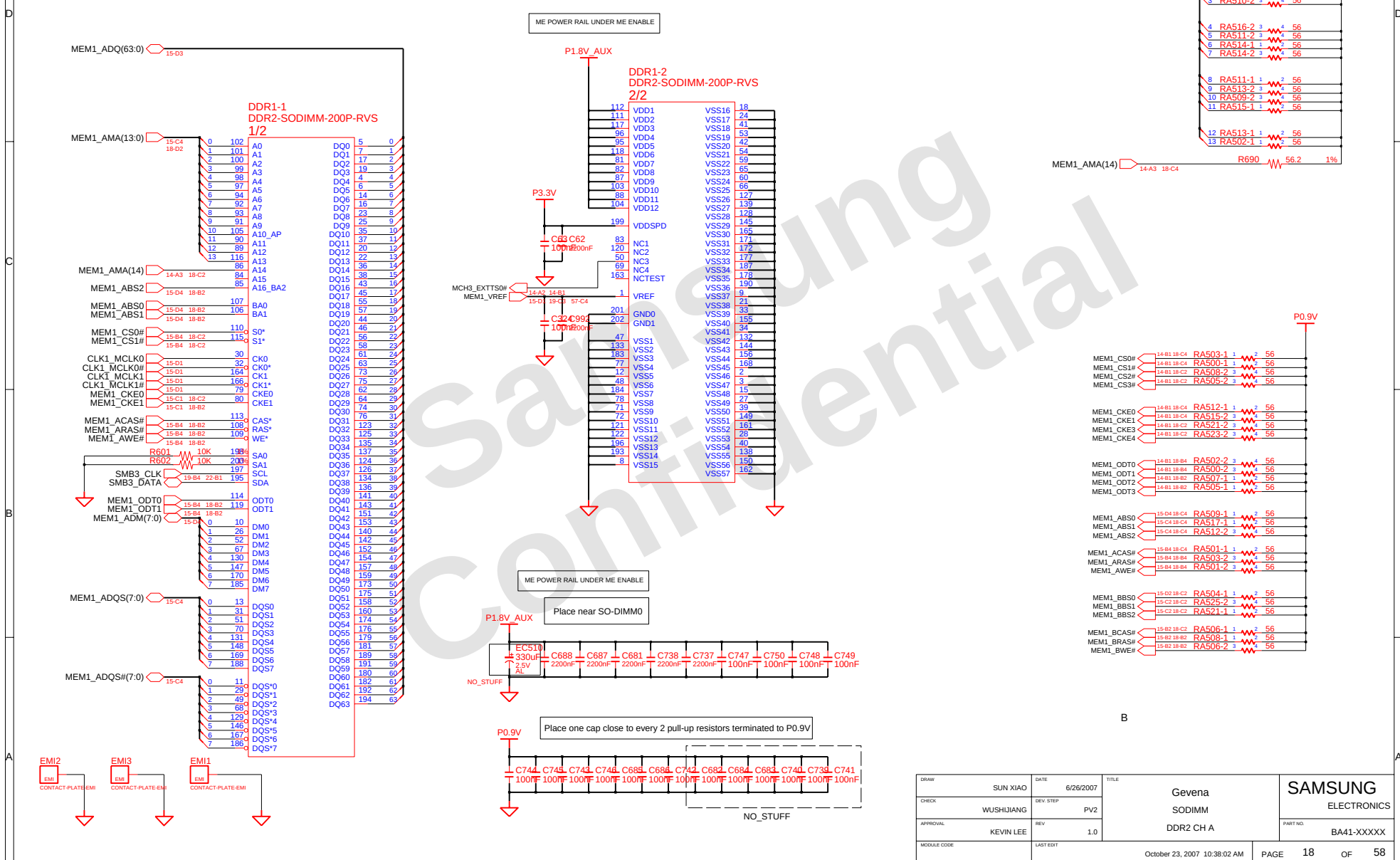
DRAW	SUN XIAO	DATE	6/26/2007	Gevena GMCH CRESTLINE (4/5)	SAMSUNG ELECTRONICS	
CHECK	WUSHIJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0		PART NO.	BA41-XXXXX
MODULE CODE	LAST EDIT				October 23, 2007 10:38:02 AM	PAGE 16 OF 58

[illegible]

*POCAFE11 Only (Remove in MP Model)		
Current Setting (def.: default Option)		
CFG#	Low	High
CFG(5)	DMiX2	DMiX4 (def.)
CFG(6)	Reserved	DDR-II (def.)
CFG(7)	DT/Transportable	Mobile CPU (def.)
CFG(9)	PEG Reversal	Normal
CFG(16)	Dynamic ODT Disabled	Dynamic ODT Enabled (def.)
CFG(18)	VCC 1.05V (def.)	VCC 1.5V
CFG(19)	DMI Lane Normal	DMI Lane Revers.
CFG(20)	SDVO or PCIE x1 Only(def.)	SDVO and PCIE x1 Simultaneously

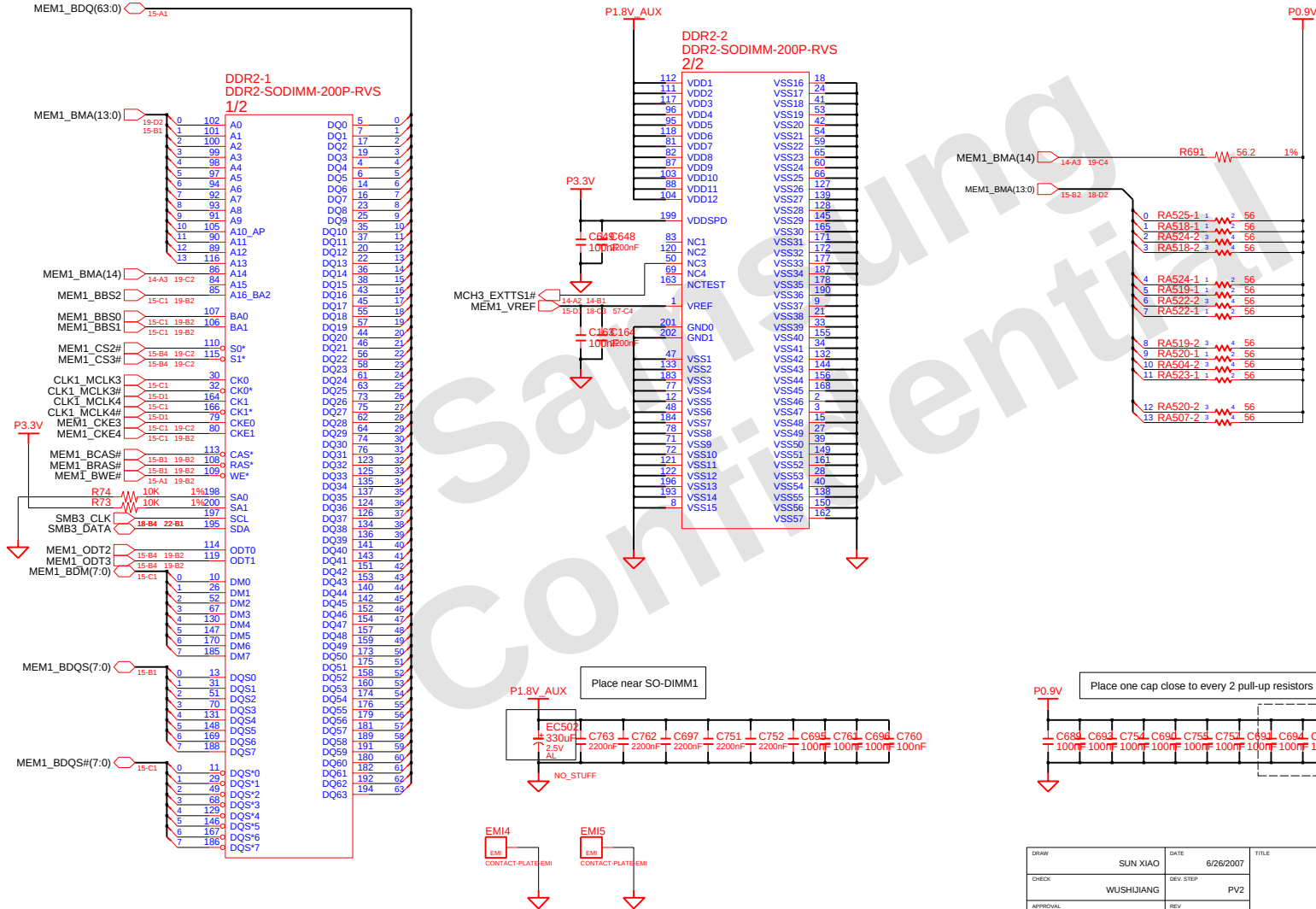
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Array resistors & Single resistors used to improve layout & routing.



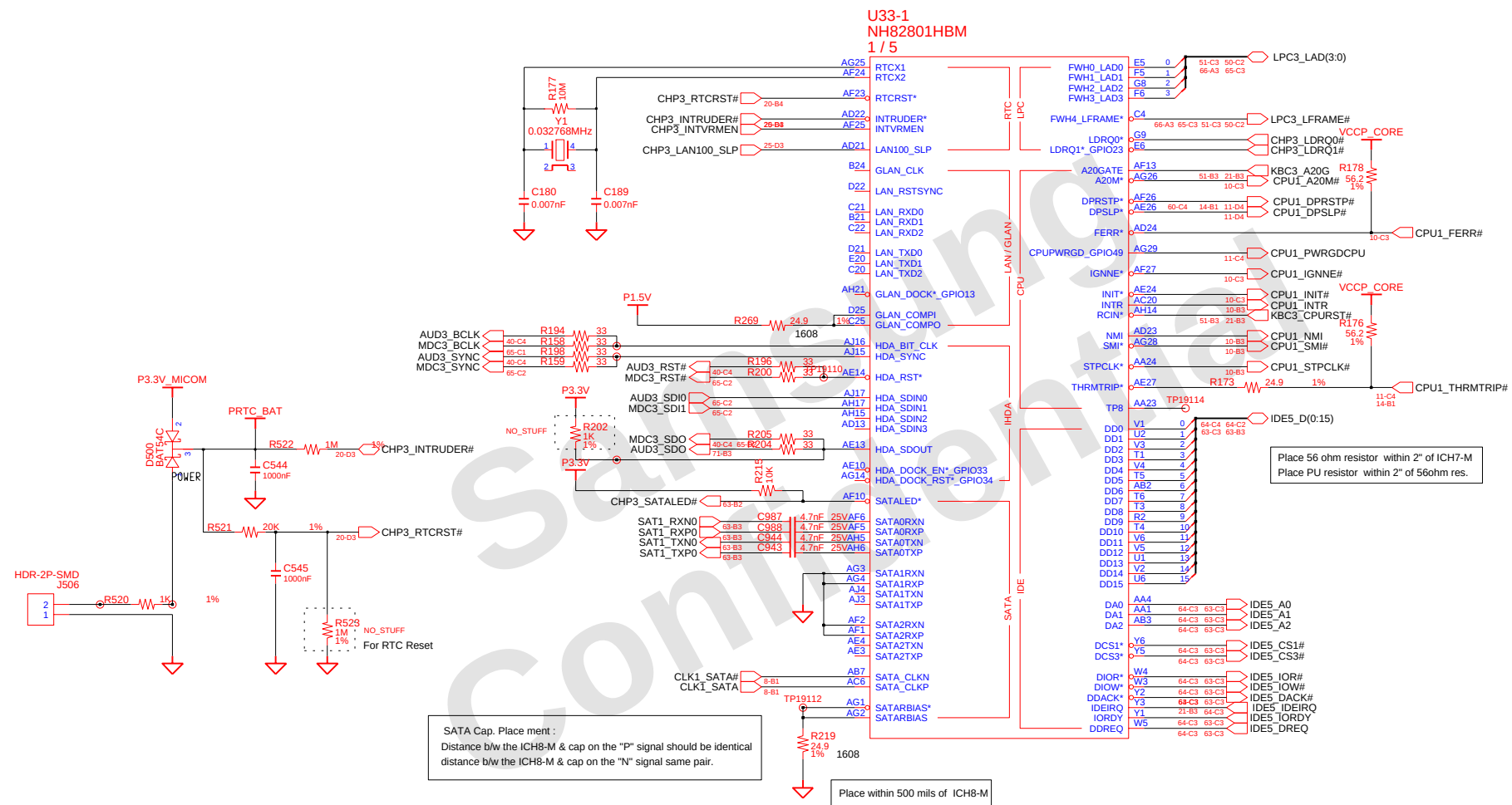
DDR SO-DIMM #1

Array resistors & Single resistors used to improve layout & routing.

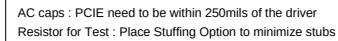


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena SODIMM DDR2 CH B	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2			PART NO. BA41-XXXXX
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				
				October 23, 2007 10:38:02 AM	PAGE	19 OF 58

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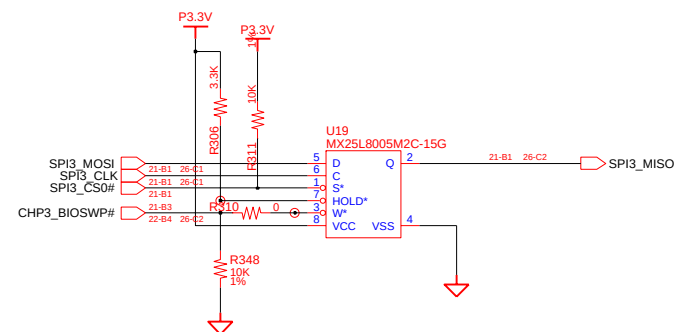
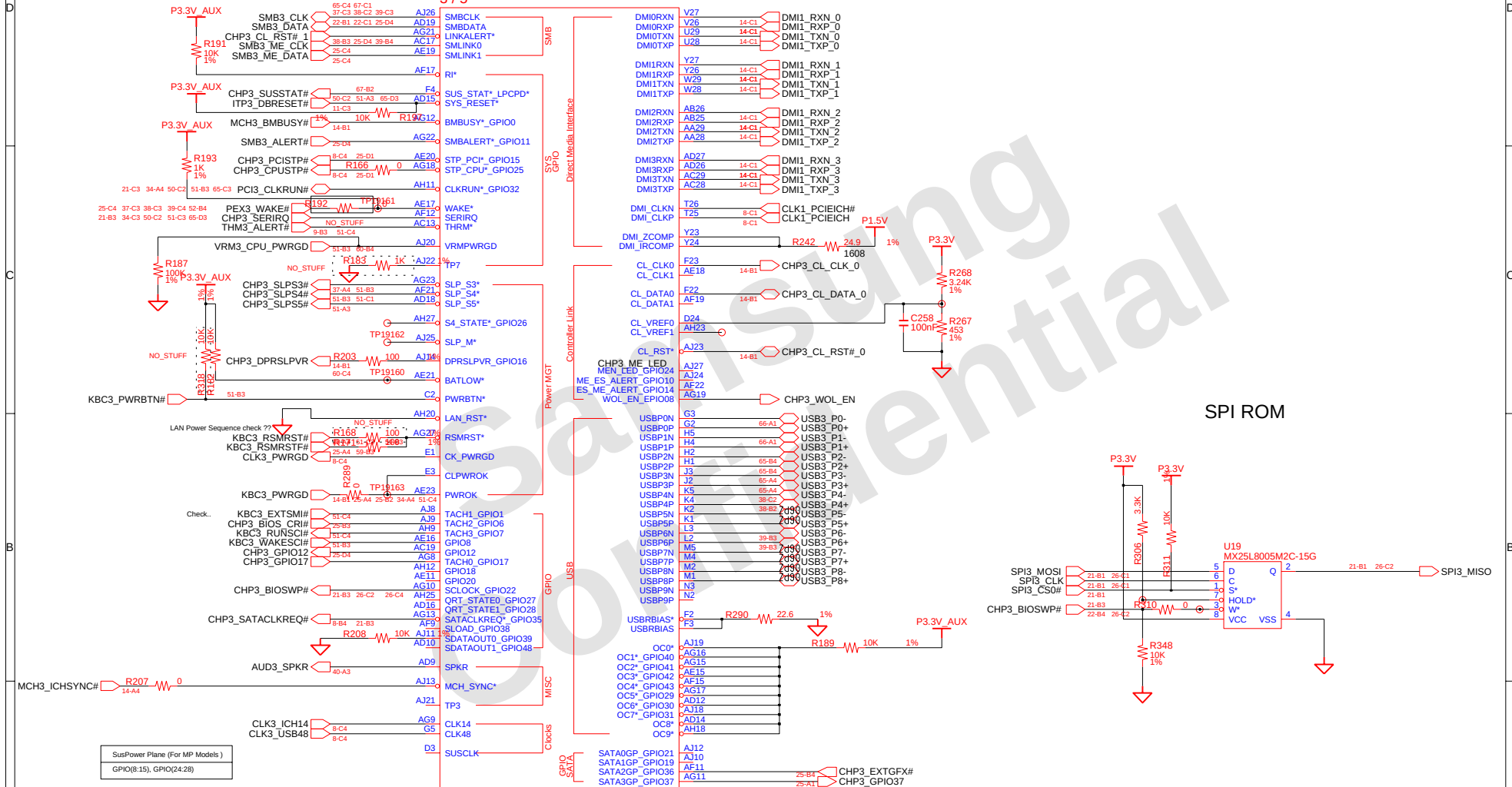


DRAW	SUN XIAO	DATE	6/26/2007	Gevena ICH8-M ICH8-M (1/5)	SAMSUNG ELECTRONICS	
CHECK	WUSHIJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0		PART NO.	BA41-XXXXXX
MODULE CODE	LAST EDIT				October 23, 2007 10:38:02 AM	PAGE 20 OF 58

D:/tingting/geneva/Geneva_pr_1023

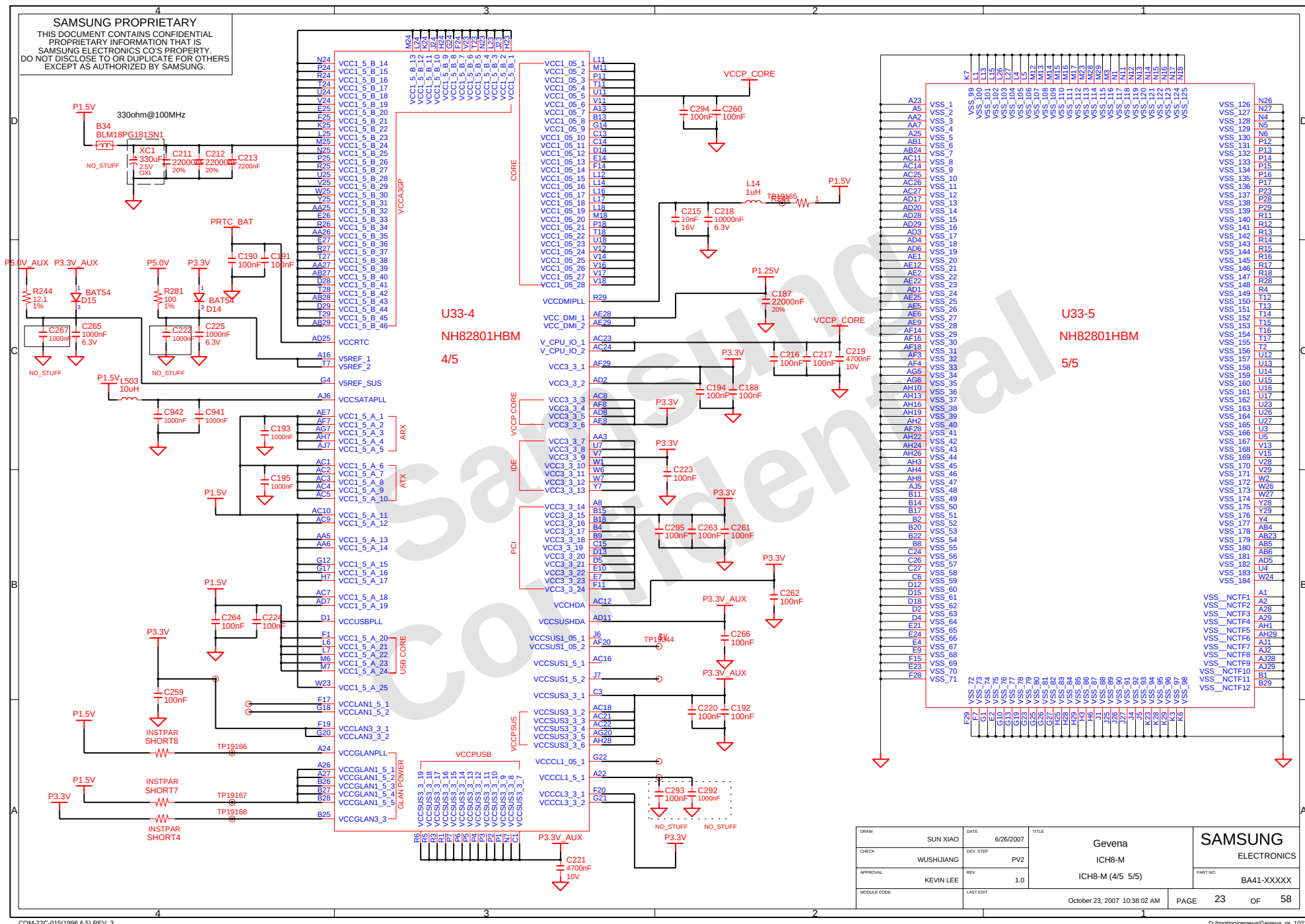
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U33-3
NH82801HBM
3 / 5



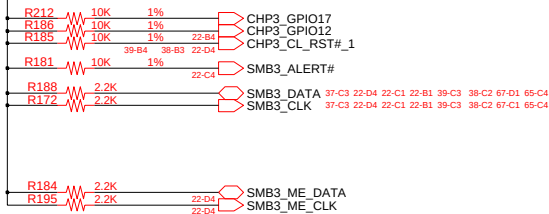
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena ICH8-M ICH8-M (3/5)	SAMSUNG		
CHECK	WUSHUJIANG	REV. STEP	PV2			ELECTRONICS		
APPROVAL	KEVIN LEE	REV	1.0			PART NO.	BA41-XXXXX	
MODULE CODE	LAST EDIT		October 23, 2007 10:38:02 AM			PAGE	22	OF

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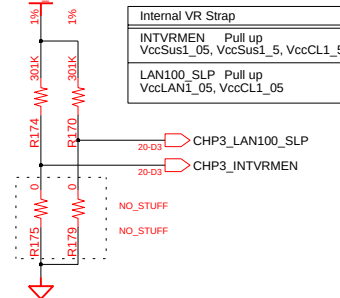


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P3.3V_AUX

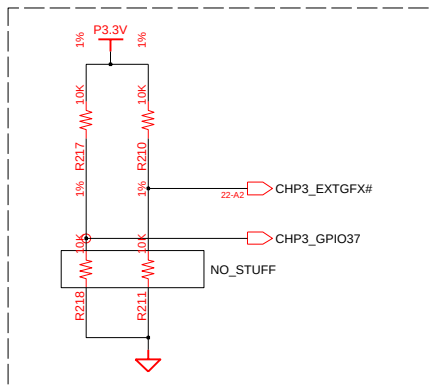


PRTC_BAT



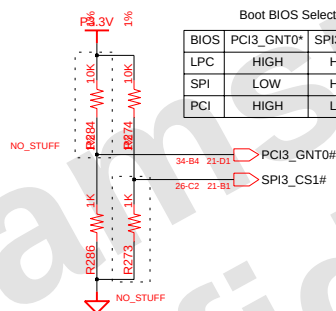
Internal VR Strap	
INTVRMEN	Pull up VccSus1_05, VccSus1_5, VccCL1_5
LAN100_SLP	Pull up VccLAN1_05, VccCL1_05

Model Dependent

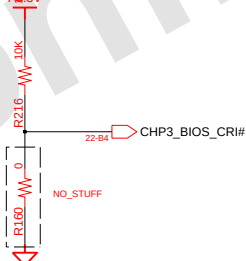


Boot BIOS Select

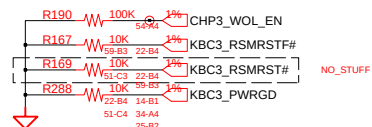
BIOS	PCI3_GNT0*	SPI3_CS1*
LPC	HIGH	HIGH
SPI	LOW	HIGH
PCI	HIGH	LOW



P3.3V

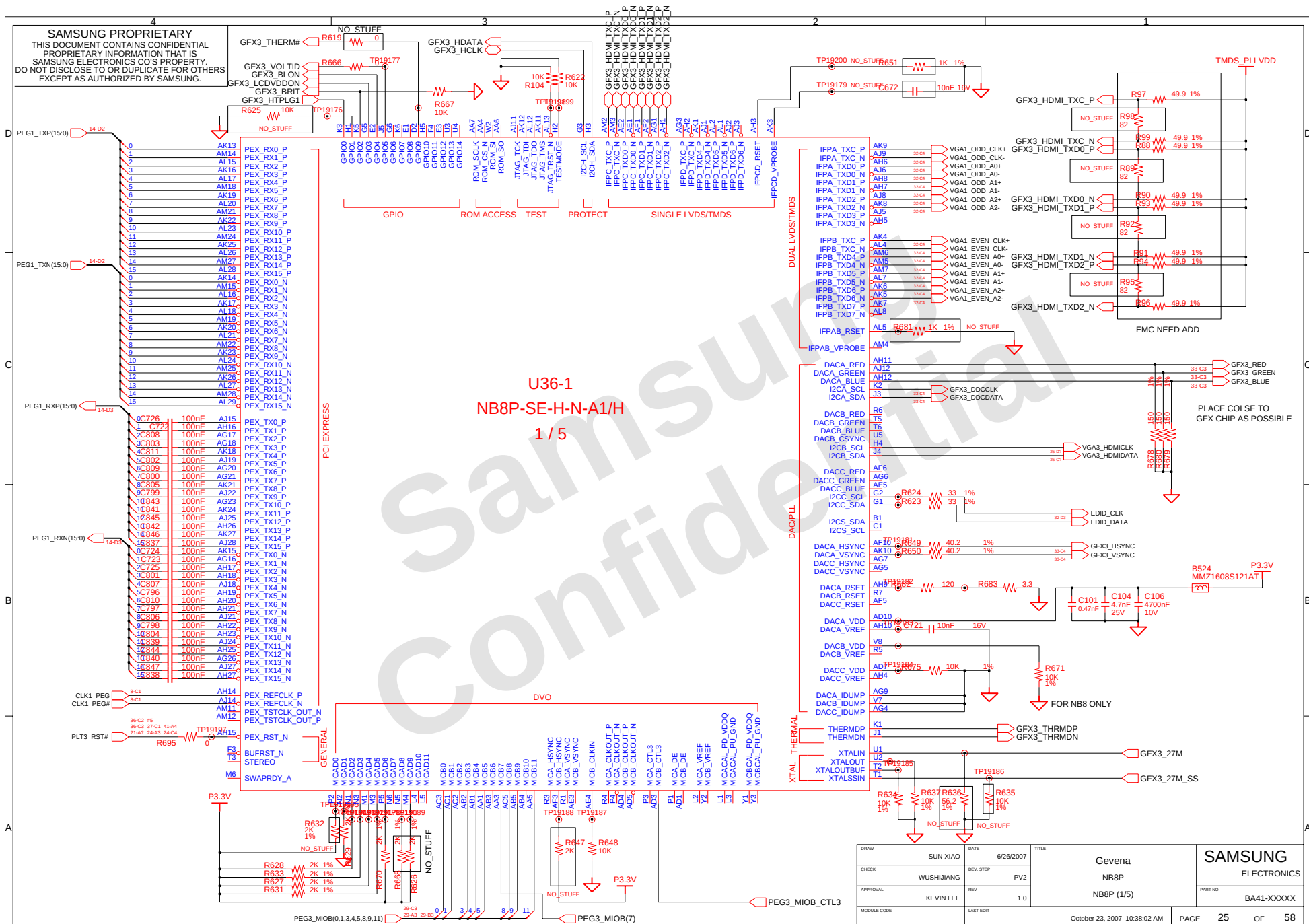


BIOS CRISIS RECOVER STRAP
PLACE NEAR KEYBOARD



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena ICH8-M ICH8-M STAP	SAMSUNG ELECTRONICS PART NO. BA41-XXXXX
CHECK	WUSHIJIANG	DEV STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	24 OF 58	

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DRAW	SUN XIAO	DATE	6/26/2007	Gevena NB8P NB8P (1/5)	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2		
APPROVAL	KEVIN LEE	REV	1.0		
PART NO.			BA41-XXXXXX		
MODULE CODE	LAST EDIT			October 23, 2007 10:38:02 AM	PAGE 25 OF 58

U36-4
NB8P-SE-H-N-A1/H
4 / 5

Pin	Description	ACTIVATE
GPIO(0)	HDMI DETECT	HIGH
GPIO(1)	TV DETECT	NC
GPIO(2)	PWM BRIGHTNESS CONTROL	HIGH
GPIO(3)	LCD VDD On ENABLE	HIGH
GPIO(4)	LCD Backlight On/OFF	HIGH
GPIO(5)	GPU Voltage ID0	HIGH=1.0V, LOW=1.1V
GPIO(6)	GPU Voltage ID1	NC
GPIO(7)	GPU Voltage ID2	NC
GPIO(8)	Thermal diode Alert	LOW
GPIO(9)	Fan Control	LOW
GPIO(10)	TBD	NC
GPIO(11)	HDTV enable	HIGH
GPIO(12)	HDTV DET	NC

Component	Value	Location
EC511	330uF	2.5V AL
C698	1000nF	10V
C882	1000nF	10V
C770	4700nF	6.3V
C880	4700nF	6.3V
C698	100uF	100nF
C771	100nF	100nF
C831	100nF	100nF
C700	22nF	25V
C820	22nF	25V
C825	22nF	25V
C822	22nF	25V
C821	4.7nF	25V
C823	4.7nF	25V
C828	4.7nF	25V
C829	4.7nF	25V
C834	25V	25V
C836	4.7nF	25V



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U36-5 NB8P-SE-H-N-A1/H 5 / 5

DRAW SUN XIAO DATE 6/26/2007 TITLE Gevena NB8P NB8P (5/5)

CHECK WUSHUIJANG DEV STEP PV2

APPROVAL KEVIN LEE REV 1.0

MODULE CODE LAST EDIT

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October 23, 2007 10:38:02 AM

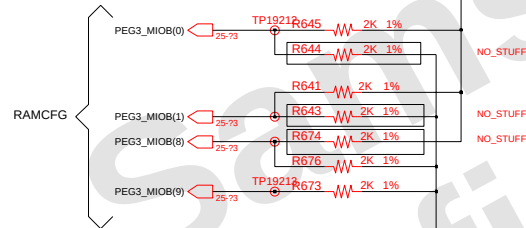
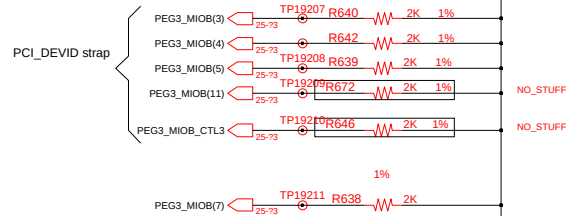
BA41-XXXXX

DRAW		DATE		Gevena NB8P NB8P (5/5)		SAMSUNG ELECTRONICS			
SUN XIAO		6/26/2007							
CHECK		DEV. STEP							
WUSHIJIANG		PV2							
APPROVAL		REV		PART NO.		BA41-XXXXX			
KEVIN LEV		1.0							
MODULE CODE		LAST EDIT		October 23, 2007 10:38:02 AM		PAGE	28	OF	58

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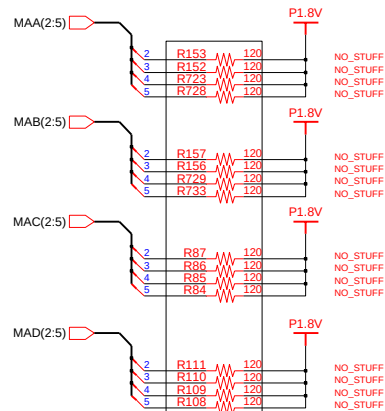
PCI_DEVID strap

	R646	R672	R640	R639	R642
NB8P-SE	NO_STUFF	NO_STUFF	STUFF	NO_STUFF	STUFF
	0	0	1	1	1
NB8M-GS	NO_STUFF	NO_STUFF	STUFF	STUFF	STUFF
	0	0	1	1	1



RAMCFG

	R674	R676	R641	R643
SS 256Mbit	STUFF	NO_STUFF	STUFF	NO_STUFF
IF 256Mbit	STUFF	NO_STUFF	NO_STUFF	STUFF
SS 512Mbit	NO_STUFF	STUFF	STUFF	NO_STUFF
IF 512Mbit	NO_STUFF	STUFF	NO_STUFF	STUFF



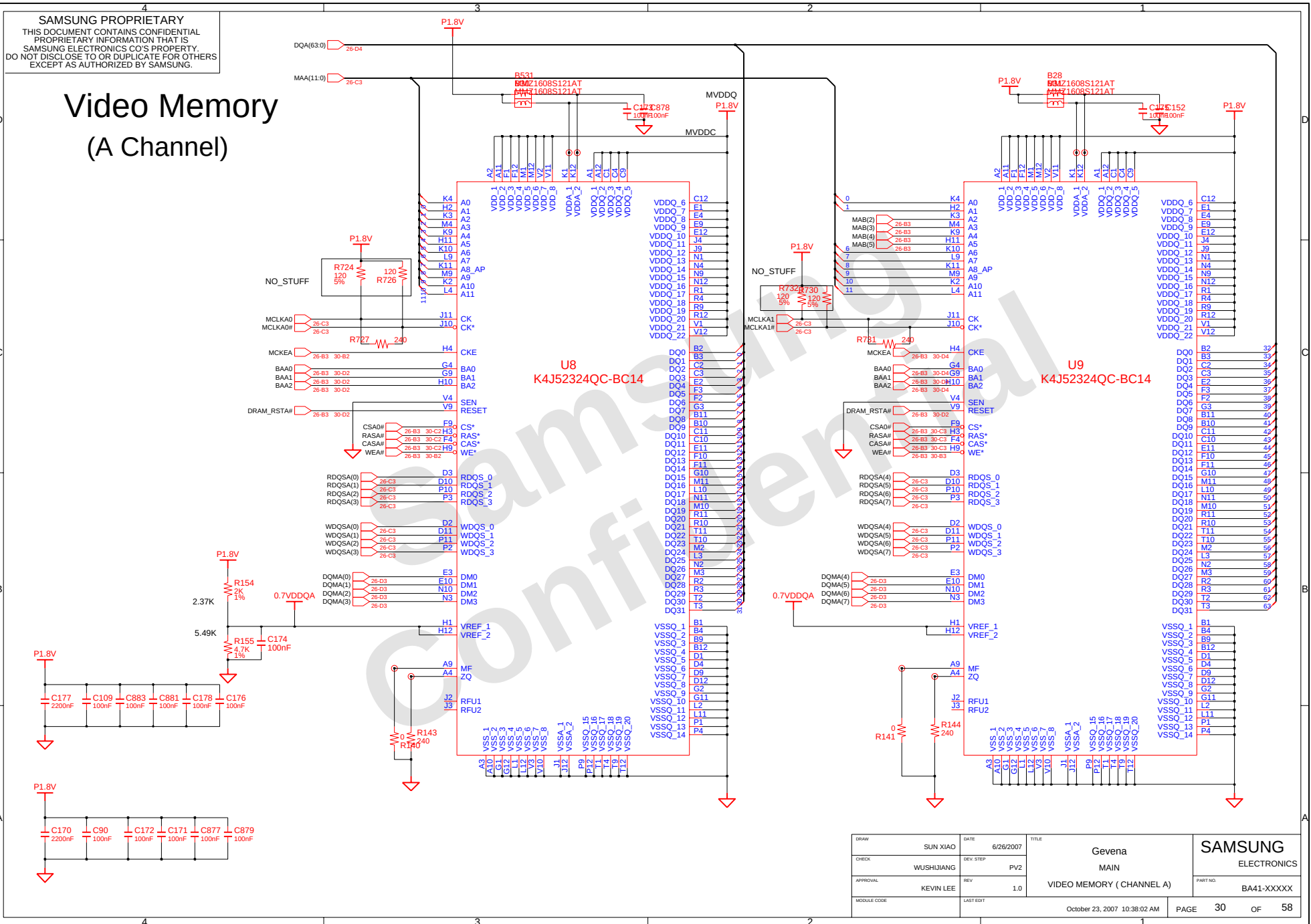
Straps	Pin # (Rev.A02)	Descriptions
SUB_VENDOR	MIOAD(1)	0 : No BIOS 1 : Read from BIOS
RAMCFG(3:0) [9,8,1,0]	MIOB(9) MIOB(8) MIOB(1) MIOB(0)	0111 : SAMSUNG GDDR3 256Mbit 0101 : INFINEON GDDR3 256Mbit 0011 : SAMSUNG GDDR3 512Mbit 0001 : INFINEON GDDR3 512Mbit
CRYSTAL(1:0)	MIOB(2) MIOB(6)	01 : 14.318 MHz 10 : 27 MHz 11 : Unknown 00 : 13.5 MHz
TV_MODE(1:0)	MIOAD(7) MIOAD(10)	00 : SECAM 01 : NTSC 10 : PAL 11 : CRT
PCI_DEVID(4:0) [CTL3,11,3,5,4]	MIOB_CTL(3) MIOB(11) MIOB(3) MIOB(5) MIOB(4)	NB8M-GS: 00111 NB8M-SE: 01000 NB8M-GT: 00110 NB8P-GS: 00111 NB8P-SE: 00101 NB8P-GT: 01000
ROM_TYPE(1:0)	MIOB(10) MIOBVSYN	NO ROM(NC)
USER STRAP	MIOAD(2:5)	EDID
GPIO(12:0)	Sheet 25	Refer to Sheet 25

Test Options for GPIO should be updated..

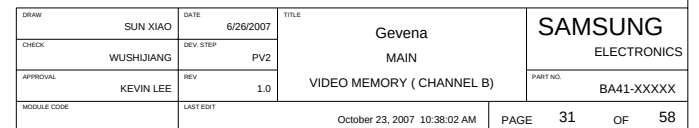
Samsung		Infineon	
256Mbit	512Mbit	256Mbit	512Mbit
K4J55323QG (1.2/1.4/1.6/2.0) K4J55323QF (1.4/1.6/2.0)	K4J52324QC (1.2/1.4/1.6/2.0)	HYB18H256321AF120(2.0) HYB18H256321AF16(1.6) HYB18H256324AF-20(2.0) HYB18H256321AF-14(1.4) HYB18H256321AF-12(1.2)	HYB18H512321AF120(2.0) HYB18H512321AF16(1.6) HYB18H512324AF-20(1.6) HYB18H512321AF-14(1.4) HYB18H512321AF-12(1.2)
8M*32BIT	16M*32BIT	8M*32BIT	16M*32BIT

DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2		NB8P	
APPROVAL	KEVIN LEE	REV	1.0		NB8P STRAP	
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM			
PAGE 29 OF 58						PART NO. BA41-XXXXX

Video Memory (A Channel)

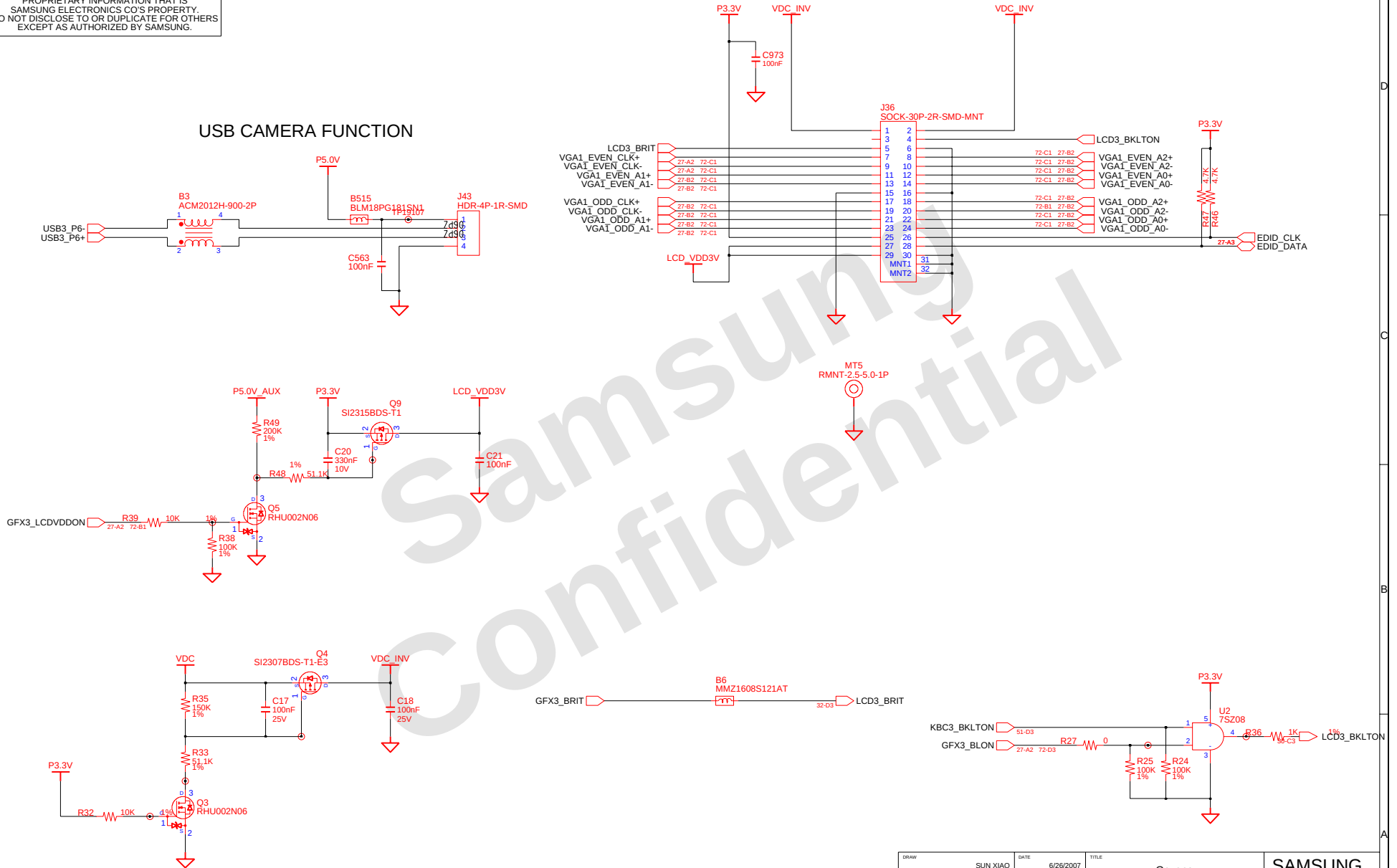


Video Memory (C Channel)



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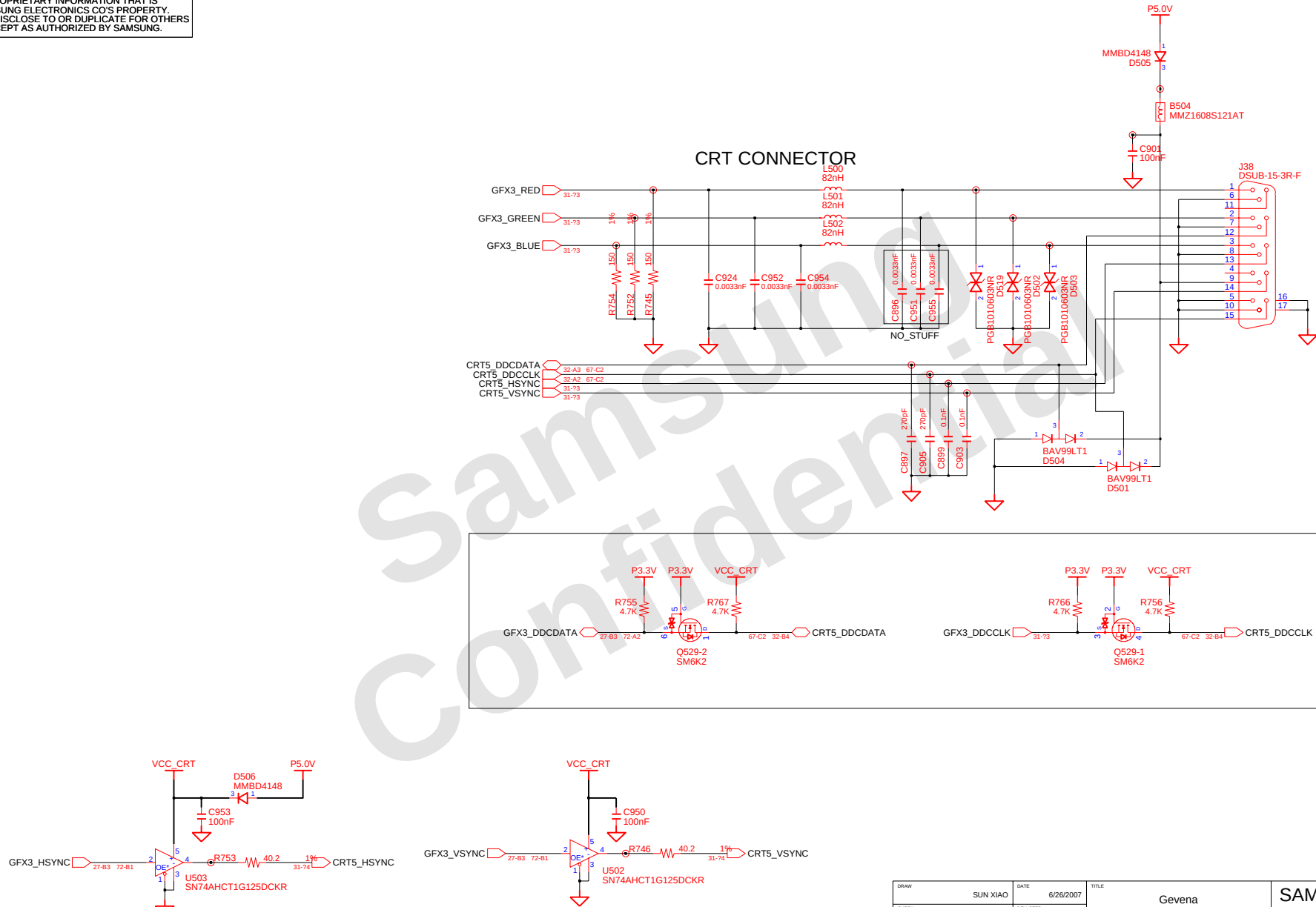
USB CAMERA FUNCTION



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV STEP	PV2	LCD CONNECTER & SPEED SPECTRUM		ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		PART NO.	BA41-XXXXX
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	32	OF 58

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CRT CONNECTOR



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena MIAN CRT PORT	SAMSUNG ELECTRONICS PART NO. BA41-XXXXX
CHECK	WUSHUJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				
October 23, 2007 10:38:02 AM						PAGE 33 OF 58

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Option

DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena
CHECK	WUSHUJIANG	DEV. STEP	PV2		SIO CHIPSET
APPROVAL	KEVIN LEE	REV	1.0		
MODULE CODE		LAST EDIT			

PAGE 34 OF 68

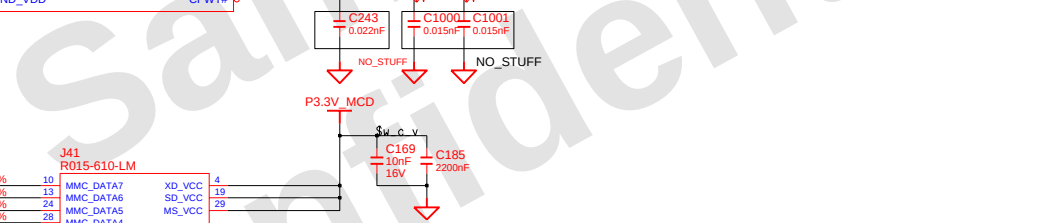
October 23, 2007 10:38:02 AM

Part No. BA41-

D:\jingling\geneva\geneva_pr_1021

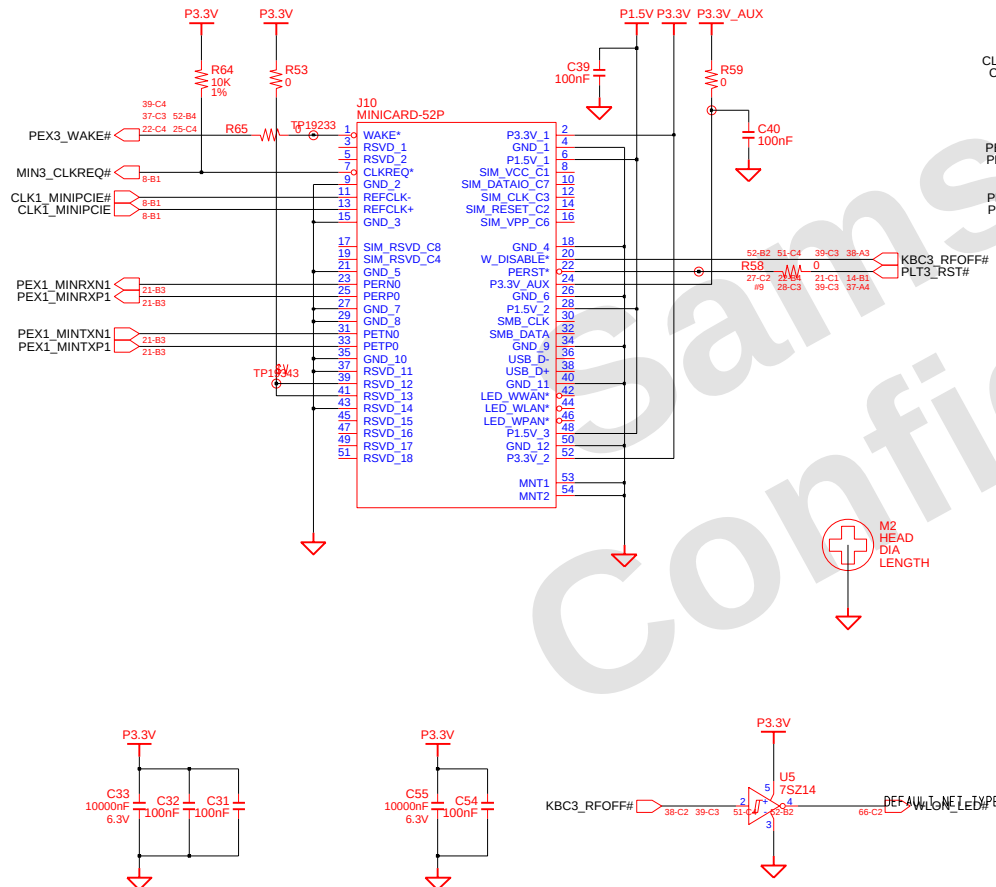
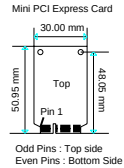
COM-22C-015(1996.6.5) REV. 3

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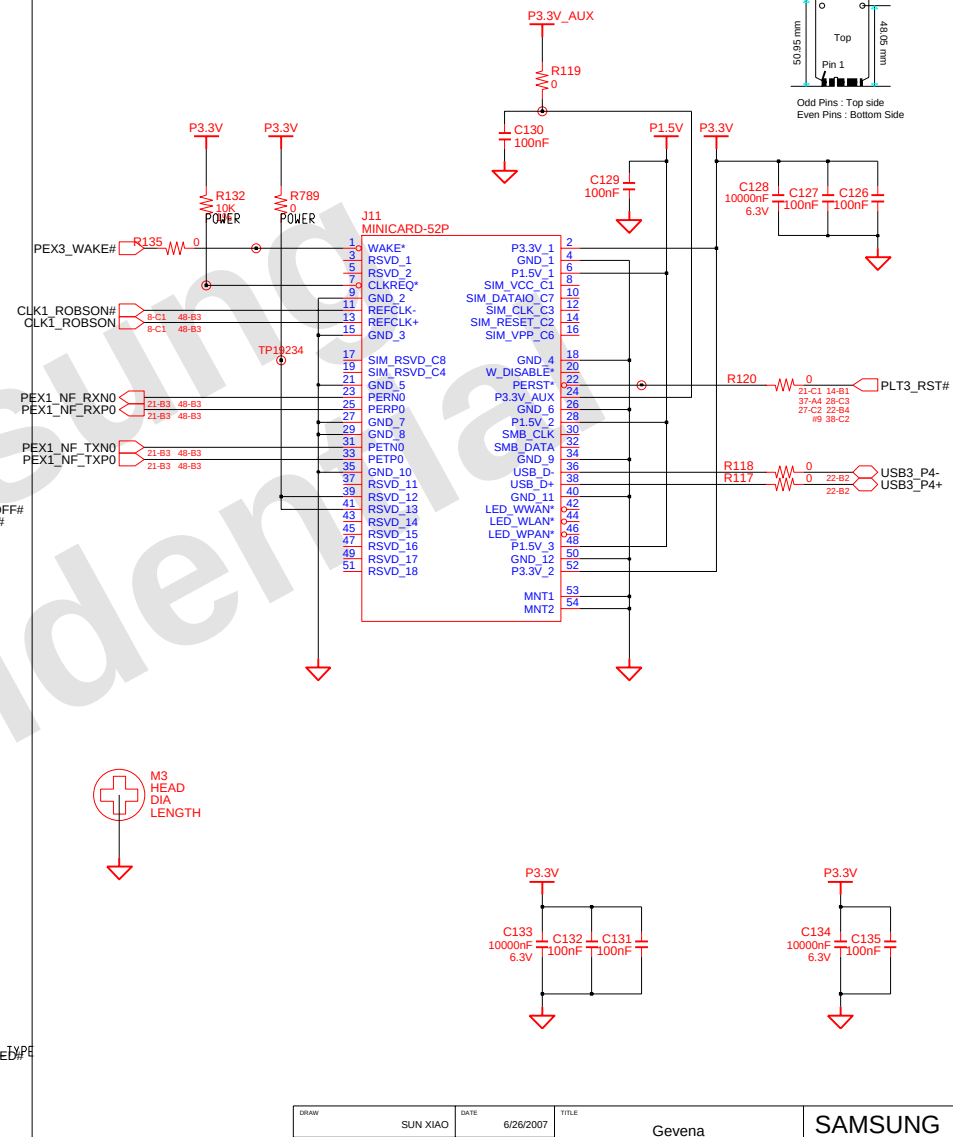
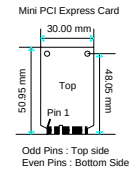
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Mini Card



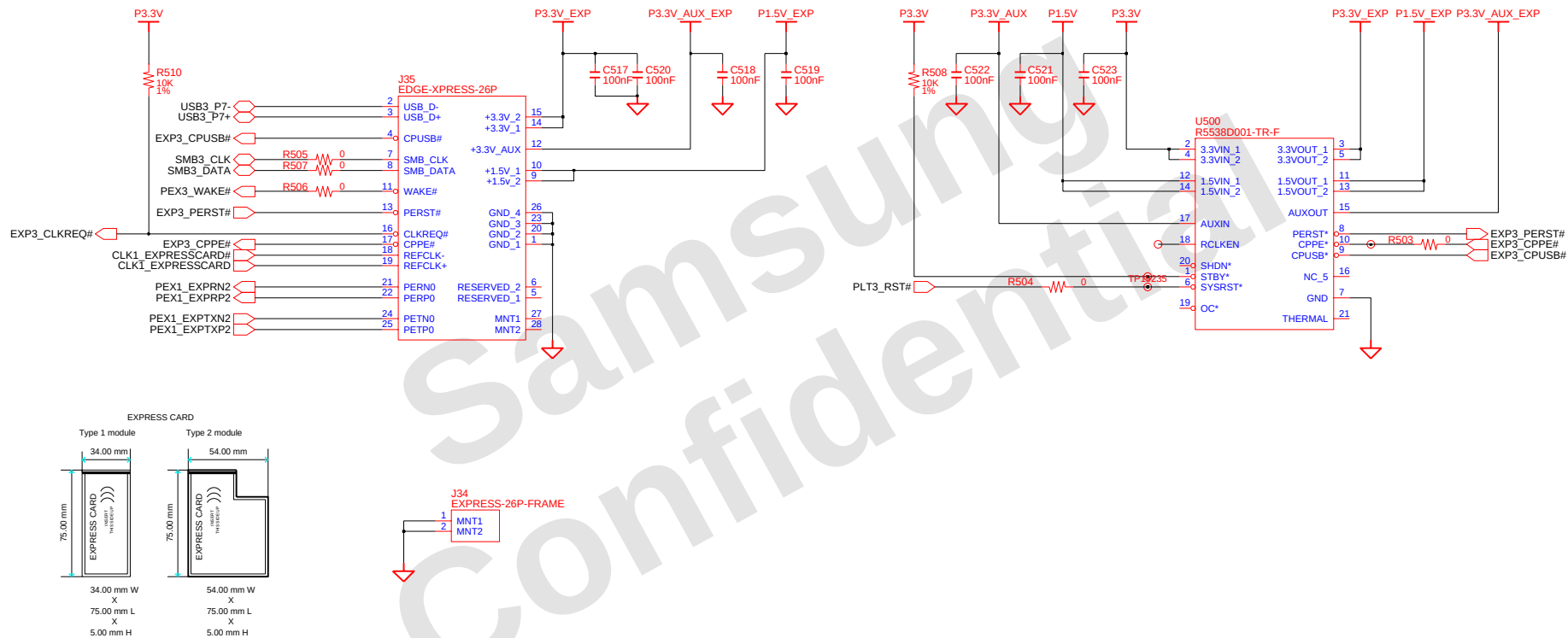
MINI CARD FOR ROBSON



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV. STEP	PV2	MINI CARD	MINI CARD	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	FRO BOBSON AND DVB-T	PART NO.	BA41-XXXXX
MODULE CODE		LAST EDIT		October 23, 2007 10:38:02 AM	PAGE	36 OF 58

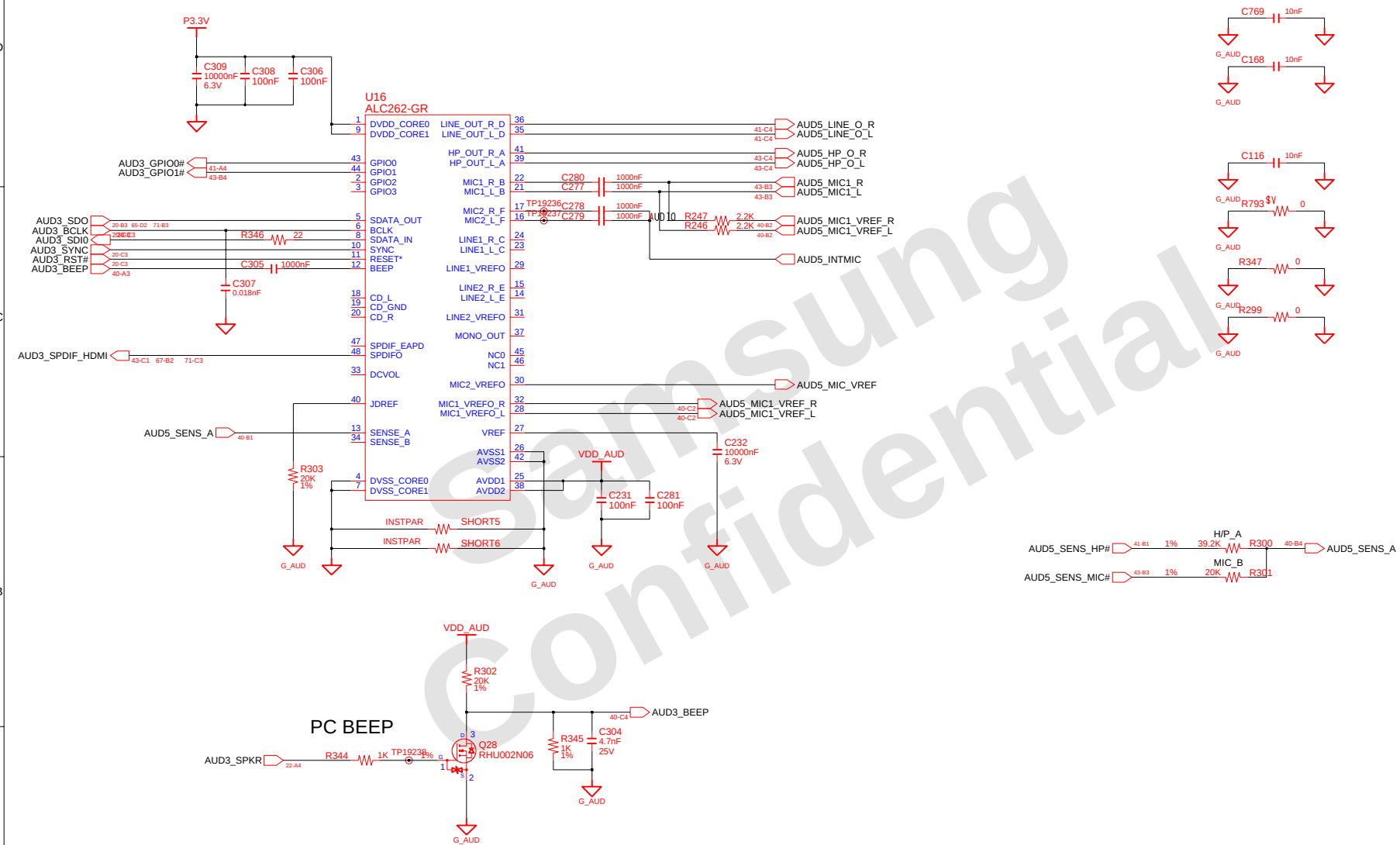
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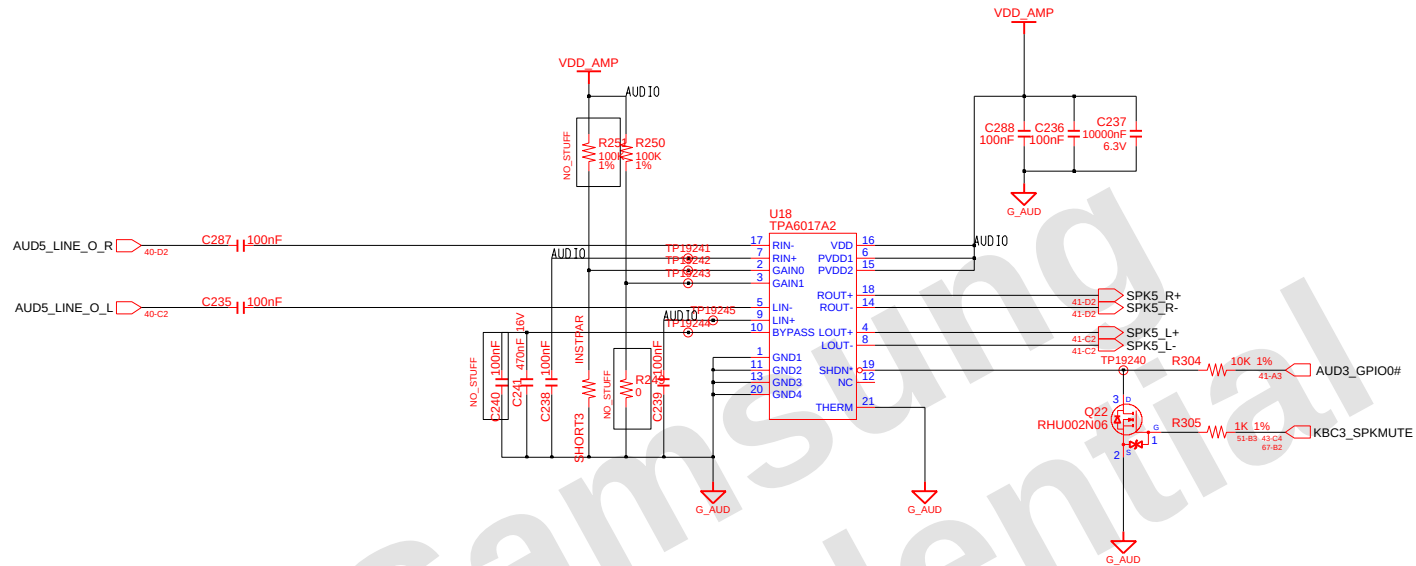
DRAW	SUN XIAO	DATE	6/27/2007	TITLE	Geneva MIO BOARD EXPRESS CARD	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2			PART NO. BA41-XXXX
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	37	OF 60

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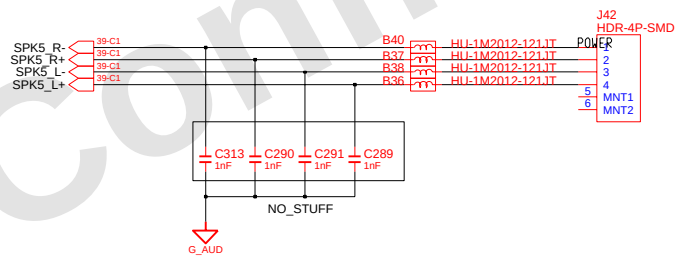


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV. STEP	PV2	AUDIO CODEC	ELECTRONICS	
APPROVAL	KEVIN LEE	REV	1.0	FRO HDMI	PART NO.	BA41-XXXXX
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	38	OF 58

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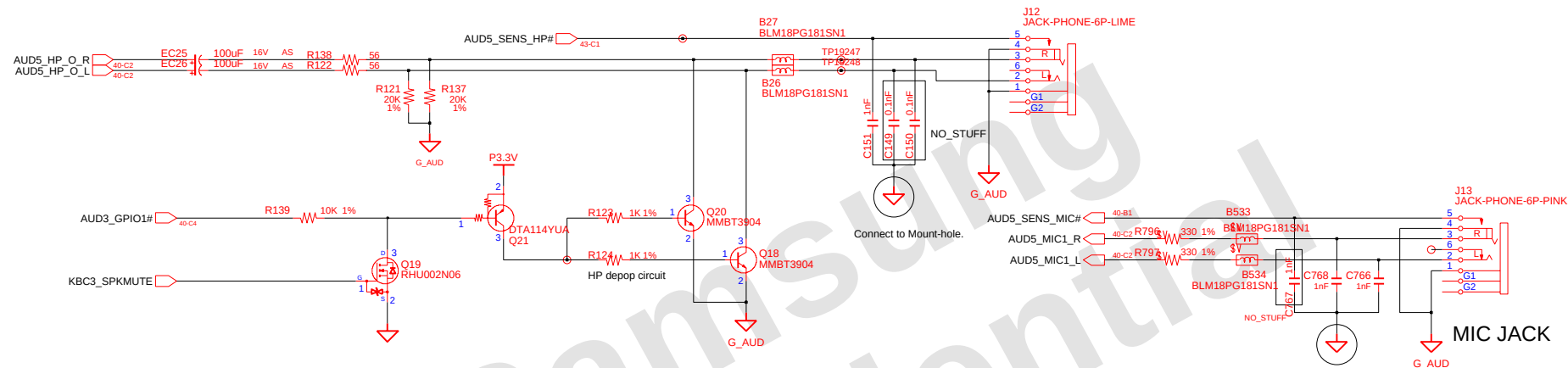
INTERNAL STEREO SPEAKERS



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2	AUDIO		
APPROVAL	KEVIN LEE	REV	1.0	AMP		
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	39	OF 58

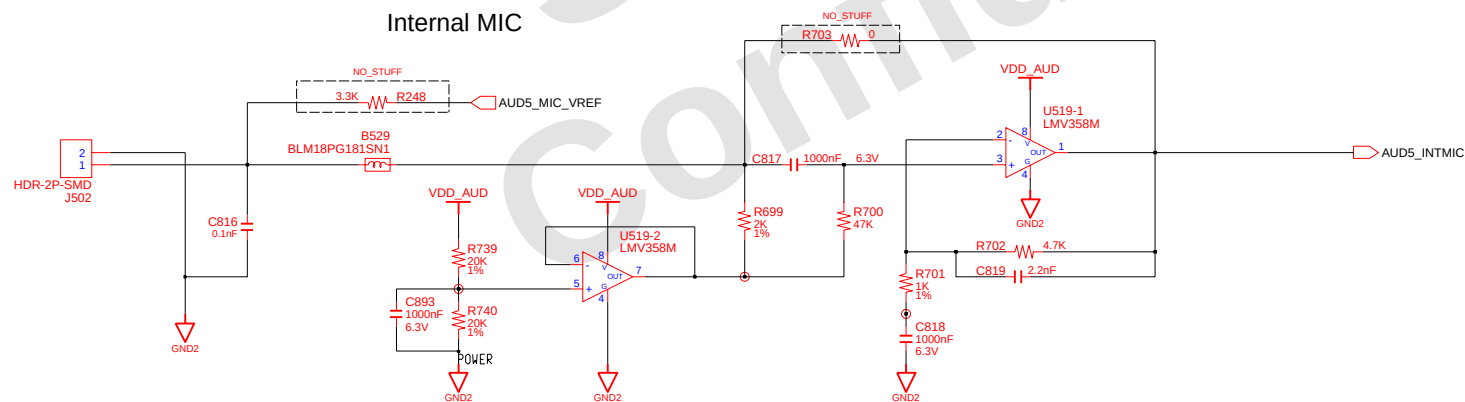
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HEADPHONE



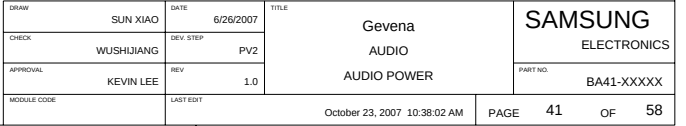
The traces led to Audio Jacks have the width over 10mil

Internal MIC



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2	HEAD PHONE JACK		
APPROVAL	KEVIN LEE	REV	1.0	AND MIC JACK		
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	40	OF 58

AUDIO POWER

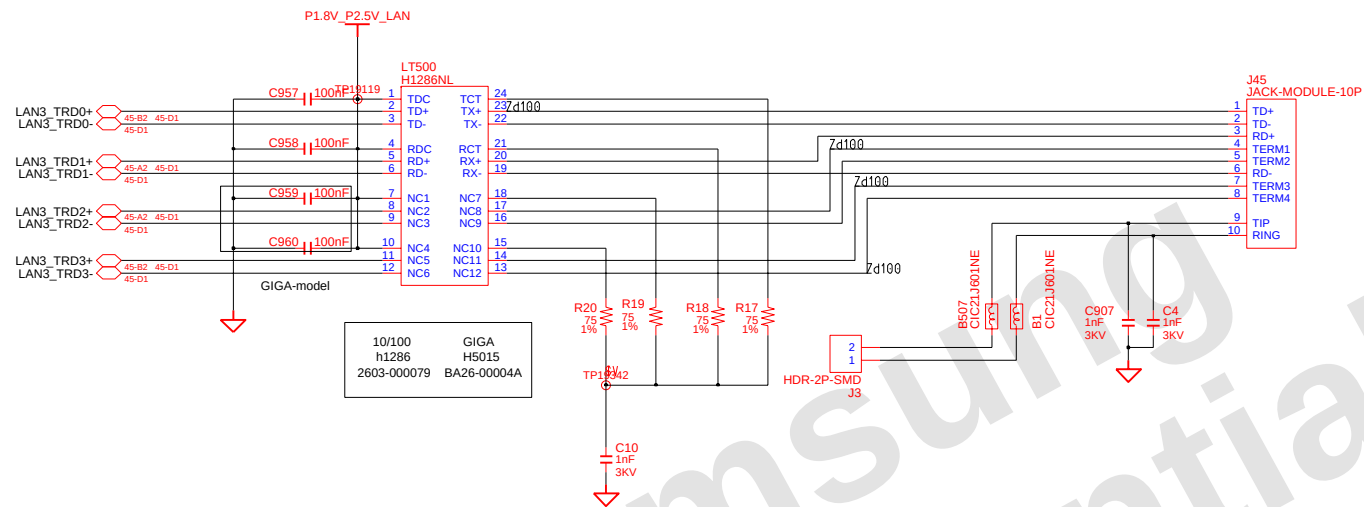


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DRAW		DATE		Gevena LAN 88E8055		SAMSUNG ELECTRONICS	
SUN XIAO		6/26/2007					
CHECK		DEV. STEP					
WUSHUJIANG		PV2					
APPROVAL		REV				PART NO.	
KEVIN LEE		1.0				BA41-XXXXXX	
MODULE CODE		LAST EDIT		October 23, 2007 10:38:02 AM		PAGE 42 OF 58	

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The distance between LAN controller
and transformer is designed to extend less than two inches.

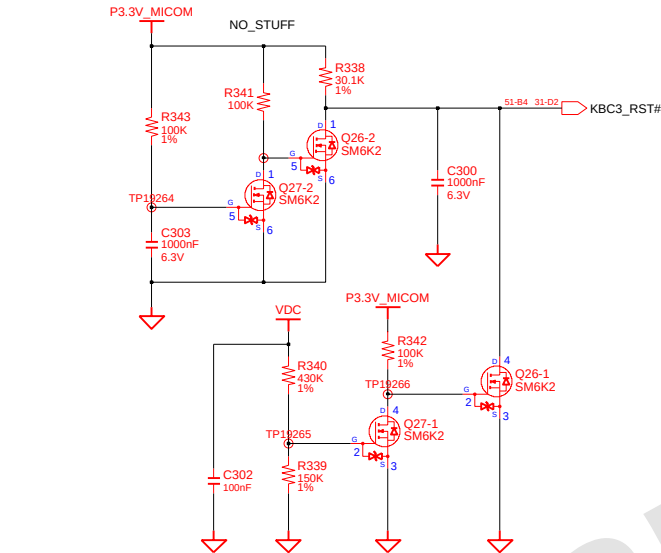


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena LAN LAN JACK RJ45	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2			PART NO. BA41-XXXXX
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	43	OF 58

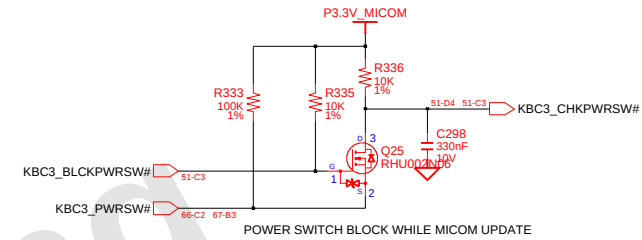
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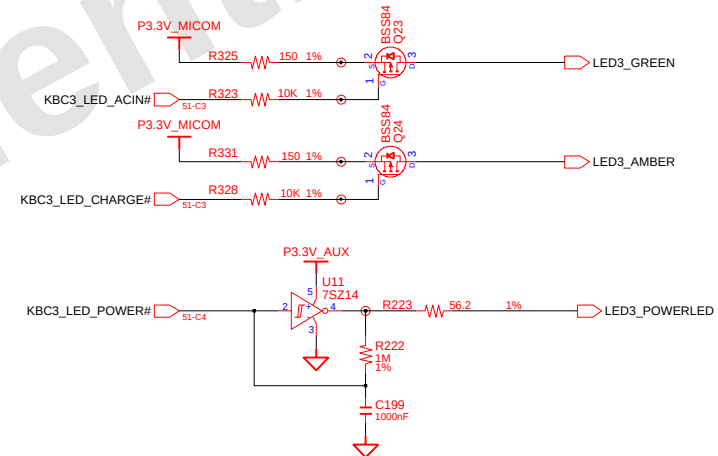
MICOM RESET



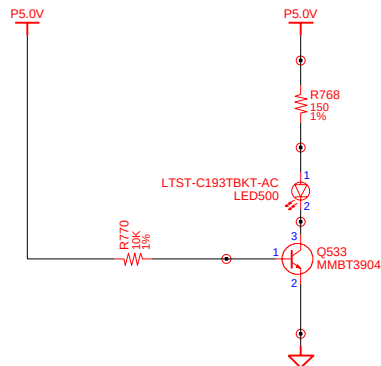
FOR MICOM UPDATE



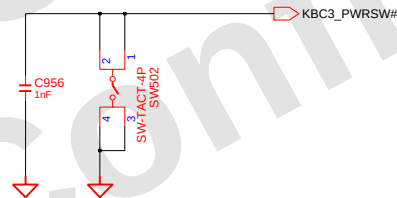
ADAPTERIN/CHARGING LED



POWER ON LED



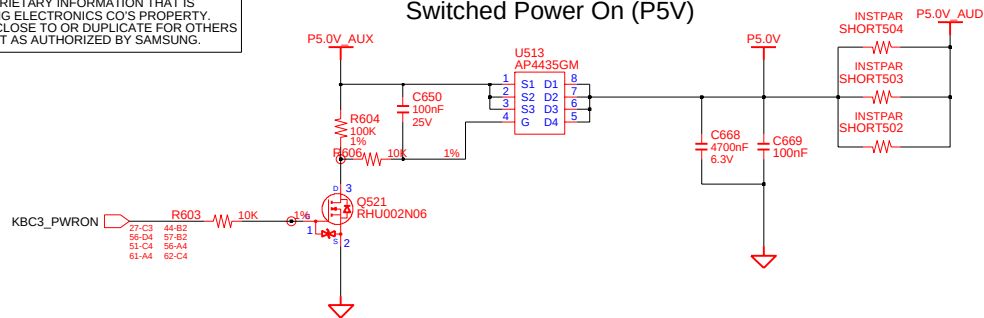
POWER ON SW BUTTON



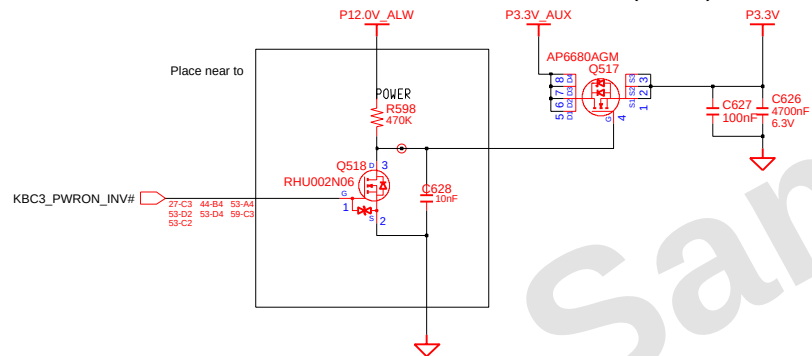
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV. STEP	PV2		MICOM	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		MICOM LOGIC SCH	PART NO.
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM			BA41-XXXXX
					PAGE	45 OF 58

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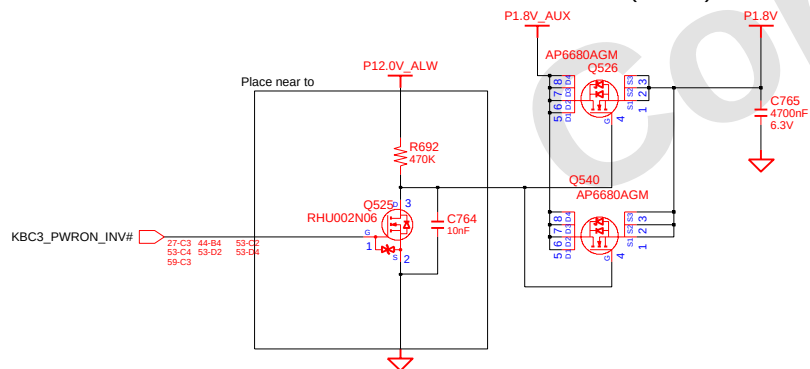
Switched Power On (P5V)



Switched Power On (P3.3V)

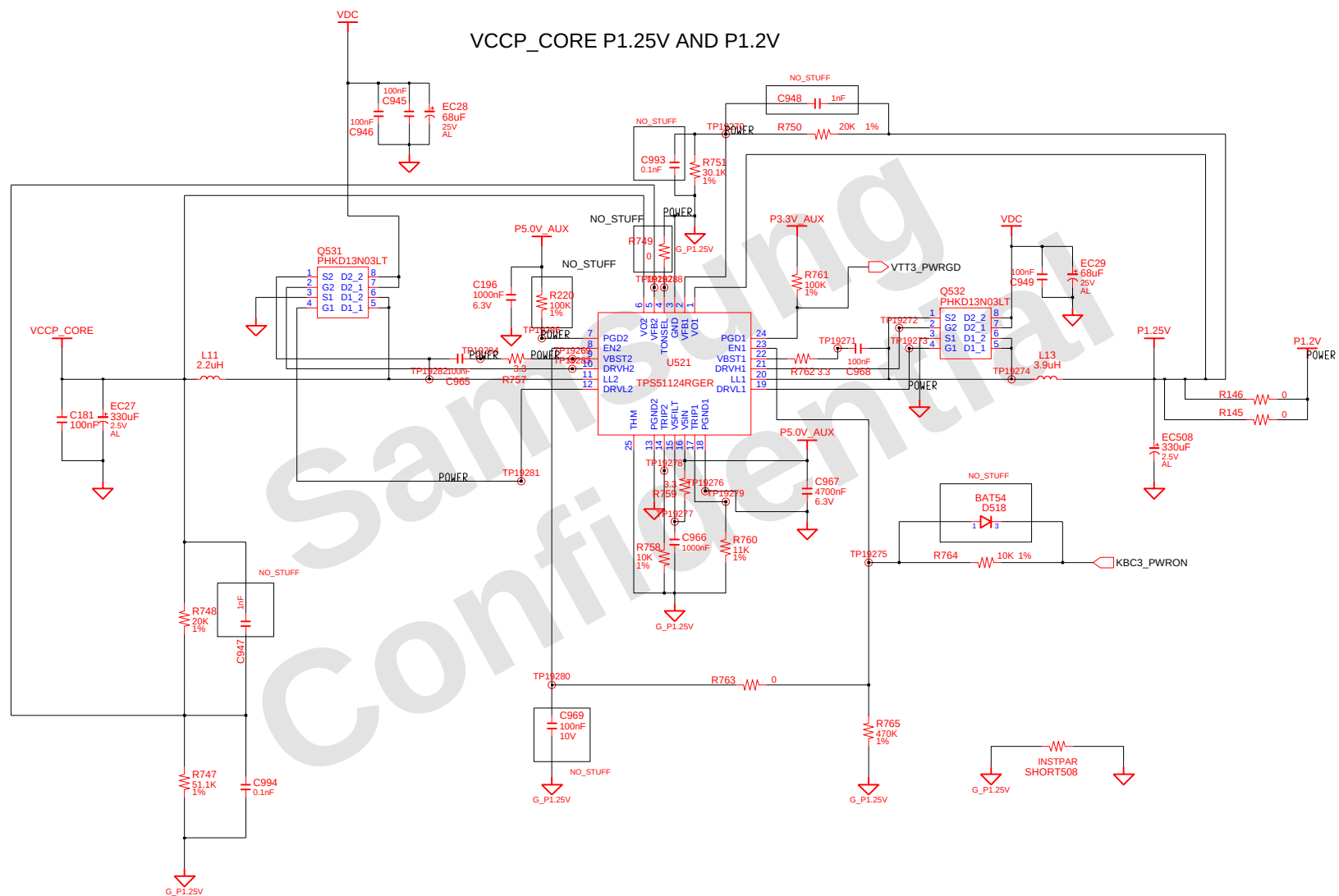


Switched Power On (P1.8V)



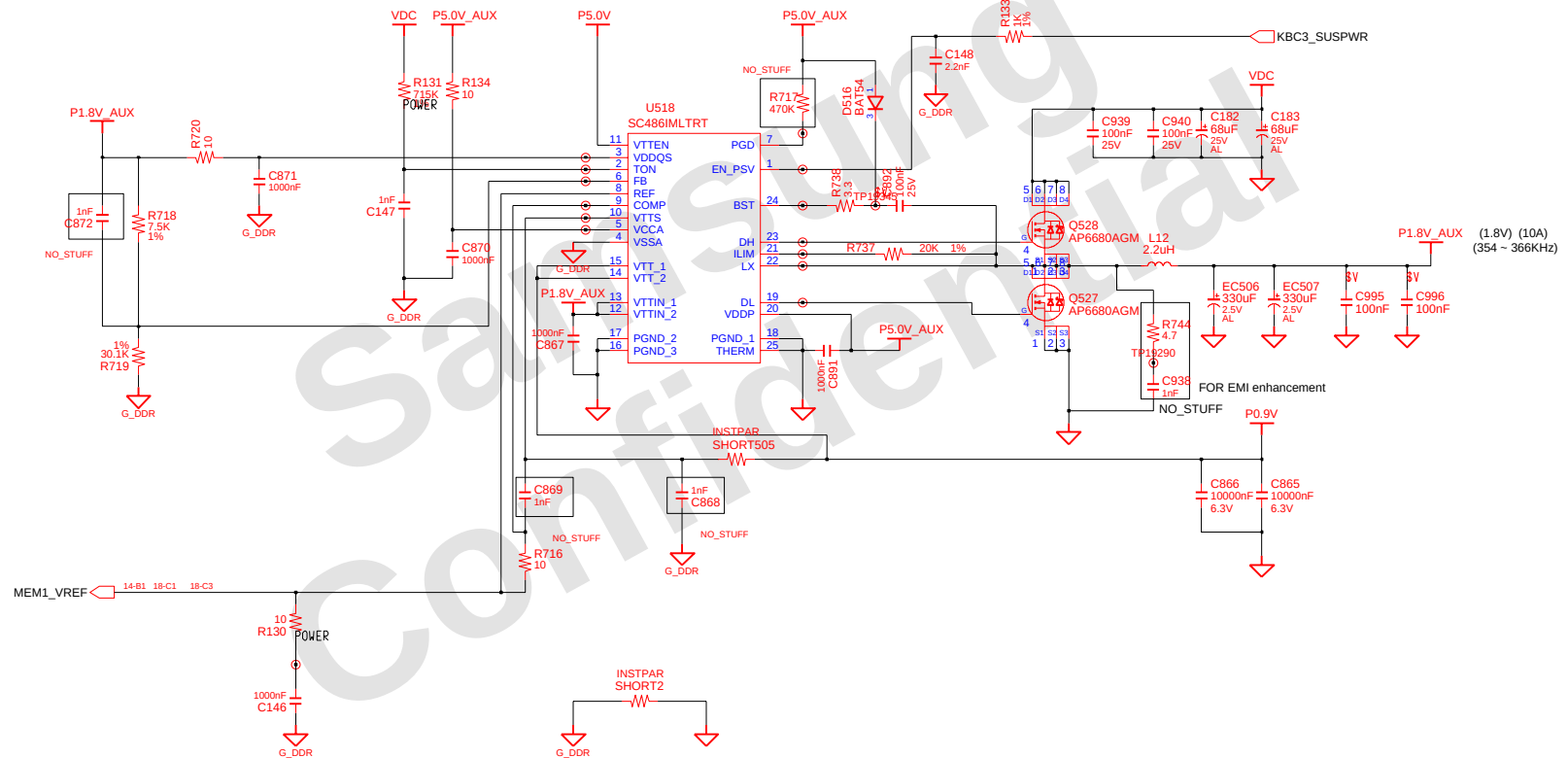
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena MAIN	SAMSUNG ELECTRONICS
CHECK	WUSHUJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	46	OF 58

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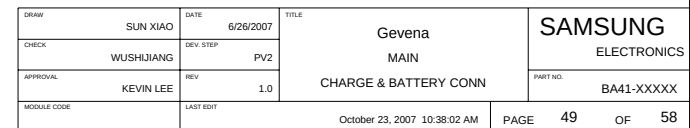
DRAW	SUN XIAO	DATE	6/26/2007	Gevena MAIN VCCP P1.25V AND P1.2V	SAMSUNG ELECTRONICS	
CHECK	WUSHIJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0		PART NO.	BA41-XXXXX
MODULE CODE	LAST EDIT				October 23, 2007 10:38:02 AM	PAGE 47 OF 58

DDR2 Power

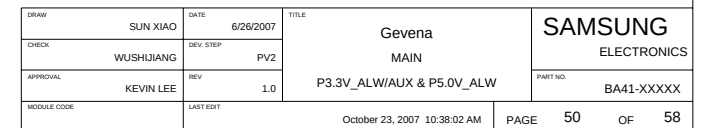


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV. STEP	PV2		MAIN	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		DDR2 POWER	PART NO. BA41-XXXXX
MODULE CODE		LAST EDIT		October 23, 2007 10:38:02 AM	PAGE 48	OF 58

CHARGER & POWER MANAGEMENT

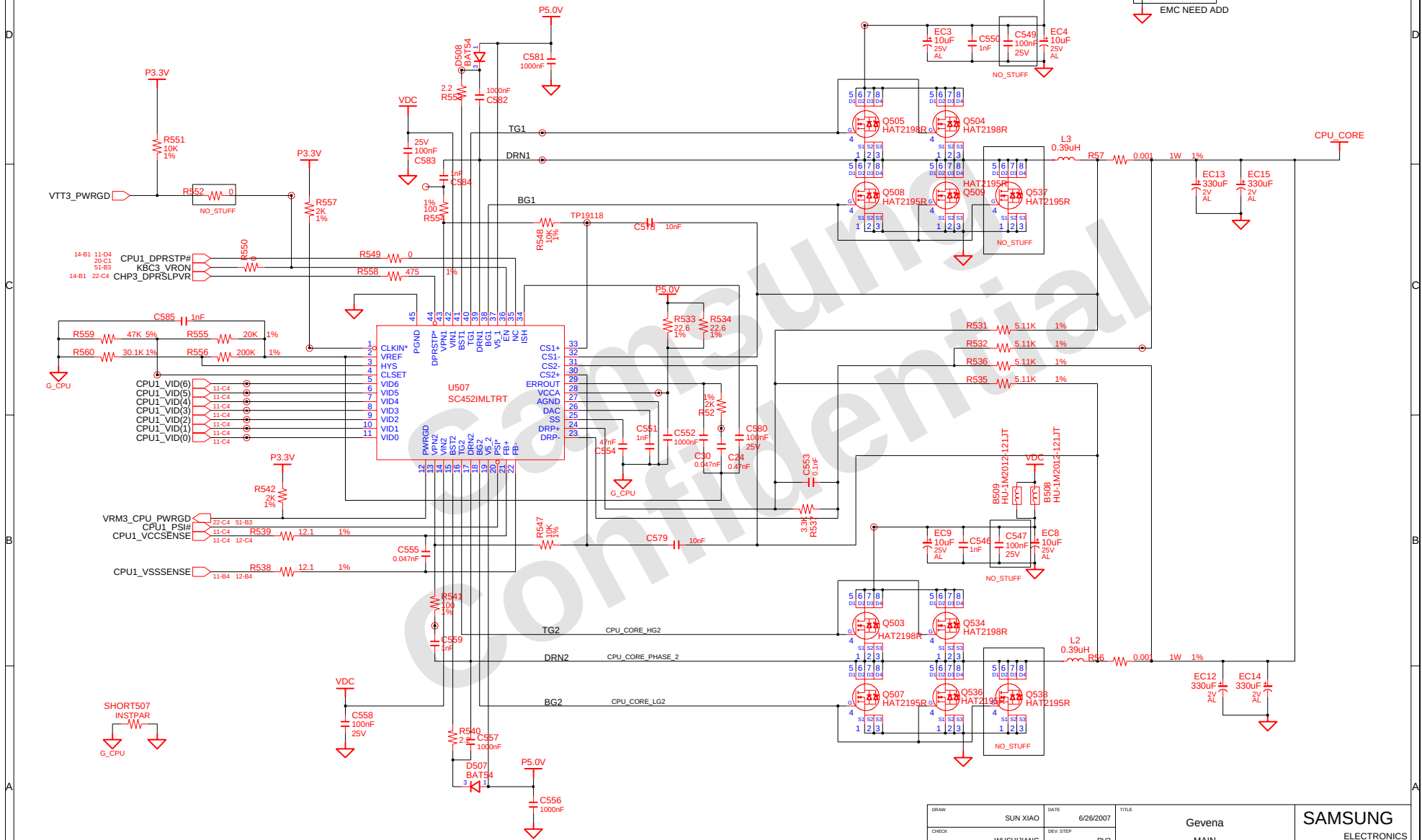


P3.3V_ALW/AUX & P5V_AUX

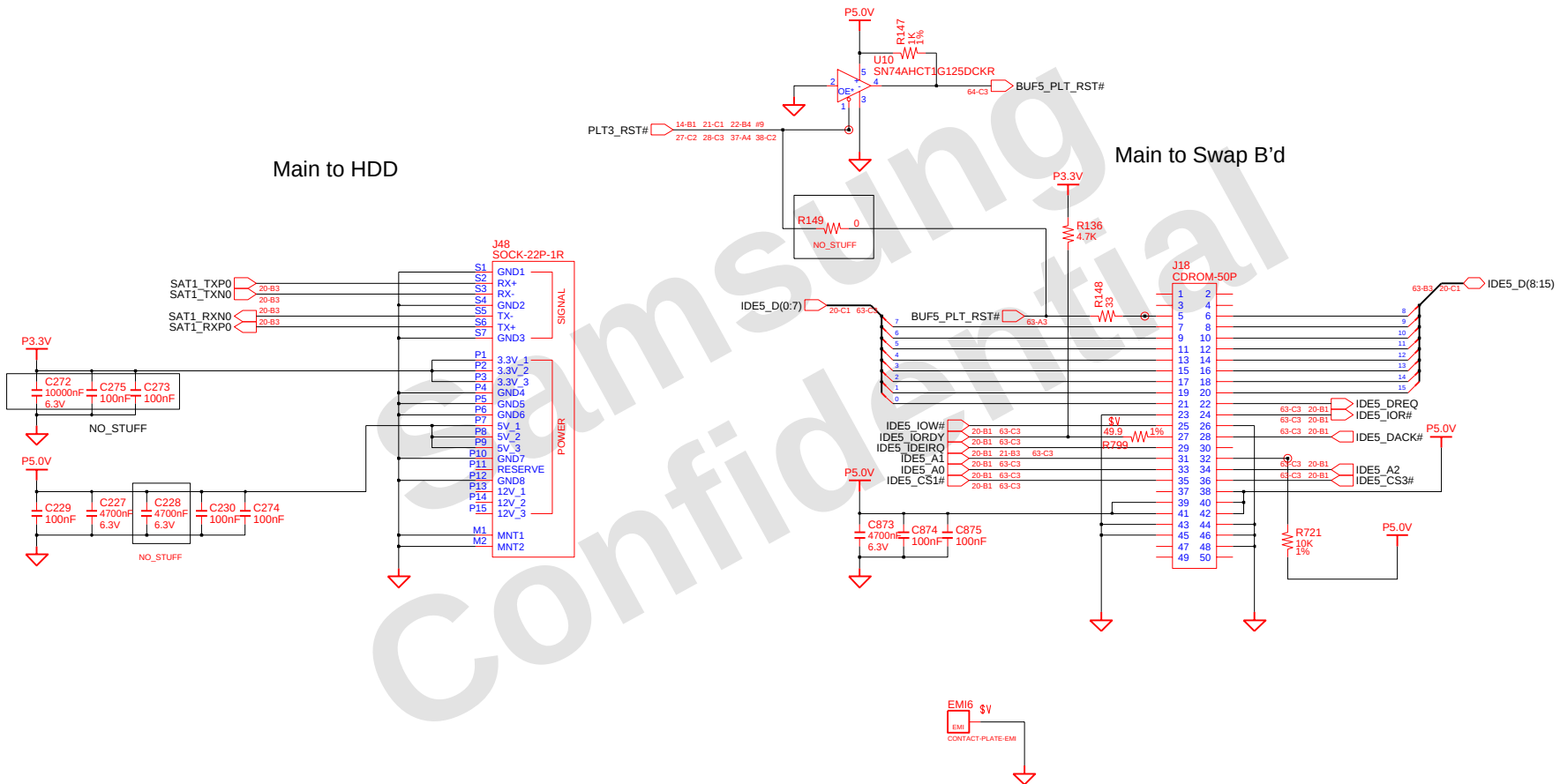


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CPU VRM [SEMTECH]



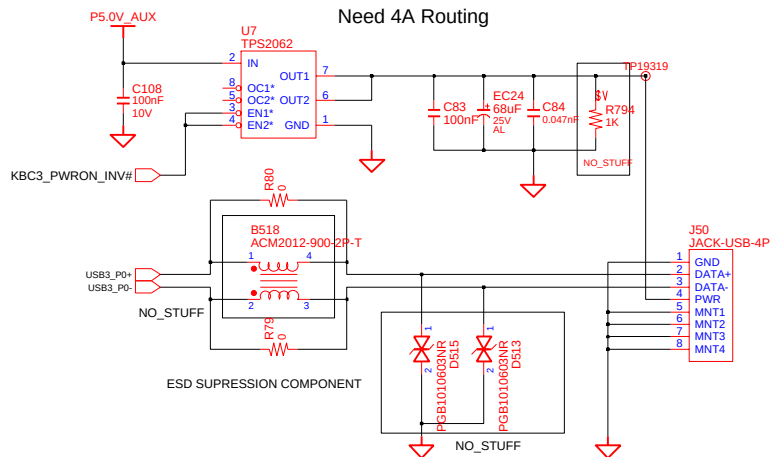
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena MAIN CPU VRM	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV STEP	PV2			PART NO. BA41-XXXXX
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT				
October 23, 2007 10:38:02 AM						PAGE 51 OF 58



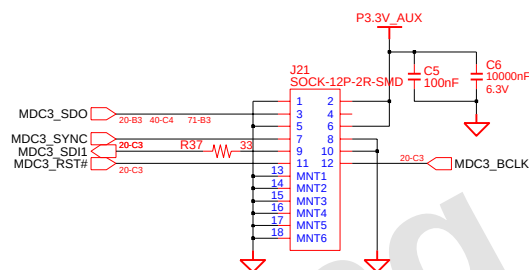
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2	CONNECTOR		
APPROVAL	KEVIN LEE	REV	1.0	HDD & ODD CONNECTOR	PART NO. BA41-XXXXX	
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM		PAGE 53 OF 58	

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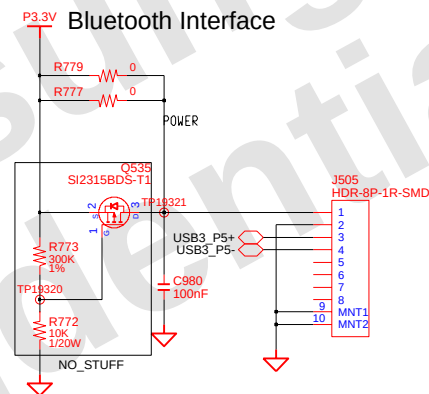
1 PORT USB CONNECTOR



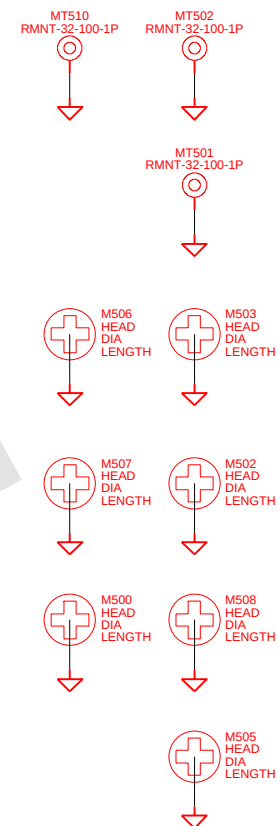
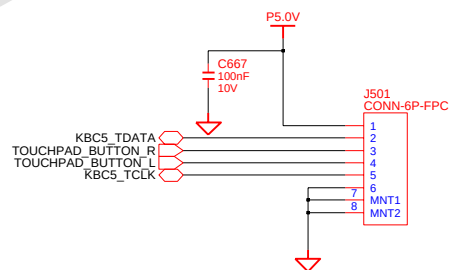
MDC Connector



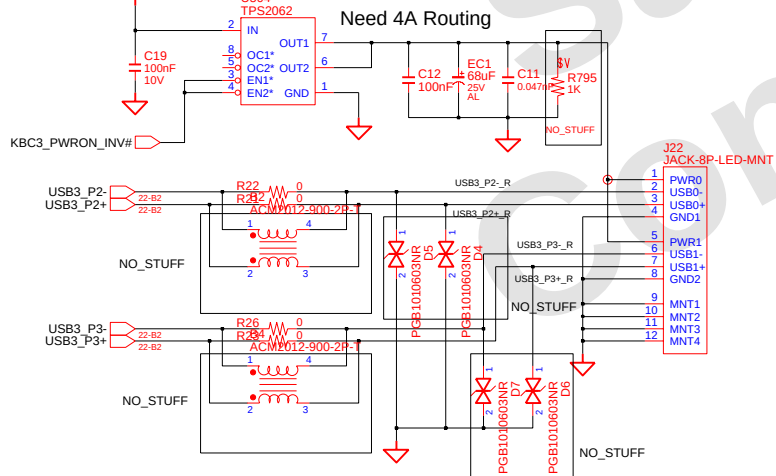
Bluetooth Interface



TOUCHPAD CONNECTOR

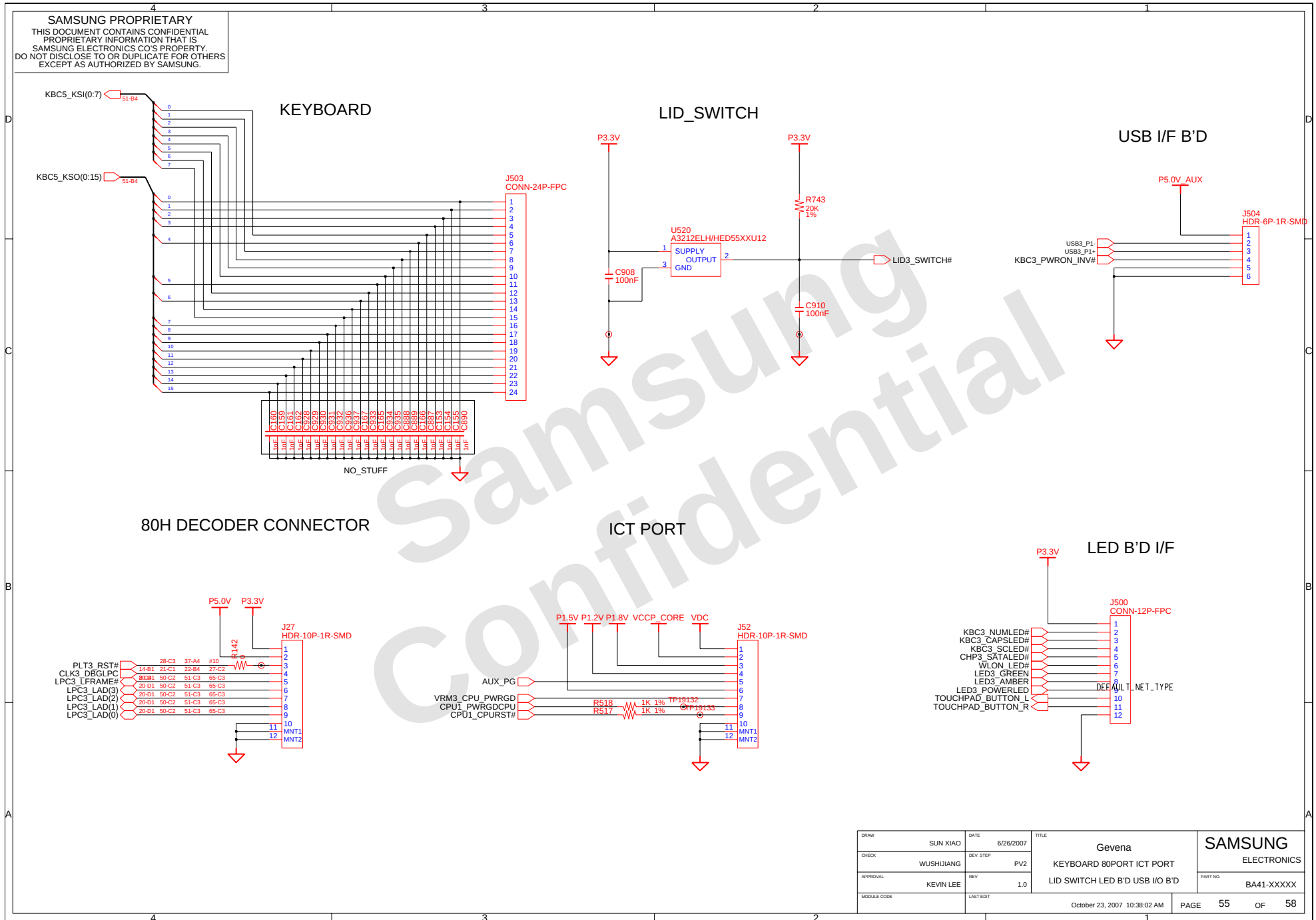


2 PORT USB CONNECTOR

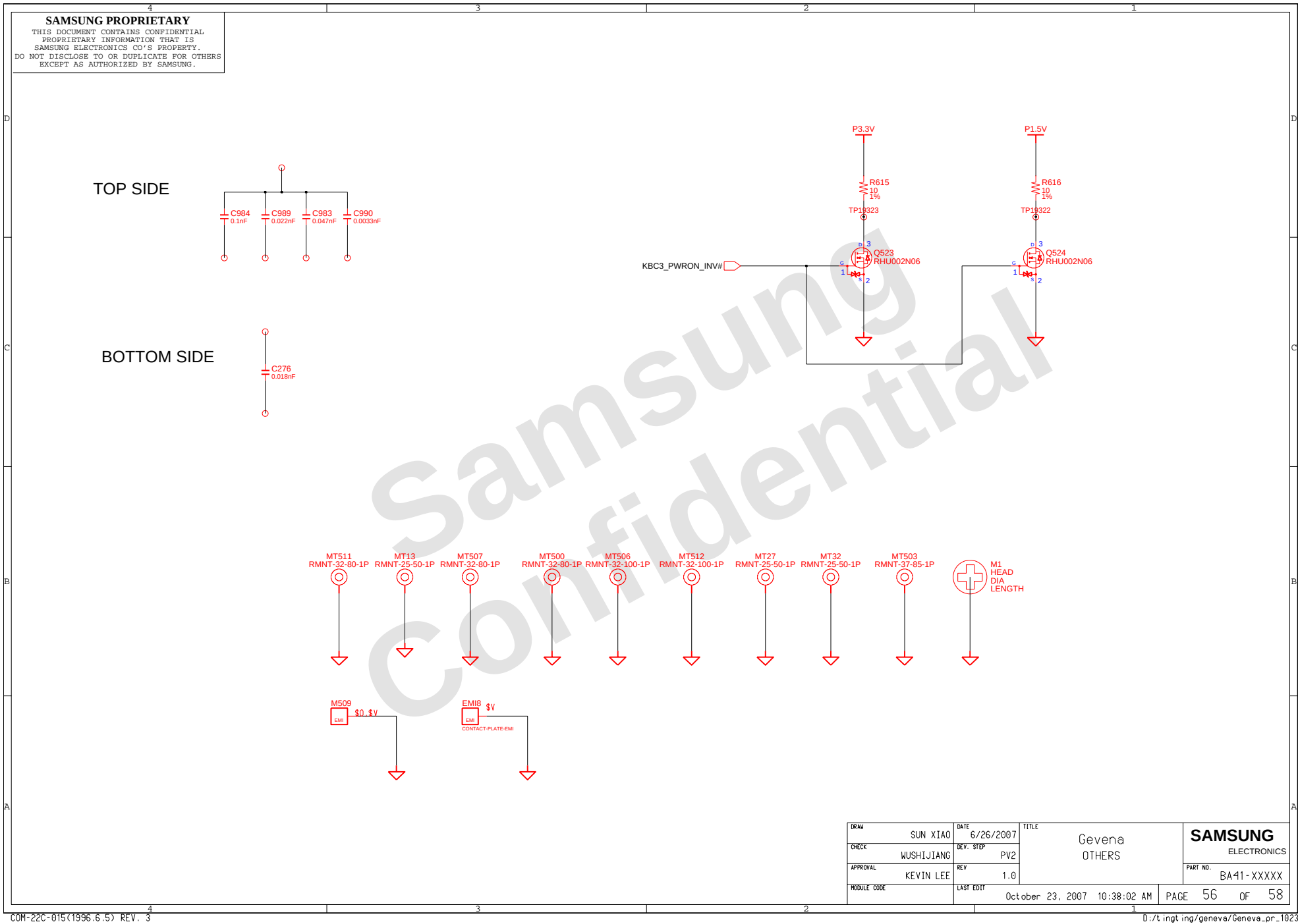


DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV STEP	PV2		USB MDC &	ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0		TOUCHPAD BLUETOOTH I/F	PART NO. BA41-XXXXX
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	54	OF 58

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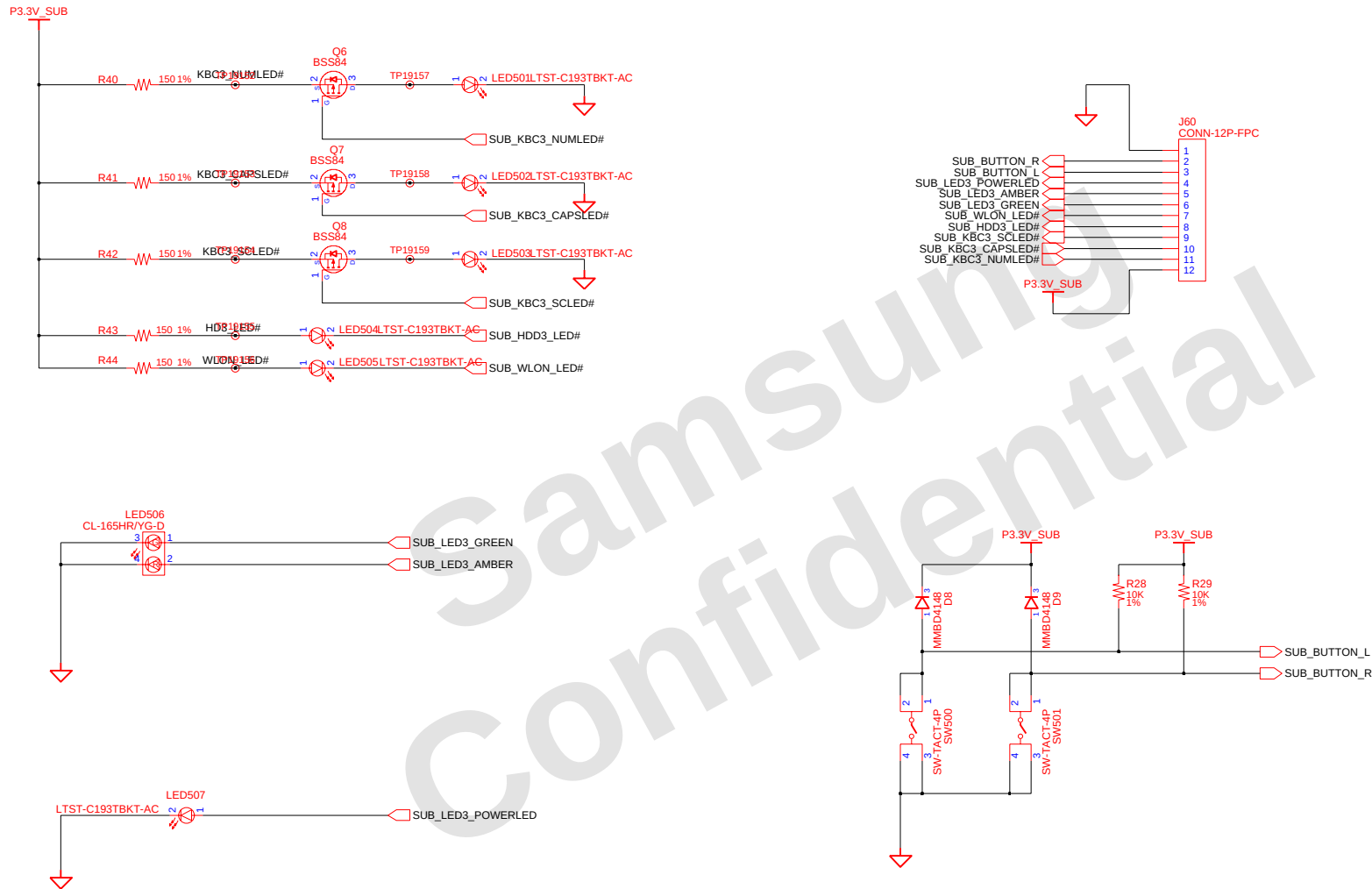
DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena	SAMSUNG
CHECK	WUSHUJIANG	DEV. STEP	PV2	KEYBOARD 80PORT ICT PORT		ELECTRONICS
APPROVAL	KEVIN LEE	REV	1.0	LID SWITCH LED B'D USB I/O B'D	PART NO.	BA41-XXXXX
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	55	OF 58



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Gevena OTHERS	SAMSUNG ELECTRONICS PART NO. BA41-XXXXX
CHECK	WUSHIJIANG	DEV. STEP	PV2			
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	56 OF 58	

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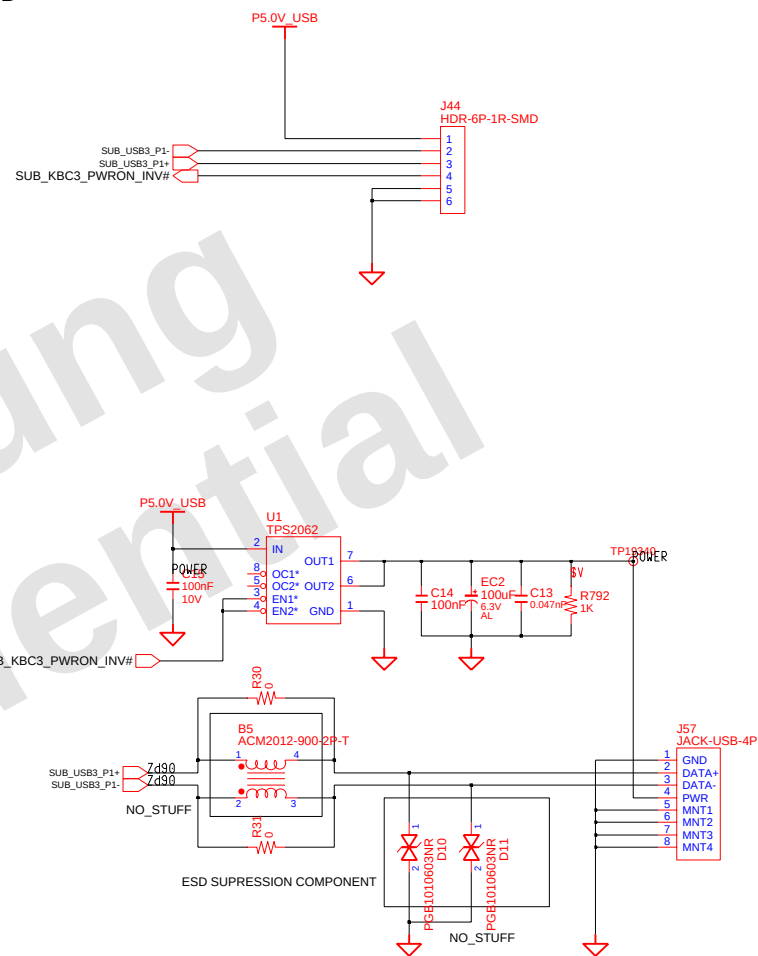
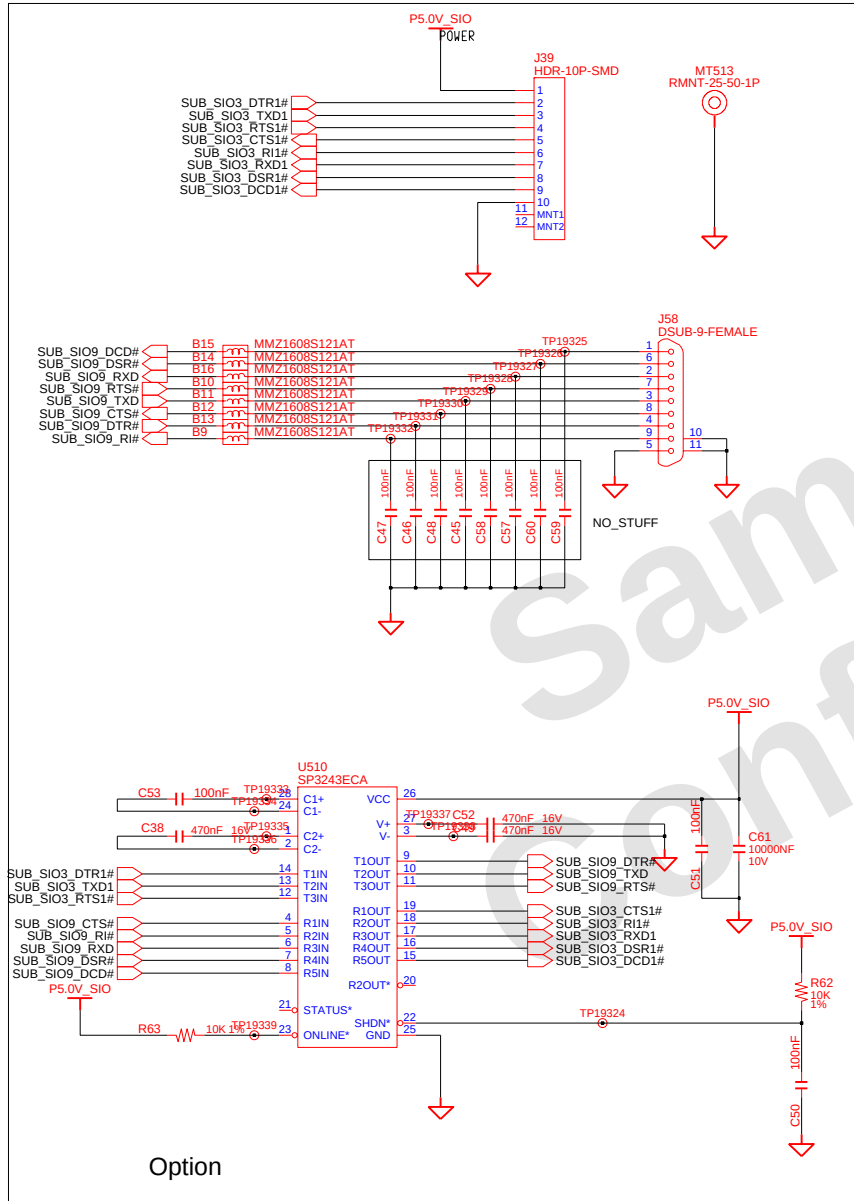
TOUCHPAD AND LED B'D



DRAW	SUN XIAO	DATE	6/26/2007	TITLE	Geneva	SAMSUNG ELECTRONICS PART NO. BA41-
CHECK	WUSHIJIANG	DEV. STEP	PV2	TOUCHPAD AND LED B'D		
APPROVAL	KEVIN LEE	REV	1.0			
MODULE CODE	LAST EDIT		October 23, 2007 10:38:02 AM	PAGE	57 OF 60	

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SIO AND USB SUB B'D



DRUM	SUN XIAO	DATE	6/30/2007	TITLE Geneva MIO BOARD USB AND SIO SUB B'D	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2		
APPROVAL	KEVIN LEE	REV	1.0		
MODULE CODE	LAST EDIT		October 23, 2007 10:38:02 AM		
PART NO. BA41-undefined					

TP18380OAU3_SEL
TP18381OAU3_BCLK
TP18382OAU3_BEEP
TP18383OAU3_BEEP_CU
TP18384OAU3_GP100#
TP18385OAU3_GP101#
TP18386OAU3_GP101#_L
TP18387OAU3_RST#
TP18388OAU3_SDIO
TP18389OAU3_SDIO_RU
TP18390OAU3_SDO
TP18391OAU3_SPKR

TP18393OAU3_SYNC
TP18394OAU5_HP_O_L
TP18395OAU5_HP_O_L_CR
TP18396OAU5_HP_O_L_RB
TP18397OAU5_HP_O_B
TP18398OAU5_HP_O_B_CR
TP18399OAU5_HP_O_B_RB
TP18400OAU5_INTMIC
TP18401OAU5_LINE_O_L
TP18402OAU5_LINE_O_L_CU
TP18403OAU5_LINE_O_L_C
TP18404OAU5_LINE_O_L_CU
TP18405OAU5_MIC1_L
TP18406OAU5_MIC1_L_BCON
TP18407OAU5_MIC1_L_CU
TP18408OAU5_MIC1_R
TP18409OAU5_MIC1_R_BCON
TP18410OAU5_MIC1_R_CU
TP18411OAU5_MIC1_VREF_L
TP18348OAU5_MIC1_VREF_R
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TP18354OAU5_PG

TP18361OAT3_SMCLK#
TP18362OAT3_SMDATA#
TP18363OAT3_CONN_TP_BB
TP18364OBU5_PLT_RST#

TP18367OCHP1_5.DMICOMP_R
TP18368OCHP3_BIOSWP#
TP18369OCHP3_BIOS_CR1#
TP18370OCHP3_CL_CLK_O
TP18371OCHP3_CL_DATA_O
TP18372OCHP3_CL_RST#_O
TP18373OCHP3_CL_RST#_I
TP18374OCHP3_CPUSTP#
TP18375OCHP3_CPUSTP#_R_ICH
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TP18378OCHP3_EXTGFX#
TP18379OCHP3_GP1012
TP18328OCHP3_GP1017
TP18329OCHP3_INTRUDER#
TP18330OCHP3_INTVMEN
TP18331OCHP3_LANT100_SLP
TP18332OCHP3_LDR00#
TP18333OCHP3_LDR01#
TP18334OCHP3_PC1STP#
TP18335OCHP3_R1#_R
TP18336OCHP3_R1CRST#
TP18337OCHP3_SATACLKREQ#
TP18338OCHP3_SATALED#
TP18339OCHP3_SERIRQ
TP18340OCHP3_SLP3#
TP18341OCHP3_SLP5#
TP18342OCHP3_SLP5#
TP18343OCHP3_SUSSTAT#

TP18344OCHP3_TPT_R
TP18345OCHP3_USBRRIAS_R
TP18346OCHP3_USBRROC9#_R
TP18347OCHP3_SDATAOUT_O_R
TP18319OCLK3_DBG_LPC
TP18320OCLK3_TCH14
TP18321OCLK3_PCLK1CH
TP18322OCLK3_PCLKMICOM
TP18323OCLK3_PCLKSIO
TP18324OCLK3_PCLKSIO_DS
TP18325OCLK3_PWRGD
TP18326OCLK3_S1014
TP18327OCLK3_USB48
TP18315OCPU1_A20M#
TP18316OCPU1_AD5#
TP18317OCPU1_BNR#
TP18318OCPU1_BPR1#
TP18306OCPU1_DREQ#
TP18307OCPU1_BSEL0
TP18308OCPU1_BSEL1
TP18309OCPU1_BSEL2
TP18310OCPU1_COMP0_R
TP18311OCPU1_COMP1_R
TP18312OCPU1_COMP2_R
TP18313OCPU1_COMP3_R
TP18314OCPU1_CPURST#

TP18295OCPU1_DBSY#
TP18296OCPU1_DEFER#
TP18297OCPU1_DPSTP#
TP18298OCPU1_DPS1#
TP18299OCPU1_DPWR#
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TP18302OCPU1_HIT#
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TP18304OCPU1_IERR#
TP18305OCPU1_IGNNE#
TP18259OCPU1_INT#
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TP18263OCPU1_PS1#
TP18264OCPU1_PWRGDGPU
TP18270OCPU1_RS0#
TP18271OCPU1_RS1#
TP18272OCPU1_RS2#
TP18273OCPU1_SLP#
TP18274OCPU1_SMI#
TP18275OCPU1_STPCLK#
TP18276OCPU1_TCK
TP18277OCPU1_TDI
TP18278OCPU1_TEST1_R
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TP18281OCPU1_TEST4_R
TP18282OCPU1_TEST5_R
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TP18284OCPU1_THRMTRIP_R
TP18285OCPU1_TMS
TP18286OCPU1_TRDY#
TP18287OCPU1_TRST#
TP18288OCPU1_VCCSENSE
TP18289OCPU1_VSSSENSE
TP18290OCPU2_THERMDA
TP18251OCPU2_THERMDC
TP18252OCPU3_THRMTRIP#
TP18253OCPU_CORE_HG2
TP18254OCPU_CORE_LG1
TP18255OCPU_CORE_LG2
TP18256OCPU_CORE_PHASE_2

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TP17812ODRAM_RSTC#
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TP17814CEDID_DATA
TP17815EXP3_CLKREQ#
TP17816EXP3_CPPE#
TP17817EXP3_CPUSB#
TP17818EXP3_FERR1#
TP17819EXP3_FDBACK#
TP17820EXP3_VDD
TP17821GFX3_B1ON
TP17822GFX3_BRIT

TP17799OGFX3_HSYNC
TP17800OGFX3_LCVDODON
TP17801OGFX3_THERM#
TP17802OGFX3_THRMDN
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TP17804OGFX3_VOL1D
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TP17809ODE5_CS2#
TP17810ODE5_DACK#
TP17767ODE5_DREQ
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TP17769ODE5_IOW#
TP17770ODE5_IORDY
TP17771ODE5_IOW#
TP17772OILM3
TP17773OITP3_DBRESET#
TP17774OITP3_DBRESET#_R
TP17775OITP3_DBRESET#_L
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TP17777OKBC3_BKLTON
TP17778OKBC3_BCLKPWRSW#
TP17779OKBC3_CPSLED#
TP17780OKBC3_CHGEN
TP17781OKBC3_CHKPWRSW#
TP17782OKBC3_CPURST#
TP17783OKBC3_EXTS1#
TP17784OKBC3_LED_ACIN#
TP17785OKBC3_LED_CHARGE#
TP17786OKBC3_LED_POWER#
TP17787OKBC3_NUMLED#
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TP17789OKBC3_PWRCD
TP17790OKBC3_PWRON
TP17791OKBC3_PWRON_INV#
TP17792OKBC3_PWRON_INV#_P1.8V
TP17793OKBC3_PWRON_P1.5V
TP17794OKBC3_PWRSW#
TP17795OKBC3_RSMRST#
TP17796OKBC3_RSMRST#_R_ICH
TP17797OKBC3_RSMRST#
TP17798OKBC3_RST#
TP17735OKBC3_RUNSCI#
TP17736OKBC3_SLED#
TP17737OKBC3_SMCLK#
TP17738OKBC3_SMDATA#
TP17739OKBC3_SPKMUTE_R
TP17740OKBC3_SPKMUTE_RQ_L
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TP17742OKBC3_SUSPWR
TP17743OKBC3_THERM_SMCLK
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TP17748OKBC5_KSI(1)
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TP17769OKBC5_KSI(8)
TP17770OKBC5_KSI(9)
TP17723OKBC5_TCLK
TP17724OKBC5_TDATA
TP17725OCD3_BKLTON
TP17726OCD3_BRIT
TP17727OCD3_AMBER
TP17728OCD3_GREEN
TP17729OCD3_POWERLED
TP17730OCD3_LAD(0)
TP17731OCD3_LAD(1)
TP17732OCD3_LAD(2)
TP17733OCD3_LAD(3)
TP17734OCD3_LFRAME#
TP17735MAX8734_BST3
TP17736MAX8734_BST5
TP17737MAX8734_FB3
TP17738MAX8734_SKIP#
TP17739MAX8734_THM3_STP#_00
TP17740MAX8734_VCC

DRAW	SUNXIAO	DATE	9/10/2007	TITLE	Gevena TEST POINT	SAMSUNG ELECTRONICS
CHECK	WUSHIJIANG	DEV. STEP	PV2			PART NO. BA41 - undef ined
APPROVAL	KEVIN.LEE	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	59	OF 59

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TP186065#MCH1.HRCOMP#
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TP186069#MCH1.HXSING
TP186100#MCH1.VITL1FC
TP186101#MCH1.VITL1FC
TP185855#MCH1.VITL1_3_C
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TP185887#MCH3.CLKREQ#
TP185898#MCH3.CL_VREF
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TP185900#MCH3.EXITSS#
TP185911#MCH3.ICHSYNCH#
TP185912#MCH3.ICHSYNCH#_R
TP185993#MCH1.TEST1_R
TP185994#MCH1.TEST2_R
TP185995#MDC3.BCLK
TP185996#MDC3.RS#
TP185997#MDC3.SD11
TP185998#MDC3.SD11_R
TP185999#MDC3.SD0
TP186000#MDC3.SYNC
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TP185839#MIN3.CLKREQ#

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TP18541 OMODEM_TIP_BCON

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T196182|P01_25V_M_VCC_ADC  
T196183|P01_5V_CRT_TYXAD  
T196184|P01_5V_GLAN_CMPIO_R  
T196185|P01_8V_AUX_RCOPM#_R  
T196186|P01_8V_AUX_RCOPM_E  
T196187|P01_8V_AUX_RCOPM_VOH_R  
T196188|P01_8V_AUX_RCOPM_VOL_R  
T196189|P01_8V_AUX_VCC_SM_CHK  
T196200|P05_ALU_SREF_SUS  
T196201|P05_ALU_SREF_SUS  
T196222|P05_OV_HUG_HC  
T196223|P05_OV_HUG_HC  
T196223|P05_OV_HUG_LG  
T196224|P05_OV_AUX_PHASE  
T196225|P05_OV_AUX_PHASE  
T196225|P05_OV_AUX_PHASE  
T196227|PC13_DEVSEL#  
T196228|PC13_FRAME#  
T196229|PC13_CNTIO#  
T196230|PC13_CNTIO#  
T196231|PC13_INT#  
T196232|PC13_INT#  
T196233|PC13_INT#  
T196234|PC13_INT#  
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T196236|PC13_INT#  
T196237|PC13_INT#  
T196267|PC13_IRQD#  
T196268|PC13_IRQD#  
T196269|PC13_PLOCK#  
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T196273|PC13_STRDY#
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TP18053 OPEX3_WAKE#
TP18054 OPI_T3_RST# MCH R

TP18057QSC452 CS1-
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 TP18023QSC452 BS12
 TP18024QSC452 BS12 RC
 TP18025QSC452 BS12
 TP18026QSC452 CS2 CS2
 TP18027QSC452 CS2
 TP18028QSC452 DPKSLP
 TP18029QSC452 DPKSLP
 TP18030QSC452 DDP+
 TP18031QSC452 DDP
 TP18032QSC452 ERKROU
 TP18033QSC452
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 TP18035QSC452 1SH
 TP18036QSC452 SS
 TP18037QSC452 VPNI
 TP18038QSC452 VREF
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 TP18043QSI03 D1R1#
 TP18044QSI03 R1T1#
 TP18045QSI03 R1S1#
 TP18046QSI03 RX1#
 TP18047QSI03 RX1#
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 TP18052QSM3P M CLK
 TP18053QSM3P M DATA

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TP19054VR33.CPU.PWRGD
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TP18200C0C.CPU
TP18204C0C.DDR
TP18205C0C.DDR
TP18212C0C.P1_5V
TP18217C0C.P3_3V
TP18222C0C.VD03V
TP18223C0C.VD03V
TP18224C0C.P2_5V
TP18225C0C.P2_5V
TP18116PRTC.BAT
TP18122VDDMS.PLL_VD0
TP18124VCCP.CORE
TP18125VCCP.CORE
TP18130VCC.CRT
TP18131VCC.CRT
TP18081VDDC
TP18084VDDC.ADPT
TP18089VDDC.INV
TP18090VDDC.INV
TP18098VDDC.ADPT

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TP18164 OP0.9V
TP18170 OP1.05V_PEG
TP18172 OP1.25V
TP18178 OP1.2V
TP18183 OP1.5V
TP18187 OP1.5V_EXP
TP18188 OP1.8V
TP18192 OP1.8V_AUX
TP18134 P12.0V_ALW
TP18136 P3.3V_AUX
TP18141 P3.3V_AUX_EXP
TP18146 P3.3V_EXP
TP18150 P3.3V_MCD
TP18152 P3.3V_MICOM
TP18158 P3.3V_SVB
TP18161 P5.0V_ALW
TP18104 P5.0V_AUX
TP18109 P5.0V_SIO
TP18112 P5.0V_SIB

DATE	9/10/2007	TITLE	Gevena TEST POINT		SAMSUNG ELECTRONICS	
CHECK	WUSHUIJIANG,	DEV. STEP	PV2		PART NO.	BA41-undef i
APPROVAL	KEVIN.LEE,	REV	1.0			
MODULE CODE		LAST EDIT	October 23, 2007 10:38:02 AM	PAGE	60	OF 60