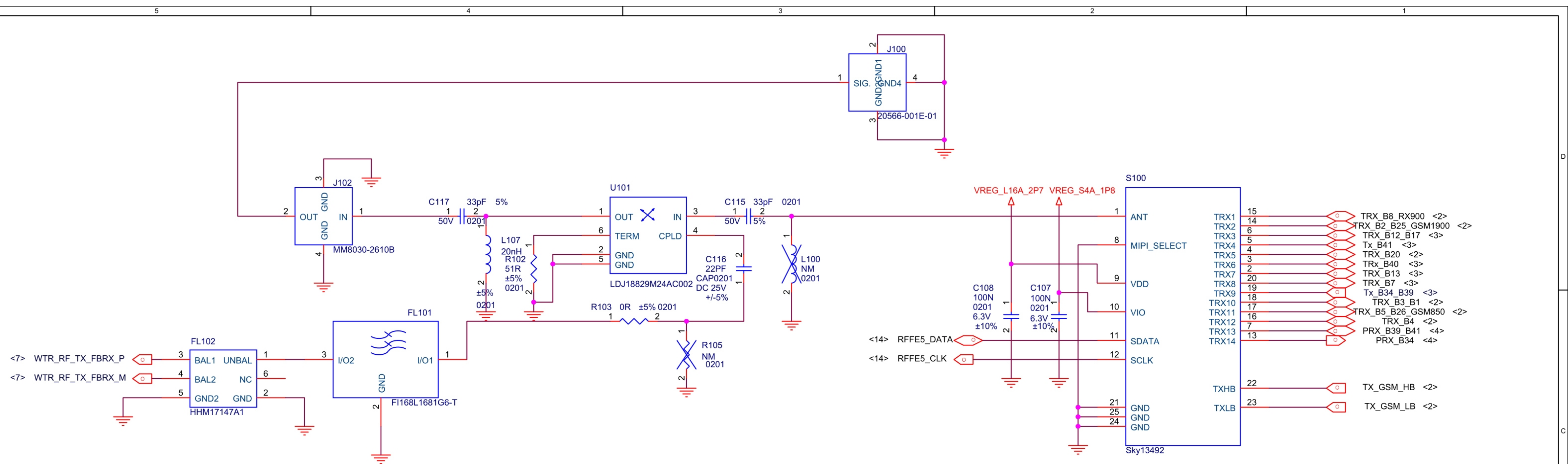
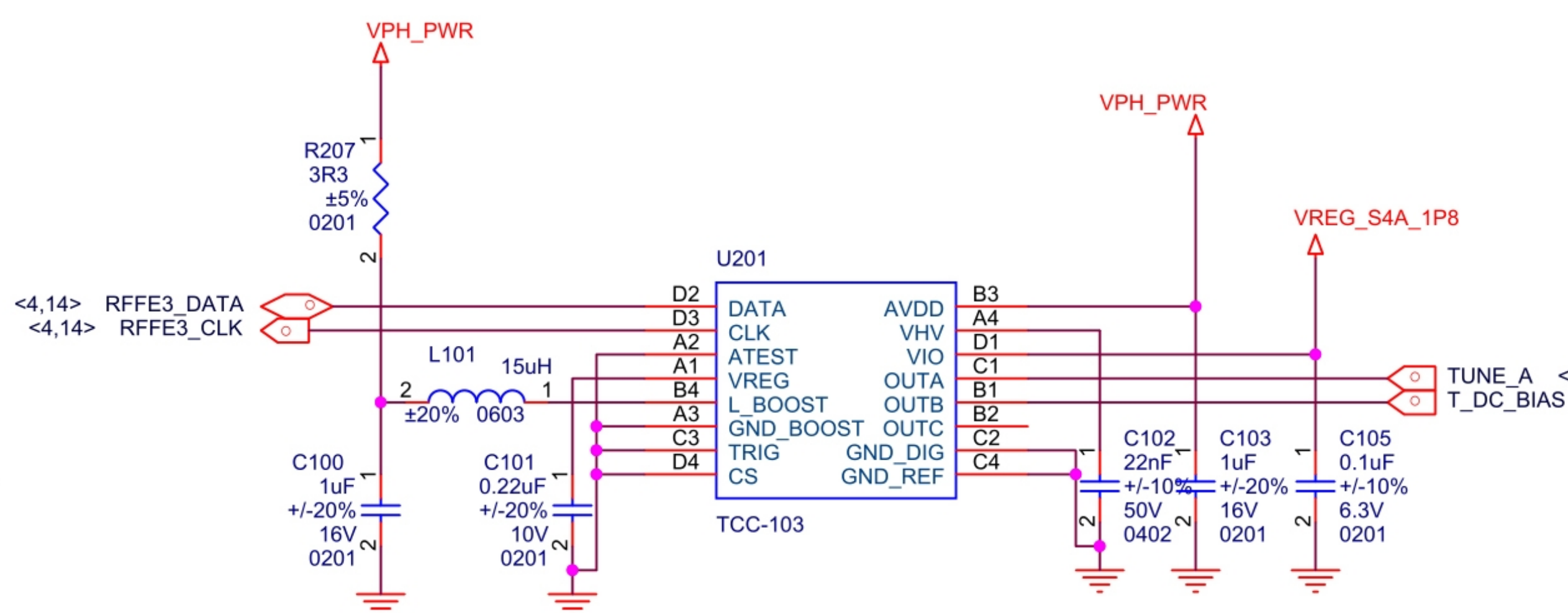




PRIMARY SWITCH(SP16T) LOGIC

Antenna Path	Bit[7]	Bit[6]	Bit[5]	Bit[4]	Bit[3]	Bit[2]	Bit[1]	Bit[0]
Sleep mode (standby)	X	0	0	0	0	0	0	0
2G transmit low band	X	0	0	0	1	0	1	0
2G transmit high band	X	0	0	0	1	0	0	0
TRX1	X	0	0	0	0	1	0	0
TRX2	X	0	0	0	0	1	0	1
TRX3	X	0	0	0	0	1	1	0
TRX4	X	0	0	0	0	1	1	1
TRX5	X	0	0	0	1	0	0	1
TRX6	X	0	0	0	1	0	1	1
TRX7	X	0	0	0	1	1	0	0
TRX8	X	0	0	0	0	0	0	1
TRX9	X	0	0	0	0	0	1	0
TRX10	X	0	0	0	0	0	1	1
TRX11	X	0	0	0	1	1	0	1
TRX12	X	0	0	0	1	1	1	0
TRX13	X	0	0	1	0	0	0	0
TRX14	X	0	0	1	0	0	0	1
Isolation mode (warm-up)	X	1	1	1	1	1	1	1

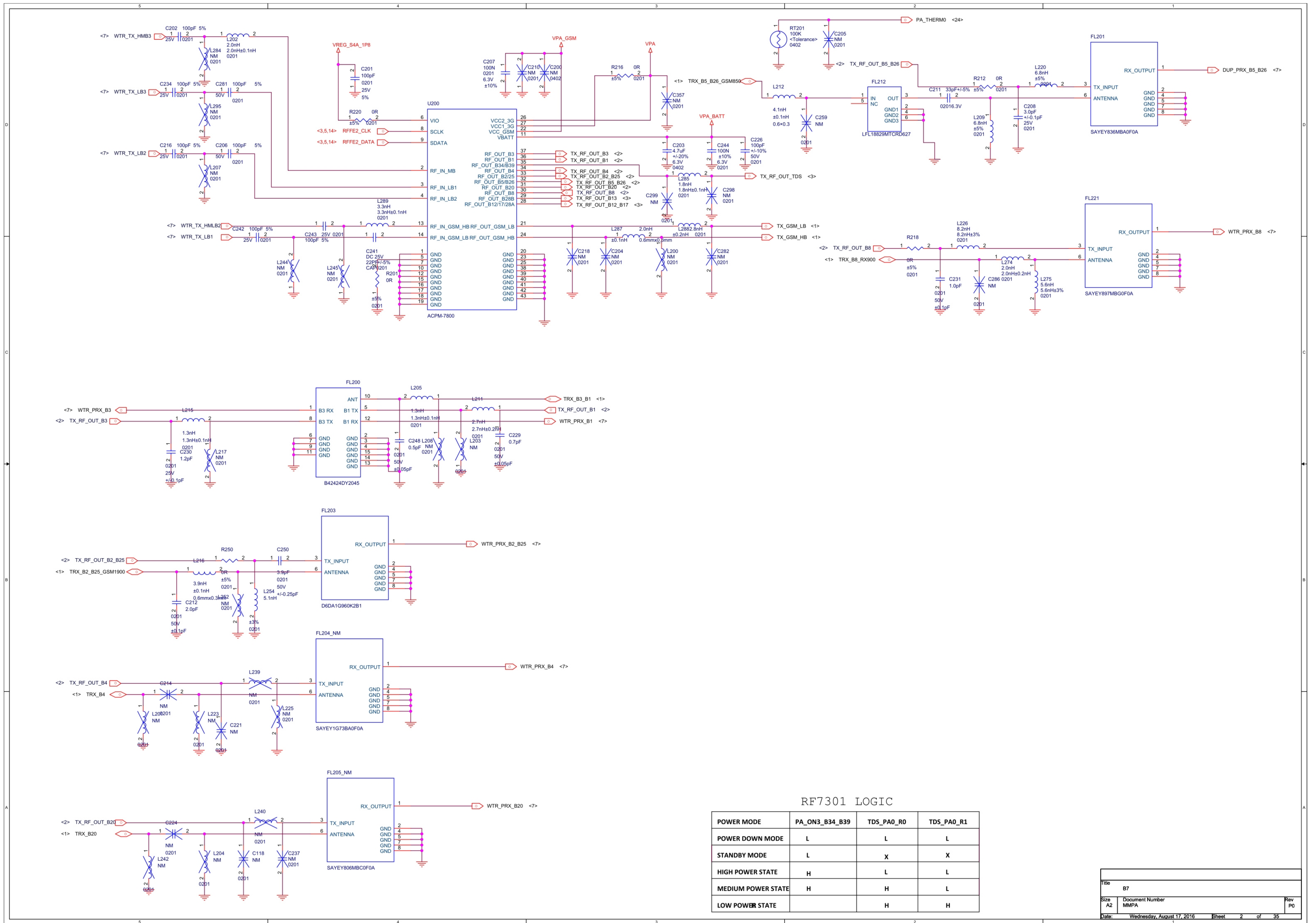


CXM3593

CTL 1	CTL 2	Active path
L	H	RF1-RF4 RF2-RF3
H	L	RF1-RF3 RF2-RF4

Primary Ant Connector, Coupler and Primary Switchplexer

Title			
B7			
Size A3	Document Number ANT CON&COUPLER&SWITCHPL		Rev P0
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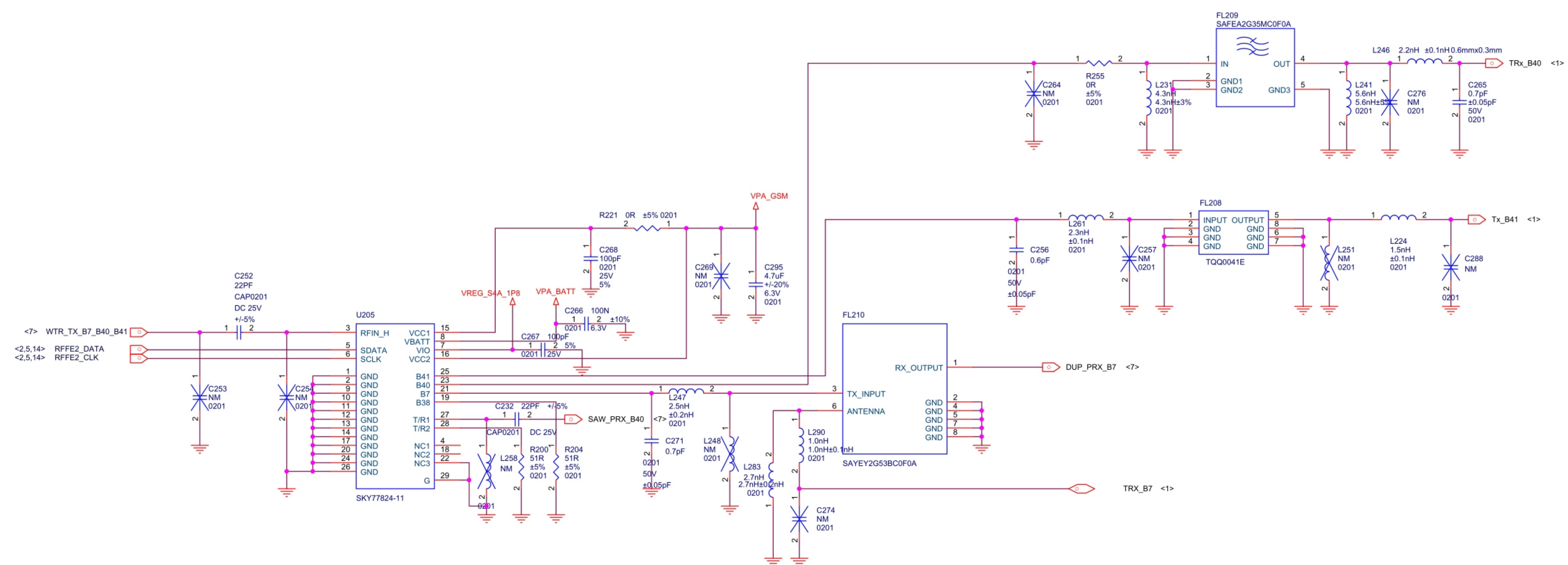
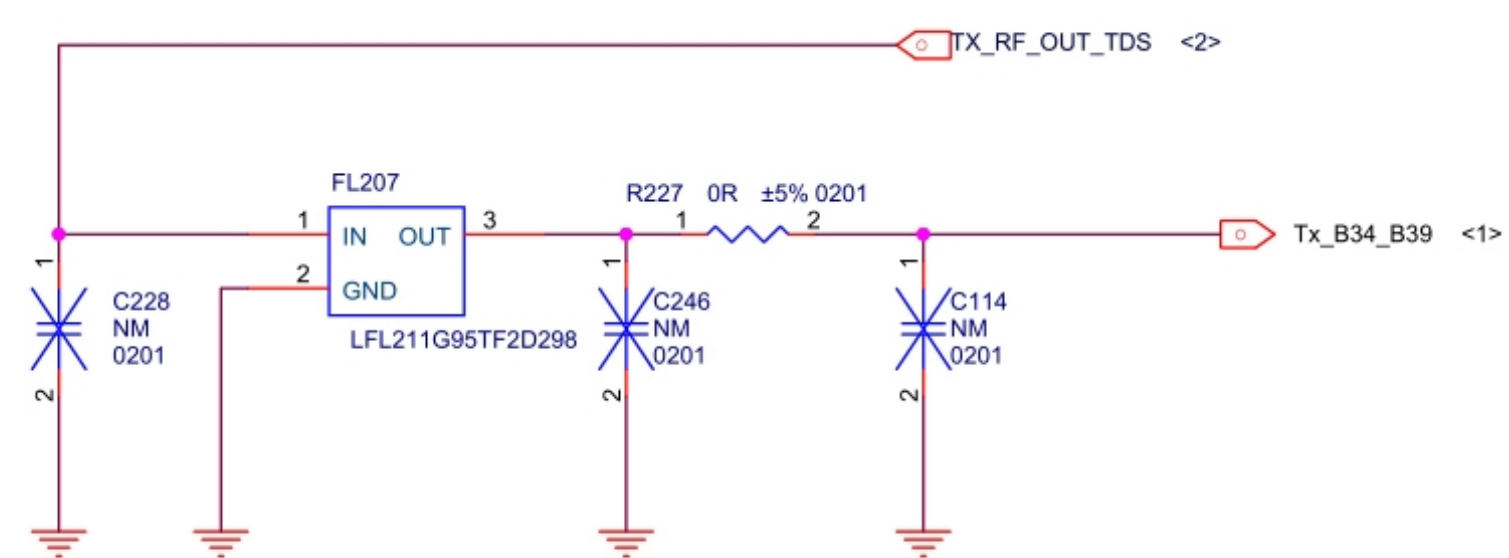


RF7301 LOGIC

POWER MODE	PA_ON3_B34_B39	TDS_PA0_R0	TDS_PA0_R1
POWER DOWN MODE	L	L	L
STANDBY MODE	L	X	X
HIGH POWER STATE	H	L	L
MEDIUM POWER STATE	H	H	L
LOW POWER STATE		H	H

Title			B7
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A2	MMPA		P0
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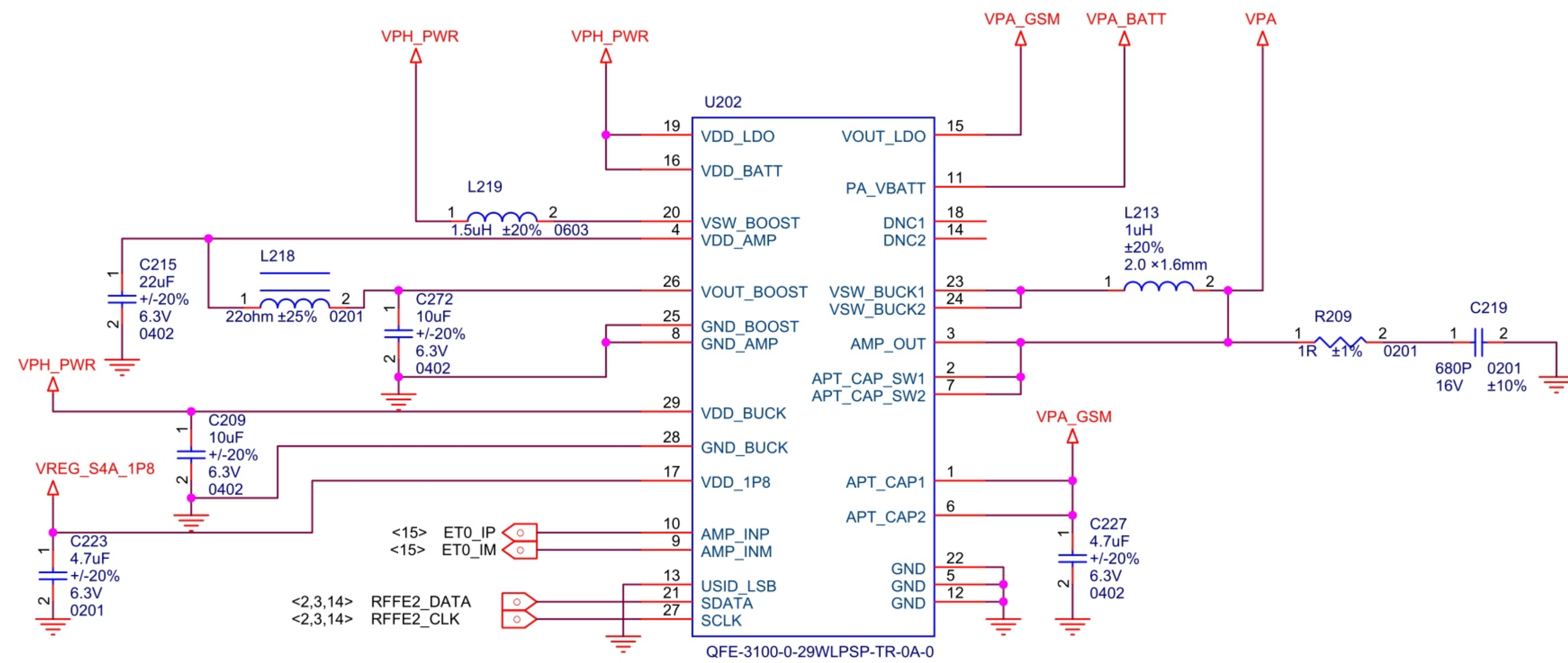




POWER MODE	PA_ON3_B34_B39	TDS_PA0_R0	TDS_PA0_R1
POWER DOWN MODE	L	L	L
STANDBY MODE	L	X	X
HIGH POWER STATE	H	L	L
MEDIUM POWER STATE	H	H	L
LOW POWER STATE		H	H

Title			
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			Rev
			P0
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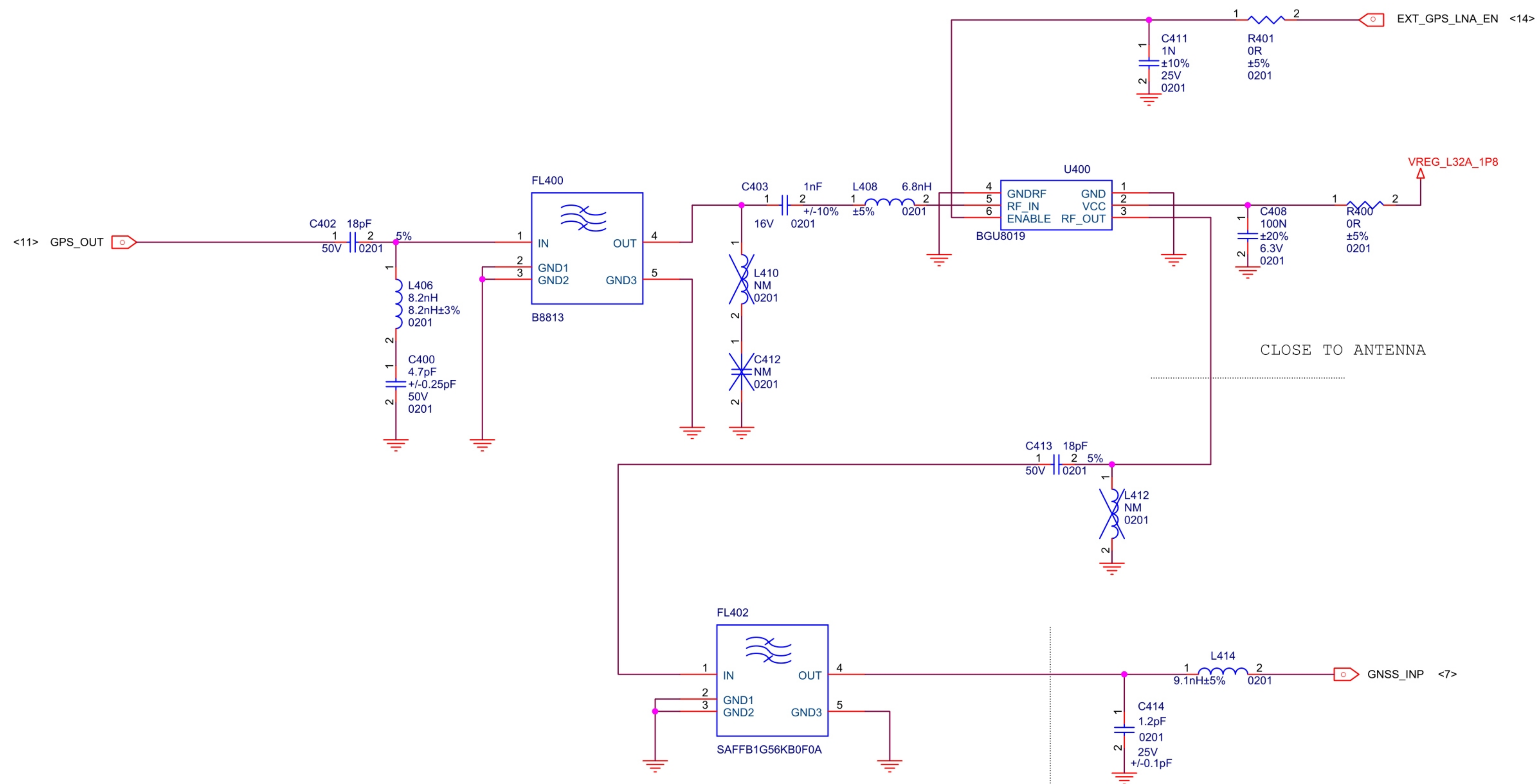
P M TGI



QFE3100

Title		
B7		
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PM TGI

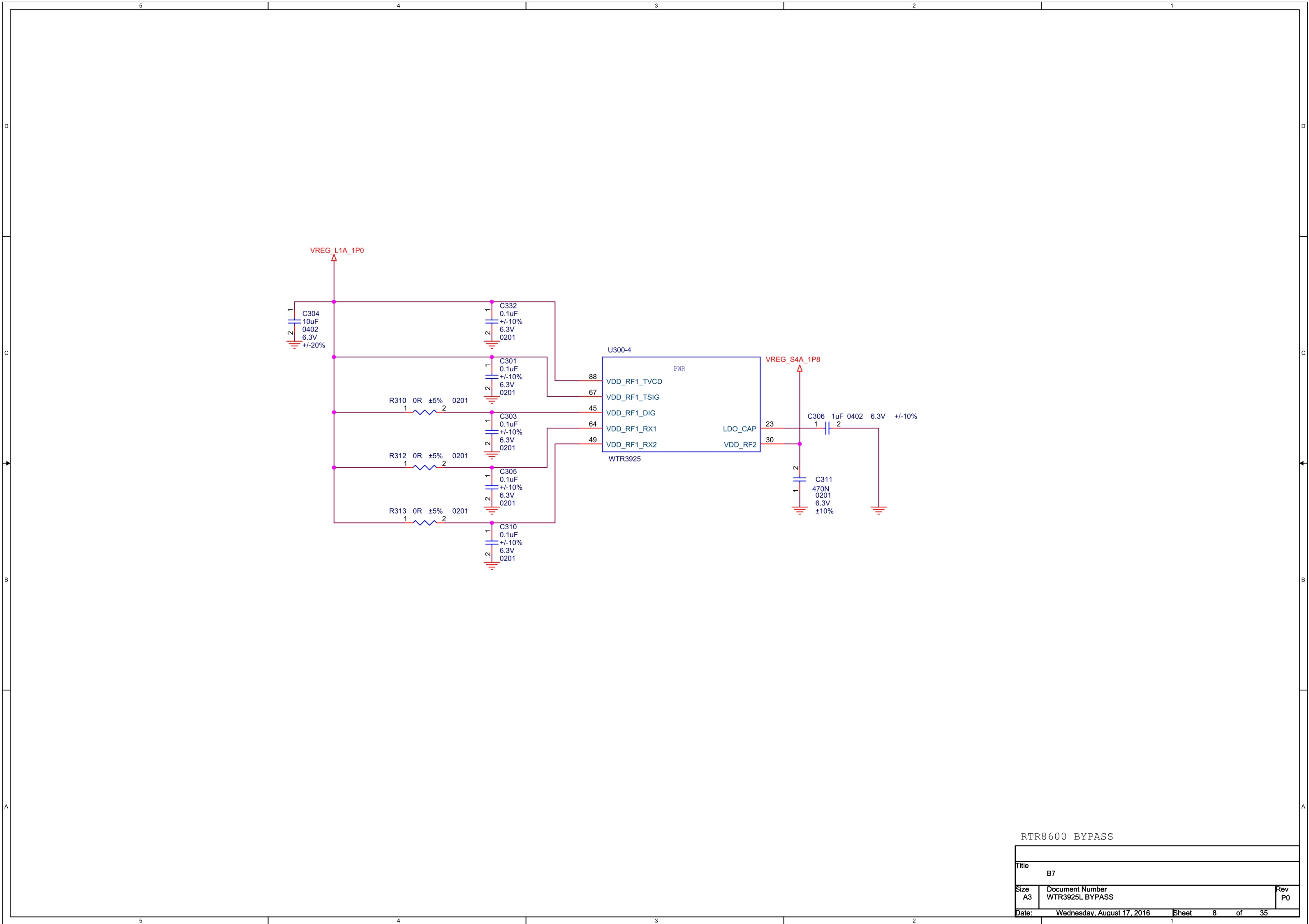


GPS

Title		
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PM TGI

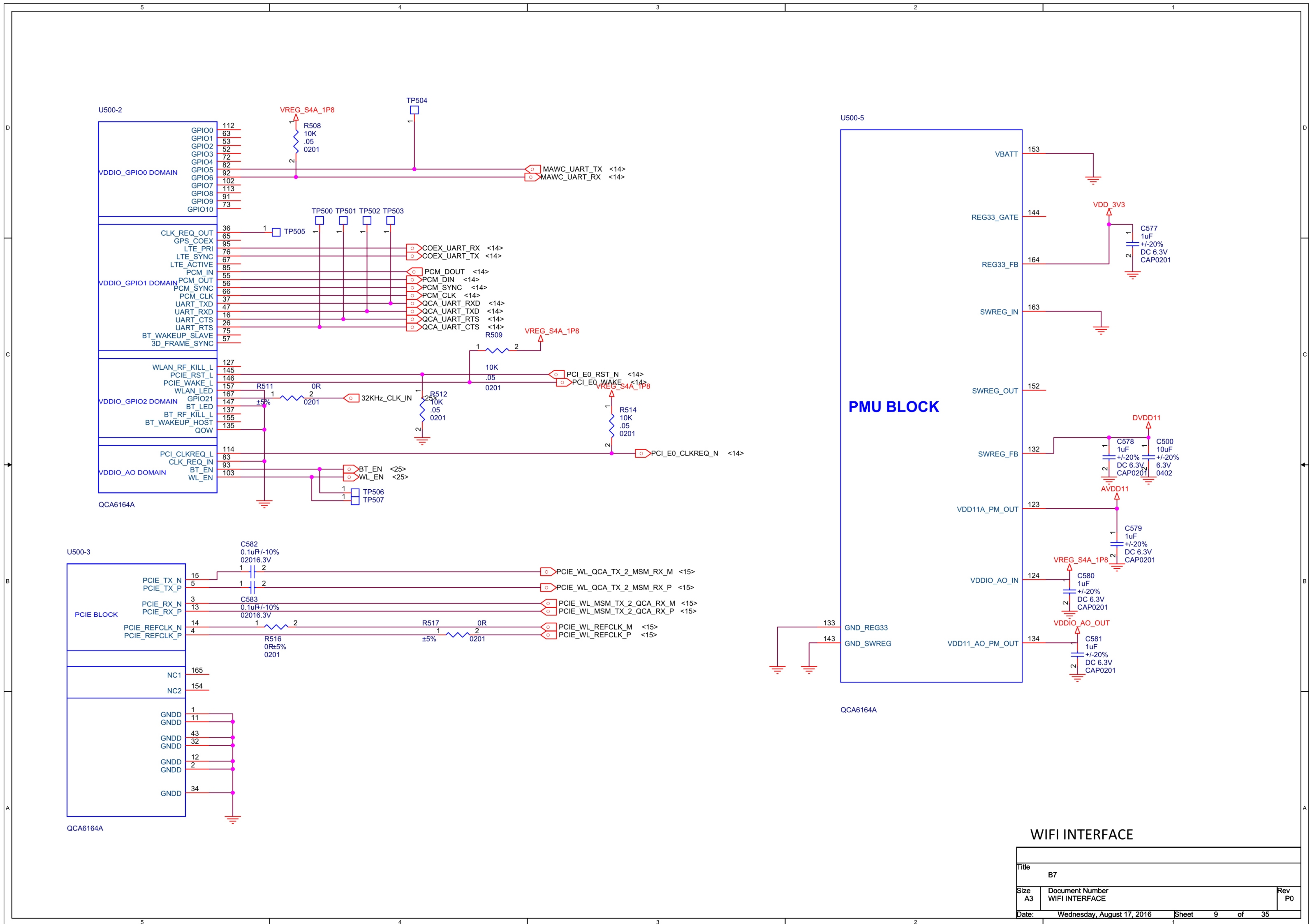




RTR8600 BYPASS

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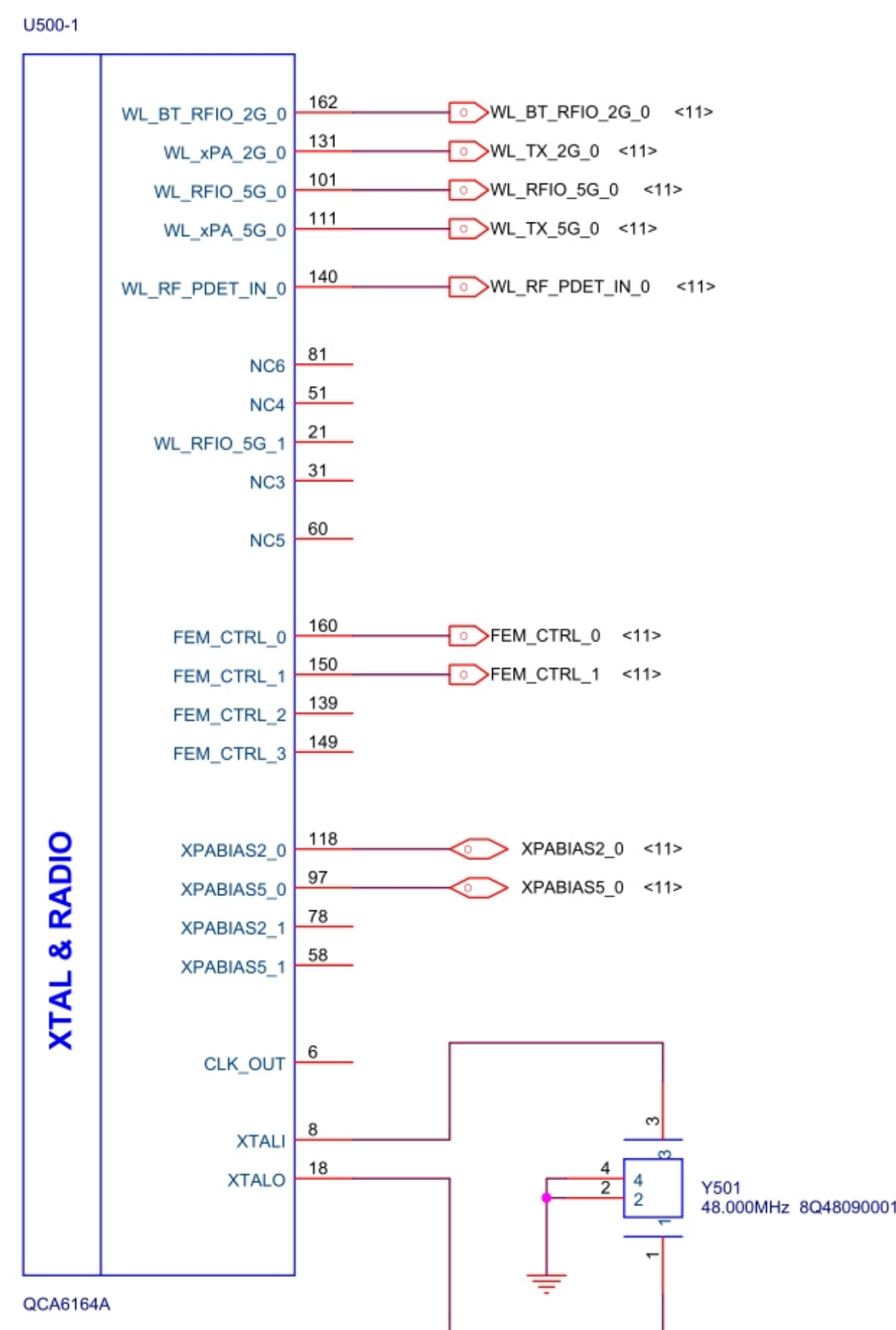
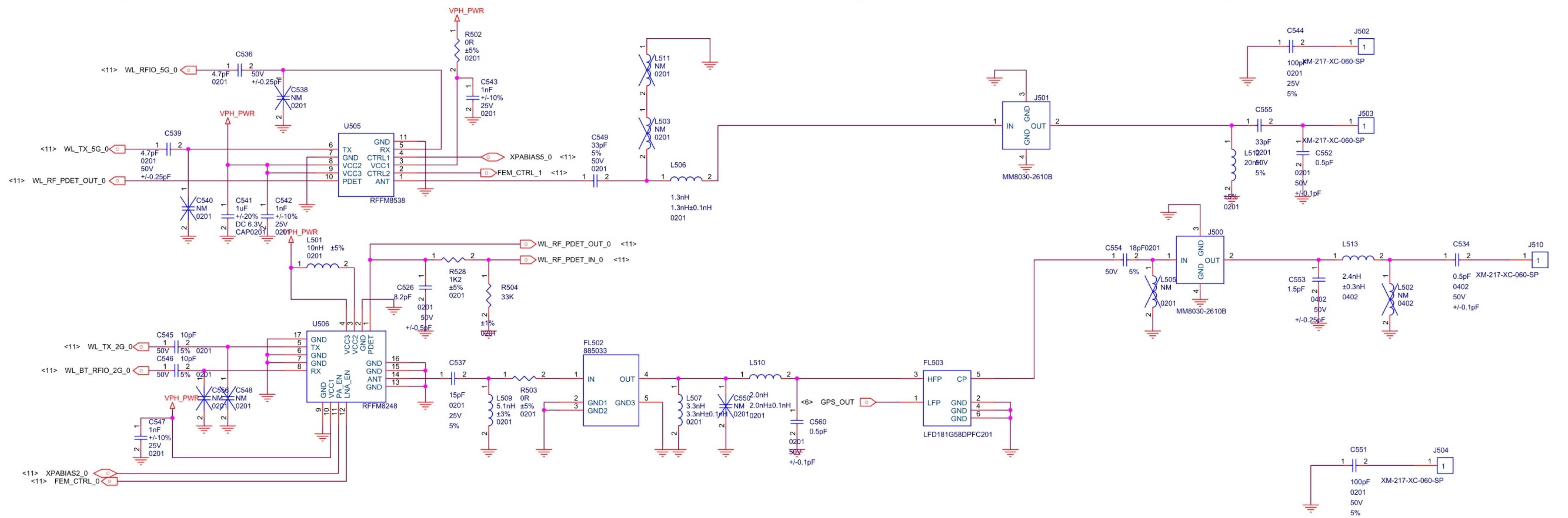
PM TGI



WIFI INTERFACE

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		Rev	P0

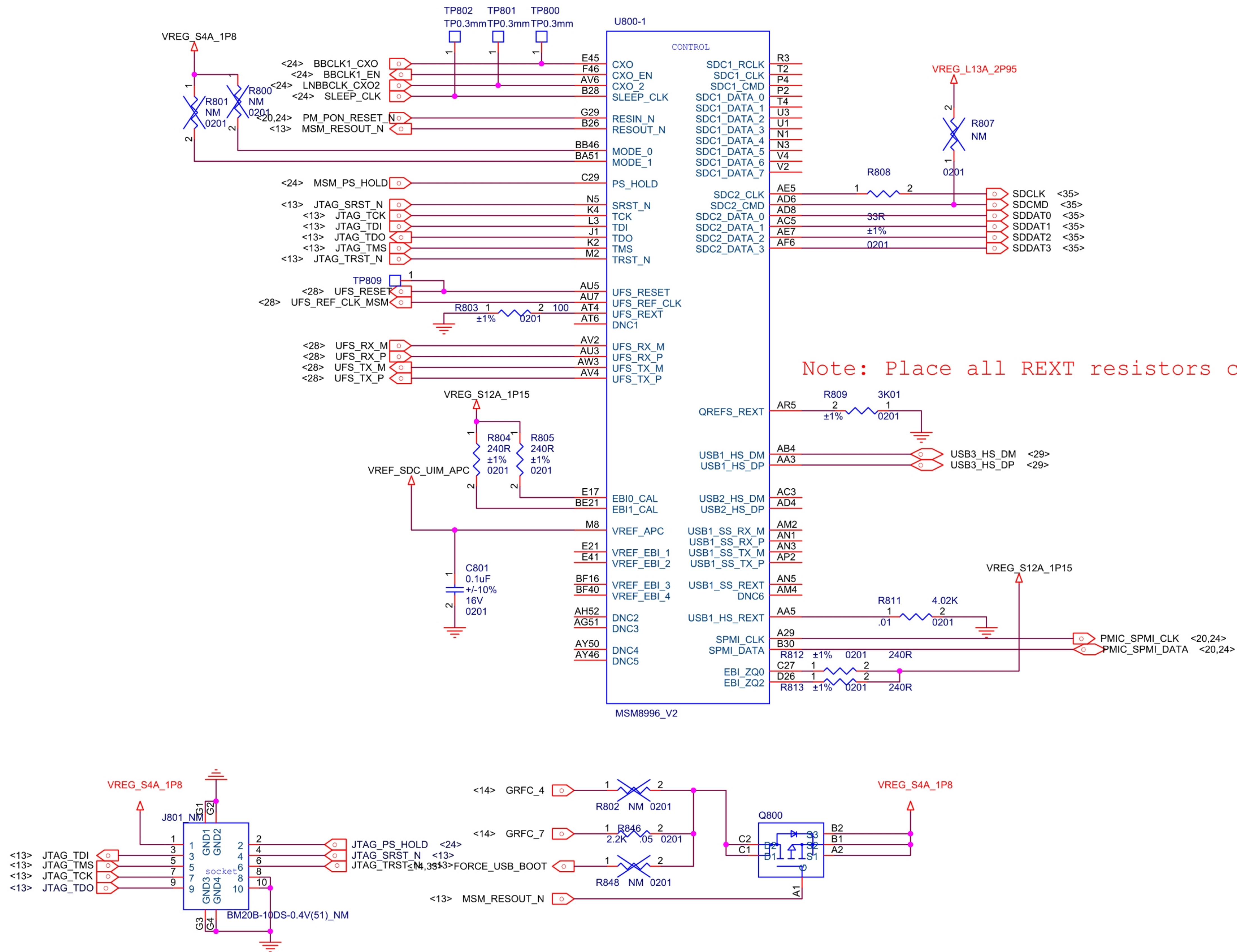




B7/WIFI FEM			
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PM TGI

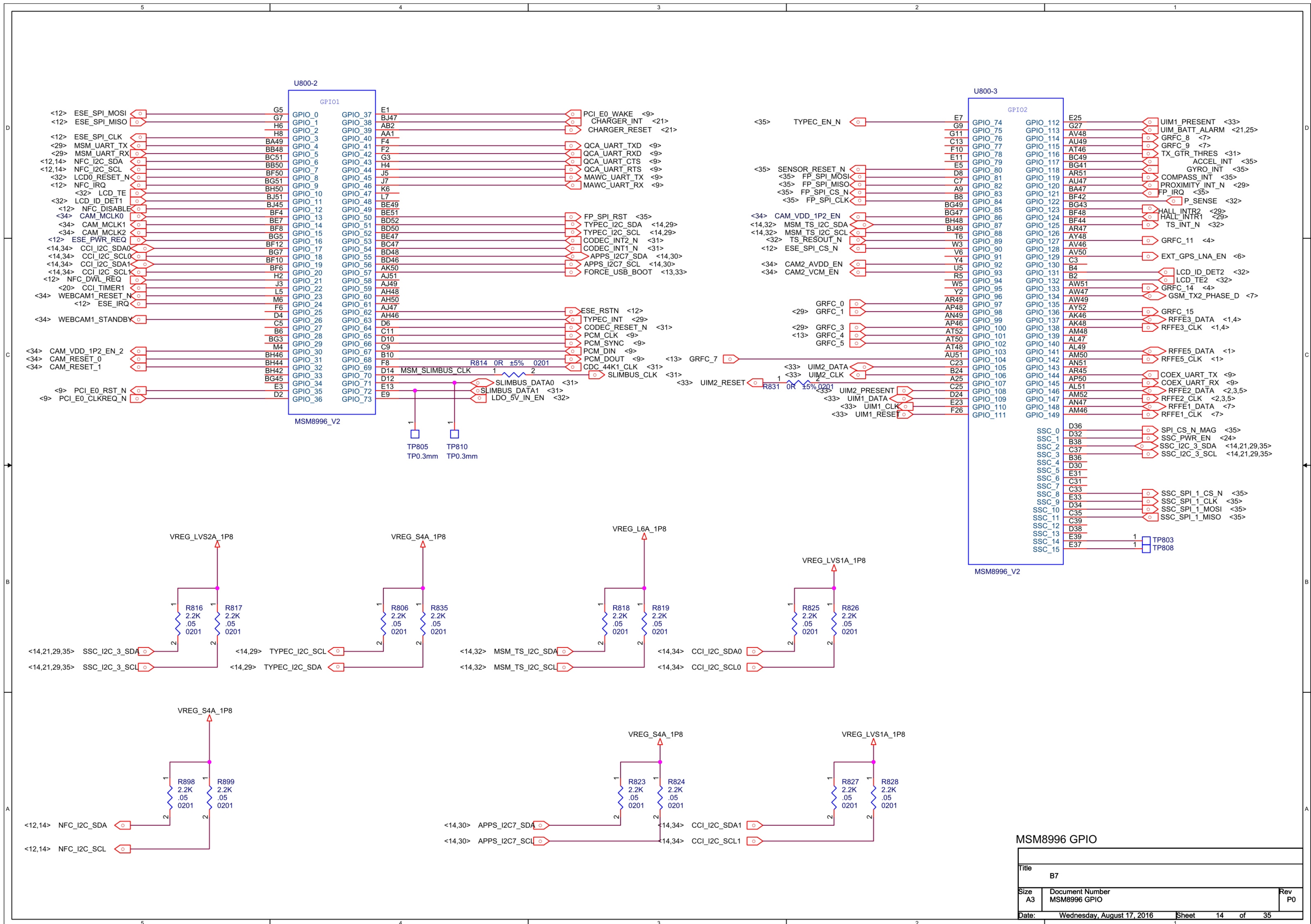


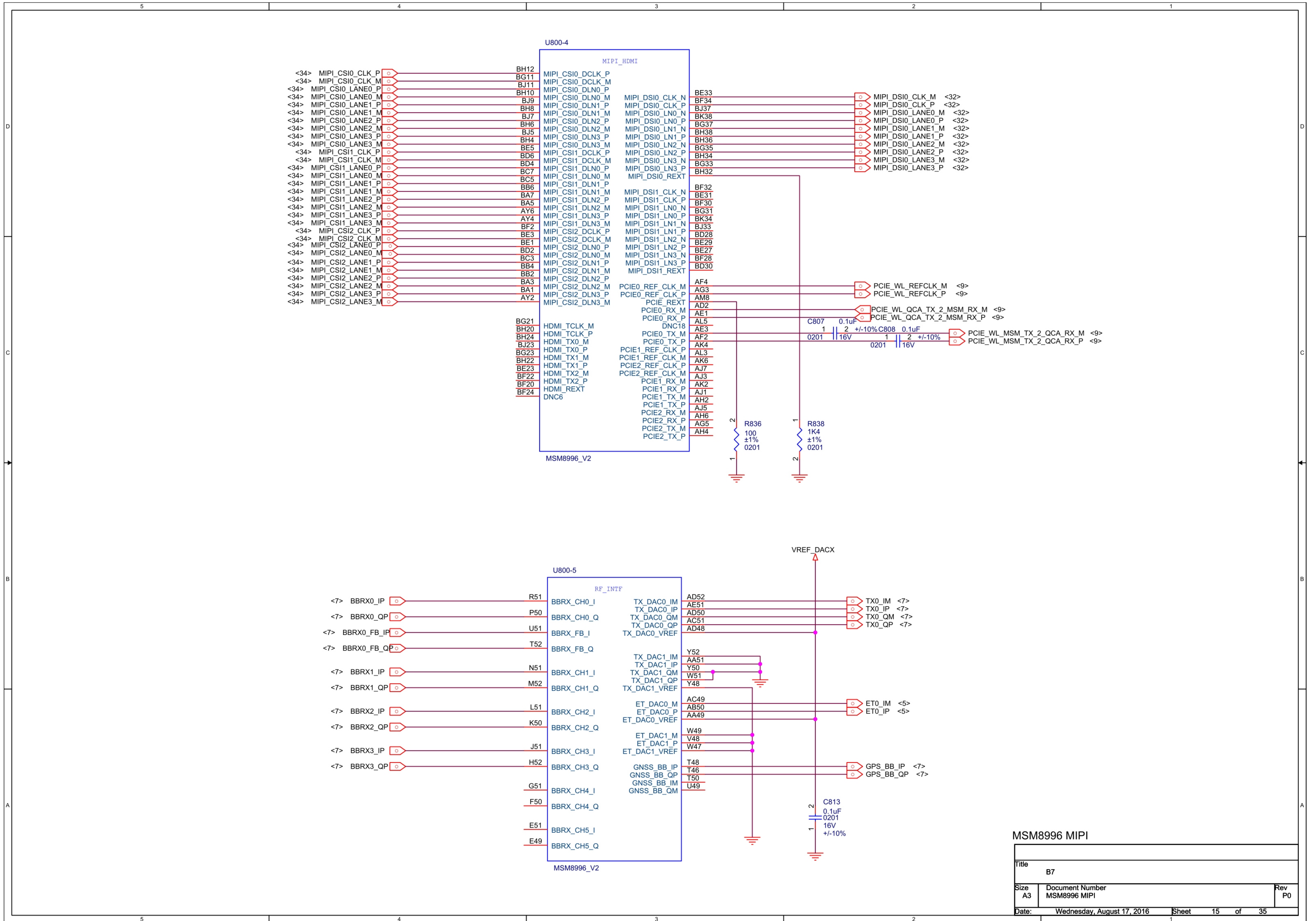


MSM8996 CONTROL

Title			B7
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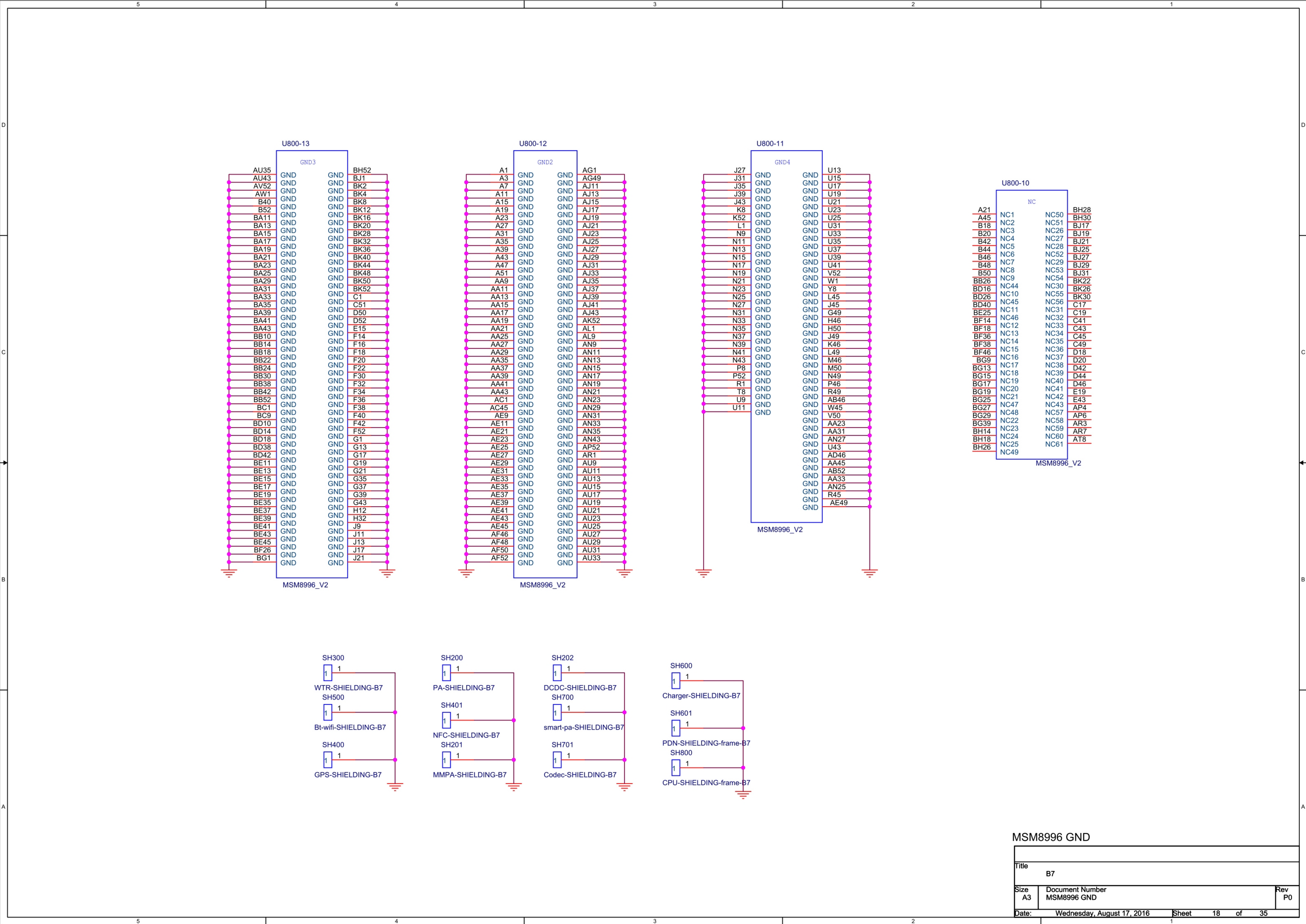
PM TGI





MSM8996 MIPI		
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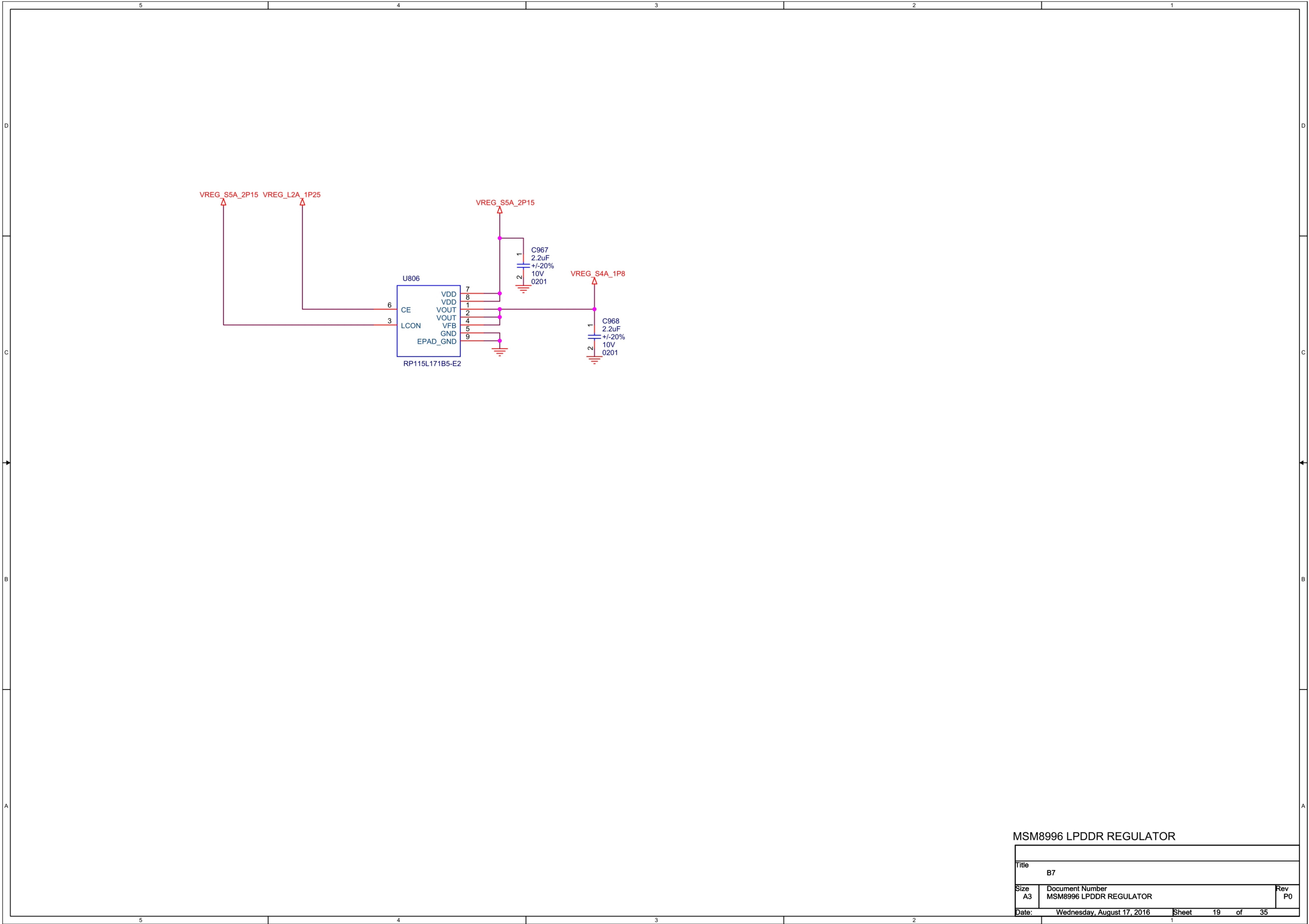


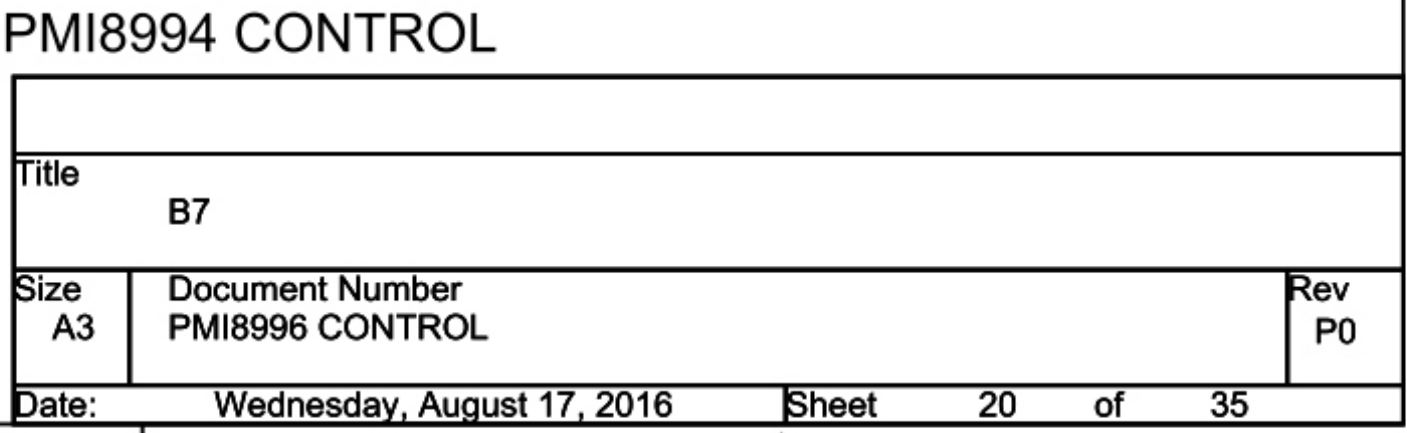


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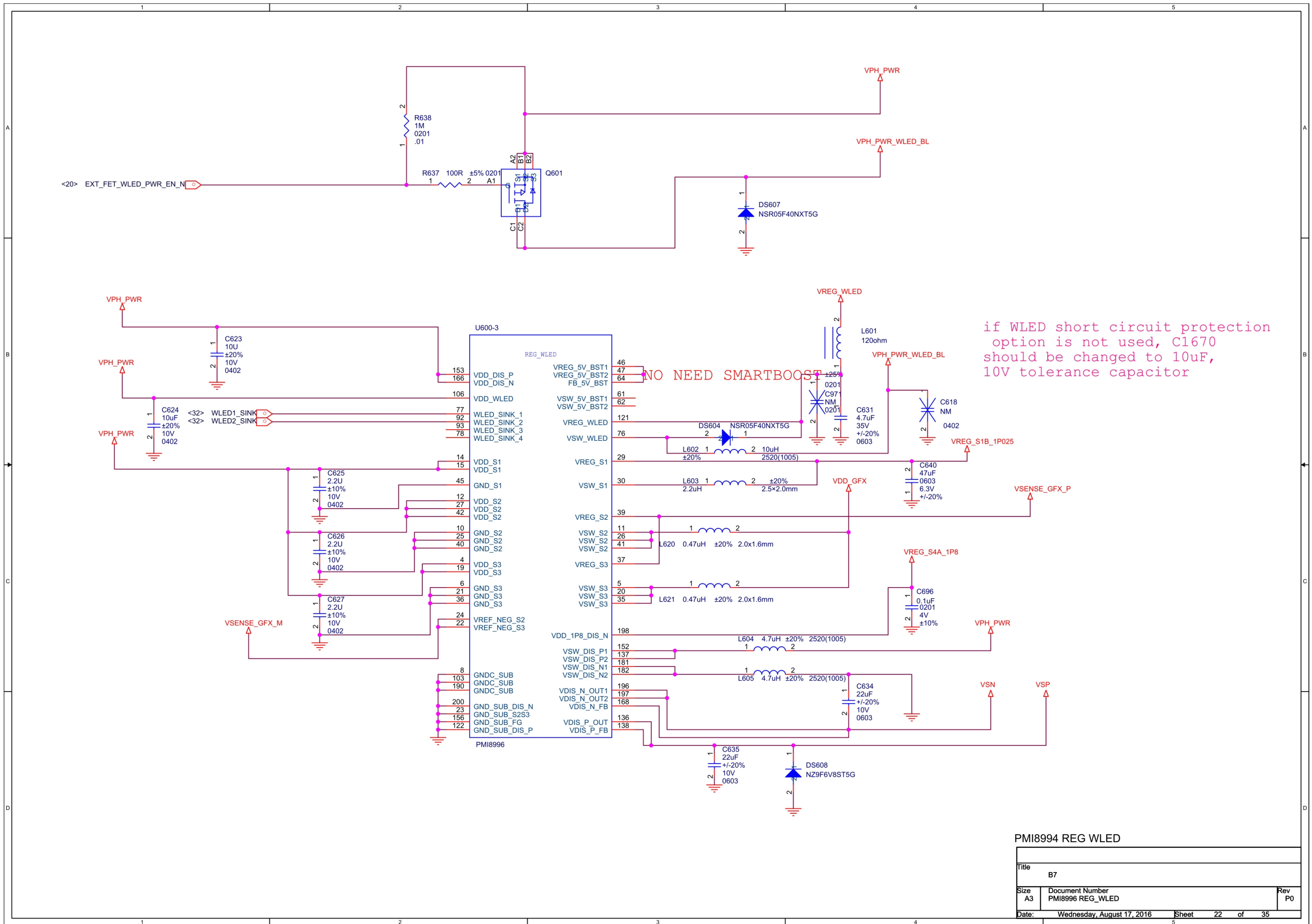
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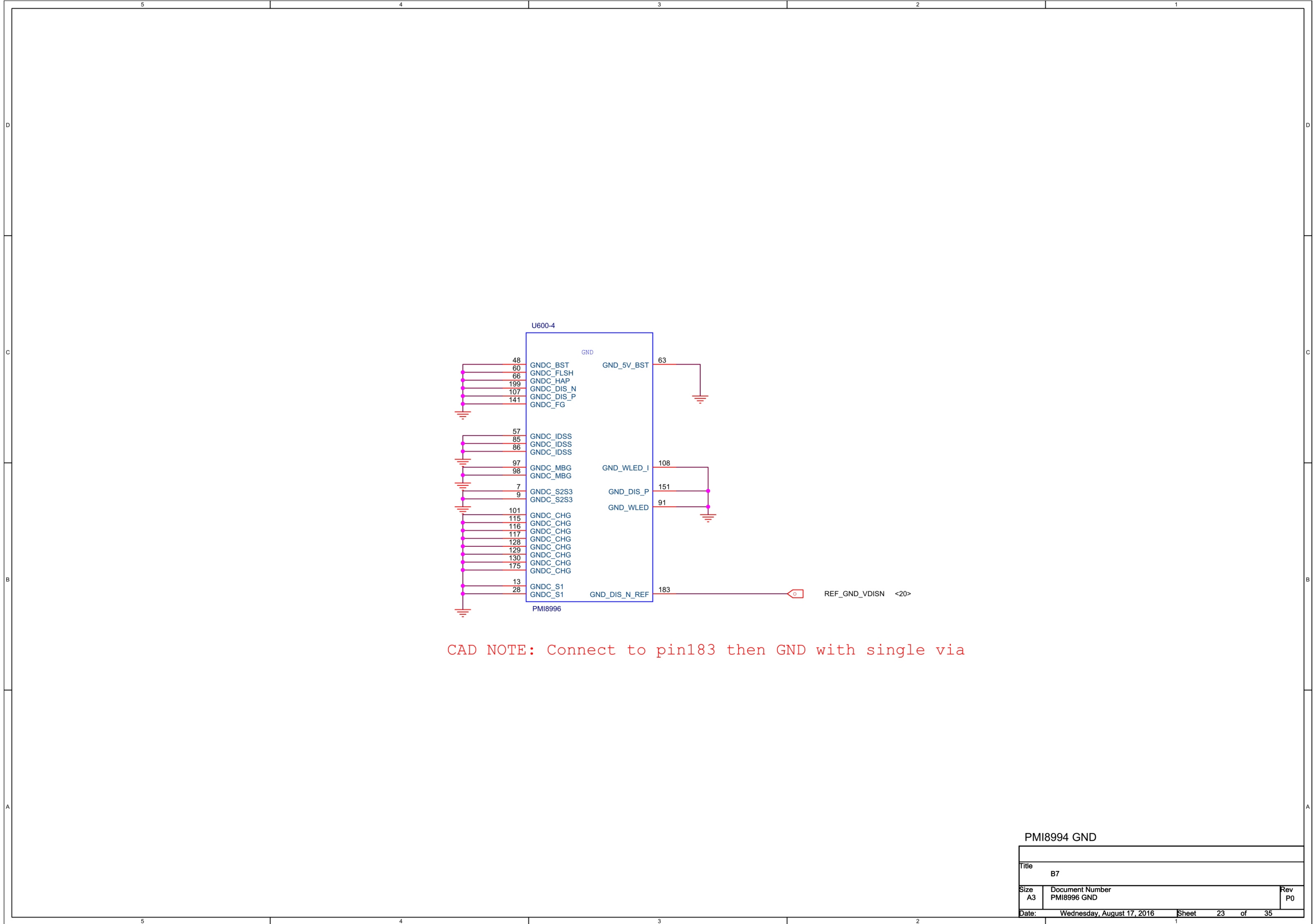


P M TGI



PM18994 REG WLED		
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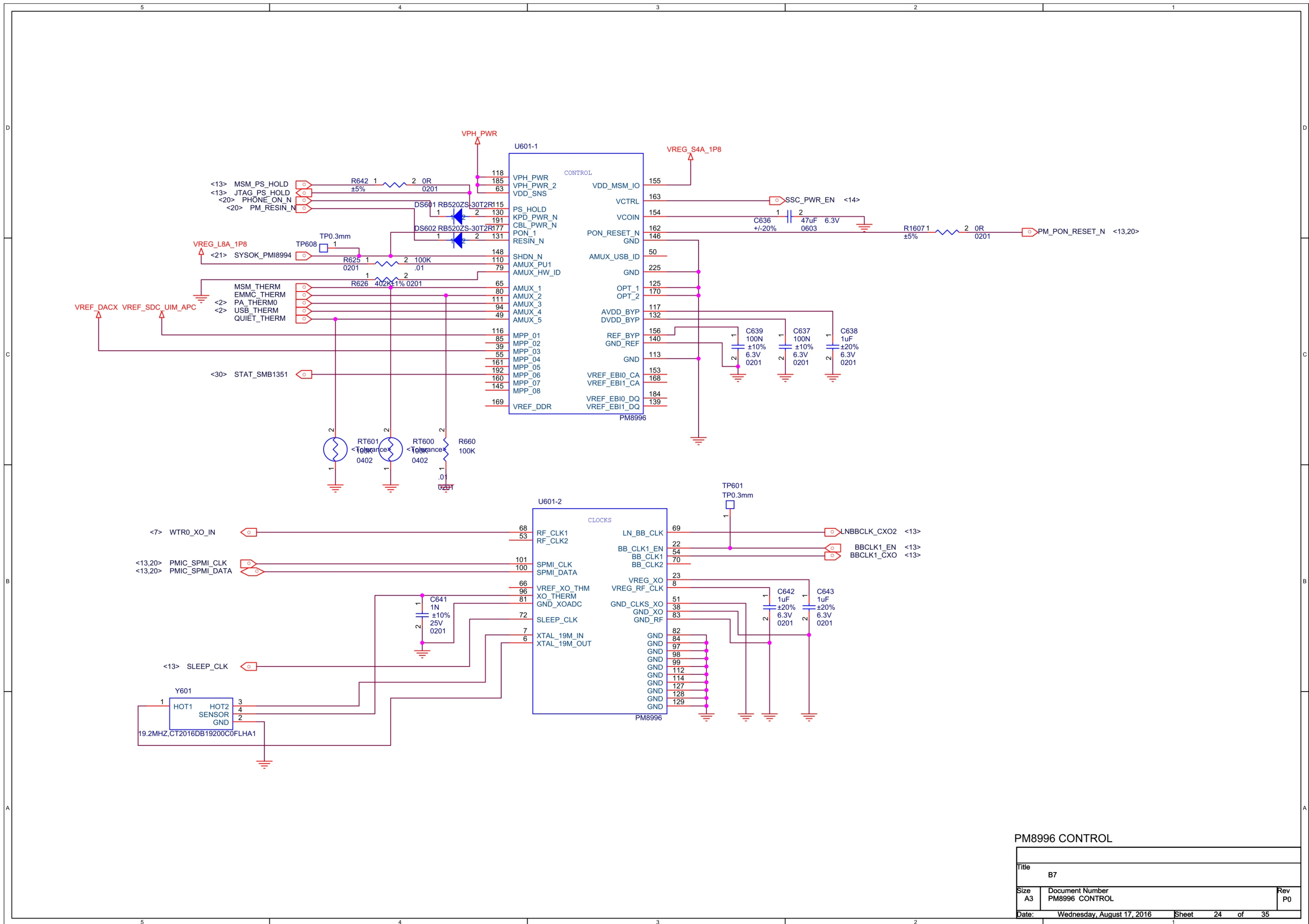




PMI8994 GND

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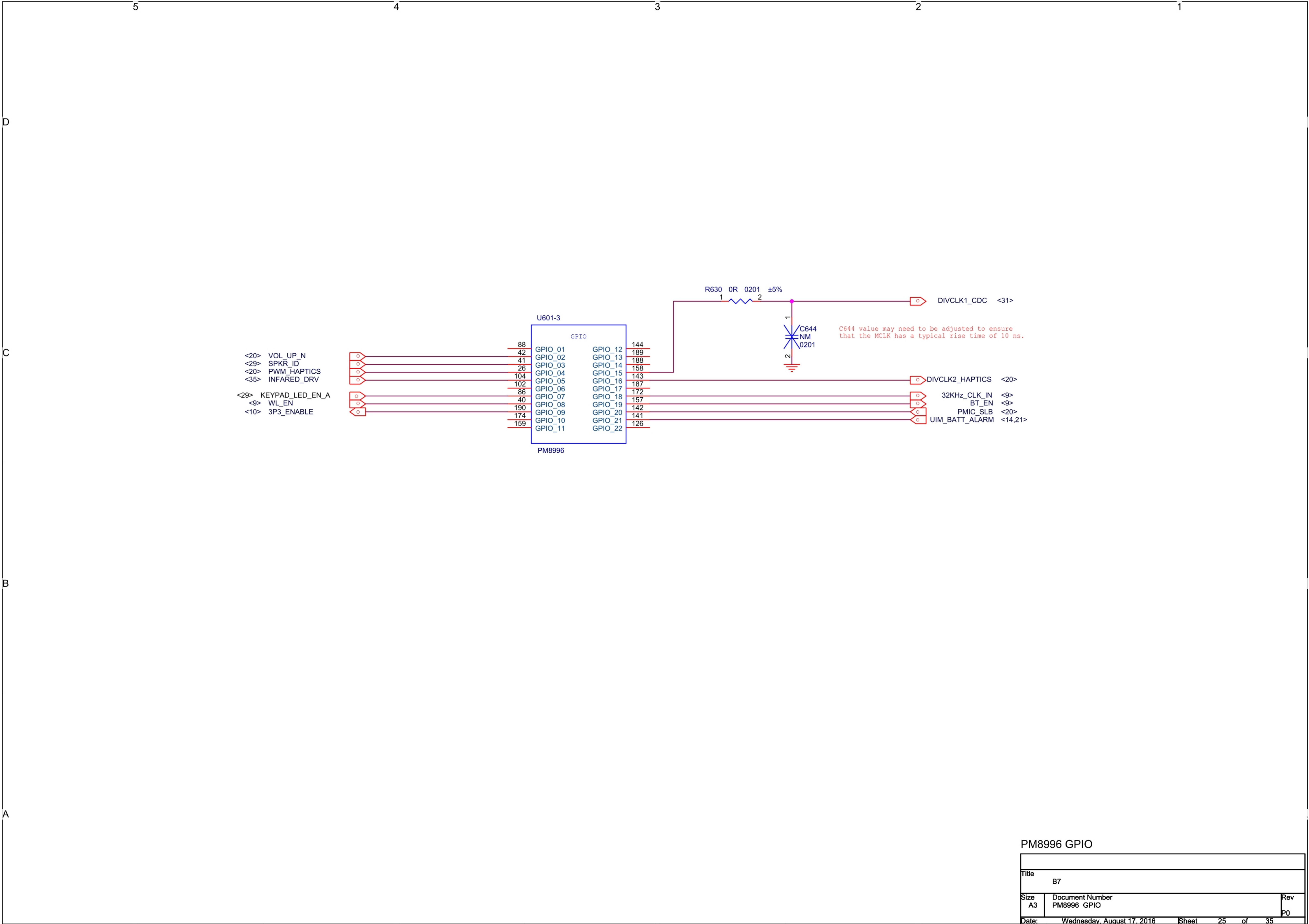




PM8996 CONTROL

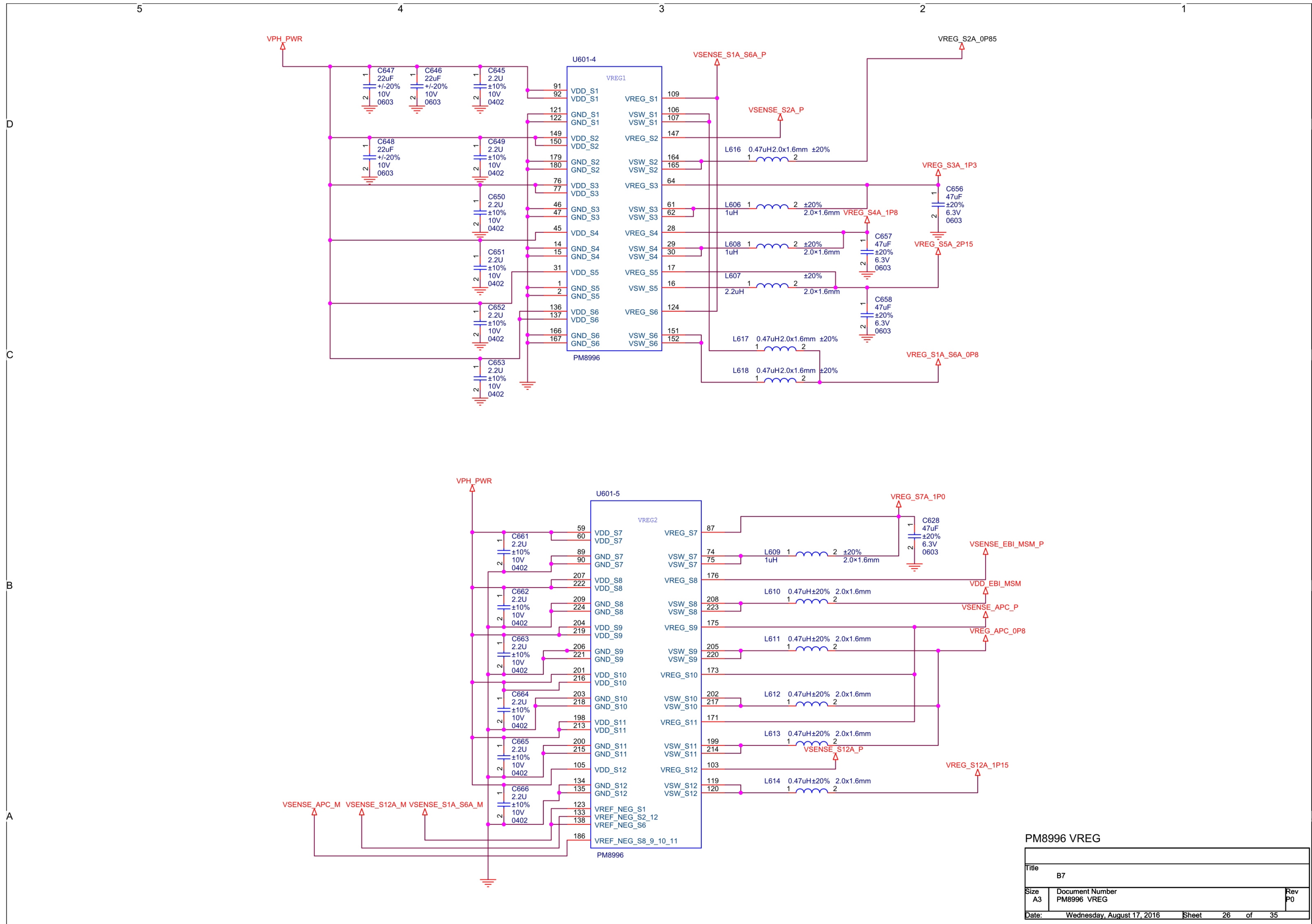
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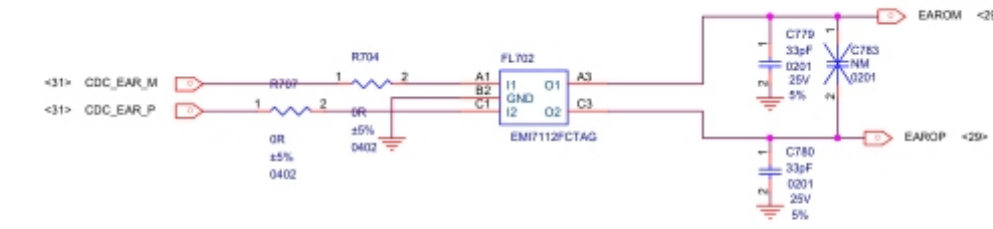


PM8996 GPIO			
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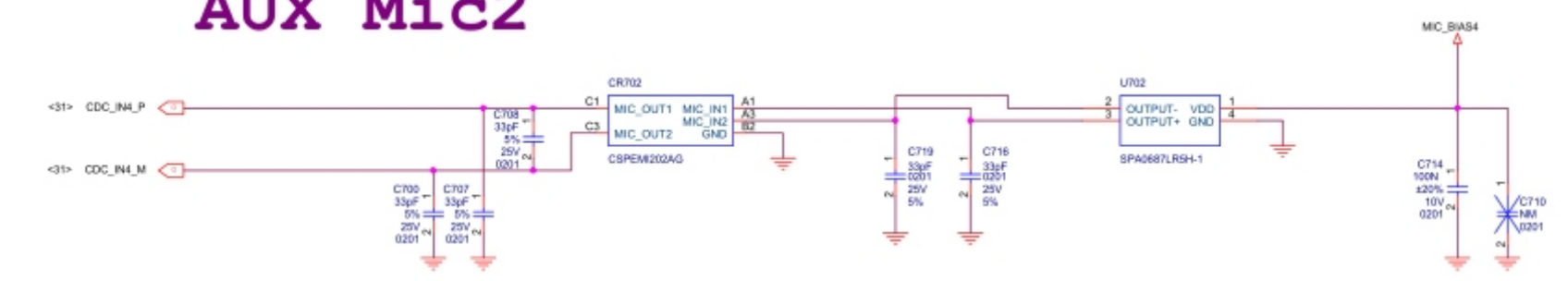




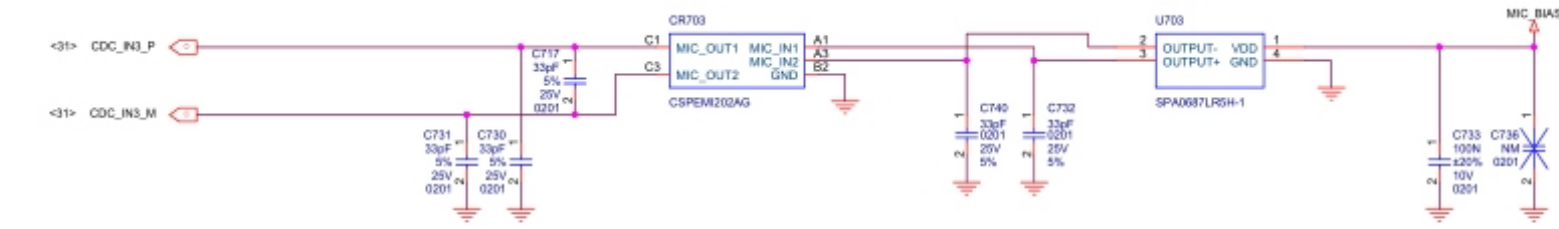
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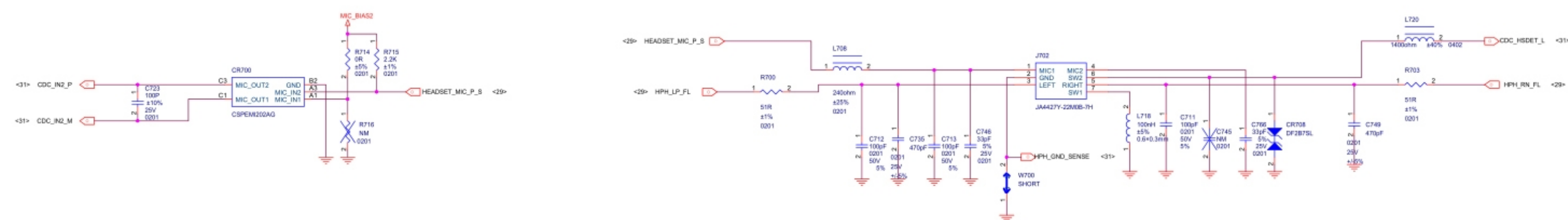
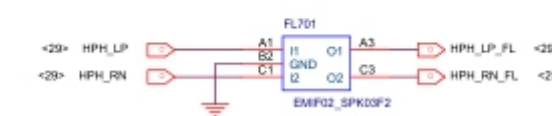
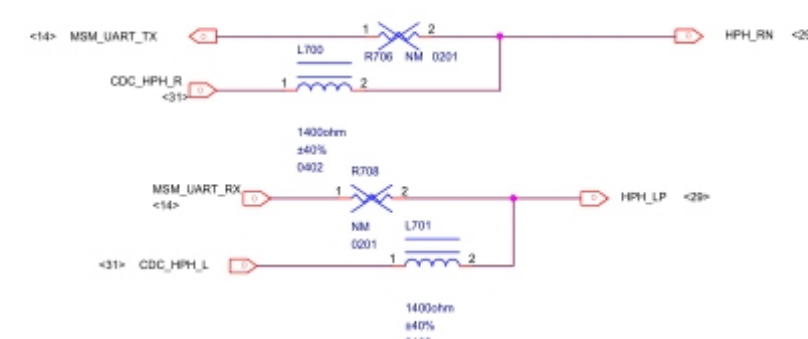
AUX Mic2



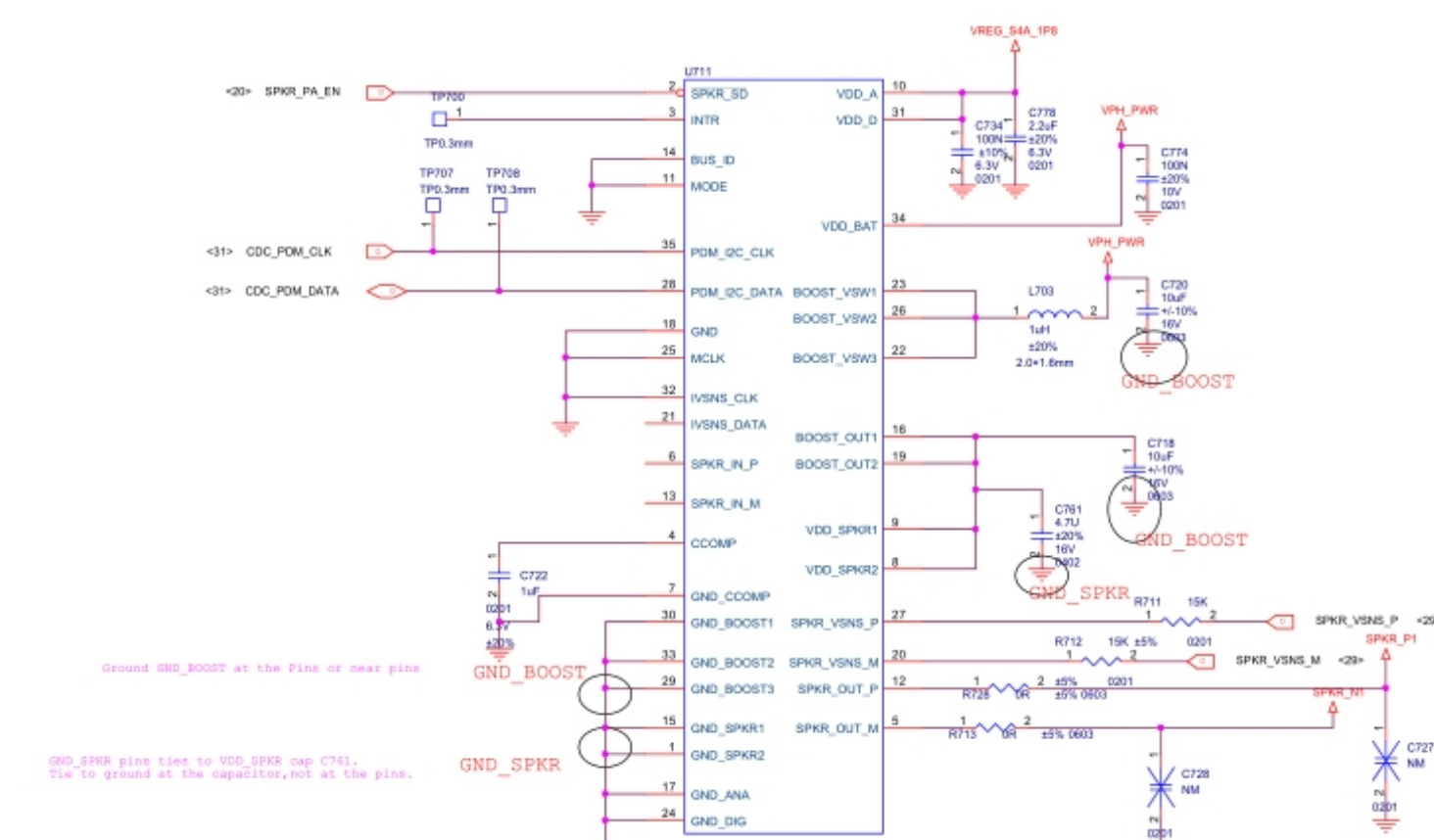
AUX Mic1



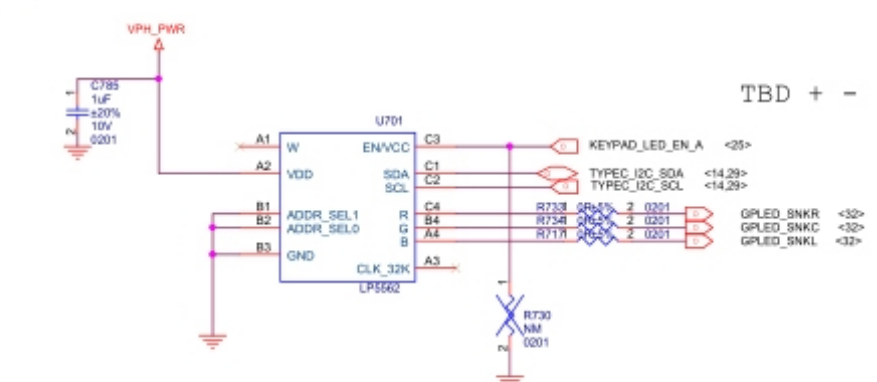
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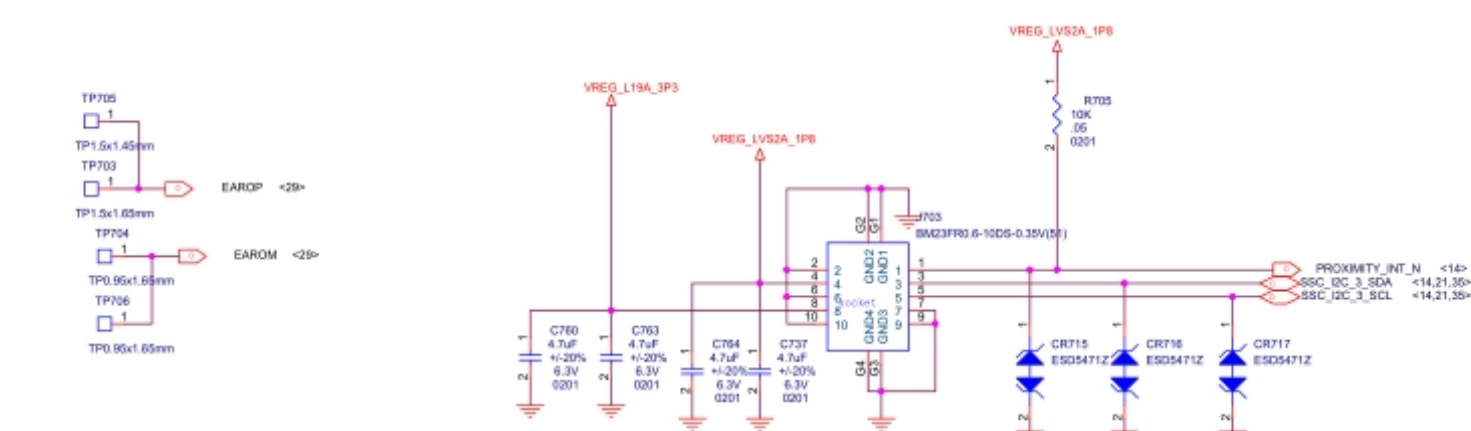
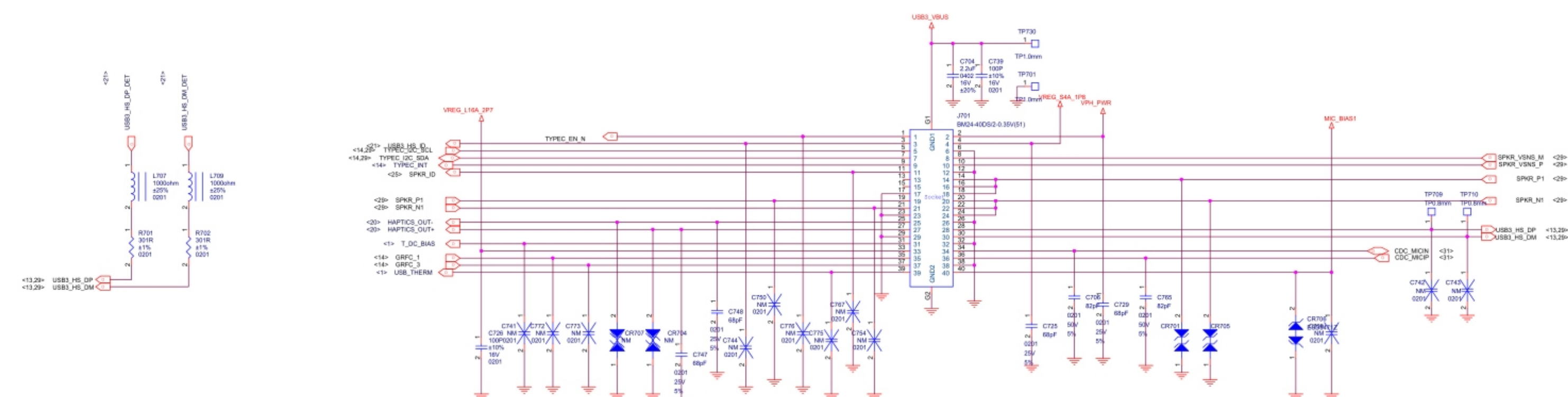
Speaker PA



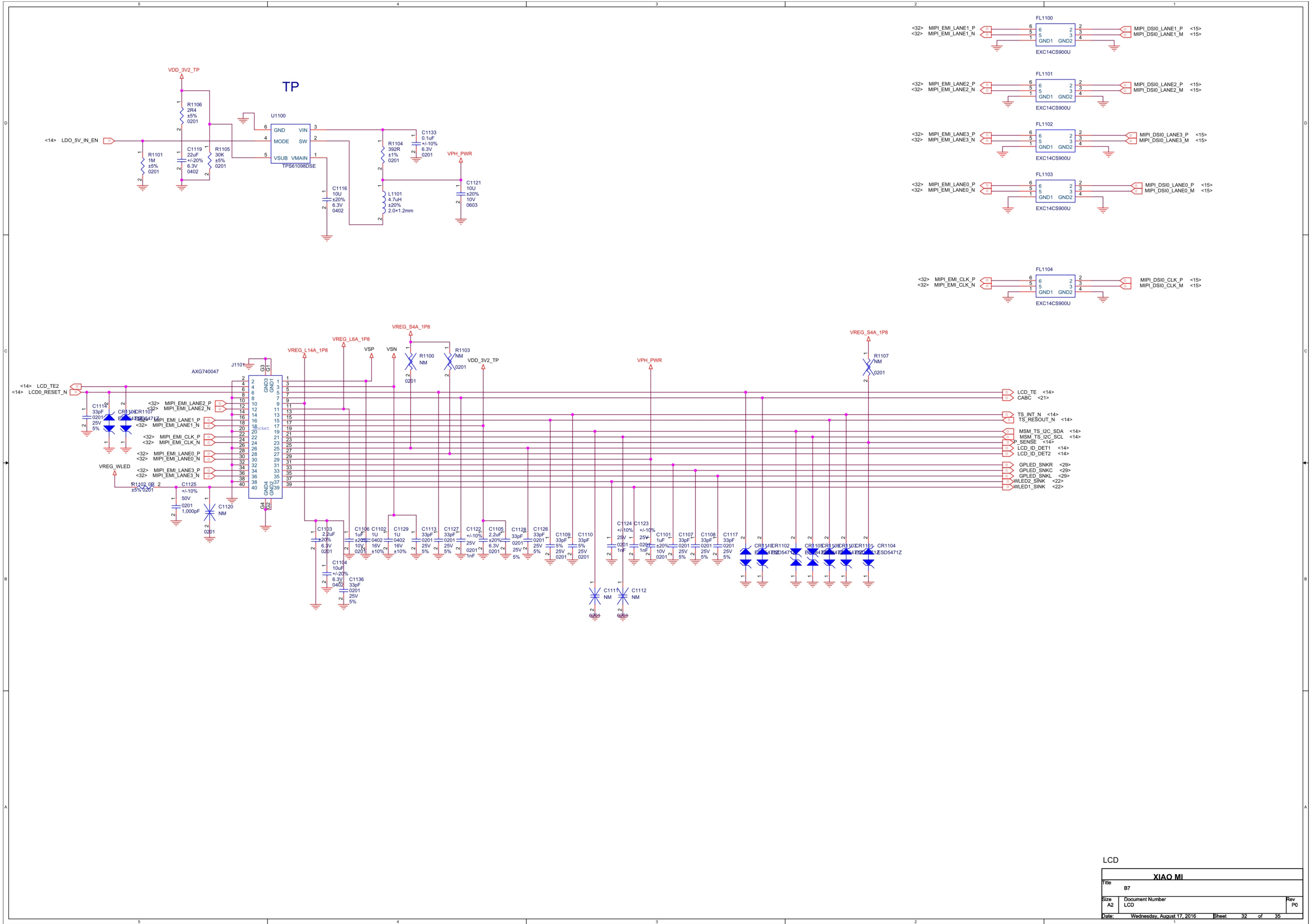
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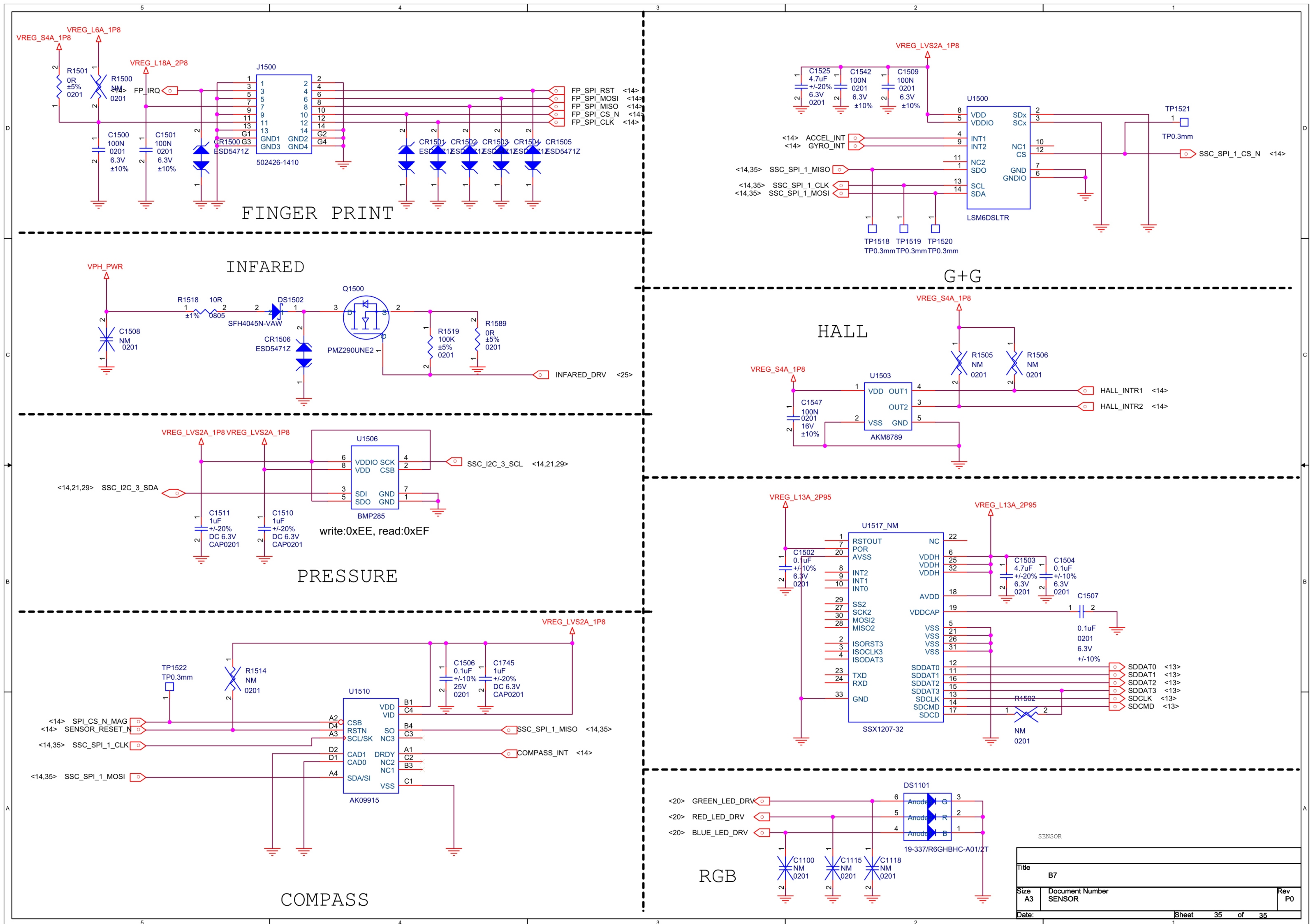
Bottom fpc Connector



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