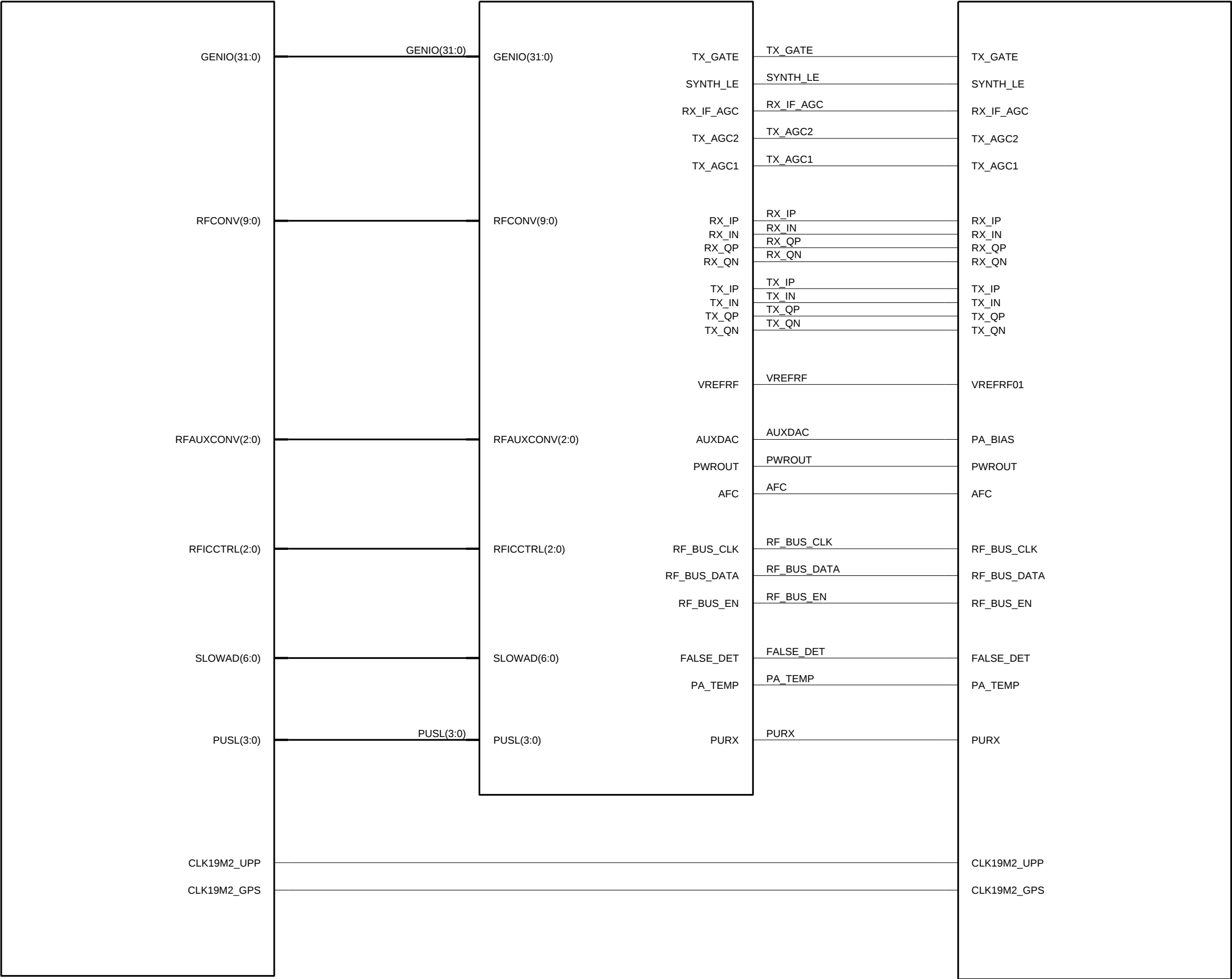


Top Level

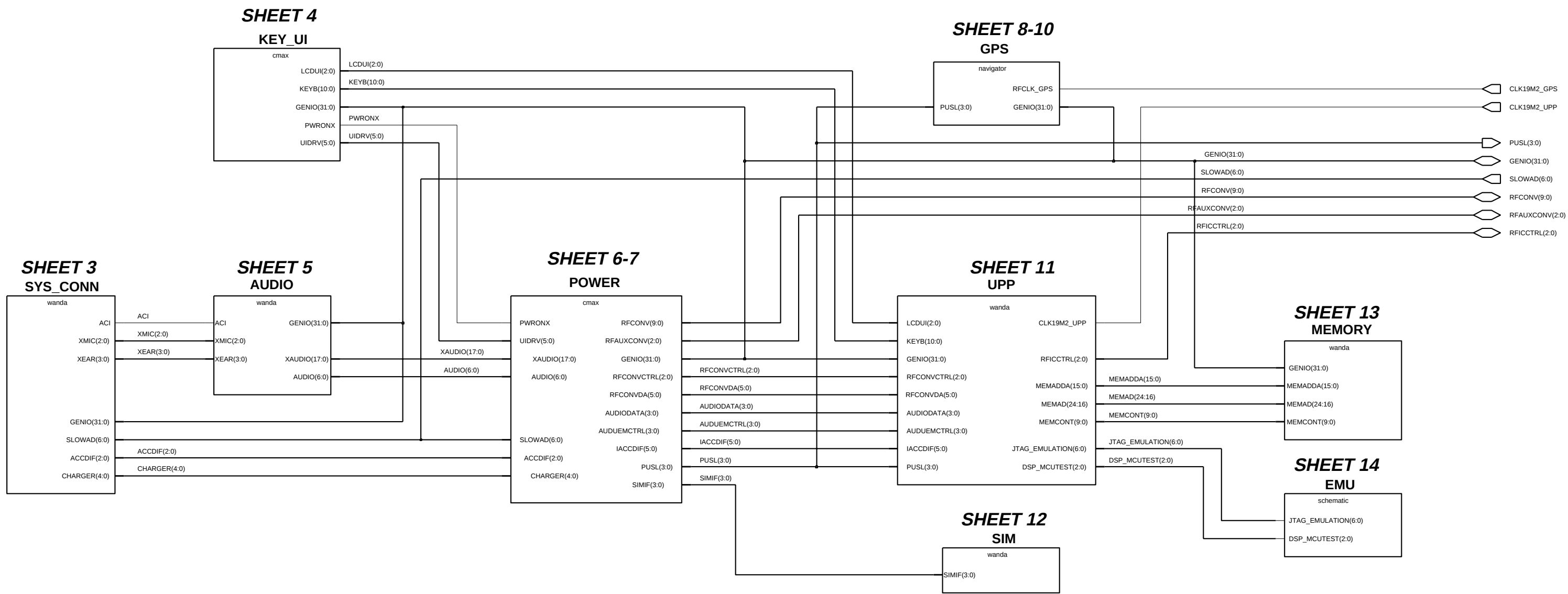
SYS

RF_BB

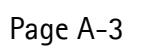
RF



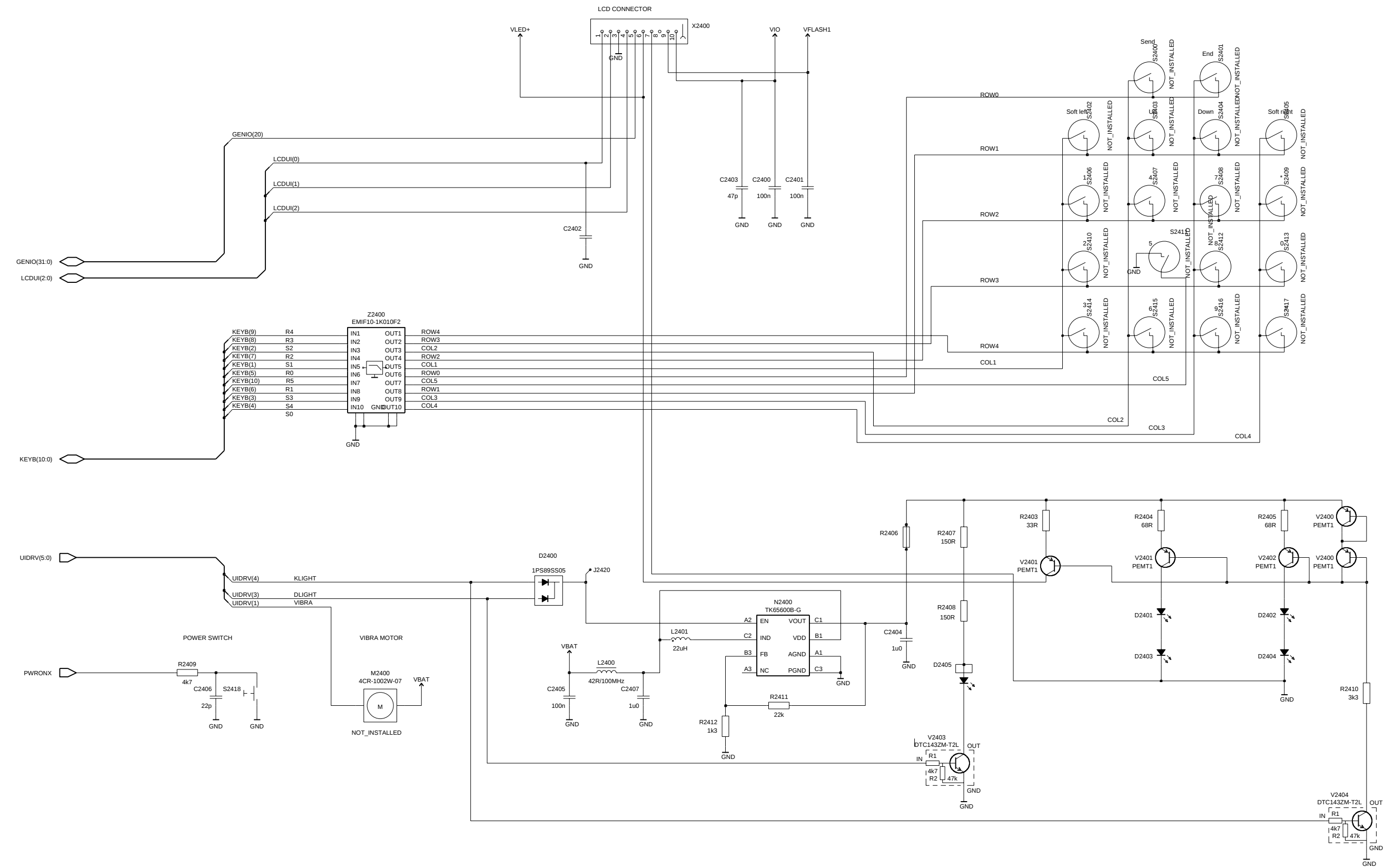
Baseband - Top Level



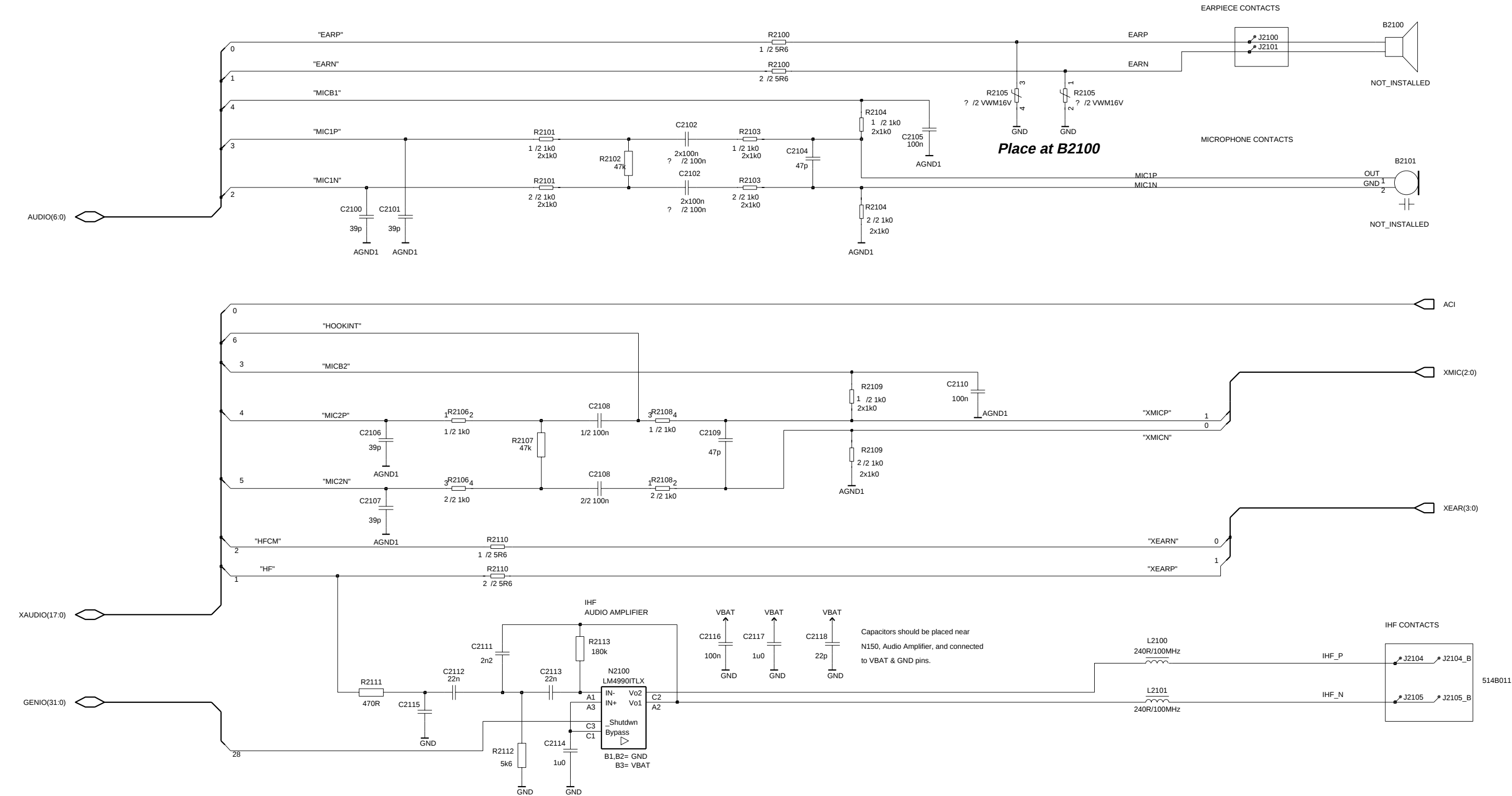
BATTERY CONNECTOR



User Interface



Audio



[illegible]

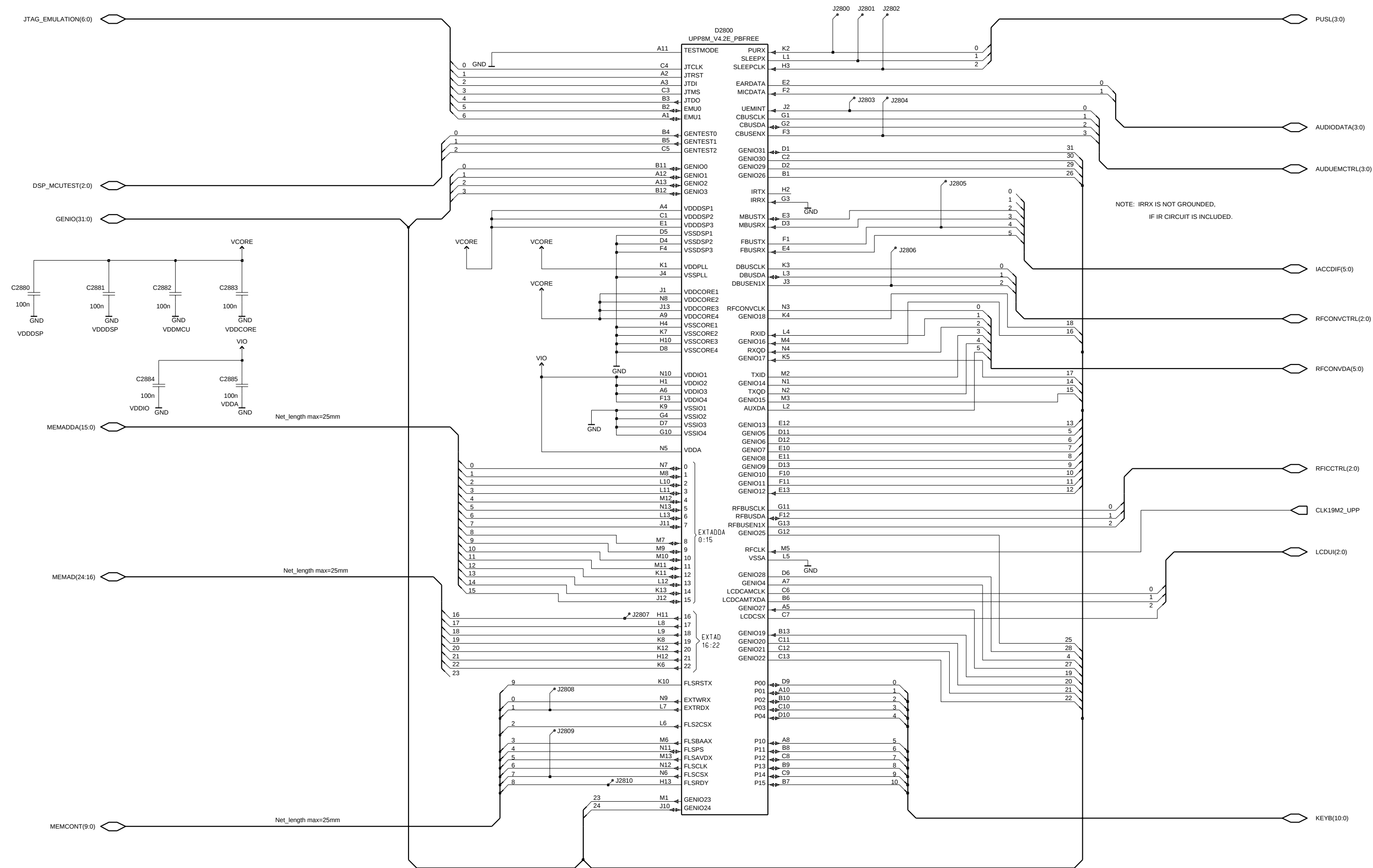
The schematic diagram illustrates the LM2708HTLX-1.35_NOPB (N2300) voltage converter circuit. The input power is supplied by VBAT, which is filtered by capacitor C2301 (100nF). The input inductor L2300 (42R/100MHz) is connected to the converter's input pin (A3). The output of the converter is filtered by capacitor C2302 (10uF) and connected to the output pin (B1), which is also connected to the feedback network (R2300, C2300) and the output filter capacitor C2300 (10uF). The output voltage is Vcore. The converter is controlled by UEMRSTX, SMPCLK, and PUSL(3:0) signals. The feedback network is connected to the feedback pin (FB) and the output pin (B1). The feedback network consists of a resistor R2300 and a capacitor C2300 (10uF) connected to ground. The feedback pin (FB) is also connected to the output pin (B1). The feedback pin (FB) is also connected to the output pin (B1). The feedback pin (FB) is also connected to the output pin (B1).

SHEET 6

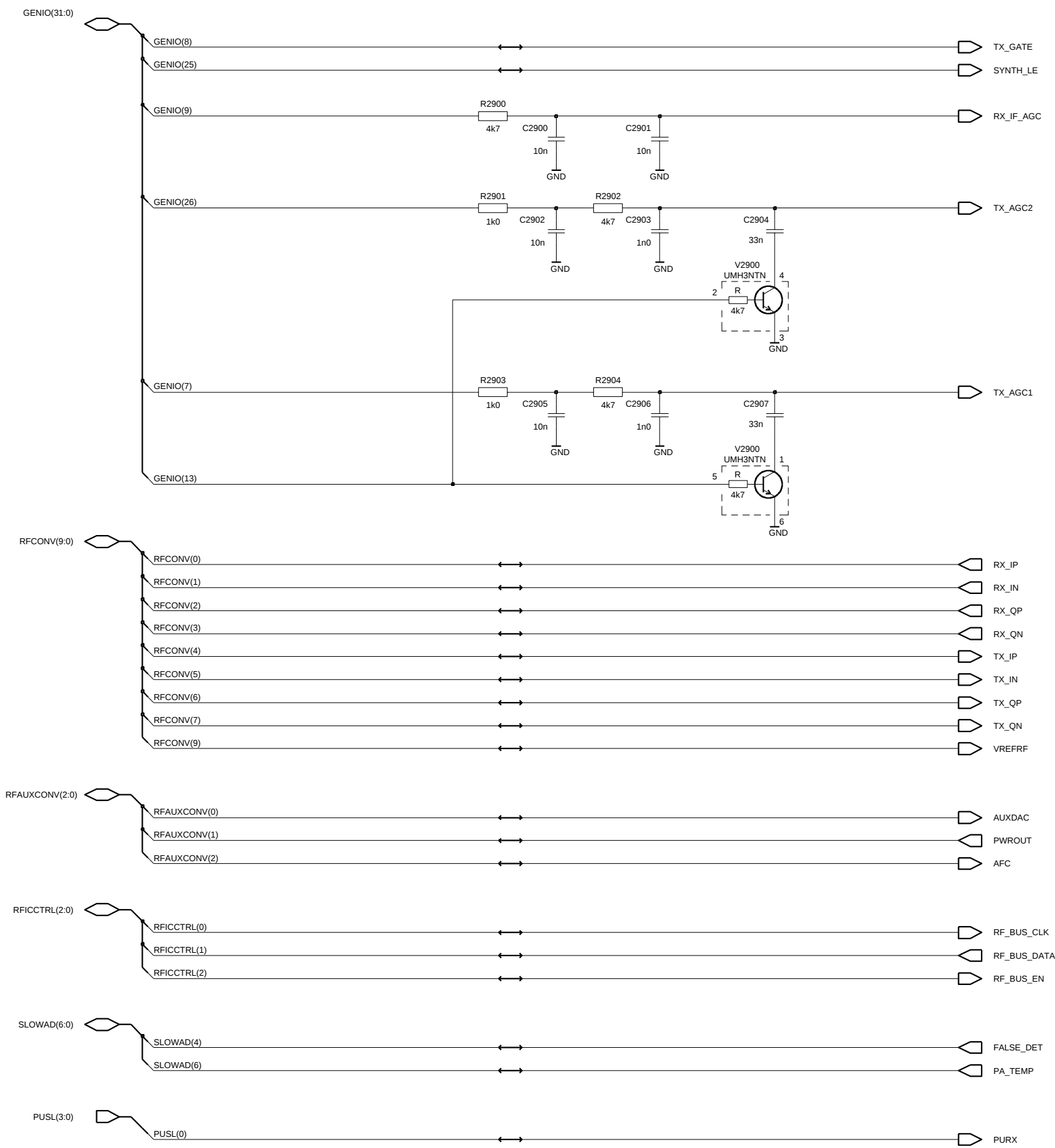


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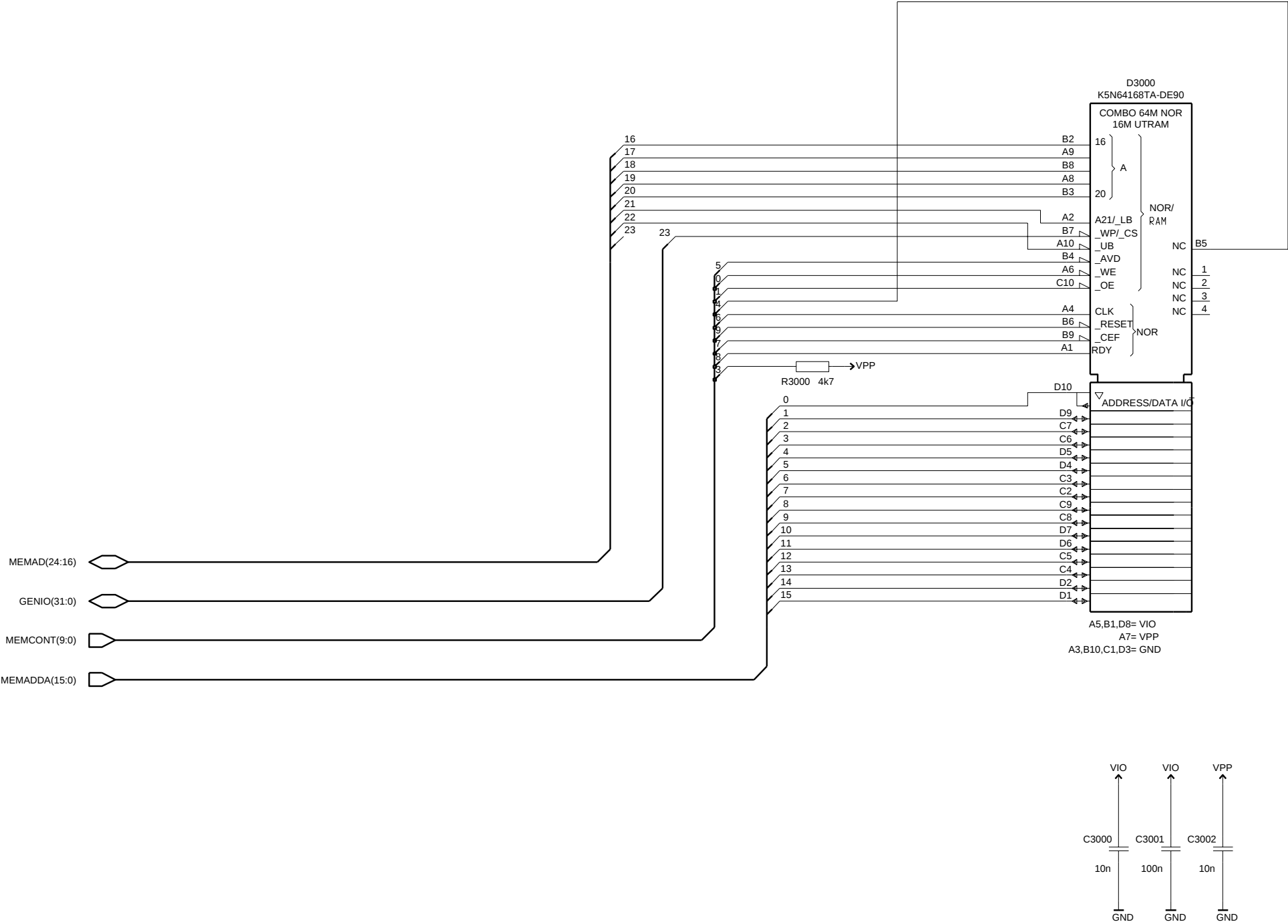
UPP



RF-BB Interface

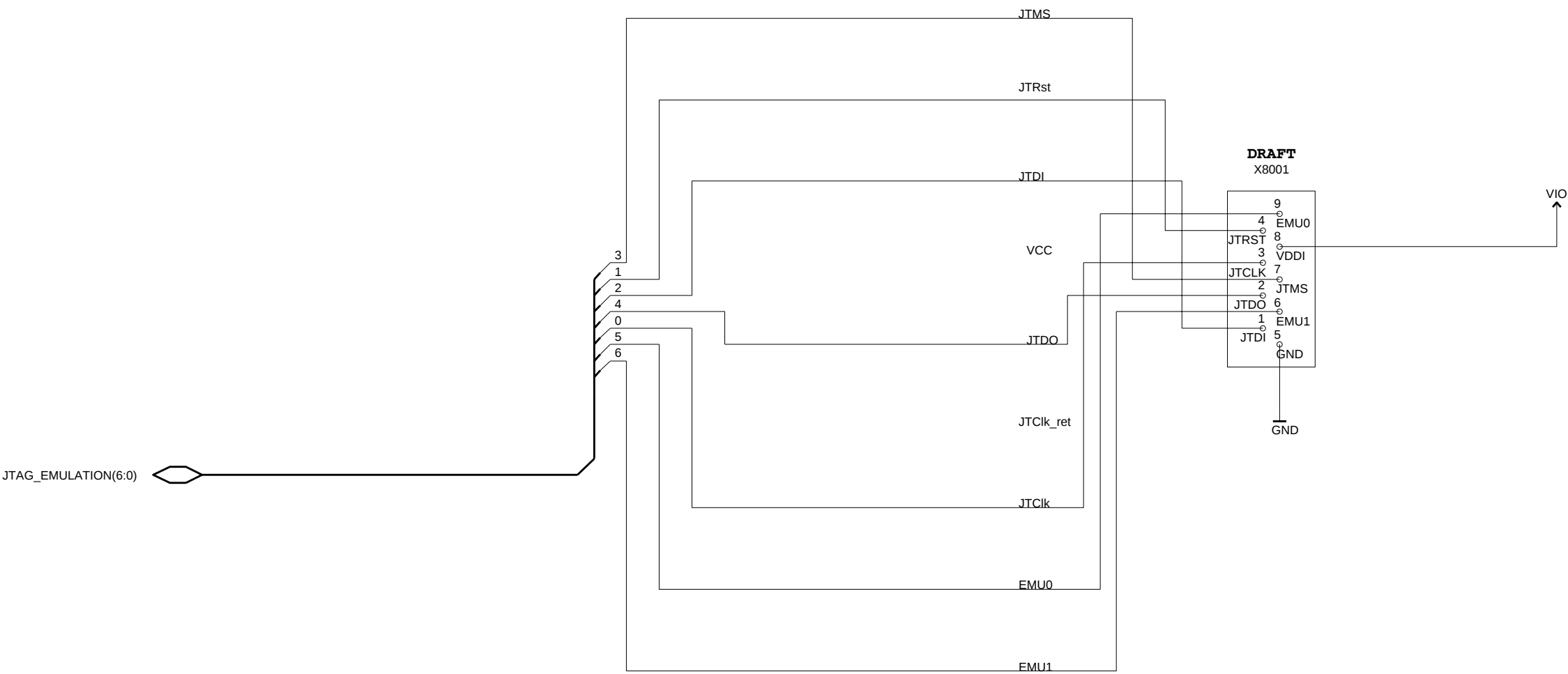


Memory



JTAG and Ostrich Connectors

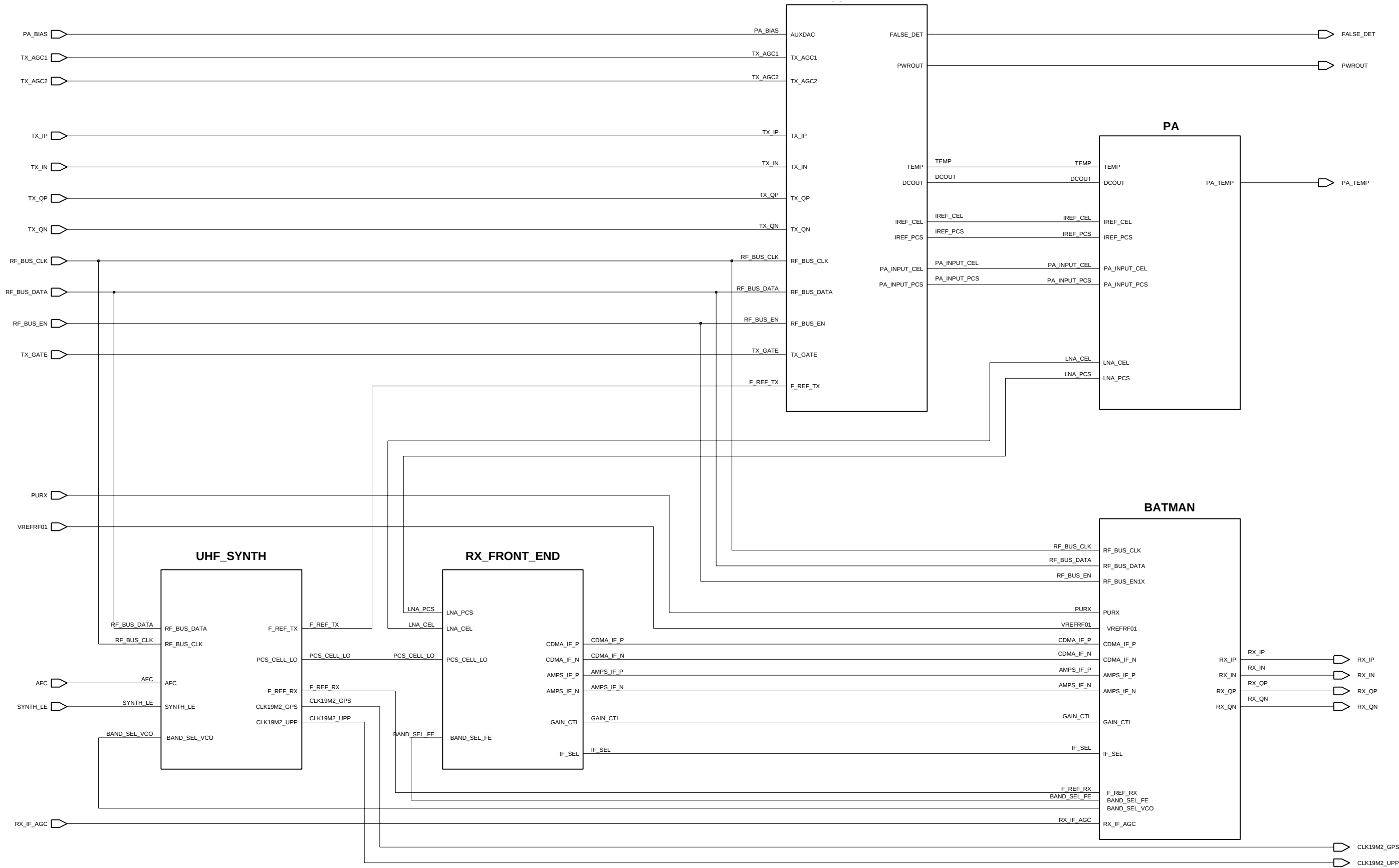
JTAG



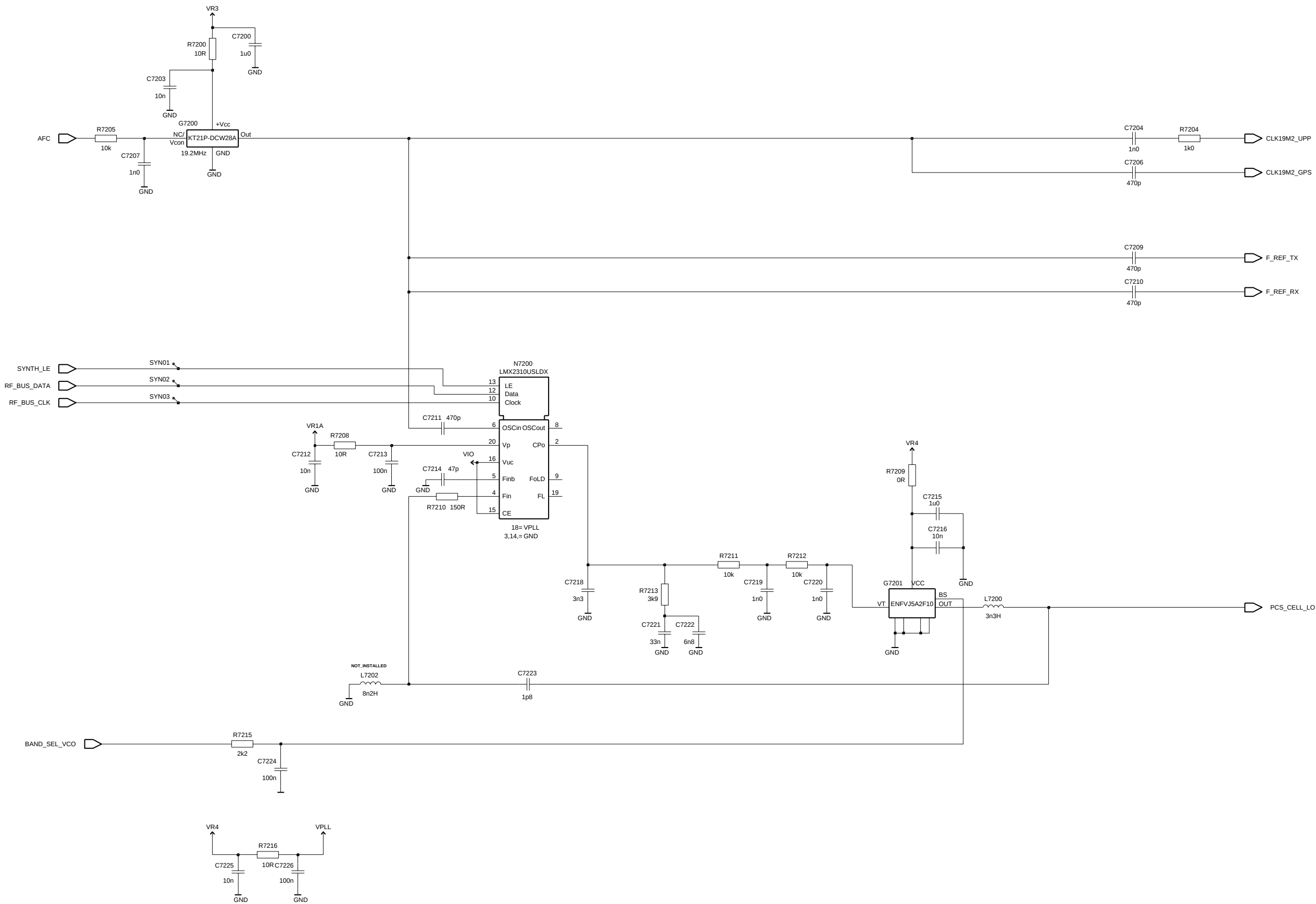
OSTRICH



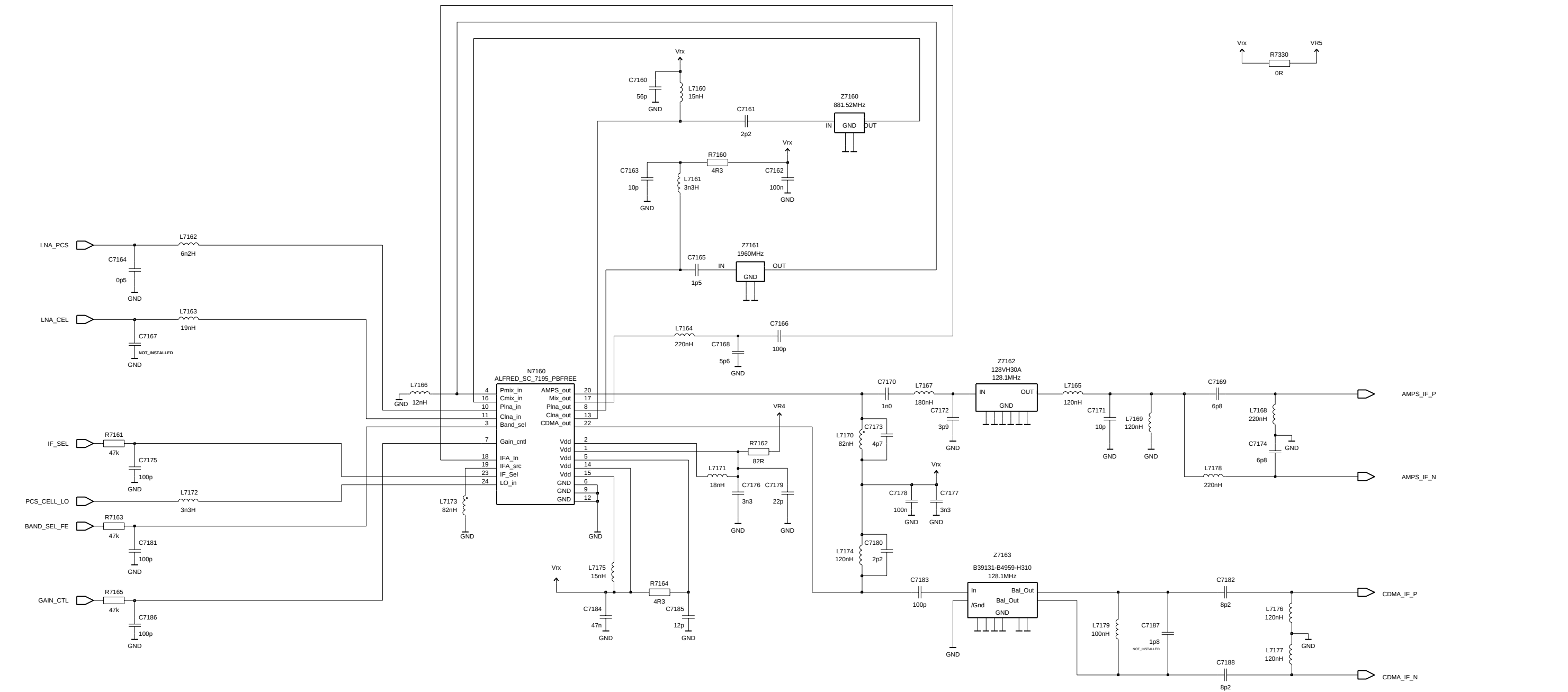
RF - Top Level



UHF Synthesizer

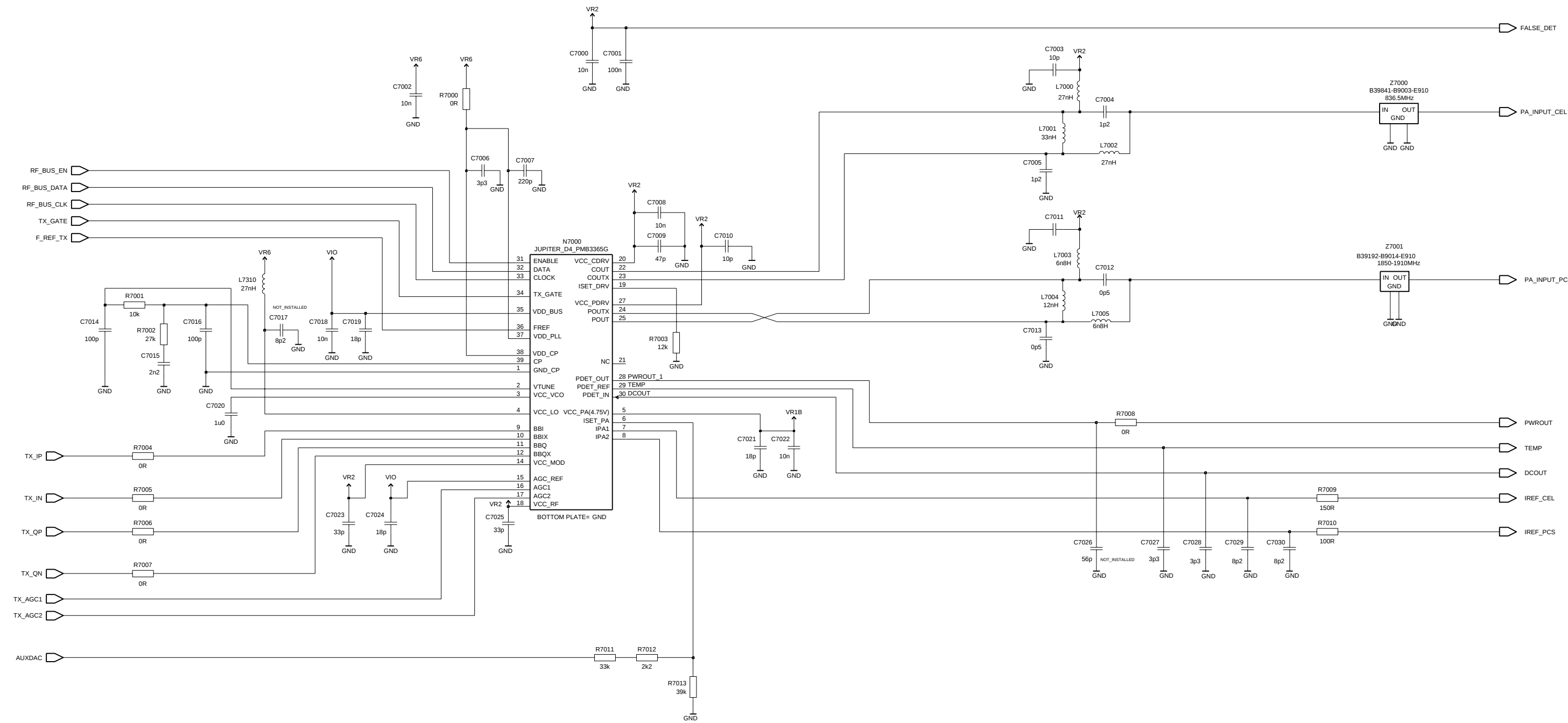


Front End (N7160)



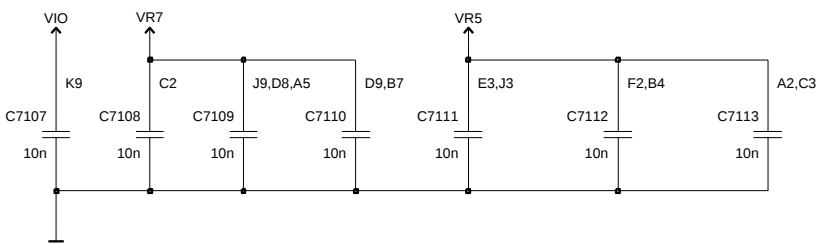
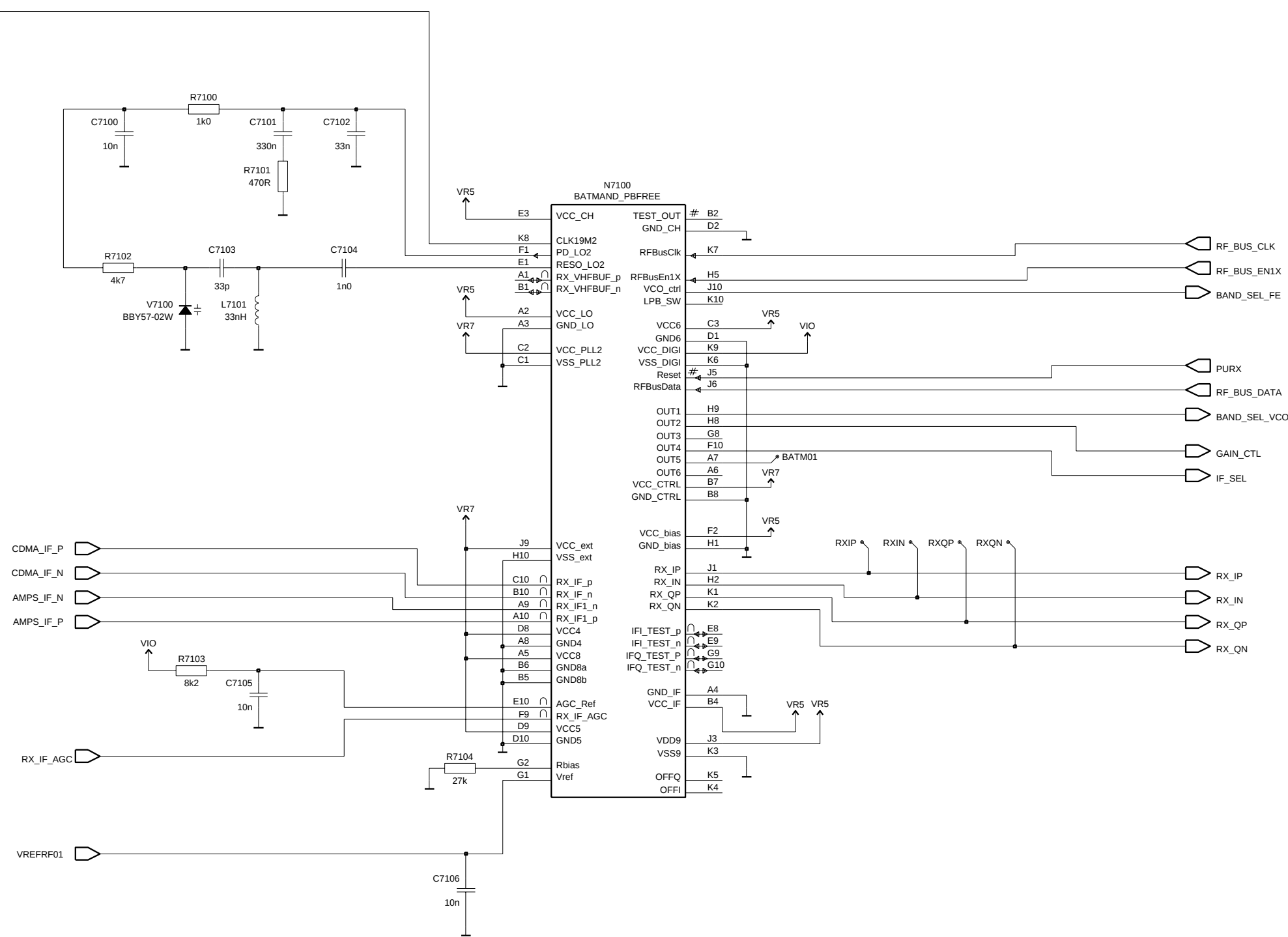
MODES	LOGIC INPUTS		
	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

N7000

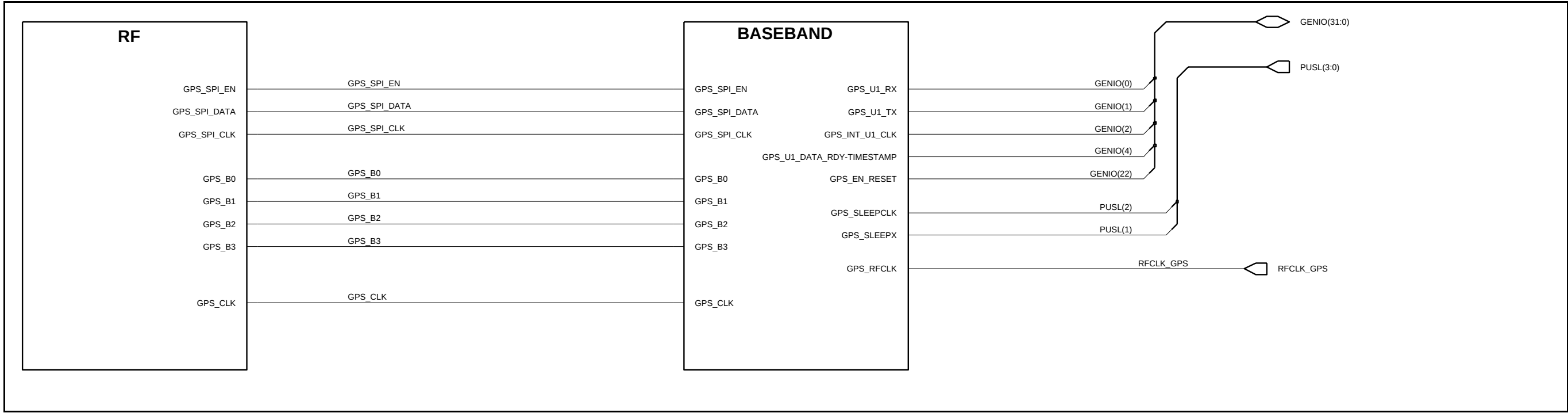


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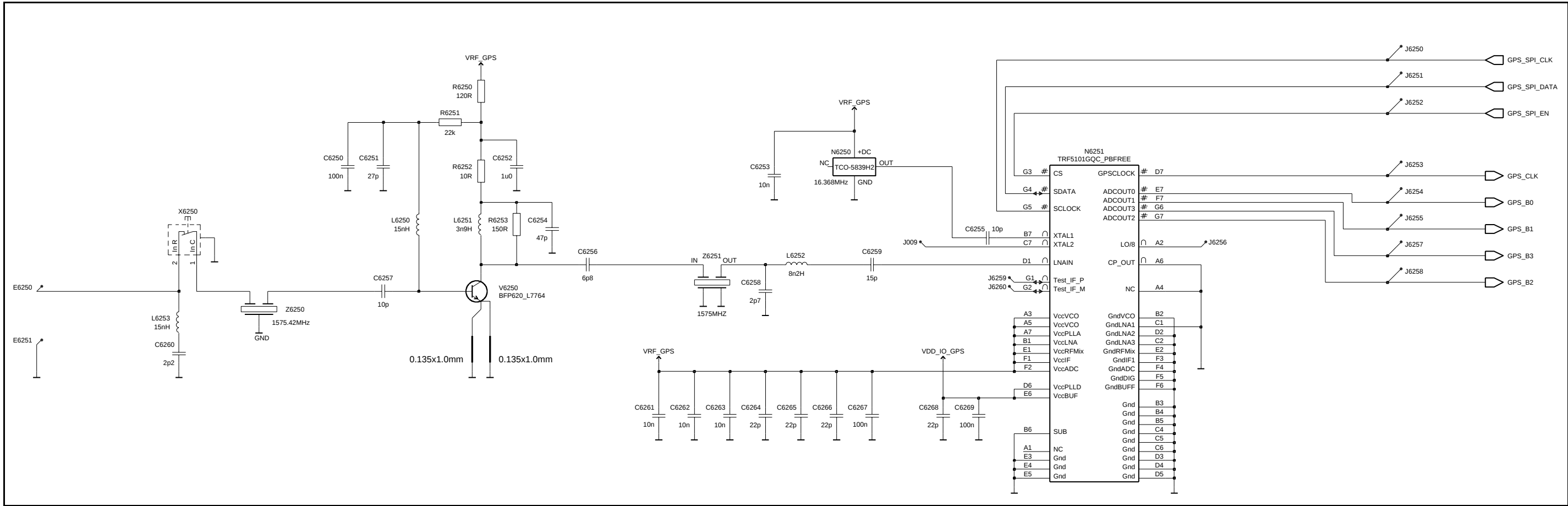
N7100



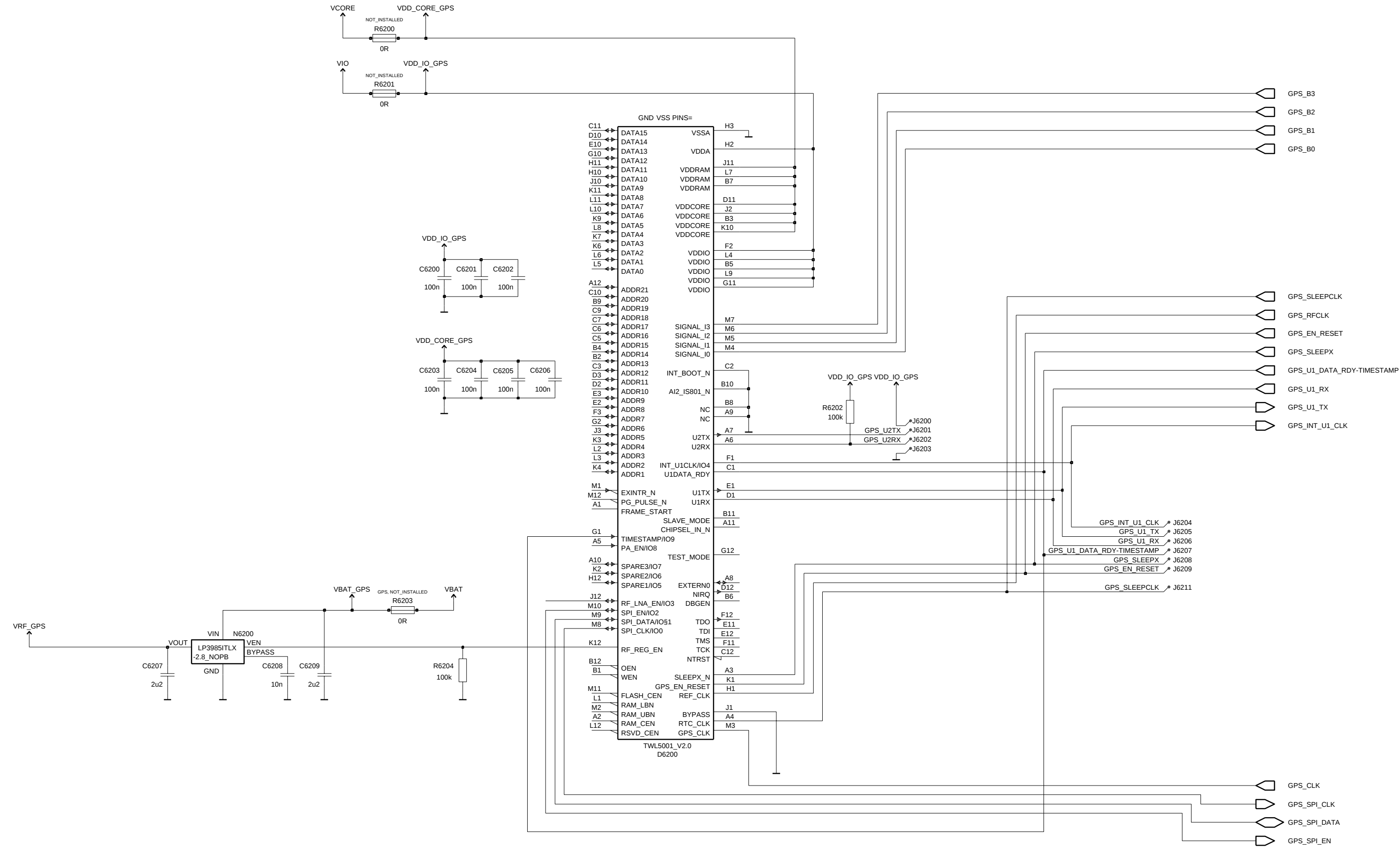
GPS RF-BB Top Level (2115i and 2116i only)



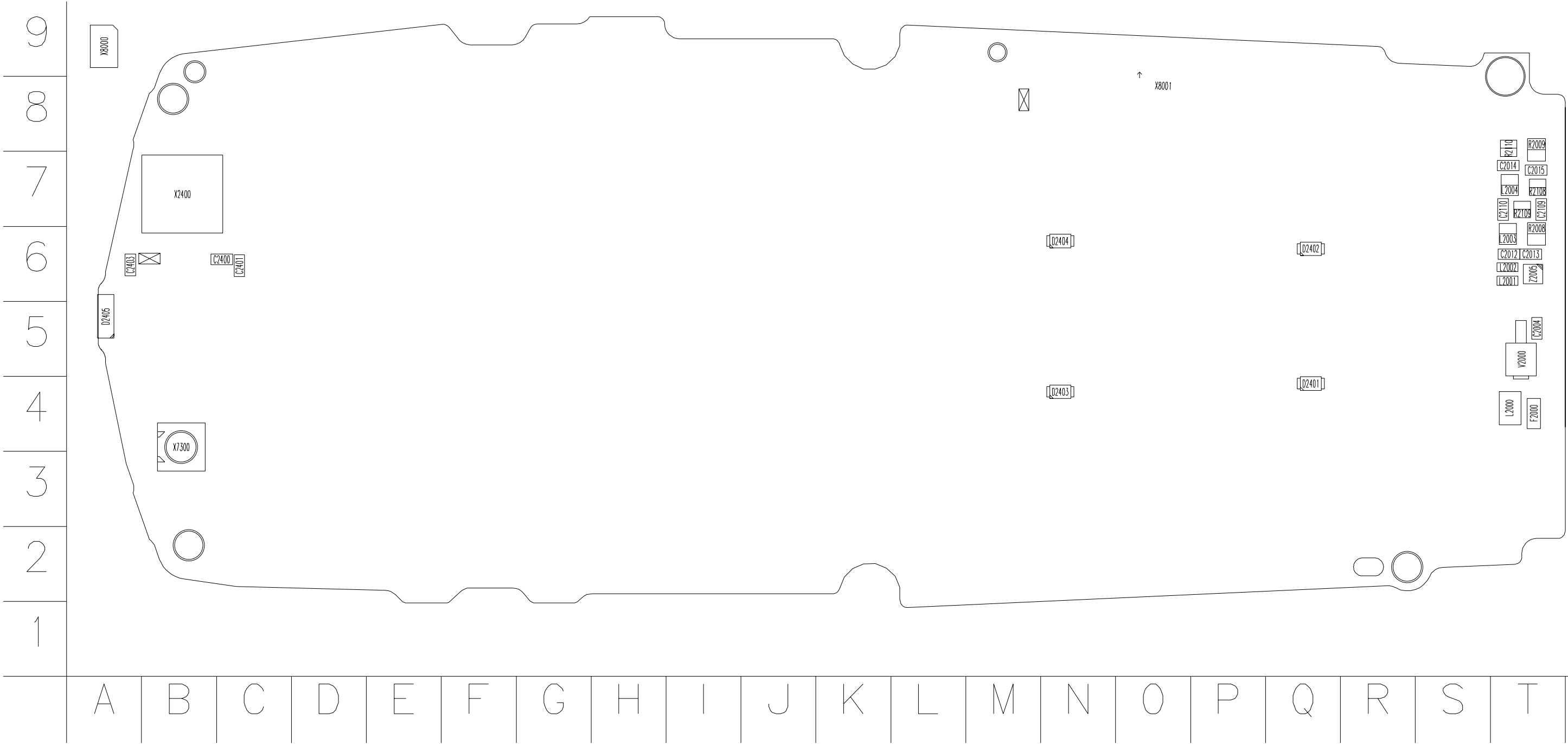
GPS RF (2115i and 2116i only)



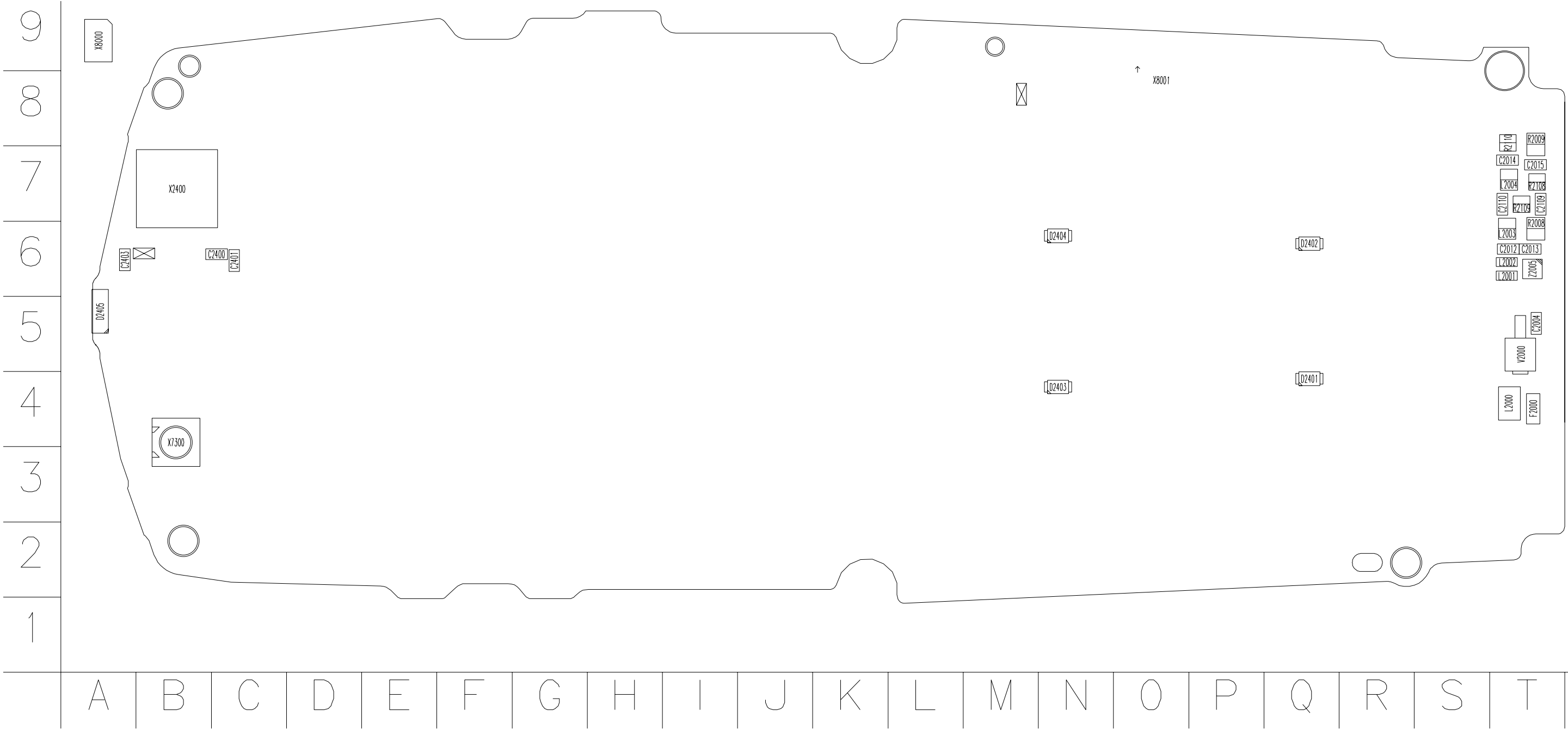
GPS Baseband (2115i and 2116i only)



2115i/2116i Component Layout - Top



2116 Component Layout - Top



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