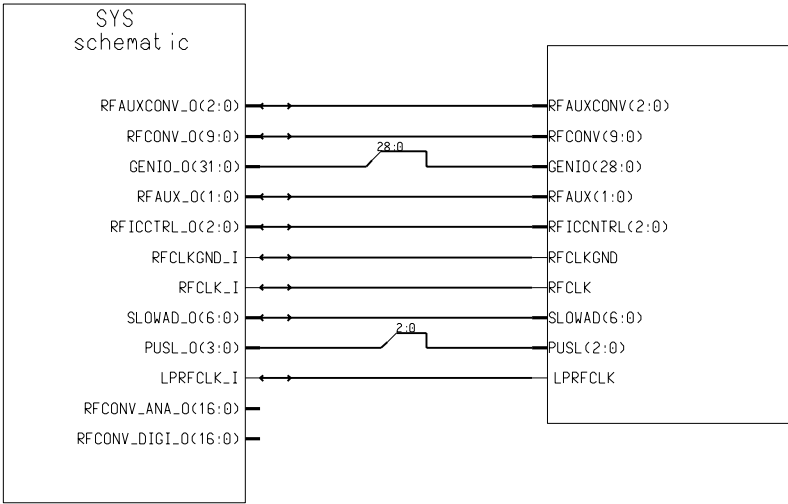
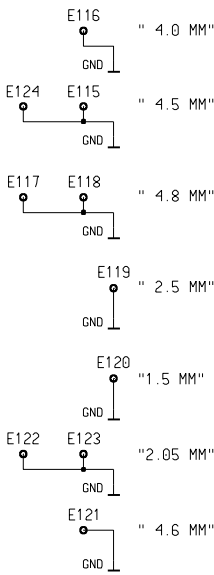
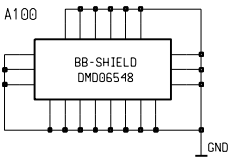


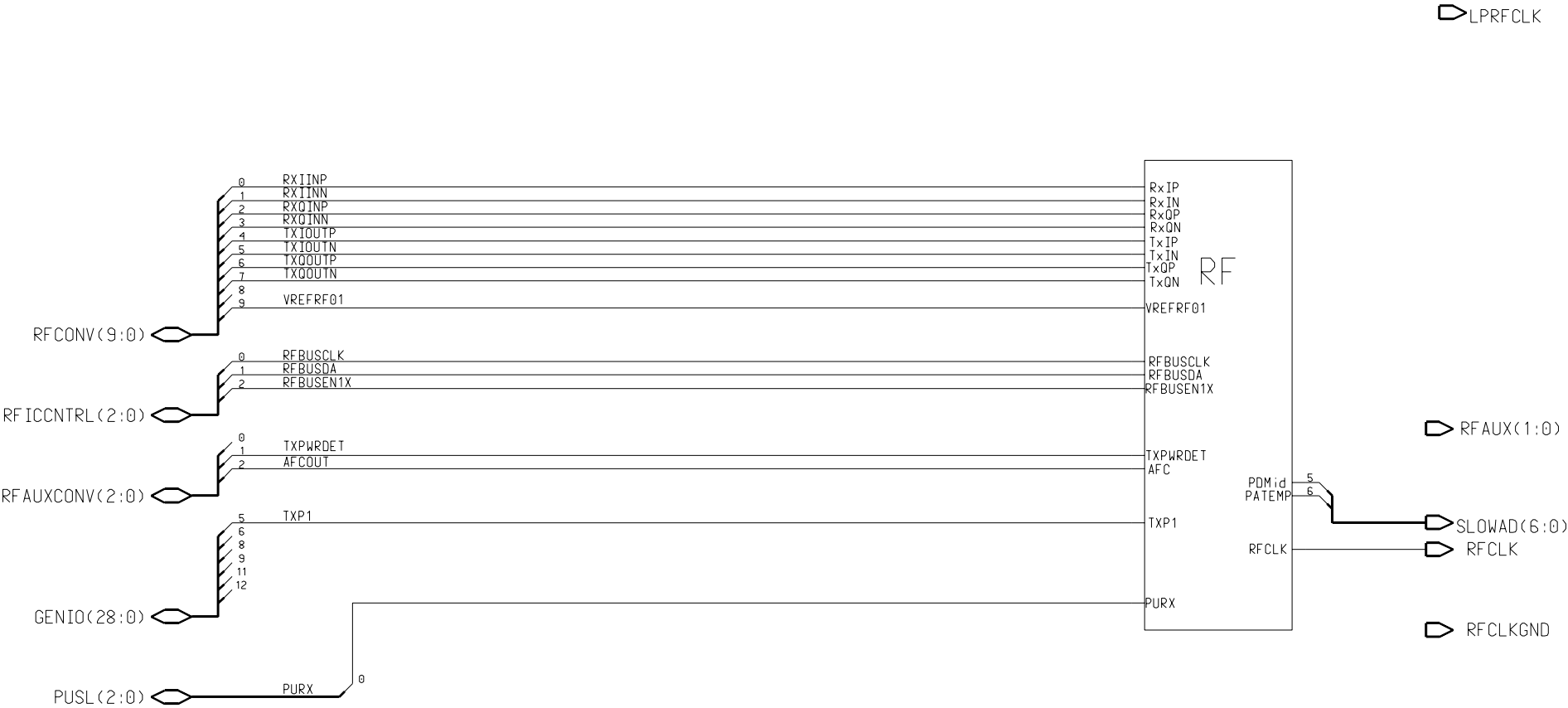
RF-BB Diagram



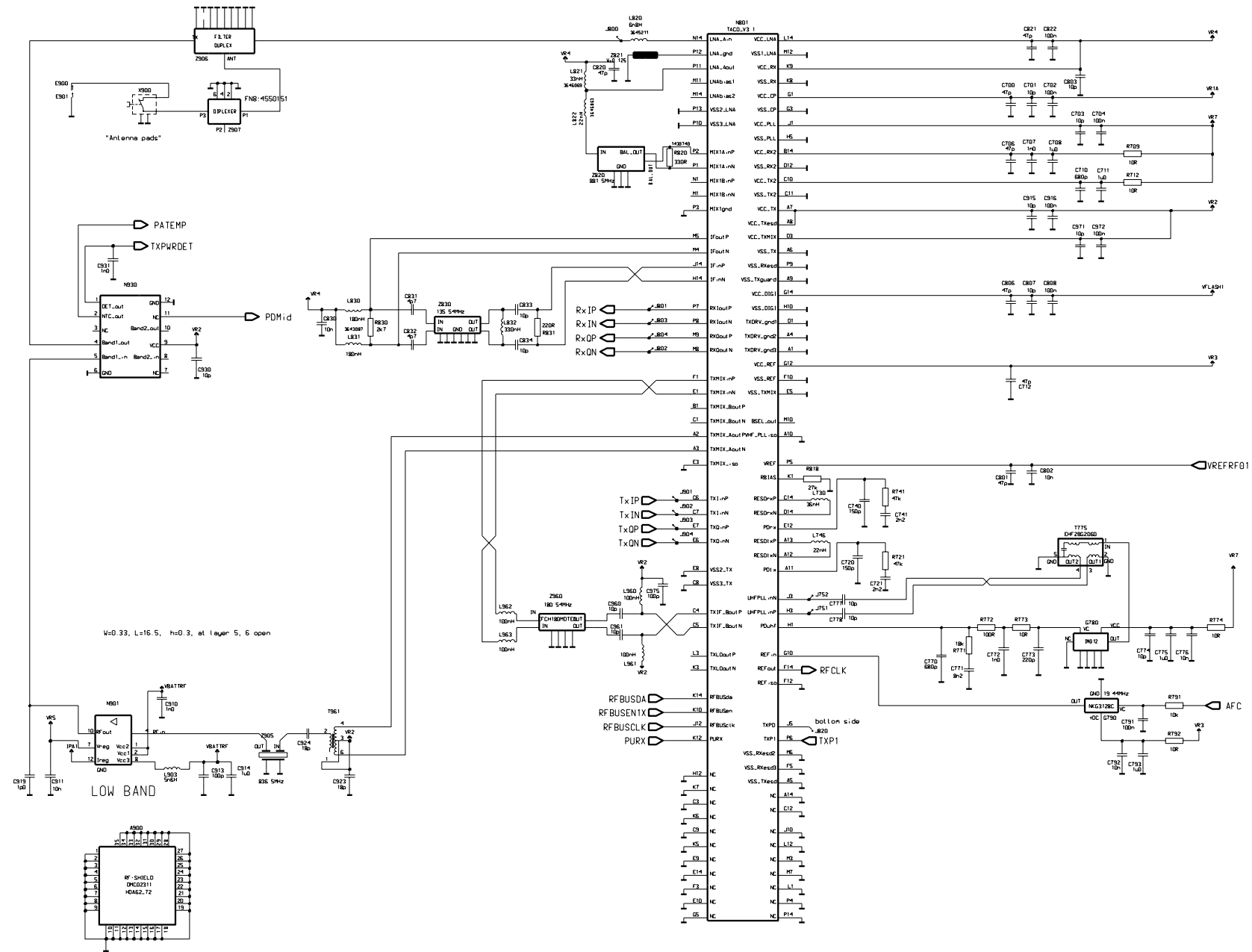
"GROUND HOLES FOR MECHANICS"



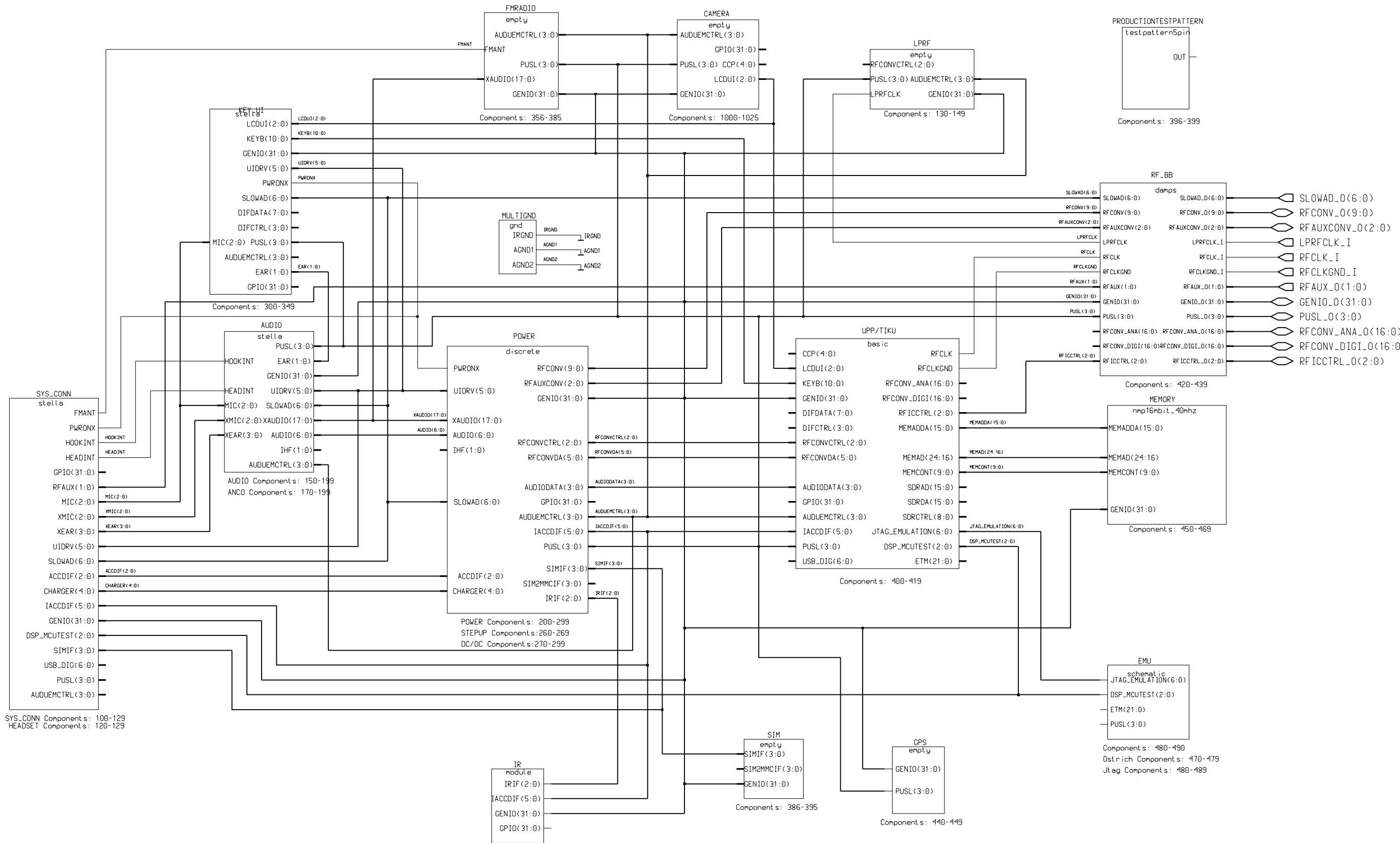
RF Block Diagram



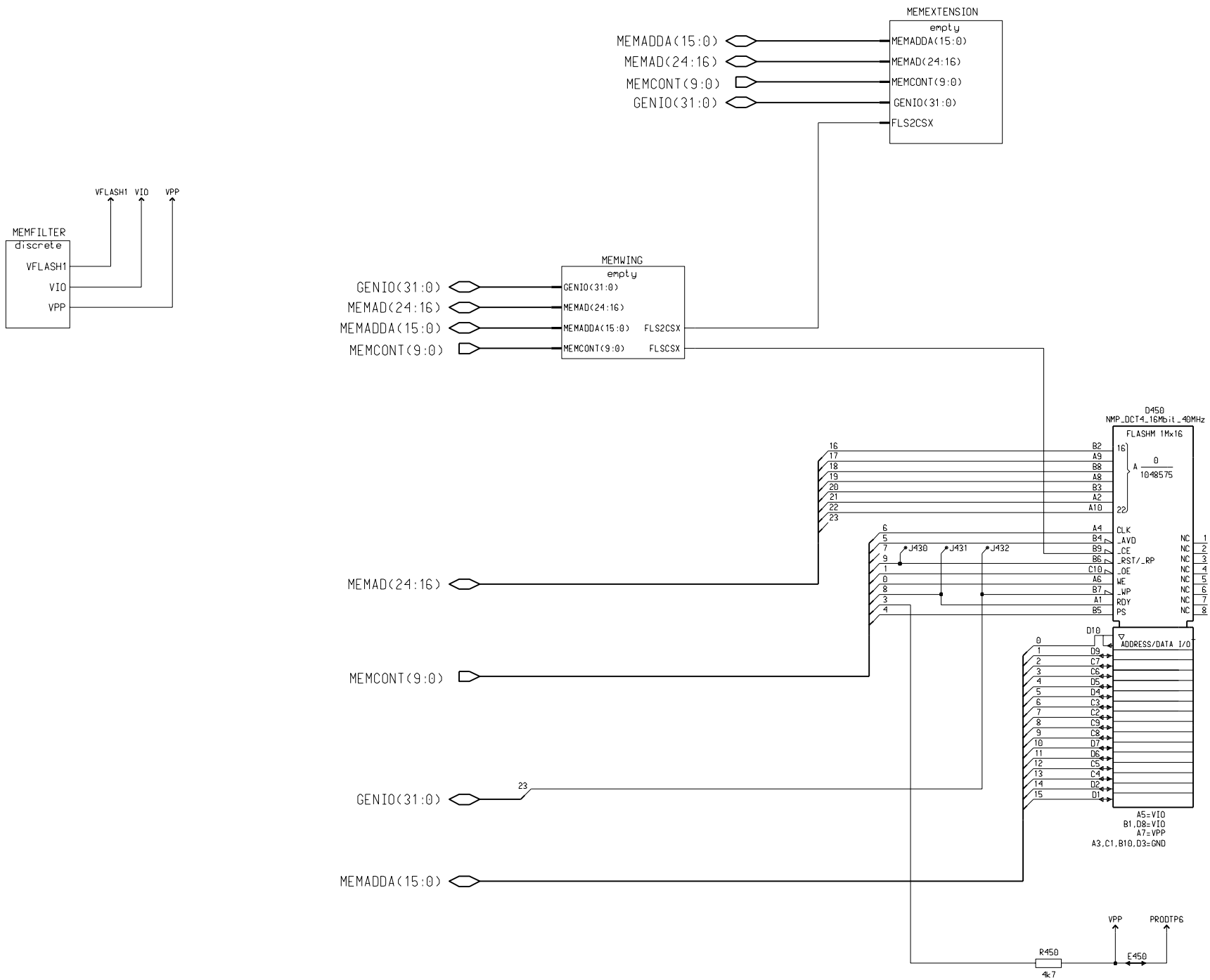
RF Module



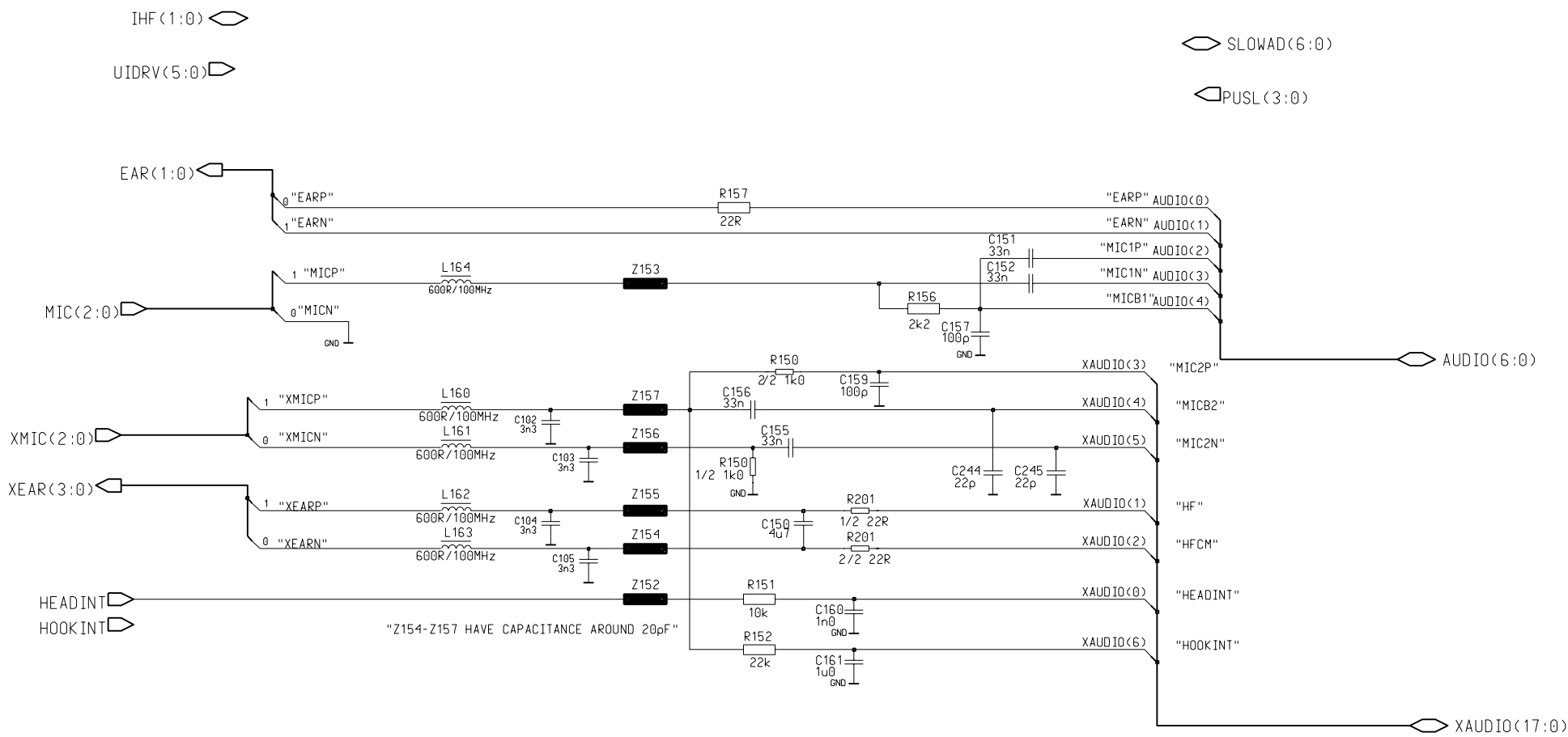
DCT4 Common Baseband (Top-level)



Flash Memory (32-bit)



Sys/Audio



Sys/System Connector

AUDUEMCTRL<3:0>

PUSL<3:0>

GENIO<31:0>

USB_DIG<6:0>

SIMIF<3:0>

DSP_MCUTEST<2:0>

IACCDIF<5:0>

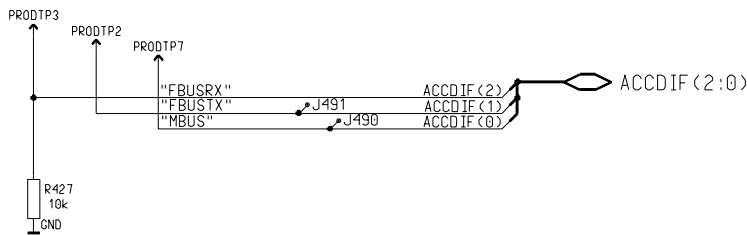
RFAUX<1:0>

PWRONX

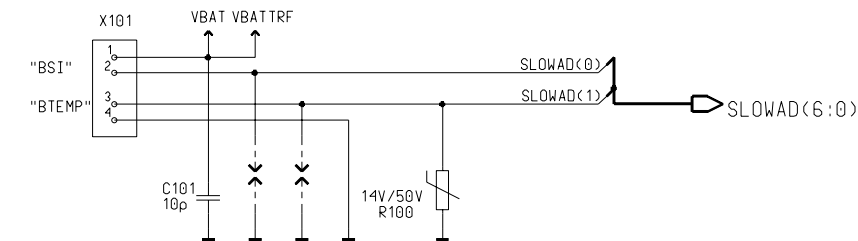
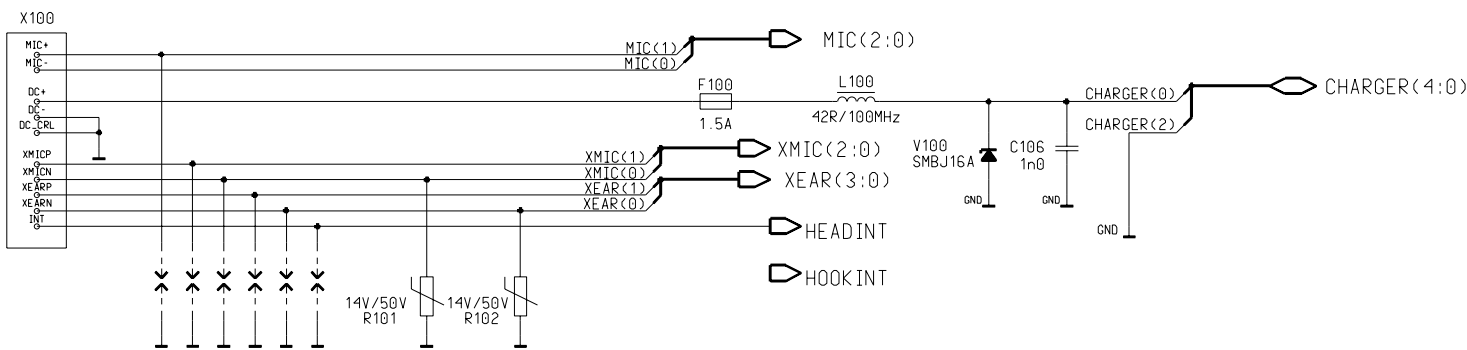
UIDRV<5:0>

FMANT

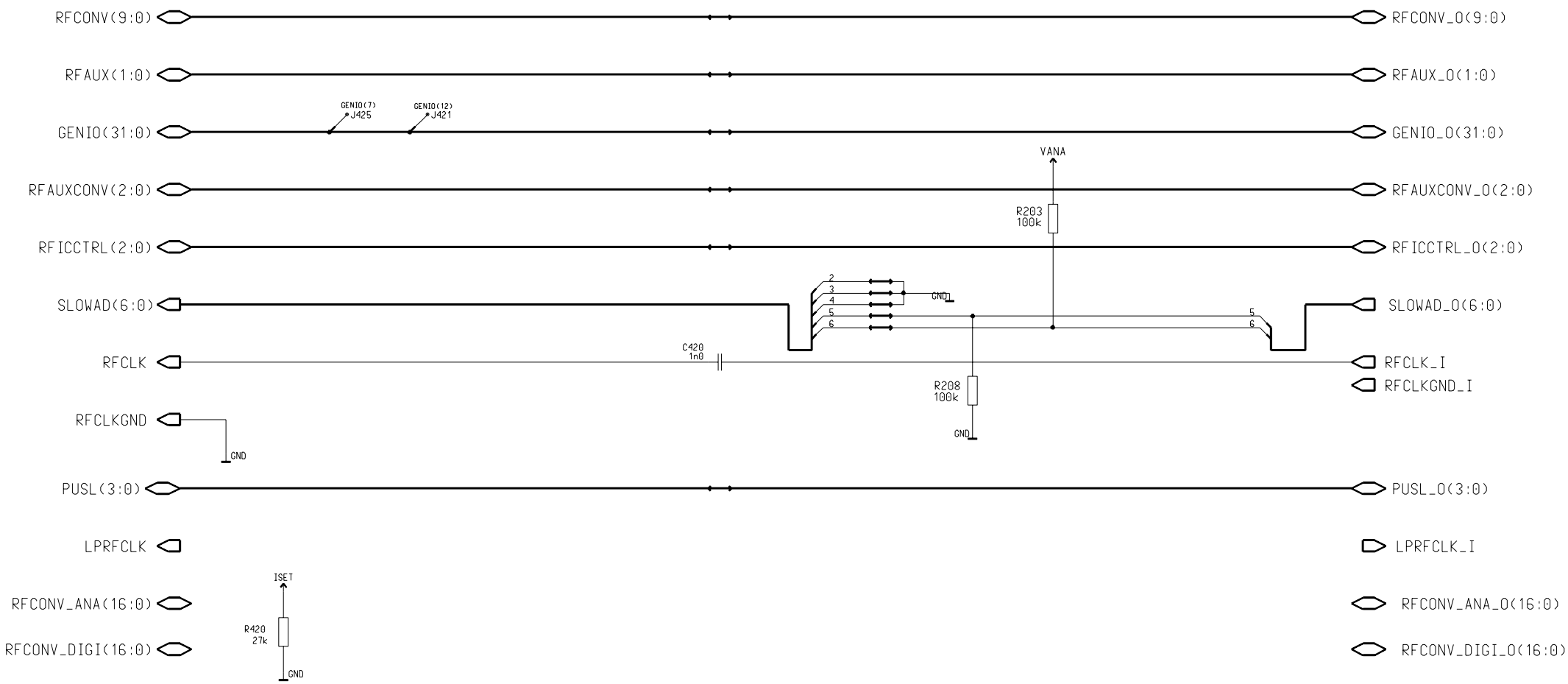
GPIO<31:0>



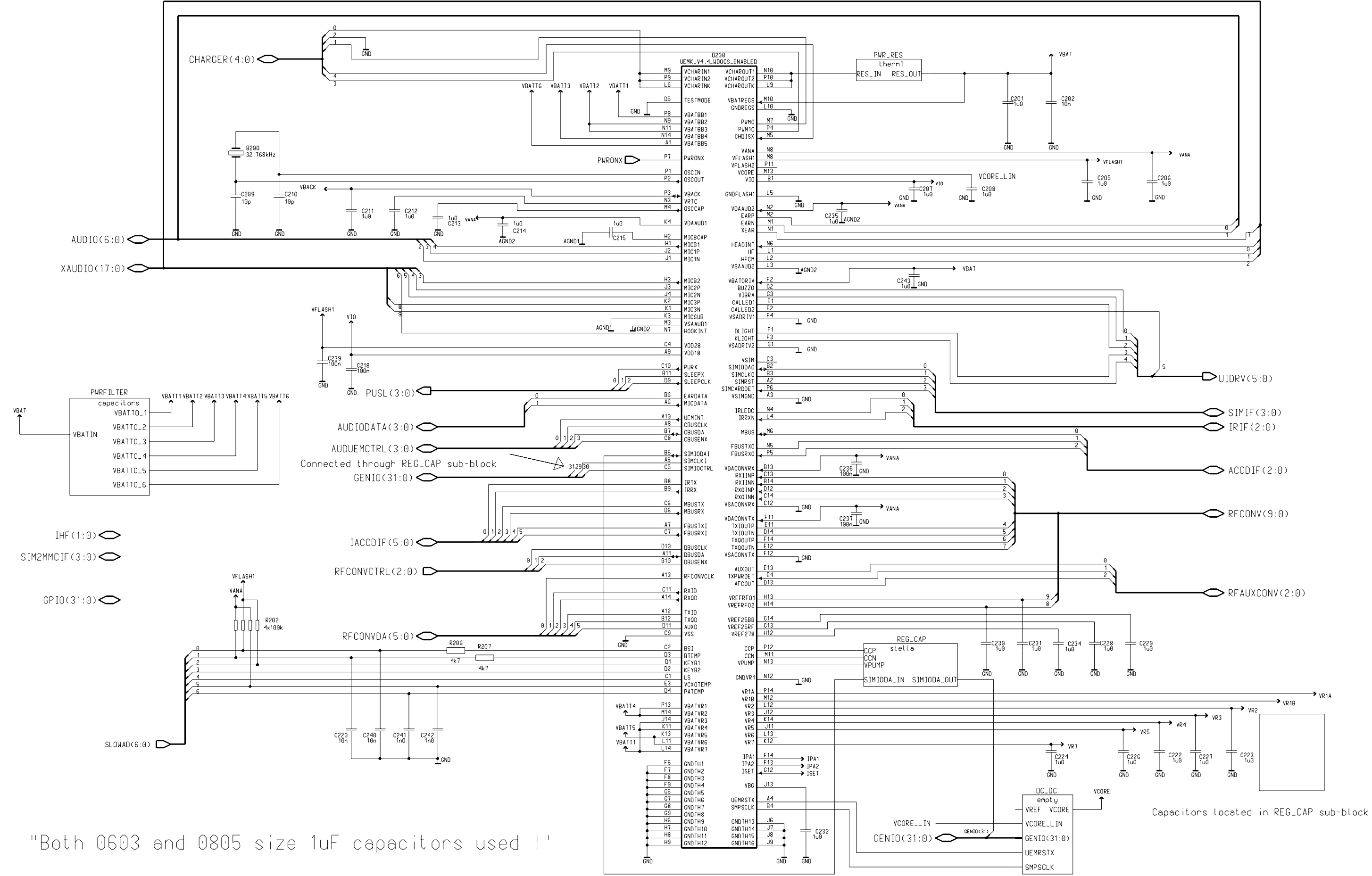
"BOTTOM CONNECTOR"



DAMPS RF – Baseband Interface



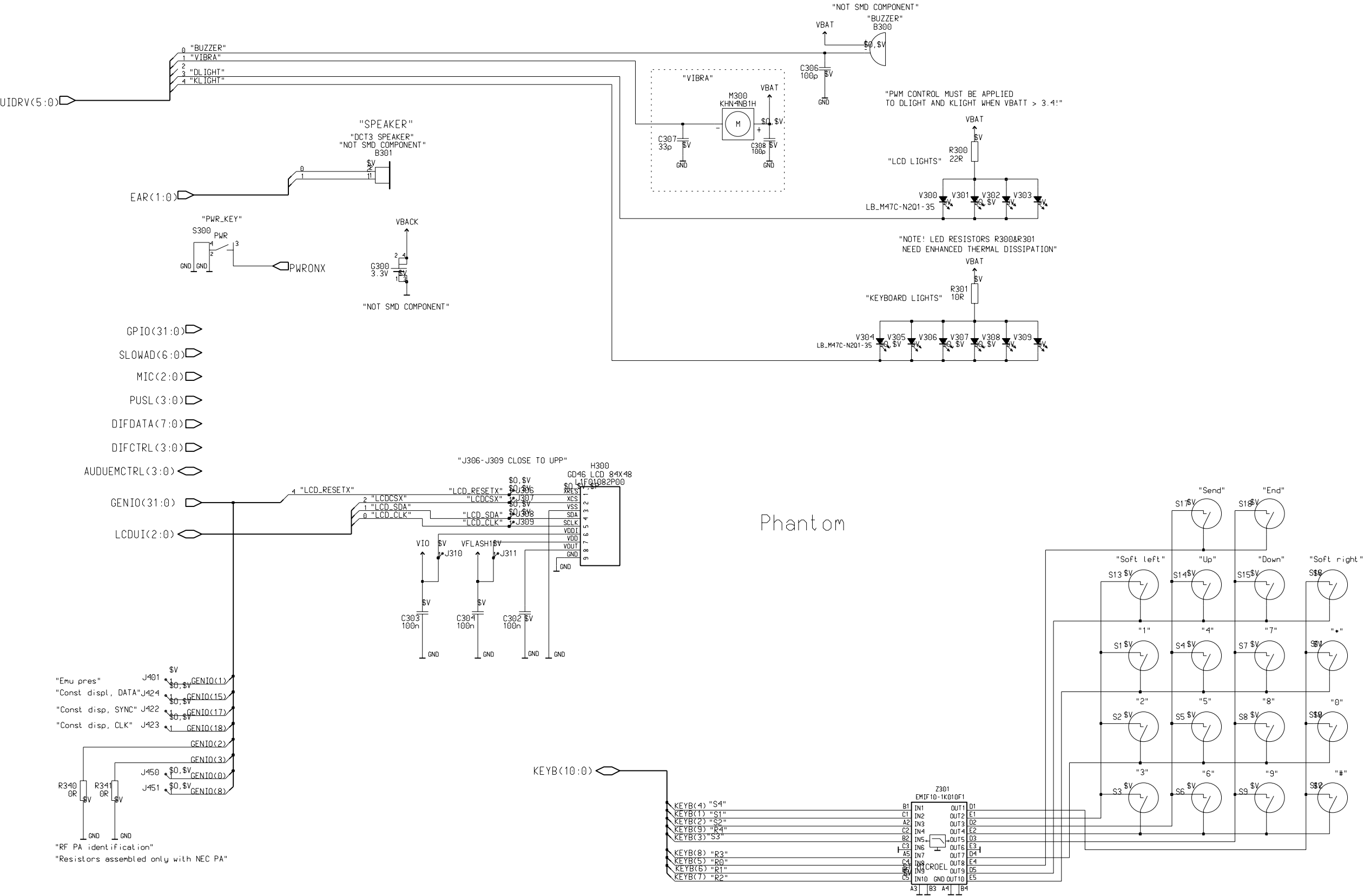
DCT4 Common BB - Discrete Power Management



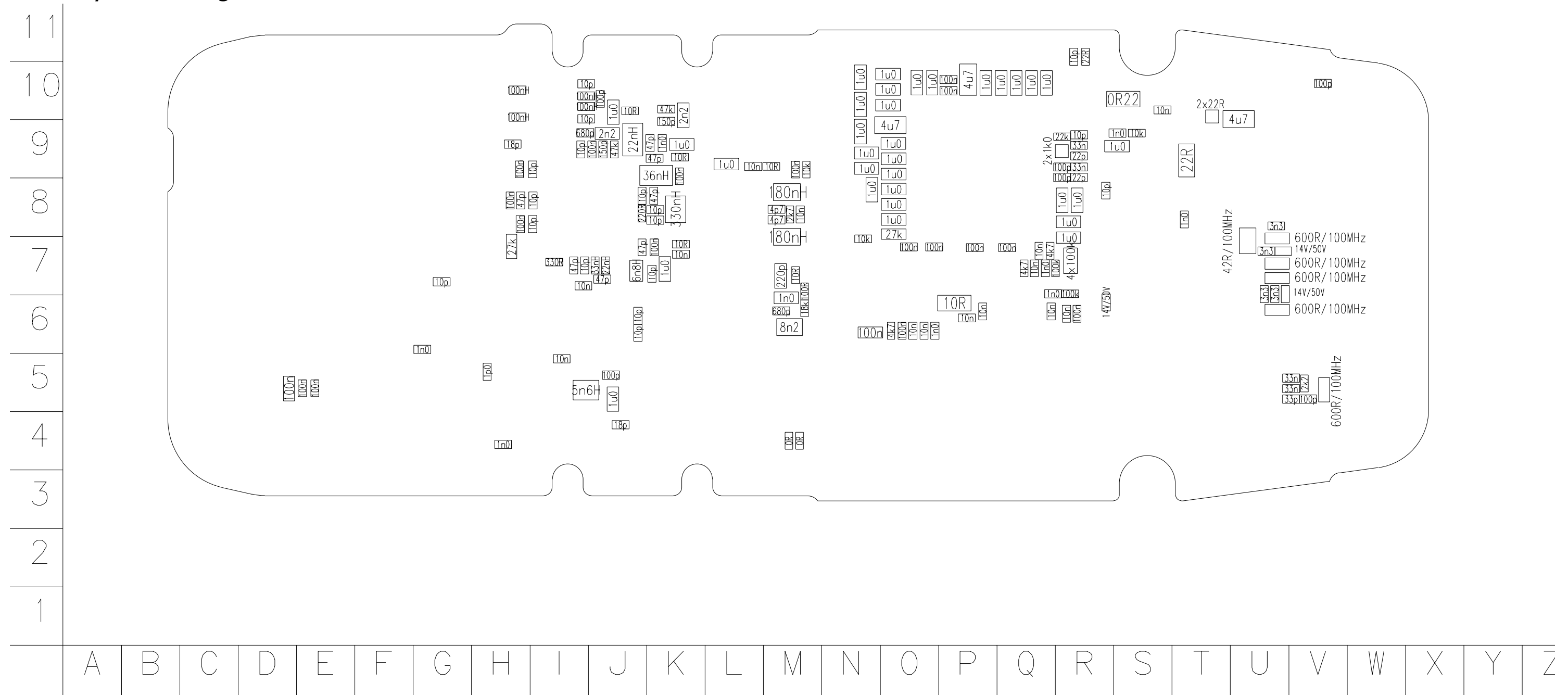
"Both 0603 and 0805 size 1uF capacitors used !"

Used refs in REG_CAP: C238, C200, C219, C233, C221 and R205

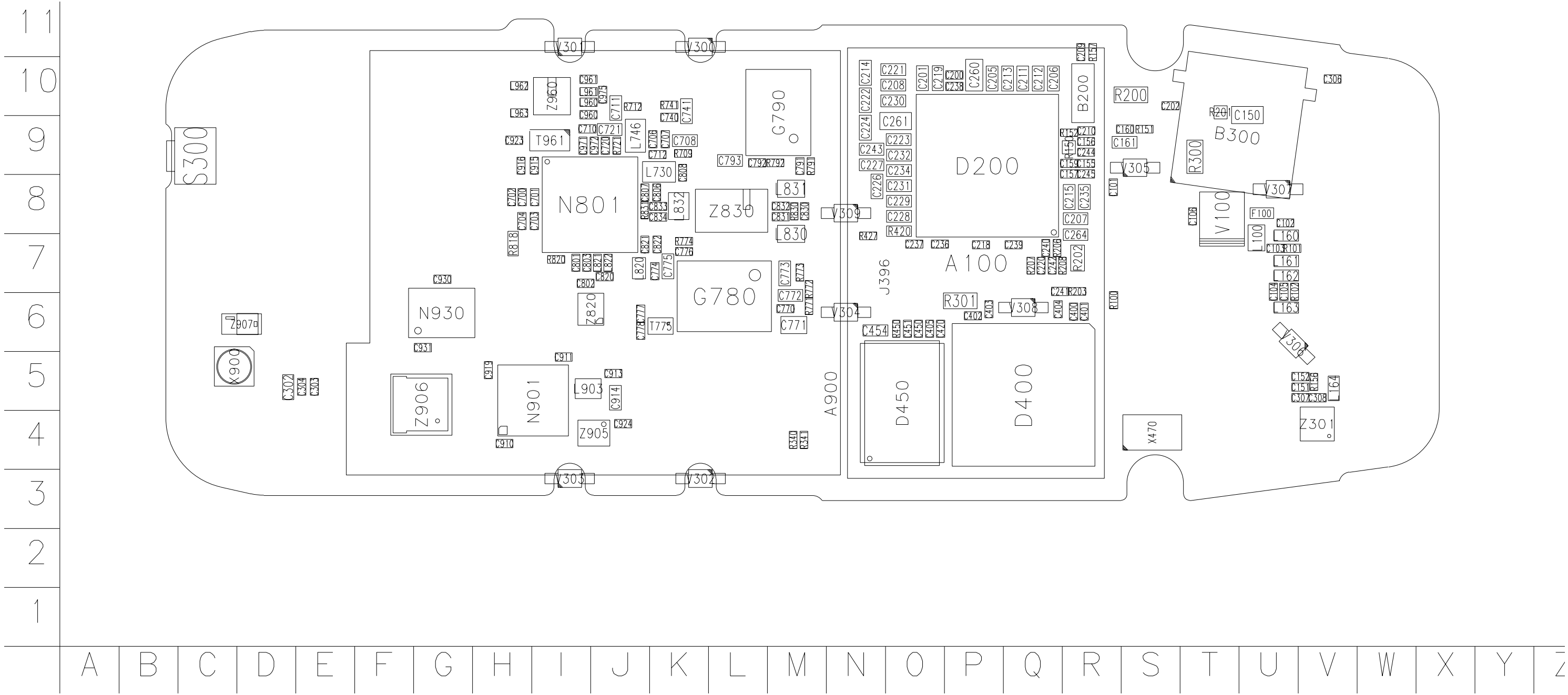
Sys - Key UI Diagram



Assembly Values Diagram



Layout Diagram – Top



Layout Diagram - Bottom

