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## Revision History

REV	DESCRIPTION
V0	First release

## Board Change History

SCHEMATIC MIN	HWVERSION	REVISION	DESCRIPTION OF CHANGE

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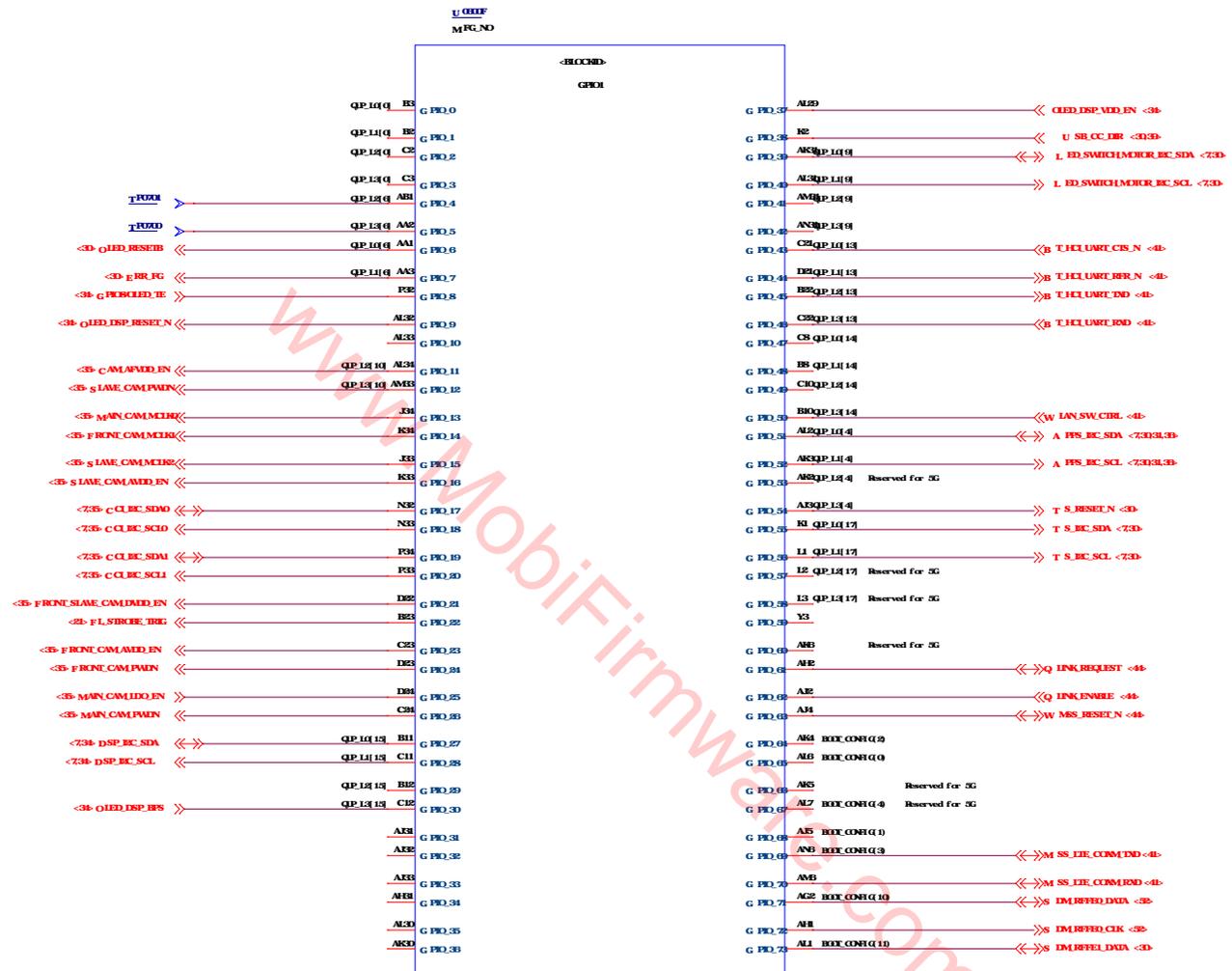
Pillar VOMS/VEEB/QPIO Assignment				
Interrupt Function	Mpio/Pad	Reset/State	Function	MMIO Function
	GPI0_0	NIB	PD	BISP1_3
Support interrupt	GPI0_1	DSP_PWR_IP2_EN	PD	BISP1_2
	GPI0_2	KEY_SDA	PD	BISP1_1
	GPI0_3	KEY_SCL	PD	BISP1_0
	GPI0_4	MEM_UART_TX	PD	BISP8_3
Support interrupt	GPI0_5	MEM_UART_RX	PD	BISP8_2
	GPI0_6	WPB	PD	BISP8_1
	GPI0_7	CAM_PWDN	PD	BISP8_0
	GPI0_8	TP2512	PD	BISP4_3
Support interrupt	GPI0_9	TP2514	PD	BISP4_2
	GPI0_10	TP2510	PD	BISP4_1
Support interrupt	GPI0_11	TP2511	PD	BISP4_0
	GPI0_12	---	PD	MP_VSYNC_E
	GPI0_13	CAM_MCLK0	PD	CAM_MCLK0
	GPI0_14	CAM_MCLK1	PD	CAM_MCLK1
	GPI0_15	CAM_MCLK2	PD	CAM_MCLK2
	GPI0_16	MP_VSYNC_P	PD	CAM_MCLK3
	GPI0_17	CC1_EC_SDA0	PD	CC1_EC_SDA
	GPI0_18	CC1_EC_SCL0	PD	CC1_EC_SCL
	GPI0_19	CC1_EC_SDA1	PD	CC1_EC1_SDA
	GPI0_20	CC1_EC_SCL1	PD	CC1_EC1_SCL
	GPI0_21	FRONT_CAM_DATA0_E	PD	CC1_TIMER0
Support interrupt	GPI0_22	EL_STROBE_TRIG	PD	CC1_TIMER1
	GPI0_23	ICD_ID1	PD	BISP1_SPL_CS3,N
Support interrupt	GPI0_24	FRONT_CAM_ID	PD	BISP1_SPL_CS3,N
	GPI0_25	BL_IRQB	PU	BISP2_SPL_CS1,N
Support interrupt	GPI0_26	REAR_CAM_ID	PD	CC1_ASYNC0
	GPI0_27	REAR_CAM_ID	PD	BISP1_SPL_CS3,N
	GPI0_28	CAM_PWDN	PD	BISP1_SPL_CS3,N
	GPI0_29	CAM_DATA0_EN	PD	BISP2_SPL_CS2,N
	GPI0_30	CAM_PWDN	PD	BISP2_SPL_CS3,N
	GPI0_31	BISP2_DSP_M0B1	PD	BISP2_3
	GPI0_32	BISP2_DSP_CLK	PD	BISP2_0
	GPI0_33	BISP2_DSP_CS	PD	BISP2_1
	GPI0_34	BISP2_DSP_M0B0	PD	BISP2_2
Support interrupt	GPI0_35	---	PD	PCIE_RST_N
Support interrupt	GPI0_36	---	PD	PCIE_CLKREQ_N
Support interrupt	GPI0_37	---	PD	---
Support interrupt	GPI0_38	CC_DIR	PD	USB_PIC_PS
	GPI0_39	DSP_PWR_IP2_EN	PD	IPASS_SIMBUS_DP
Support interrupt	GPI0_40	DSP_RSTIN	PD	SD_WRITE_PROTECT
	GPI0_41	DSP_GPO	PD	BISP3_3
Support interrupt	GPI0_42	DSP_IRQ	PD	BISP3_2
	GPI0_43	MIPA_EC3_SDA	PD	BISP3_1
	GPI0_44	MIPA_EC3_SCL	PD	BISP3_0
	GPI0_45	BU_HQ_UART_D0	PD	BISP3_3
Support interrupt	GPI0_46	BU_HQ_UART_R0D	PD	BISP3_2
	GPI0_47	BU_HQ_UART_CTS_N	PD	BISP3_1
	GPI0_48	BU_HQ_UART_RRR_N	PD	BISP3_0
	GPI0_49	---	PD	BISP3_3
Support interrupt	GPI0_50	---	PD	BISP3_2
	GPI0_51	DSP_EC3_SDA	PD	BISP9_1
	GPI0_52	DSP_EC3_SCL	PD	BISP9_0
Support interrupt	GPI0_53	CODEC_NIB_N	PD	BISP7_3
Support interrupt	GPI0_54	CODEC_NIB_N	PD	BISP7_2
	GPI0_55	APPS_EC7_SDA	PD	BISP7_1
Support interrupt	GPI0_56	APPS_EC7_SCL	PD	BISP7_0
Support interrupt	GPI0_57	FORCED_USB_BOOT	PD	BISP10_SPL_CS3,N

Support interrupt	GPI0_62	---	PD	MIS_4_DE
Support interrupt	GPI0_63	TP2521	PD	MIS_4_DB
Support interrupt	GPI0_64	CODEC_RST_N	PD	MIS_1_MCLK
	GPI0_65	MAX_DSP_EN	PD	BISP10_3
Support interrupt	GPI0_66	MAX_DSP_EN	PD	BISP10_2
	GPI0_67	MAX_DSP_EN	PD	BISP10_1
	GPI0_68	TP2335	PD	BISP10_0
	GPI0_69	SPWR_ES_CLK	PD	SPWR_ES_MCLK
	GPI0_70	IPASS_SIMBUS_CLK	PD	IPASS_SIMBUS_CLK
Support interrupt	GPI0_71	PASS_SIMBUS_DATA	PD	IPASS_SIMBUS_DP
	GPI0_72	PASS_SIMBUS_DATA	PD	IPASS_SIMBUS_DP
Support interrupt	GPI0_73	BFMS_SIMBUS_DATA	PD	BFMS_SIMBUS_DATA
	GPI0_74	BFMS_SIMBUS_CLK	PD	BFMS_SIMBUS_CLK
	GPI0_75	MIPA_ES3_SCK	PD	MIS_3_SCK
	GPI0_76	MIPA_ES3_VS	PD	MIS_3_VS
Support interrupt	GPI0_77	MIPA_ES3_DIN	PD	MIS_3_DD
Support interrupt	GPI0_78	MIPA_ES3_DOUT	PD	MIS_3_DI
Support interrupt	GPI0_79	MIPA_ES3_MCLK	PD	MIS_2_MCLK
Support interrupt	GPI0_80	MIPA_RST_N	PD	MIS_2_SCK
	GPI0_81	BISP_SPL_M0B12_FF	PD	BISP12_3
	GPI0_82	BISP_SPL_M0B12_FF	PD	BISP12_2
Support interrupt	GPI0_83	BISP_SPL_CS_N12_FF	PD	BISP12_1
	GPI0_84	BISP_SPL_CLK12_FF	PD	BISP12_0
	GPI0_85	---	PD	BISP5_3
Support interrupt	GPI0_86	---	PD	BISP5_2
	GPI0_87	P_EC_SDA_BISP5_C	PD	BISP5_1
	GPI0_88	P_EC_SCL_BISP5_C	PD	BISP5_0
	GPI0_89	TS_RESET_N	PD	TSF1_CLK
	GPI0_90	TP2337	PD	BISP1_SPL_CS1,N
Support interrupt	GPI0_91	---	PD	TSF1_DATA
Support interrupt	GPI0_92	---	PD	TSF2_ERROR
	GPI0_93	---	PD	TSF2_CLK
Support interrupt	GPI0_94	TCDD_RESET_N	PD	TSF2_EN
	GPI0_95	---	PD	TSF2_DATA
	GPI0_96	---	PD	TSF2_SYNC
Support interrupt	GPI0_97	RHEB3_CLK	PD	RHEB3_CLK
	GPI0_98	RHEB3_DATA	PD	RHEB3_DATA
	GPI0_99	---	PD	GRC_10
Support interrupt	GPI0_100	---	PD	GRC_32
	GPI0_101	GRC4	PD	GRC_4
	GPI0_102	GRC5	PD	GRC_5
	GPI0_103	GRC6	PD	GRC_6
Support interrupt	GPI0_104	---	PD	GRC_7
	GPI0_105	UM2_DATA	PD	UM2_DATA
Support interrupt	GPI0_106	UM2_CLK	PD	UM2_CLK
	GPI0_107	UM2_RESET	PD	UM2_RESET
Support interrupt	GPI0_108	UM2_PRESENT	PD	UM2_PRESENT
	GPI0_109	UM1_DATA	PD	UM1_DATA
Support interrupt	GPI0_110	UM1_CLK	PD	UM1_CLK
	GPI0_111	UM1_RESET	PD	UM1_RESET
Support interrupt	GPI0_112	UM1_PRESENT	PD	UM1_PRESENT
Support interrupt	GPI0_113	UMBATT_ALARM	PD	UMBATT_ALARM
PMREQ_BUA	GPI0_113	PULL_DOWN	UMBATT_ALARM	Function1

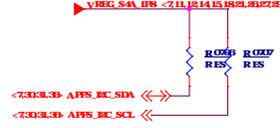
Support int	GPI0_114	GRC8	PD	GRC_8
Support int	GPI0_115	GRC9	PD	GRC_9
Support int	GPI0_116	---	PD	FORCE_MSA_ALARM
Support int	GPI0_117	ACCEL_INT	PD	QSS_TRD0A_0A
Support int	GPI0_118	GRC0_INT	PD	QSS_TRD0A_1A
Support int	GPI0_119	MIG_INT	PD	QSS_TRD0A_2A
Support int	GPI0_120	ASFG_INT	PD	QSS_TRD0A_3A
Support int	GPI0_121	FP_INT	PD	QSS_TRD0A_4A
Support int	GPI0_122	KEY_INT	PD	QSS_TRD0A_5A
Support int	GPI0_123	SEL_SW_SEL	PD	QSS_TRD1A
Support int	GPI0_124	HML_INT	PD	QSS_TRD1A
Support int	GPI0_125	TS_INT	PD	---
Support int	GPI0_126	---	PD	---
Support int	GPI0_127	GRC3	PD	GRC_3
	GPI0_128	FP_RST_N	PD	---
Support int	GPI0_129	---	PD	FASUBOOC_3
	GPI0_130	QINK_REQUEST	PD	QINK_REQUEST
Support int	GPI0_131	QINK_ENABLE	PD	QINK_ENABLE
Support int	GPI0_132	GRC2	PD	GRC_2
Support int	GPI0_133	---	PD	---
	GPI0_134	WMS_RESET_N	PD	---
	GPI0_135	PA_NDCAOR_CR	PD	GRC_0
	GPI0_136	VO_MIM_GRC1	PD	GRC_1
	GPI0_137	RHE3_DATA	PD	RHE3_DATA
	GPI0_138	RHE3_CLK	PD	RHE3_CLK
	GPI0_139	RHE4_DATA	PD	RHE4_DATA
	GPI0_140	RHE4_CLK	PD	RHE4_CLK
	GPI0_141	RHE5_DATA	PD	RHE5_DATA
	GPI0_142	RHE5_CLK	PD	RHE5_CLK
	GPI0_143	GNS_EN	PD	---
	GPI0_144	MIS_DE_COMMAND	PD	LIE_COEX_TX
	GPI0_145	MIS_DE_COMMAND	PD	LIE_COEX_RX
	GPI0_146	RHE2_DATA	PD	RHE2_DATA
	GPI0_147	RHE2_CLK	PD	RHE2_CLK
	GPI0_148	RHE1_DATA	PD	ALL_USE_SERIAL_N
	GPI0_149	RHE1_CLK	PD	---
SSC Cues				
Interrupt Function	Mpio/Pad	Signal Name	Reset State	Function
	SSC_0	---	PD	SSC_SPL_CS1,N
	SSC_1	SSC_PWR_EN	PD	SSC_PWR_EN
	SSC_2	SSC_EC_3_SDA	PD	SSC_EC_3_SDA
	SSC_3	SSC_EC_3_SCL	PD	SSC_EC_3_SCL
	SSC_4	TP2332	PD	SSC_EC_2_SDA
	SSC_5	TP2333	PD	SSC_EC_2_SCL
	SSC_6	TP2334	PD	SSC_UART_3_TX
	SSC_7	TP2335	PD	SSC_UART_3_RX
	SSC_8	TP2336	PD	SSC_SPL_CS_N
	SSC_9	TP2337	PD	SSC_SPL_CLK
	SSC_10	TP2338	PD	SSC_SPL_M0B1
	SSC_11	TP2339	PD	SSC_SPL_M0B0
	SSC_12	SSC_UART_1_TX	PD	SSC_UART_1_TX
	SSC_13	SSC_UART_1_RX	PD	SSC_UART_1_RX
	SSC_14	SSC_DEBLG_UART_TX	PD	SSC_UART_2_TX
	SSC_15	SSC_DEBLG_UART_RX	PD	SSC_UART_2_RX
	SSC_16	---	PD	SSC_SPL_3_CLK

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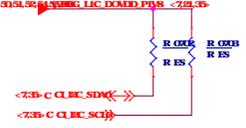


**SMART PA and SLAVE CHARGER**



**rear cam**



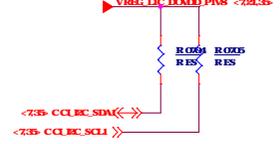
**DSP**



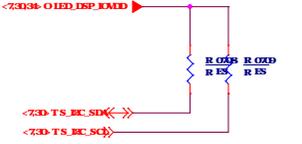
**LED SWITCH MOTOR driver**



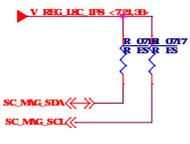
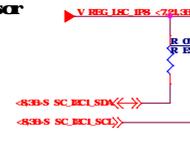
**front cam**



**Touchscreen**



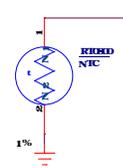
**Sensor**



<Cros Design>

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<BLOCKED>  
GPIO



<D> S DM1REFSEL_CLK <<	AM0	G_PIO_74	
<D> S DM1REFSEL_DMA0 <<	AM2	G_PIO_75	
<D> S DM1REFSEL_CLK <<	AM1	G_PIO_76	
	GPIO_CON1(0)	AM2	G_PIO_77
		AM3	G_PIO_78
		AM0	G_PIO_79
		AM0	G_PIO_80
	Reserved for SG	AM2	G_PIO_81
		AM2	G_PIO_82
	QP_L1(1) / L1(1)	AM2	G_PIO_83
	QP_L1(1) / L1(1)	AM2	G_PIO_84
<D> D EBUG_UART1_TX <<	R (0x0) N.C. 04mm U ARU QP_L1(1) / L1(1)	AM3	G_PIO_85
<D> D EBUG_UART1_RX <<	QP_L1(1) / L1(1)	AM3	G_PIO_86
		CM	G_PIO_87
	QP_L1(1)	J2	G_PIO_88
	QP_L1(1)	K2	G_PIO_89
<D> L ED_DR_INT <<	QP_L1(1)	L2	G_PIO_90
	QP_L1(1)	M2	G_PIO_91
	QP_L1(1)	L3	G_PIO_92
<D> S MPA_SPC_RST_N <<	QP_L1(1)	M3	G_PIO_93
<D> S MPA_MDC_RST_N <<	QP_L1(1)	L3	G_PIO_94
<D> S MPA_MDC_INT <<	QP_L1(1)	L3	G_PIO_95
<D> L ED_DR_EN <<	QP_L1(1)	L3	G_PIO_96
<B> TOGGLE_SWITCH(C18) <<	Reserved for SG	AM3	G_PIO_97
	QP_L1(1)	Q2	G_PIO_98
<B> S MPA_RCV_RST_N <<	QP_L1(1)	R1	G_PIO_99
<B> MBE1_HSEL1 <<	QP_L1(1)	R1	G_PIO_100
	QP_L1(1)	R2	G_PIO_101
	Reserved for SG PCIe_L1_RST_N	AM3	G_PIO_102
	Reserved for SG PCIe_L1_CLKREQ	AM3	G_PIO_103
	Reserved for SG PCIe_L1_WAKE_N	AM3	G_PIO_104
<D> U IM2_DMA0 <<	AM25	G_PIO_105	
<D> U IM2_CLK <<	AM26	G_PIO_106	
<D> U IM2_RESET <<	AM25	G_PIO_107	
<D> U IM2_PRESENT <<	AM26	G_PIO_108	
<D> U IM1_DMA0 <<	AM27	G_PIO_109	
<D> U IM1_CLK <<	AM28	G_PIO_110	
<D> U IM1_RESET <<	AM27	G_PIO_111	
<D> U IM1_PRESENT <<	AM28	G_PIO_112	
<B> A_CCEL_INT <<	AM29	G_PIO_113	
	QP_L1(1)	Y1	G_PIO_114
<D> F_P_RST_N <<	QP_L1(1)	V1	G_PIO_115
<B> A_ISPG_INT_N <<	QP_L1(1)	V2	G_PIO_116
<D> F_P_INT_N <<	QP_L1(1)	V3	G_PIO_117
<B> G_Y0_INT <<	QP_L1(1)	V4	G_PIO_118
<B> P_PRESS_INT_N <<	QP_L1(1)	V5	G_PIO_119
<B> P_PRESS_INT_N <<	QP_L1(1)	V6	G_PIO_120
<D> T_S_INT_N <<	QP_L1(1)	Y2	G_PIO_122
<B> C_ODEC_INT_N <<	AM4	G_PIO_123	

G_PIO_124	AL5	<< C_ODEC_INT_N <B>
G_PIO_125	AMQ3	
G_PIO_126	FBIC128_QIP_L1(1)	<< B ISP_SFUMSRG_PP <D>
G_PIO_127	UBIC128_QIP_L1(1)	>> B ISP_SFUMSRG_PP <D>
G_PIO_128	UBIC128_QIP_L1(1)	>> B ISP_SFUMSRG_PP <D>
G_PIO_129	VBIC128_QIP_L1(1)	>> B ISP_SFUMSRG_PP <D>
G_PIO_130	VBIC128_QIP_L1(1)	>> B ISP_SFUMSRG_PP <D>
G_PIO_131	VBIC128_MLK	
G_PIO_132	DBIC128_DMA	
G_PIO_133	DBIC128_MLK	<< T_FUR04
G_PIO_134	FBIC128_SCLK	
G_PIO_135	FBIC128_W	
G_PIO_136	FBIC128_DWD0	Reserved for SG
G_PIO_137	AL3(OBE) USB_BOOT/QMR128_MLK	C (0x0) C AP    << C_OBE_USB_BOOT <D>
G_PIO_138	AMQPAR128_SCLK	>> S MPA_SPC_RCV_MCHDR_B24_SCLK <D>
G_PIO_139	AMQPAR128_W	>> S MPA_SPC_RCV_MCHDR_B24_WS <D>
G_PIO_140	AMQPAR128_DWD0	>> S MPA_SPC_RCV_B24_DOUT <D>
G_PIO_141	AMQPAR128_DMA	>> S MPA_MCHDR_B24_DOUT <D>
G_PIO_142	AMQPAR128_DWD2	Reserved for SG
G_PIO_143	AMQPAR128_DWD3	Reserved for SG
G_PIO_144	V3_FH128_MLK	>> C_ODEC_RST_N <B>
G_PIO_145	U3_FH128_SCLK_QIP_L1(1)	<< C_ODEC_SFUMSRG <B>
G_PIO_146	F3_FH128_W/QIP_L1(1)	<< C_ODEC_SFUMSRG <B>
G_PIO_147	P3_FH128_DWD0/QIP_L1(1)	<< C_ODEC_SFUMSRG <B>
G_PIO_148	T3_FH128_DMA/QIP_L1(1)	<< C_ODEC_SFUMSRG <B>
G_PIO_149	T2	
G_PIO_150	N1_IPSS_SFUMSRG_CLK	>> L_IPSS_SFUMSRG_CLK <B>
G_PIO_151	N3_IPSS_SFUMSRG_DWD0	<< L_IPSS_SFUMSRG_DWD0 <B>
G_PIO_152	N2_IPSS_SFUMSRG_DMA	<< L_IPSS_SFUMSRG_DMA <B>
G_PIO_153	P1_IPSS_SFUMSRG_DWD2	<< U_SFUMSRG_INT <D>
G_PIO_154	M3	<< B_TFUMSRG_DMA <B>
G_PIO_155	M2	<< B_TFUMSRG_CLK <B>
G_PIO_156	AL20_SSC_QIP_L1(1)	<< S_SC_MG_SDA <B>
G_PIO_157	AL20_SSC_QIP_L1(1)	<< S_SC_MG_SCL <B>
G_PIO_158	AMBS5SC_QIP_L1(1)	<< S_SC_SPL_LMBS0 <B>
G_PIO_159	AMBS5SC_QIP_L1(1)	<< S_SC_SPL_LMBS1 <B>
G_PIO_160	AL25_SSC_QIP_L1(1)	>> S_SC_SPL1_CLK <B>
G_PIO_161	AL27_SSC_QIP_L1(1)	>> S_SC_SPL1_CS_N <B>
G_PIO_162	AMBS5SC_QIP_L1(1)	<< S_SC_BCL_SDA <B>
G_PIO_163	AL25_SSC_QIP_L1(1)	>> S_SC_BCL_SCL <B>
G_PIO_164	AMBS5SC_QIP_L1(1) / QIP_L1(1)	
G_PIO_165	AL24_SSC_QIP_L1(1) / QIP_L1(1)	
G_PIO_166	AM7	SSC_QIP_L1(1)
G_PIO_167	AL16	SSC_QIP_L1(1)
G_PIO_168	AM4SSSC_QIP_L1(1)	>> S_SC_BT_UART1_RX <B>
G_PIO_169	AL13_SSC_QIP_L1(1)	<< S_SC_BT_UART1_RX <B>
G_PIO_170	AC23	>> W_CSS1_BBD_BFA_CMD_CLK <B>
G_PIO_171	AC22	<< W_CSS1_BBD_BFA_CMD_DMA <B>
G_PIO_172	AC21	>> W_CSS2_BBD_BFA_CMD_CLK <B>
G_PIO_173	AEH4	<< W_CSS2_BBD_BFA_CMD_DMA <B>
G_PIO_174	AEH3	>> W_CSS_CM1_BFA_CMD_CLK <B>
G_PIO_175	AEH2	<< W_CSS_CM1_BFA_CMD_DMA <B>

SSC EGPIO

WSS EGPIO

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MP1

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PCB

TO DSP

48MHz Core, M/N C/W/D

48MHz Core, C/W/E

20MHz Core, C/W/E

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 A69 C3E13C0,IND,N  
 B65 C3E13A1,IND,P  
 B67 C3E13B1,IND,N  
 C65 C3E13C1,IND,P  
 C67 C3E13A2,IND,N  
 C68 C3E13B2,IND,P  
 C69 C3E13C2,IND,N

D16 >>M FLDS10,LANE1,P <36>  
 D15 >>M FLDS10,LANE0,M <36>  
 D14 >>M FLDS10,LANE1,P <36>  
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 C10  
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 B10

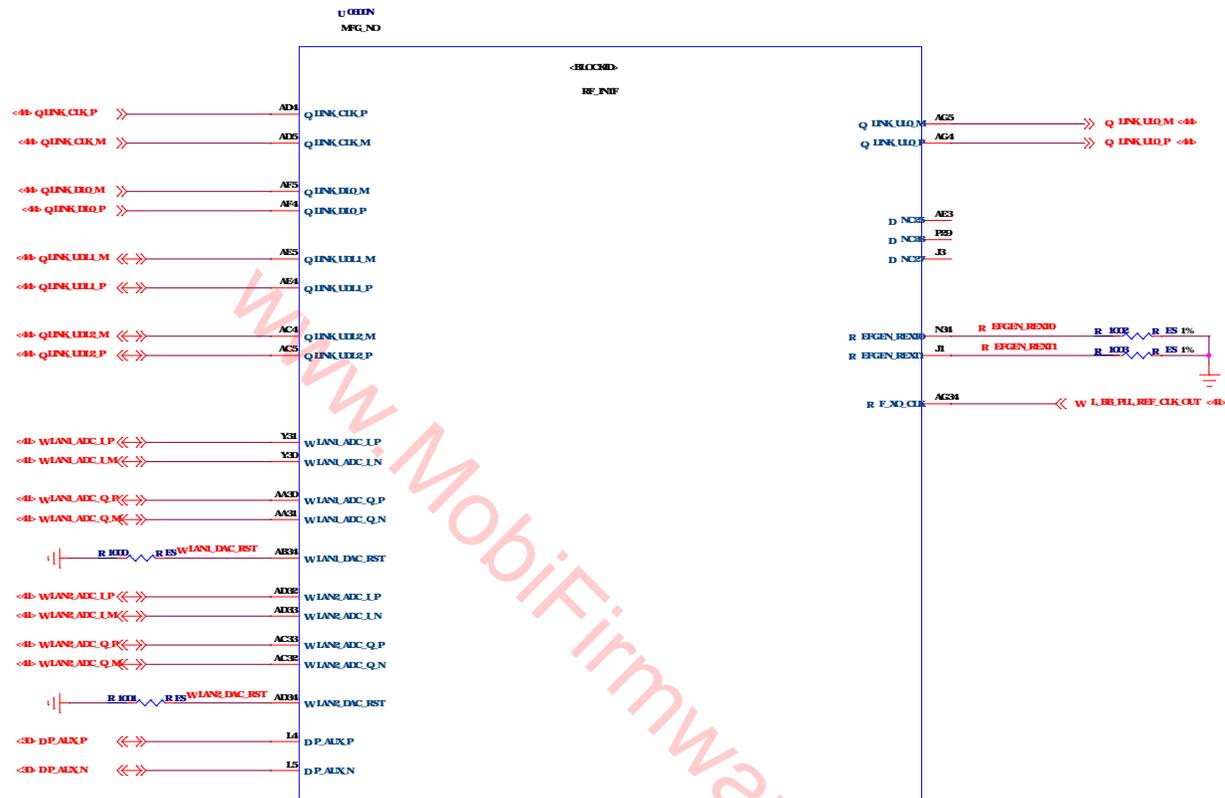
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 C3E12B1,IND,N  
 C3E12C1,IND,P  
 C3E12A2,IND,N  
 C3E12B2,IND,P  
 C3E12C2,IND,N

V20 P CE12REFCLK,M  
 V21 P CE12REFCLK,P  
 U20 P CE12RX,M  
 U21 P CE12RX,P  
 T20 P CE12TX,M  
 T21 P CE12TX,P  
 Y20 P CE12REFCLK,M  
 Y21 P CE12REFCLK,P  
 T22 P CE12RX,M  
 T23 P CE12RX,P  
 W20 P CE12RX,M  
 W21 P CE12RX,P  
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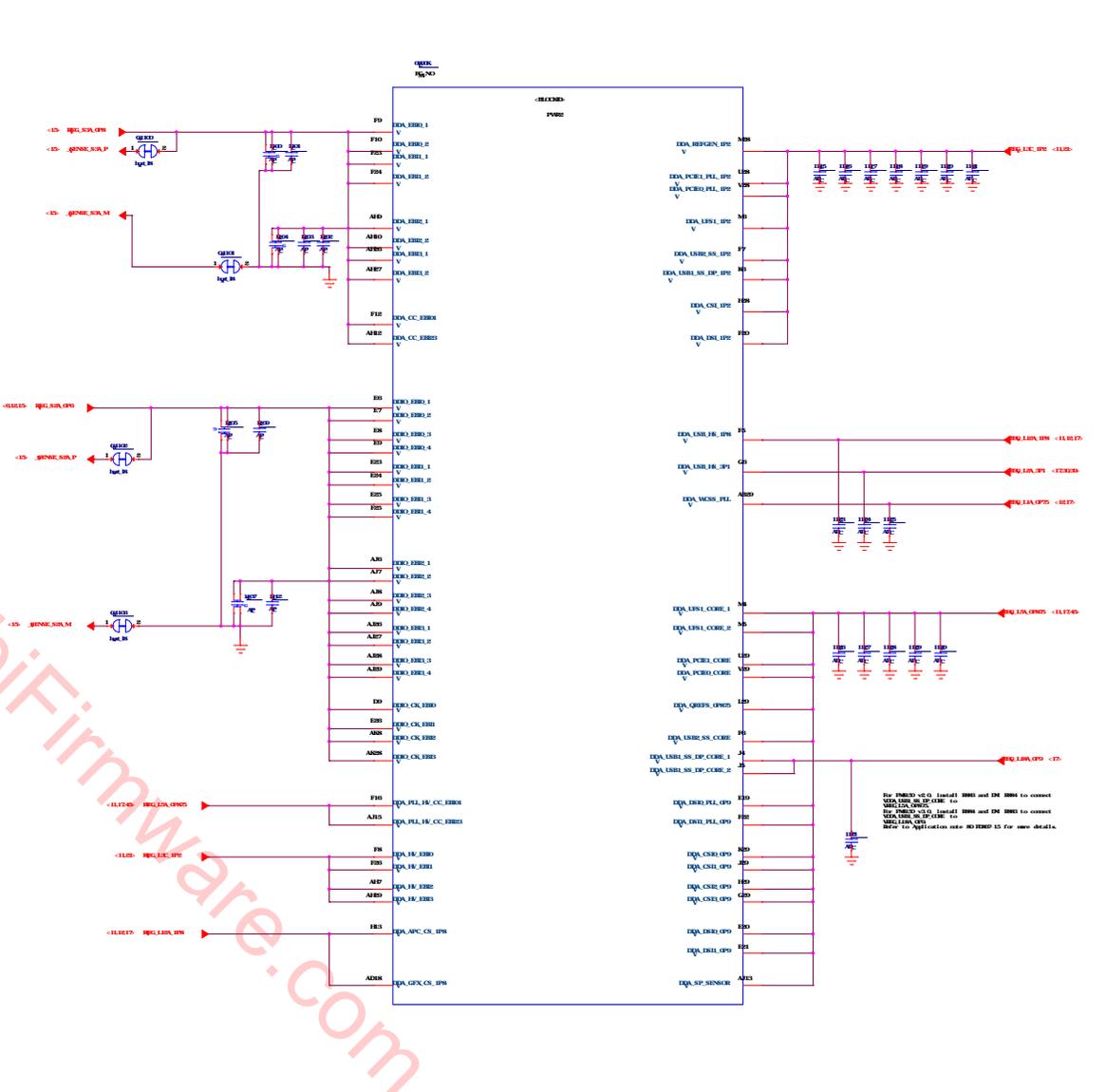
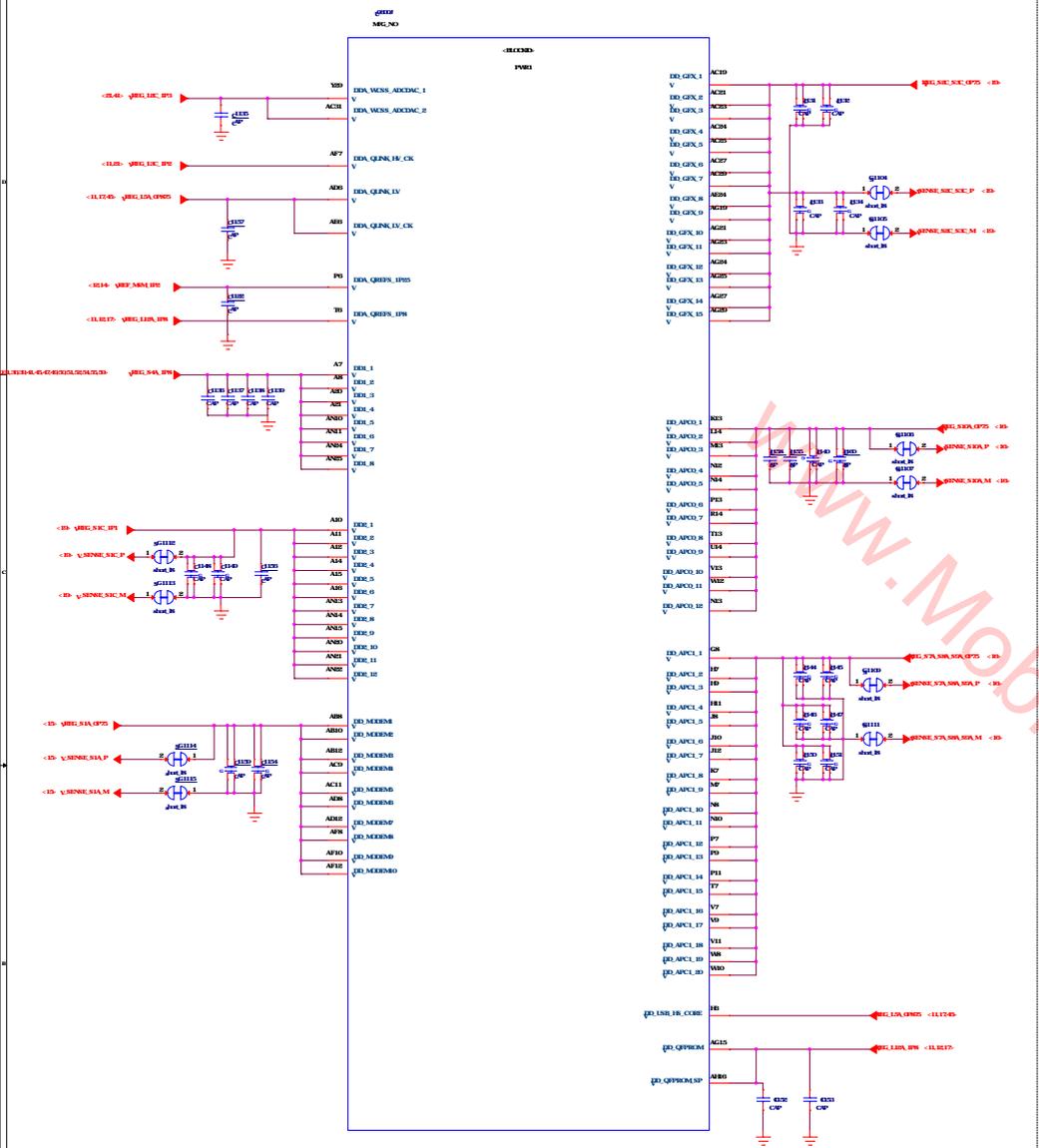
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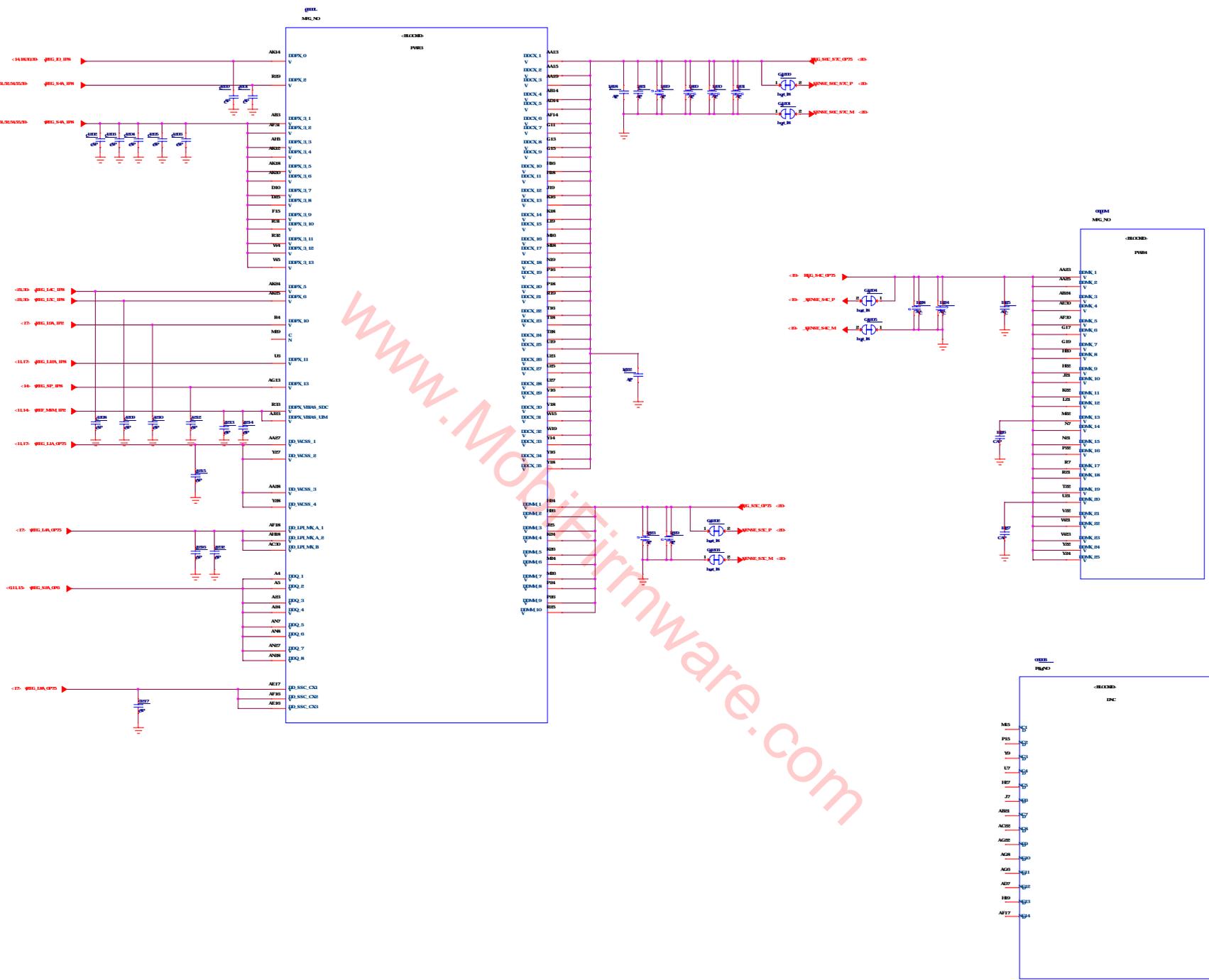
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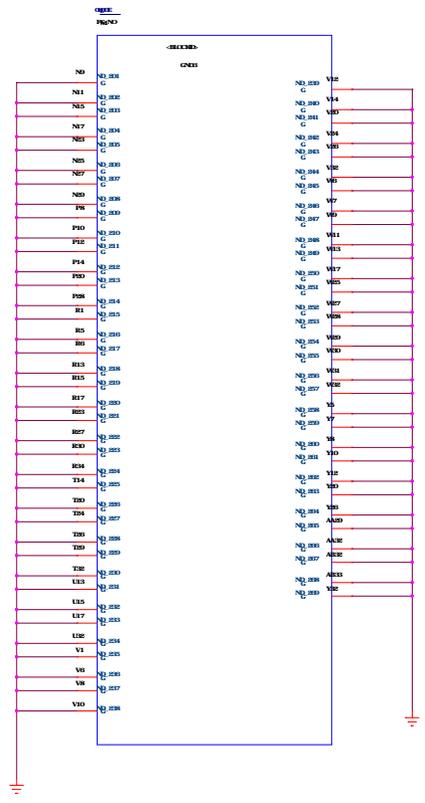
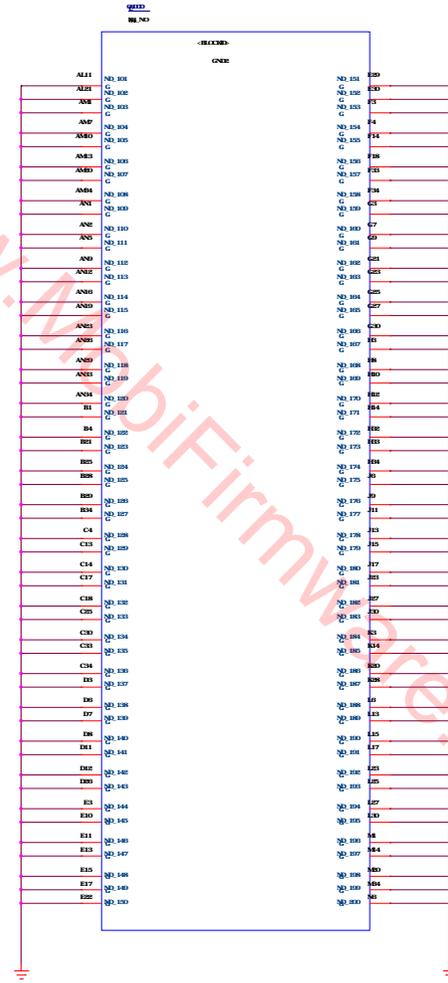
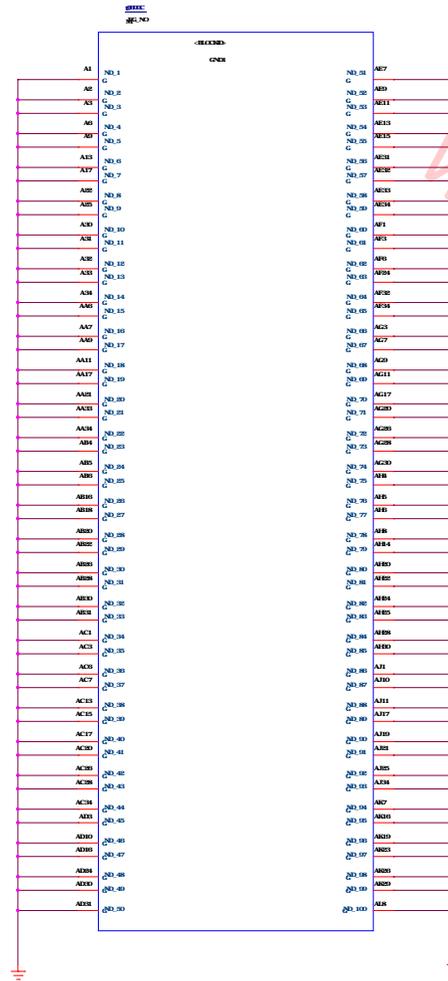


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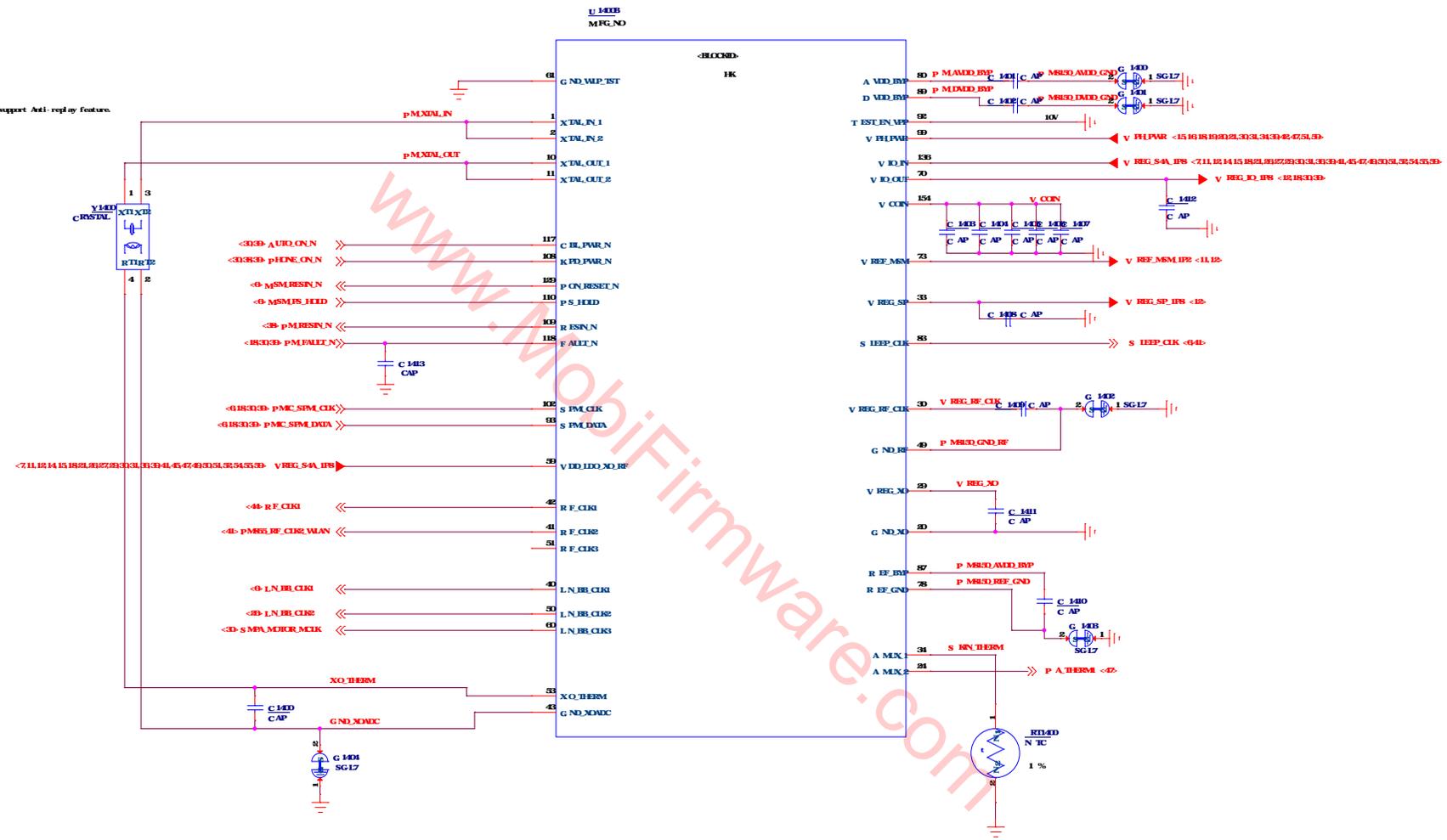
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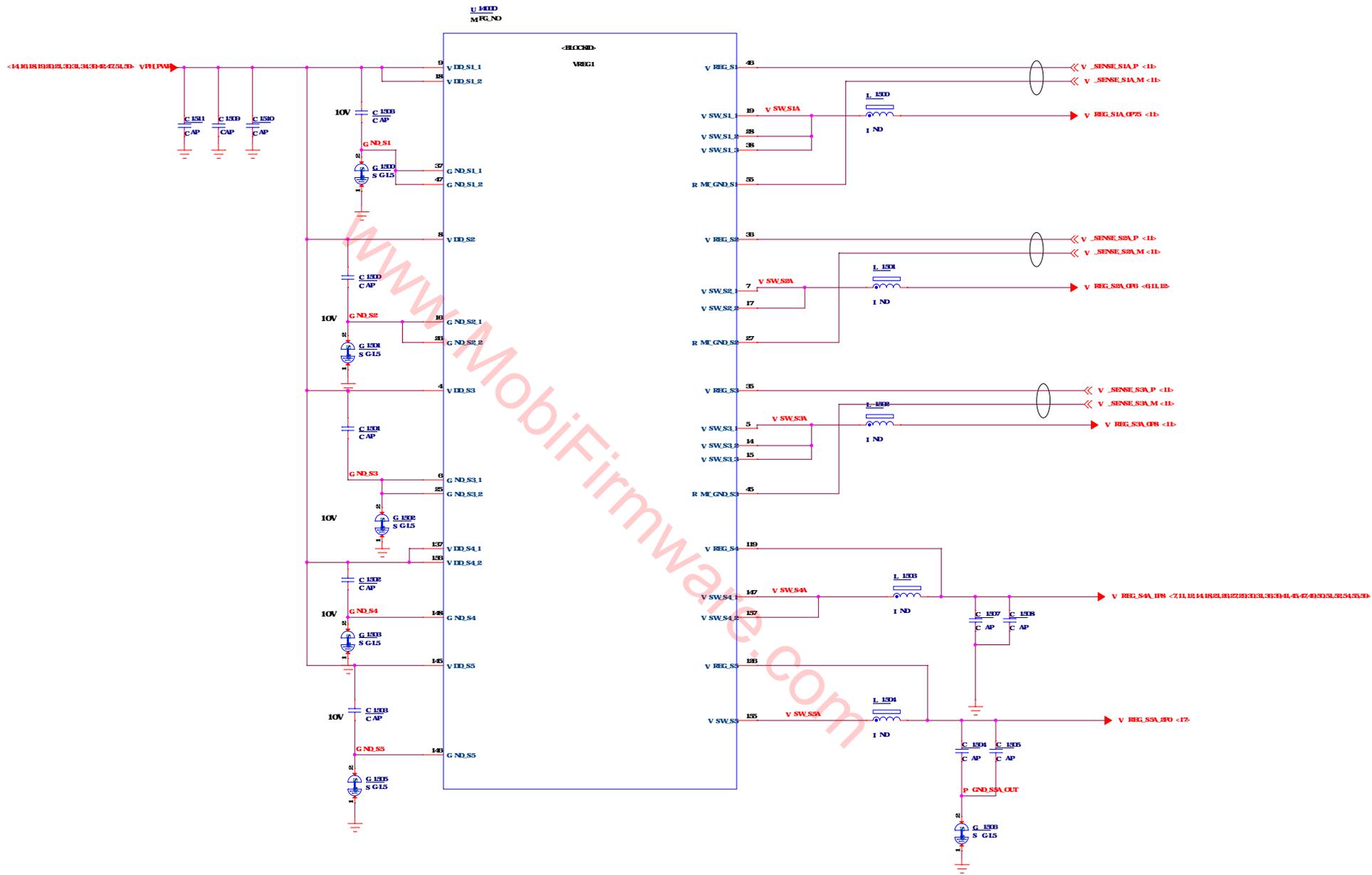
5x 10uF capacitors must be installed on VCCIN to support Anti-replay features.



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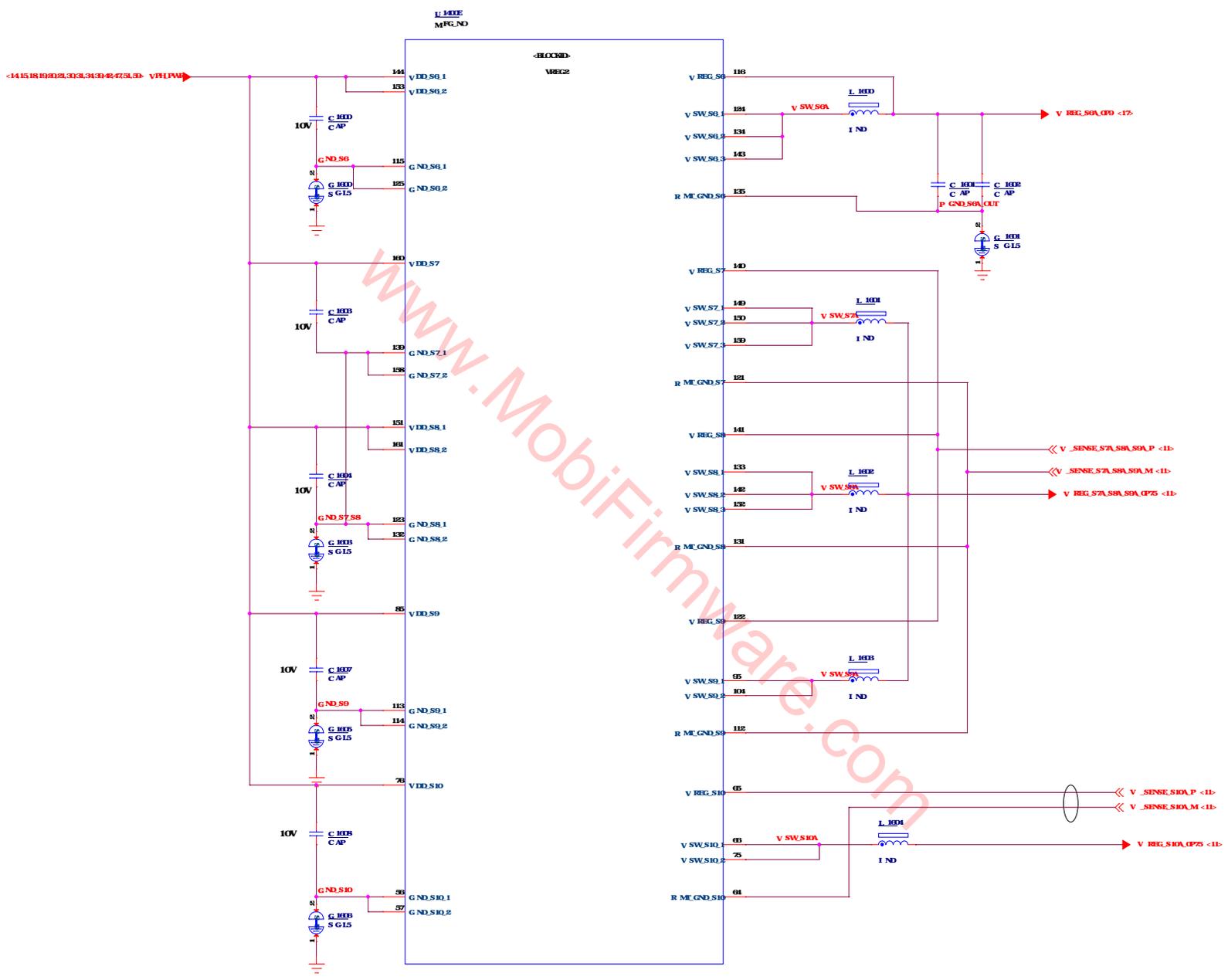


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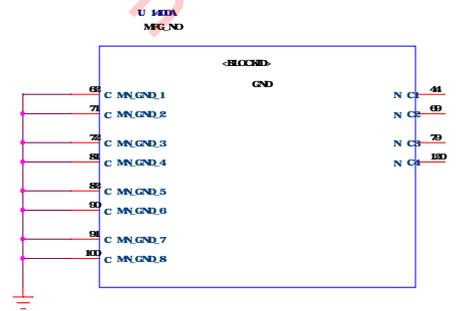
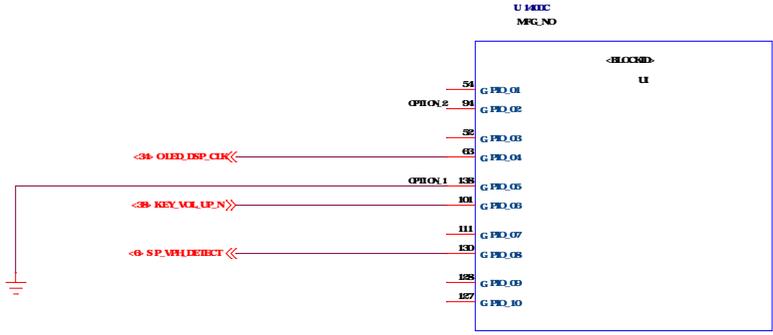
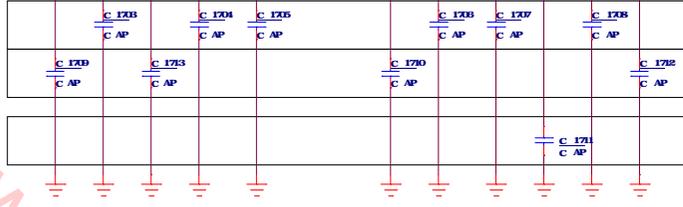
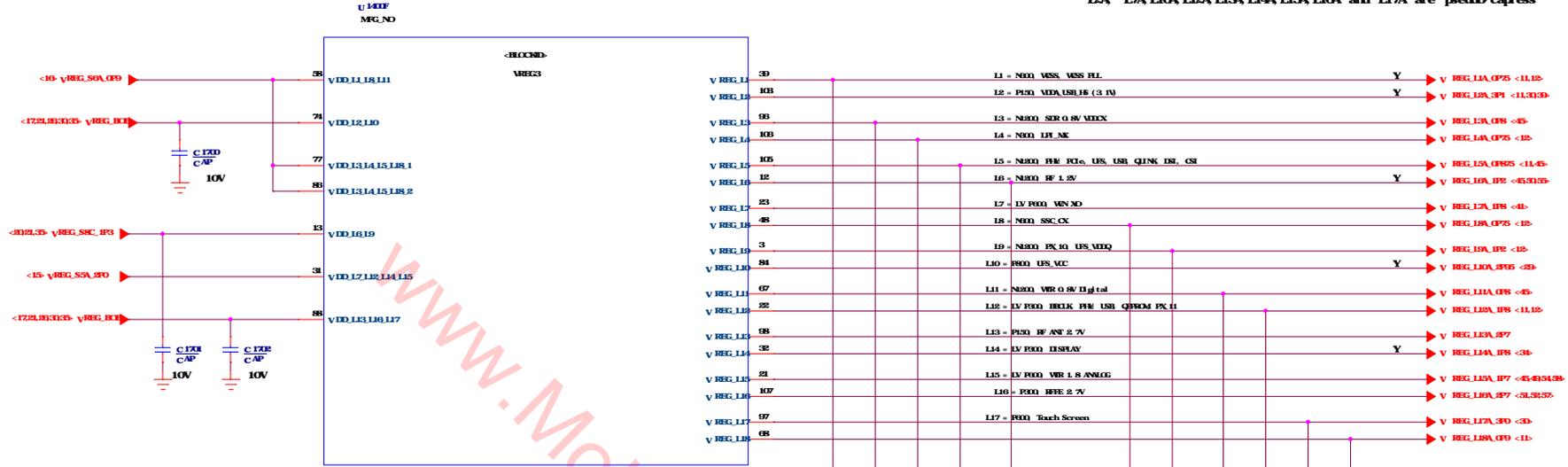


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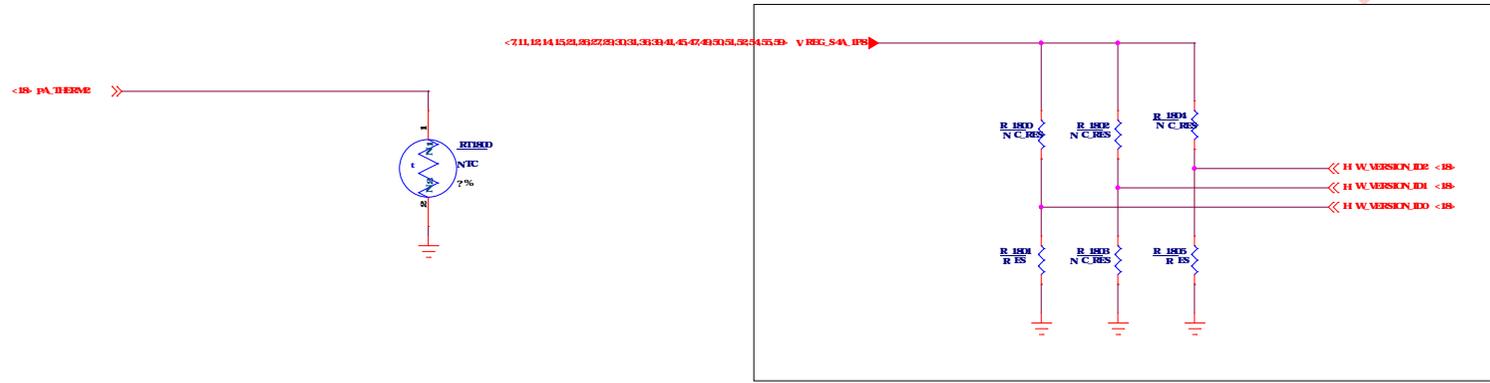
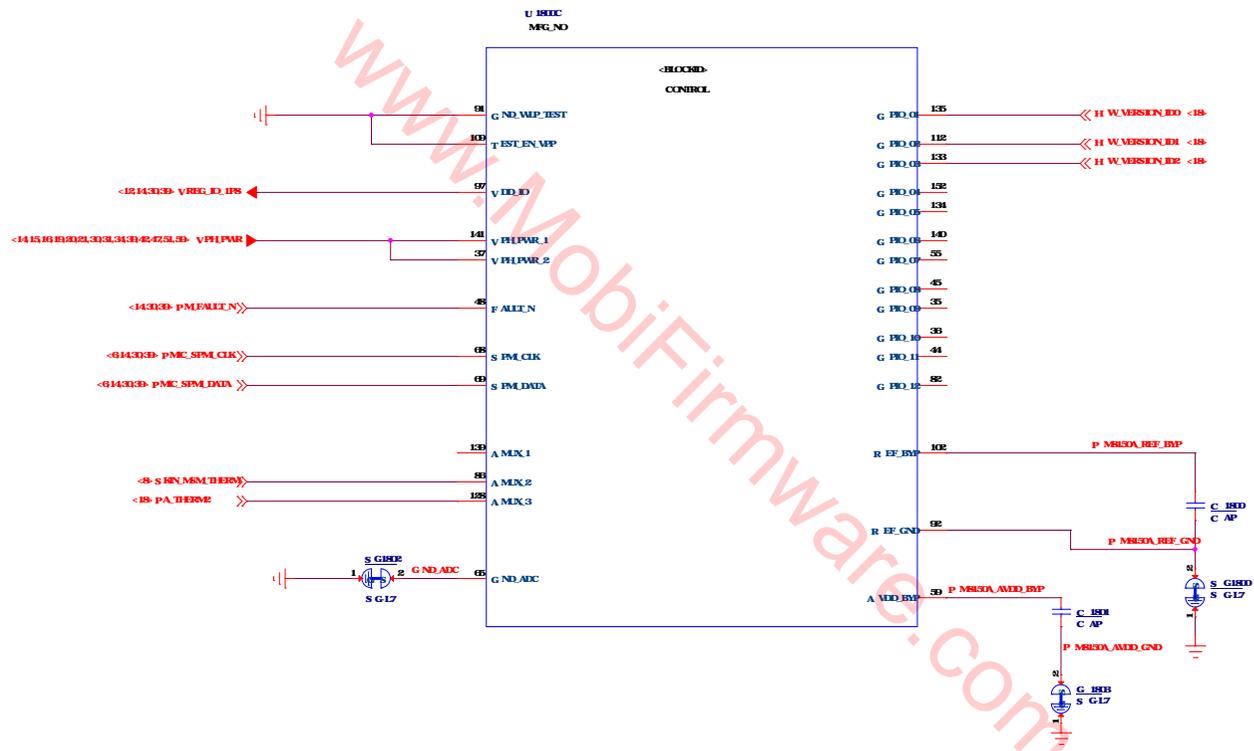
L2A, L7A, L10A, L12A, L13A, L14A, L15A, L16A and L17A are pseudocaps



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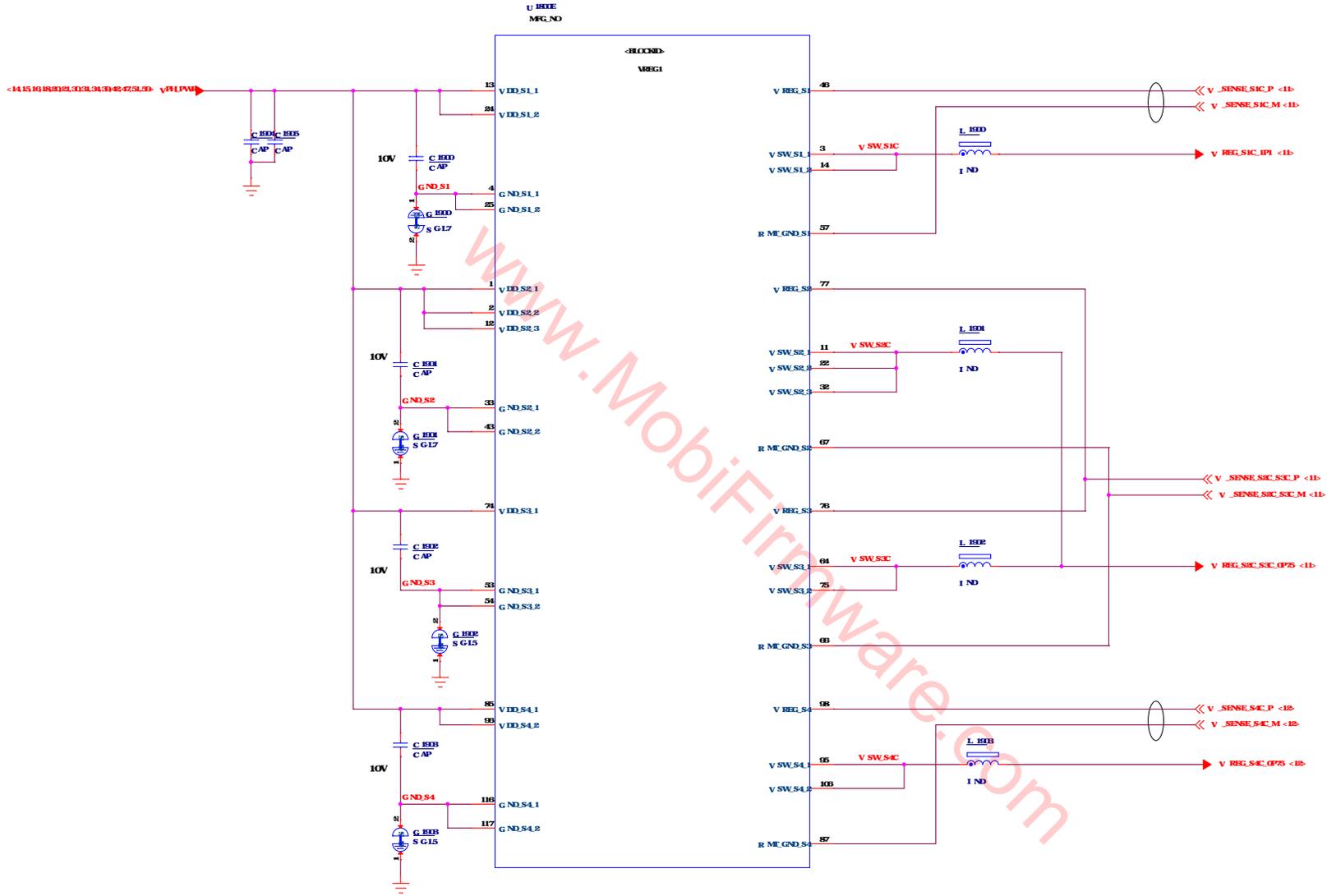
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# 18PMB150A CONTROL GPIO



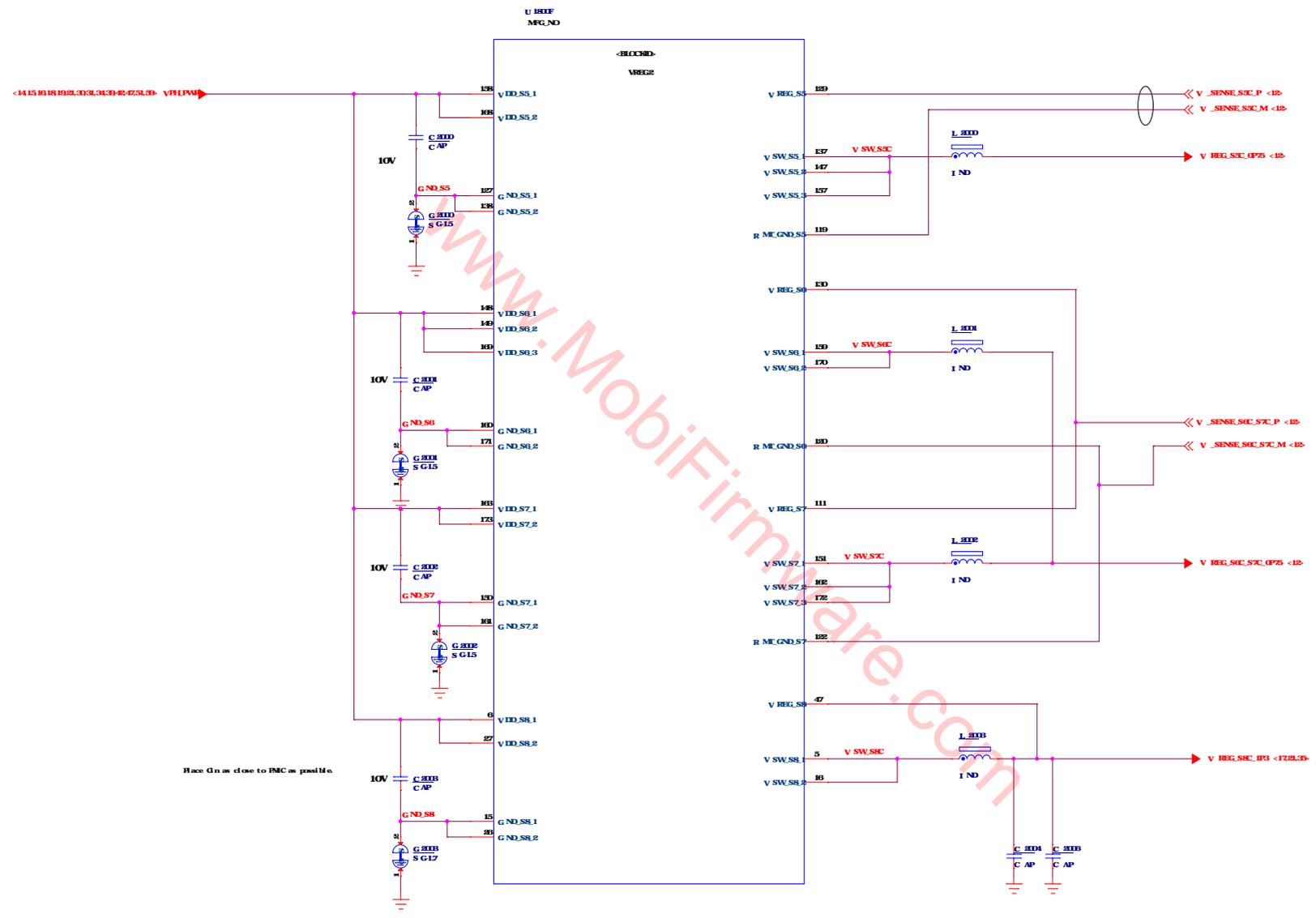
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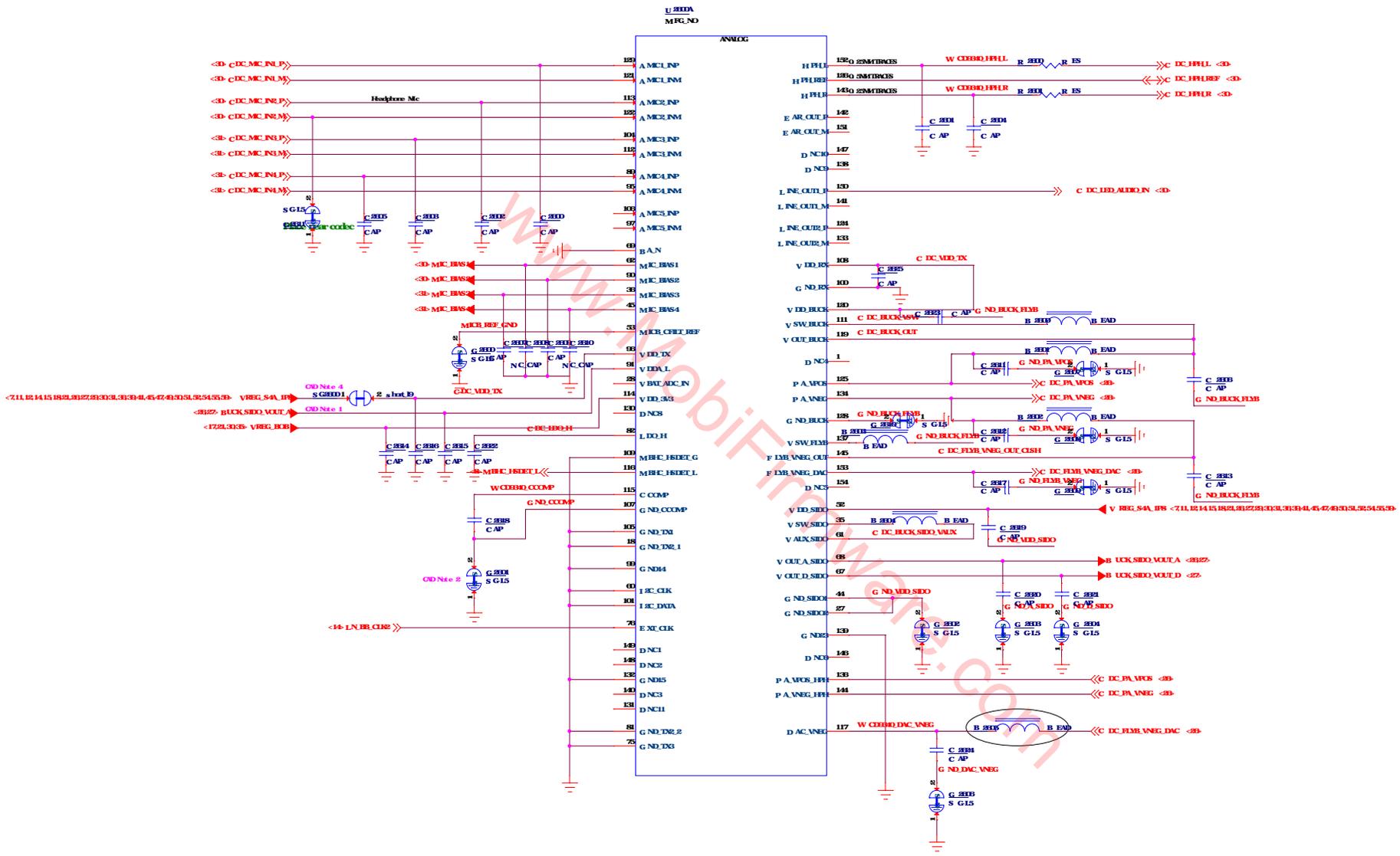
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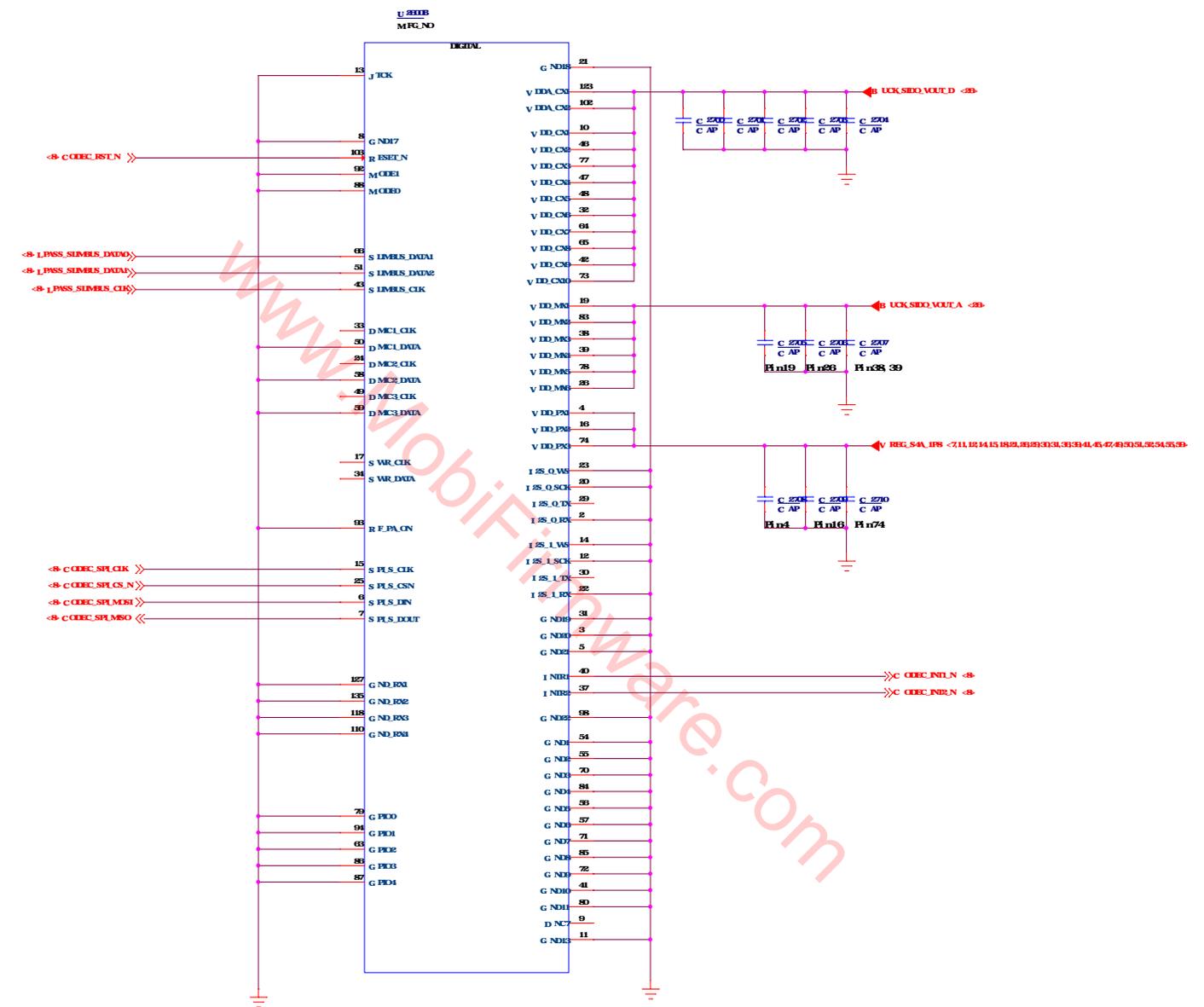
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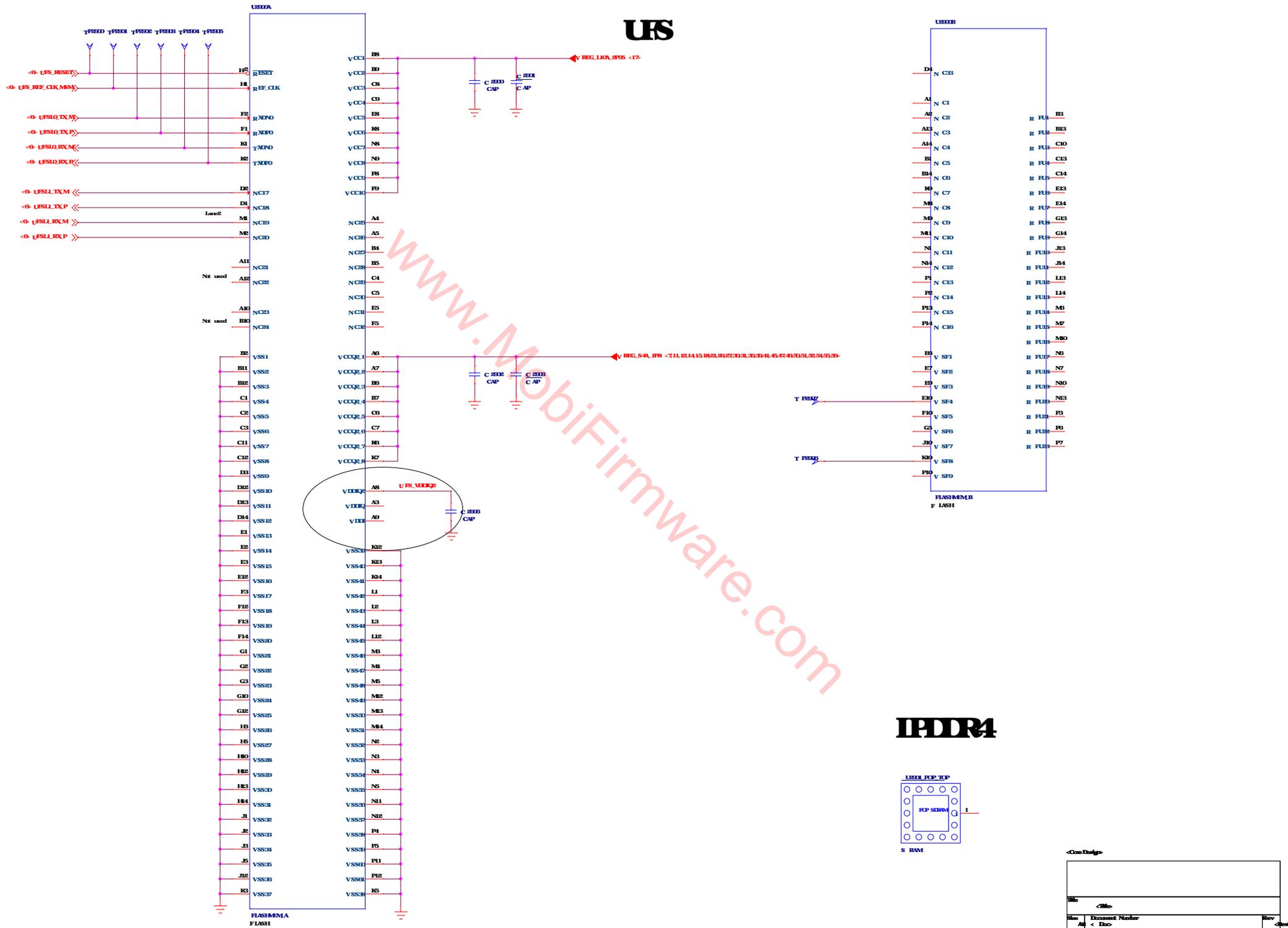
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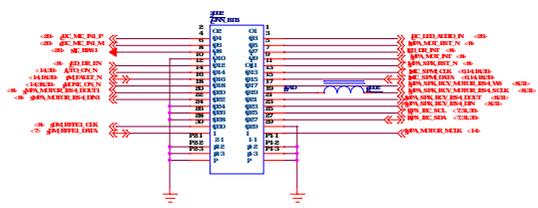
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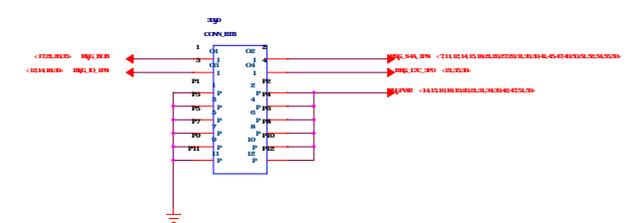


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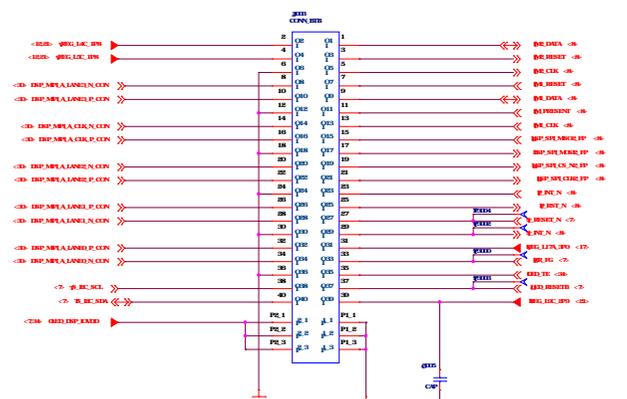
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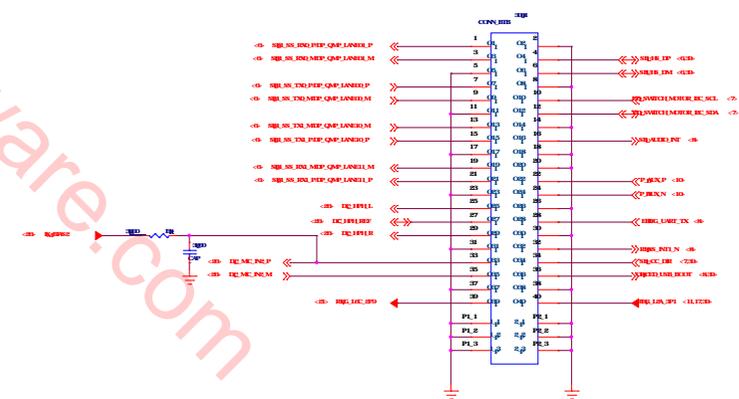
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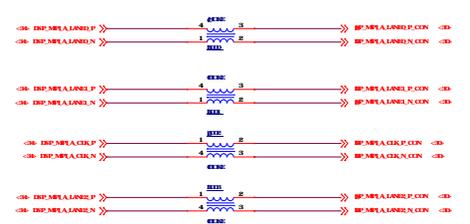
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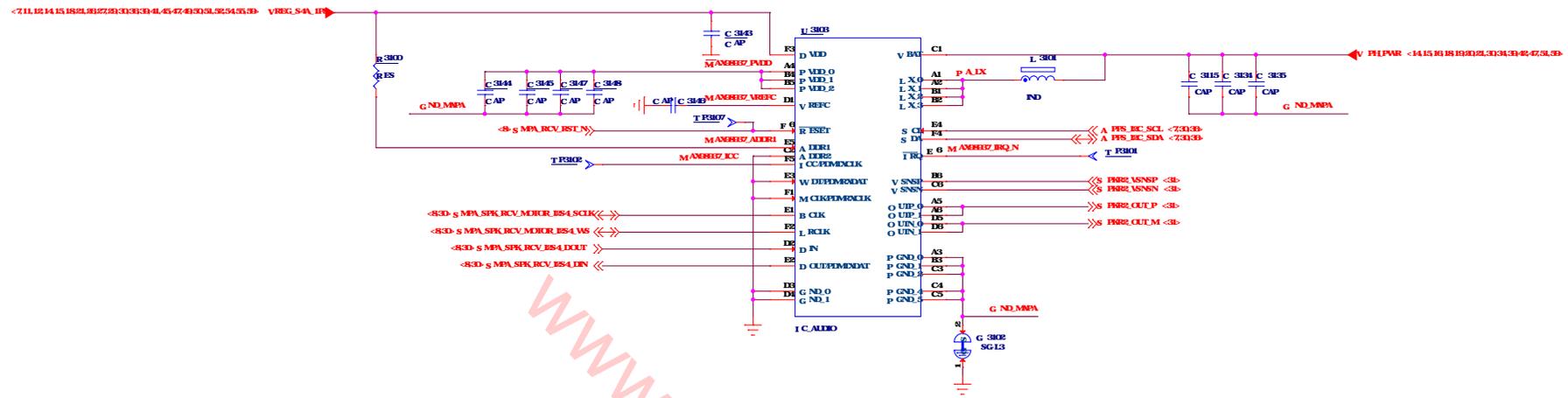


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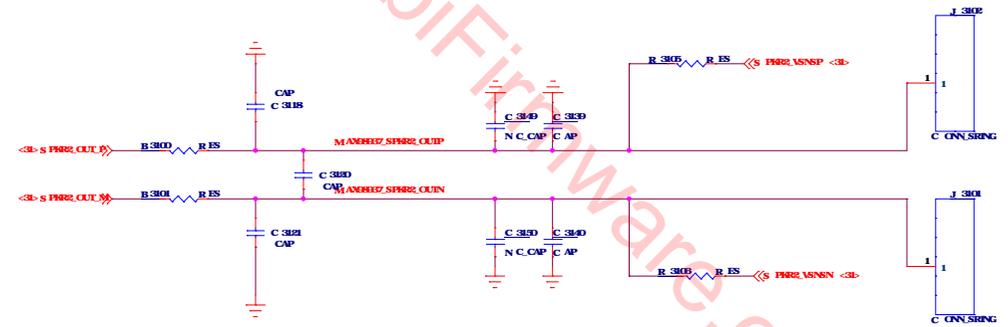


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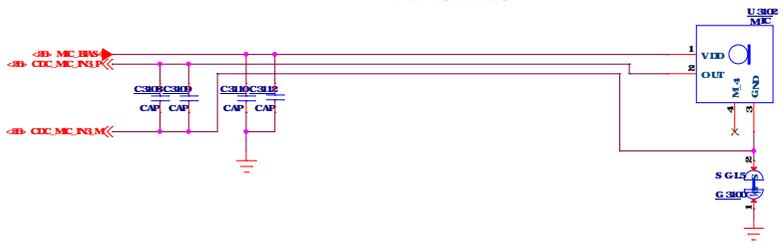


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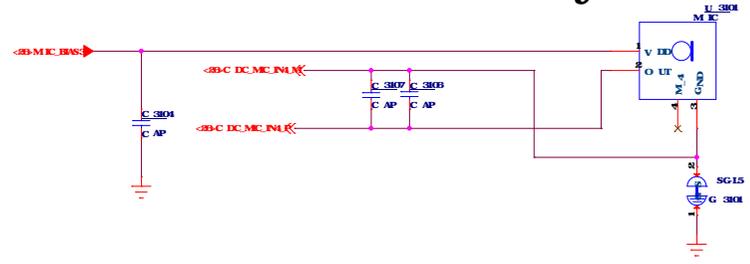


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ANCMC



SecordayMC



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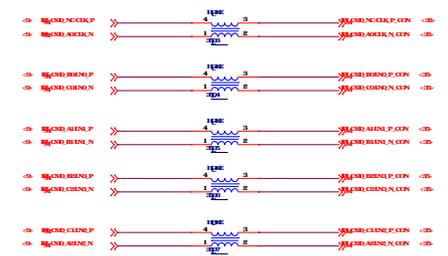
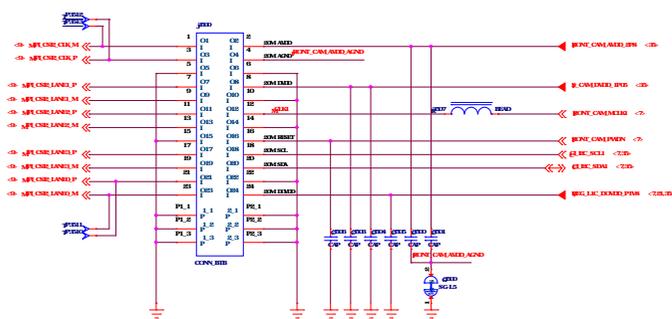
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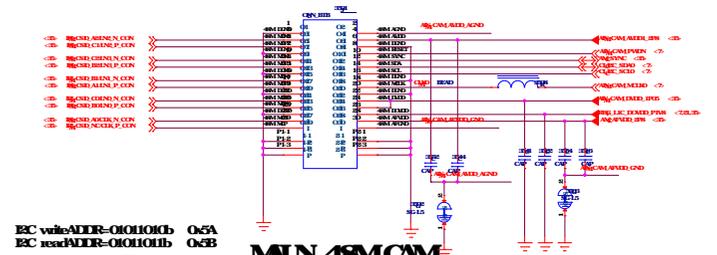
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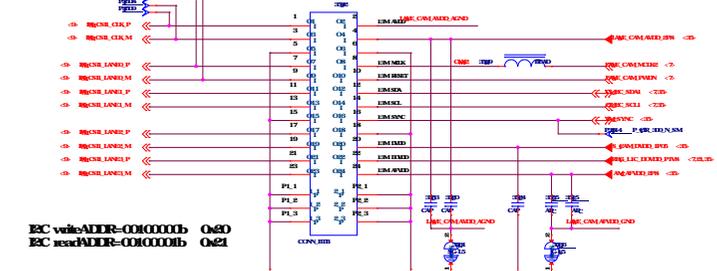
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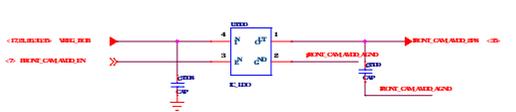


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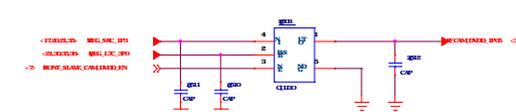


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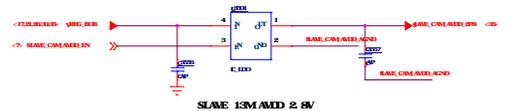
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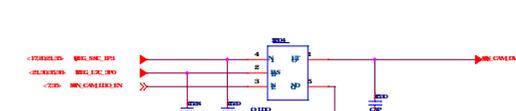
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FRONT 20M/SLAVE 13M/AMD 1.0V



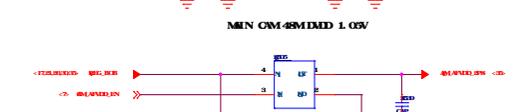
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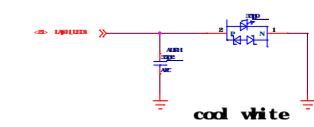


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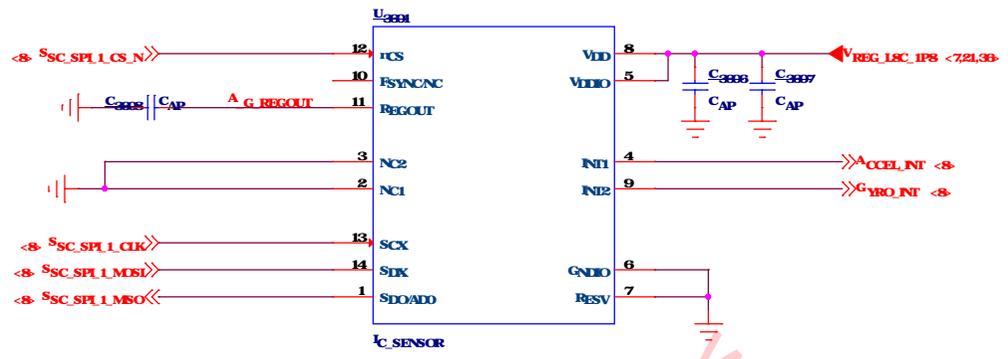
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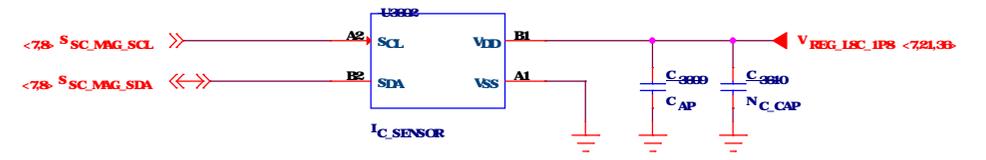


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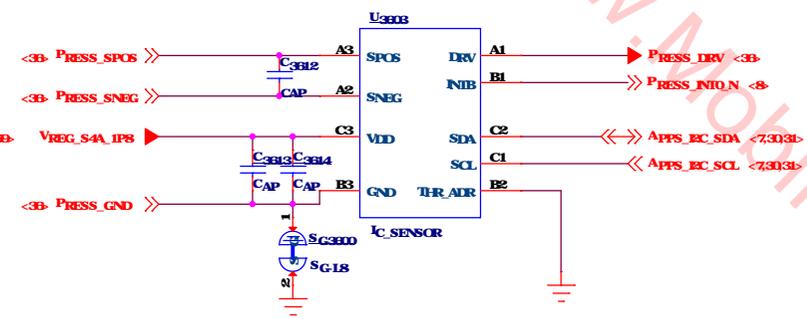
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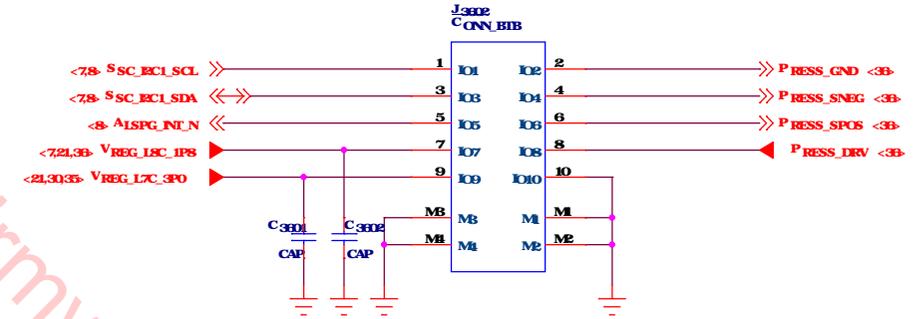
A+G



E-COMPASS



press sensor controller



press sensor

ALS-PROX

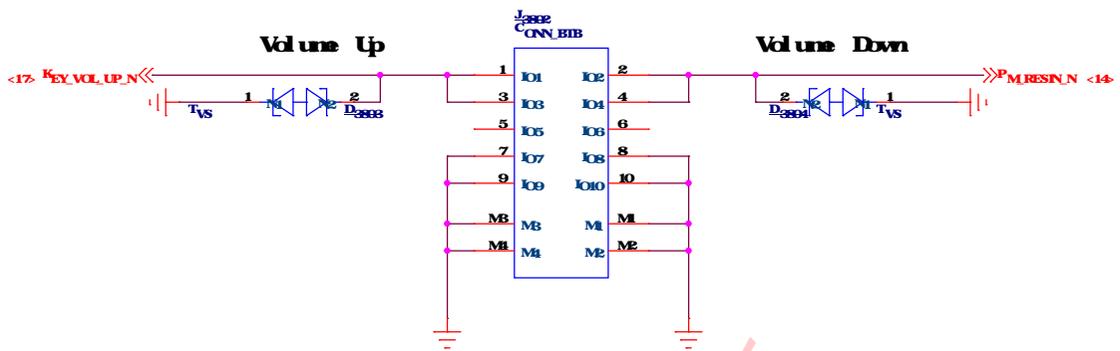
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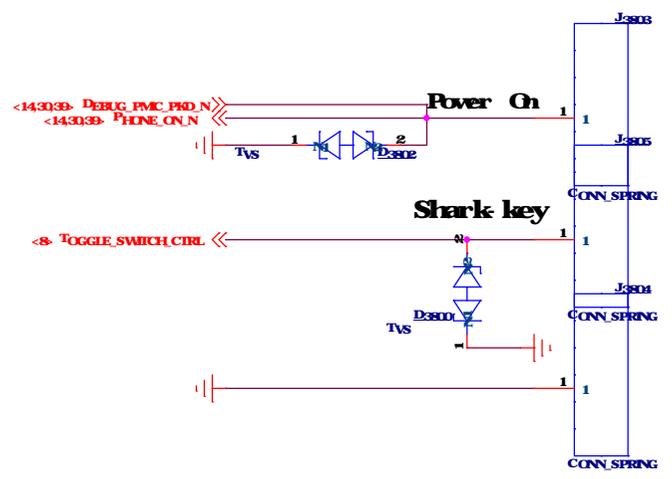
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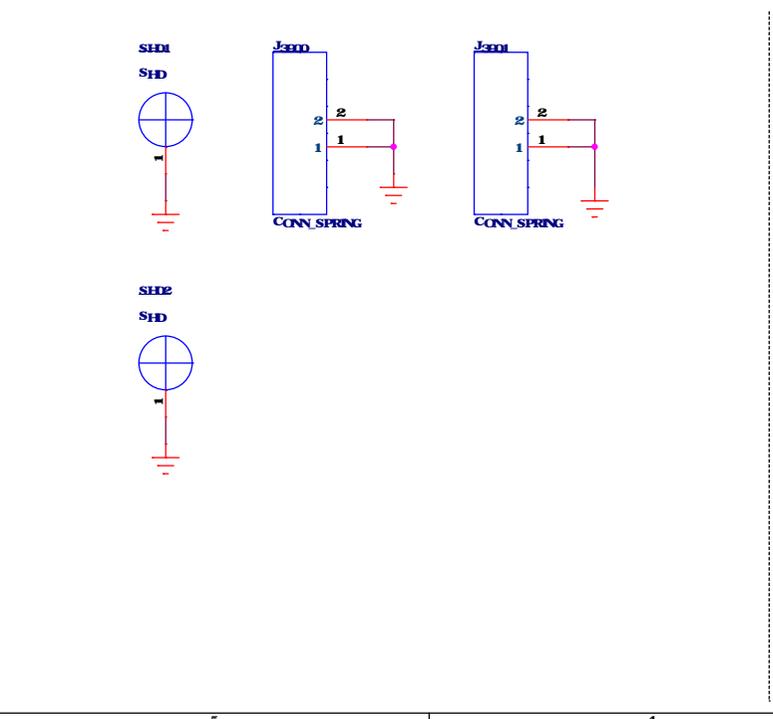
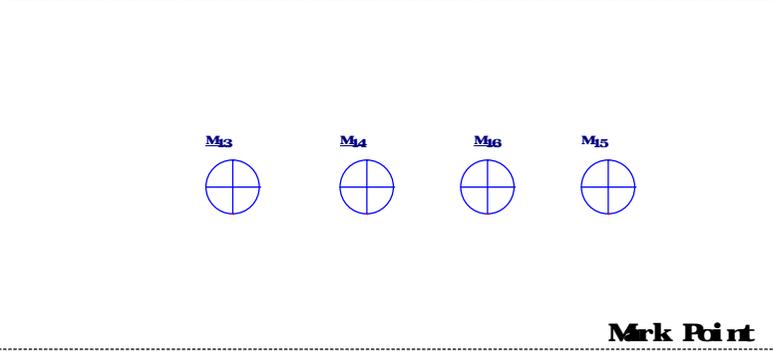
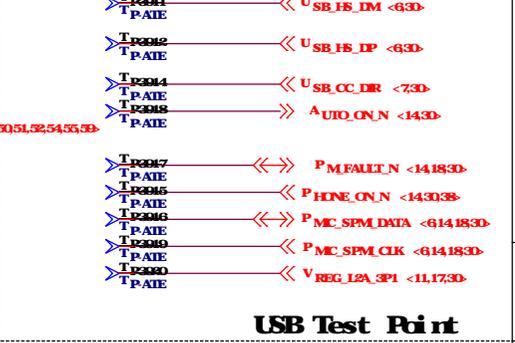
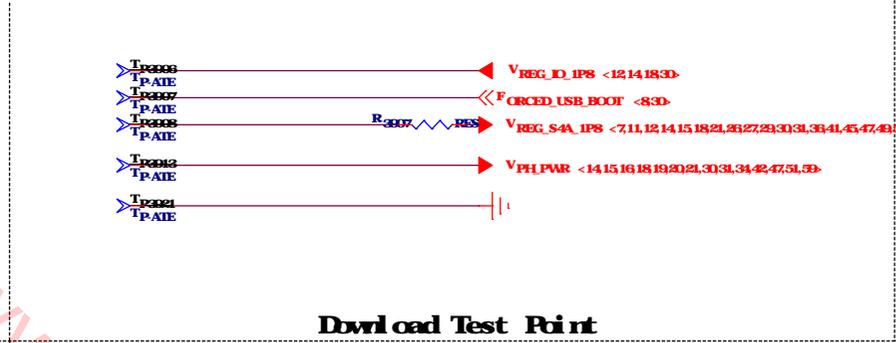
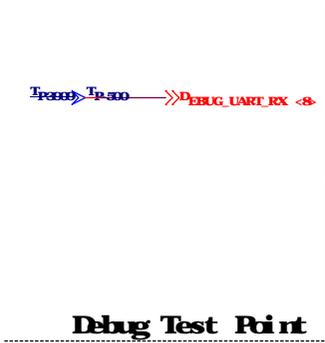
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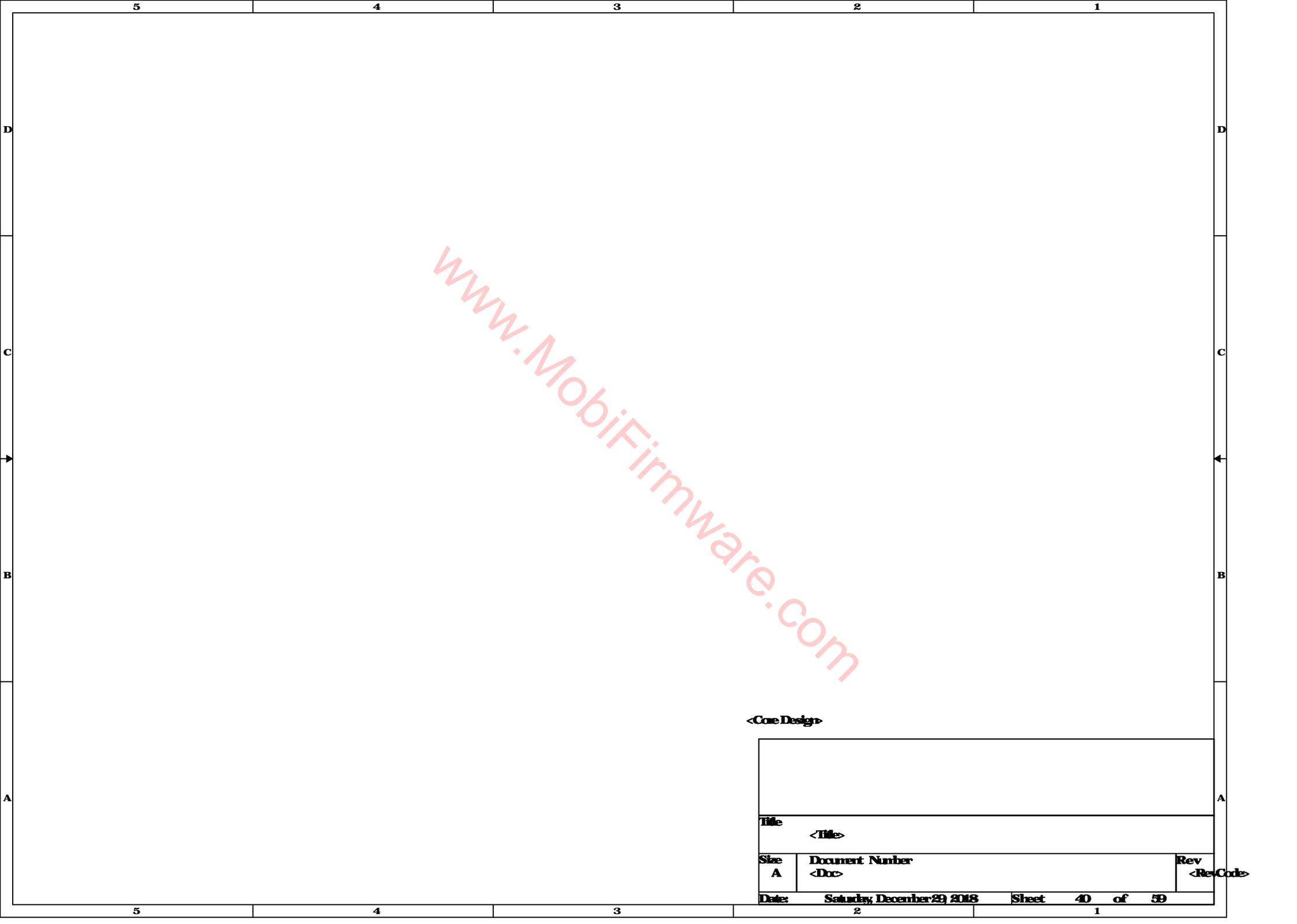
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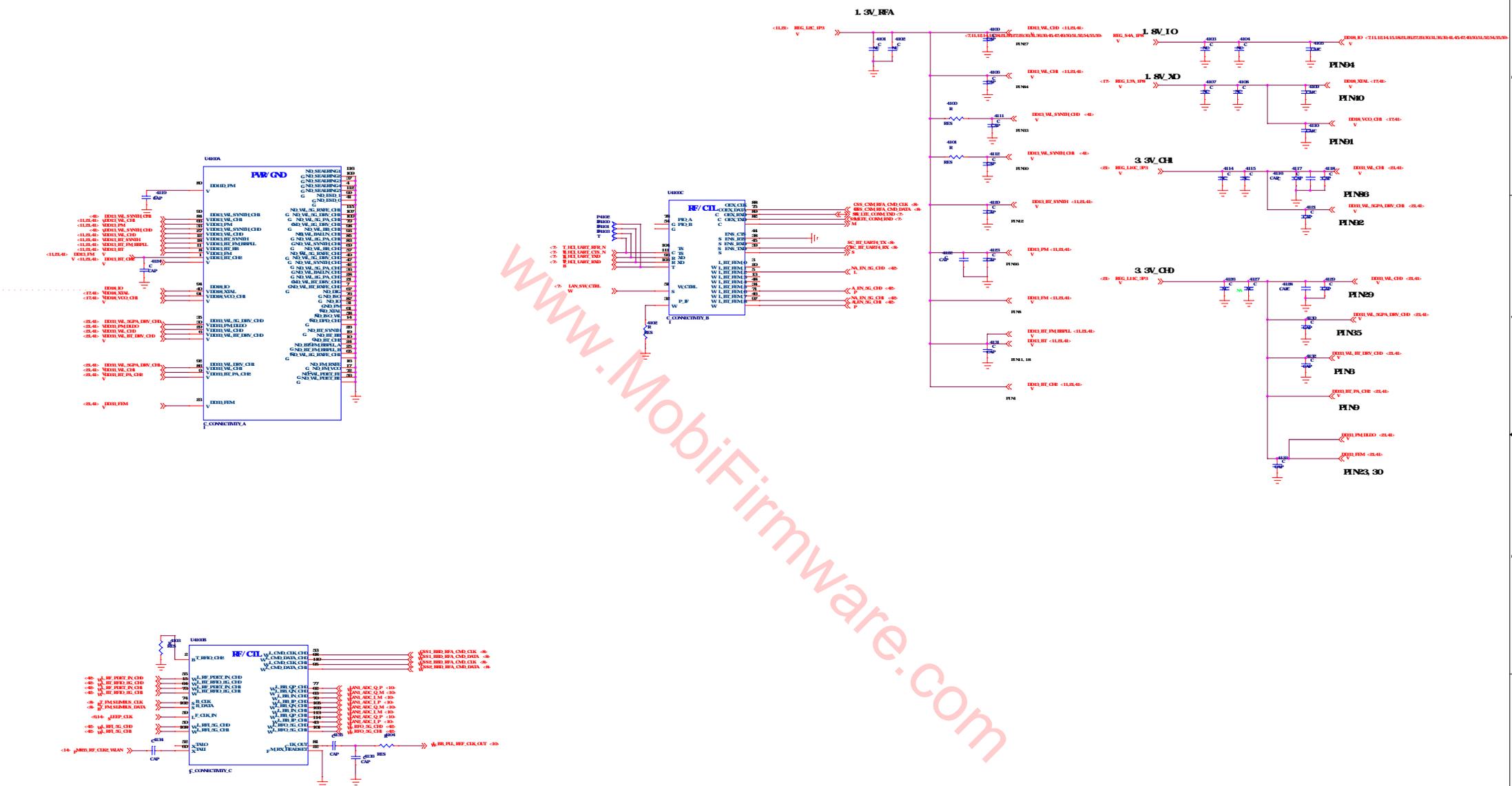
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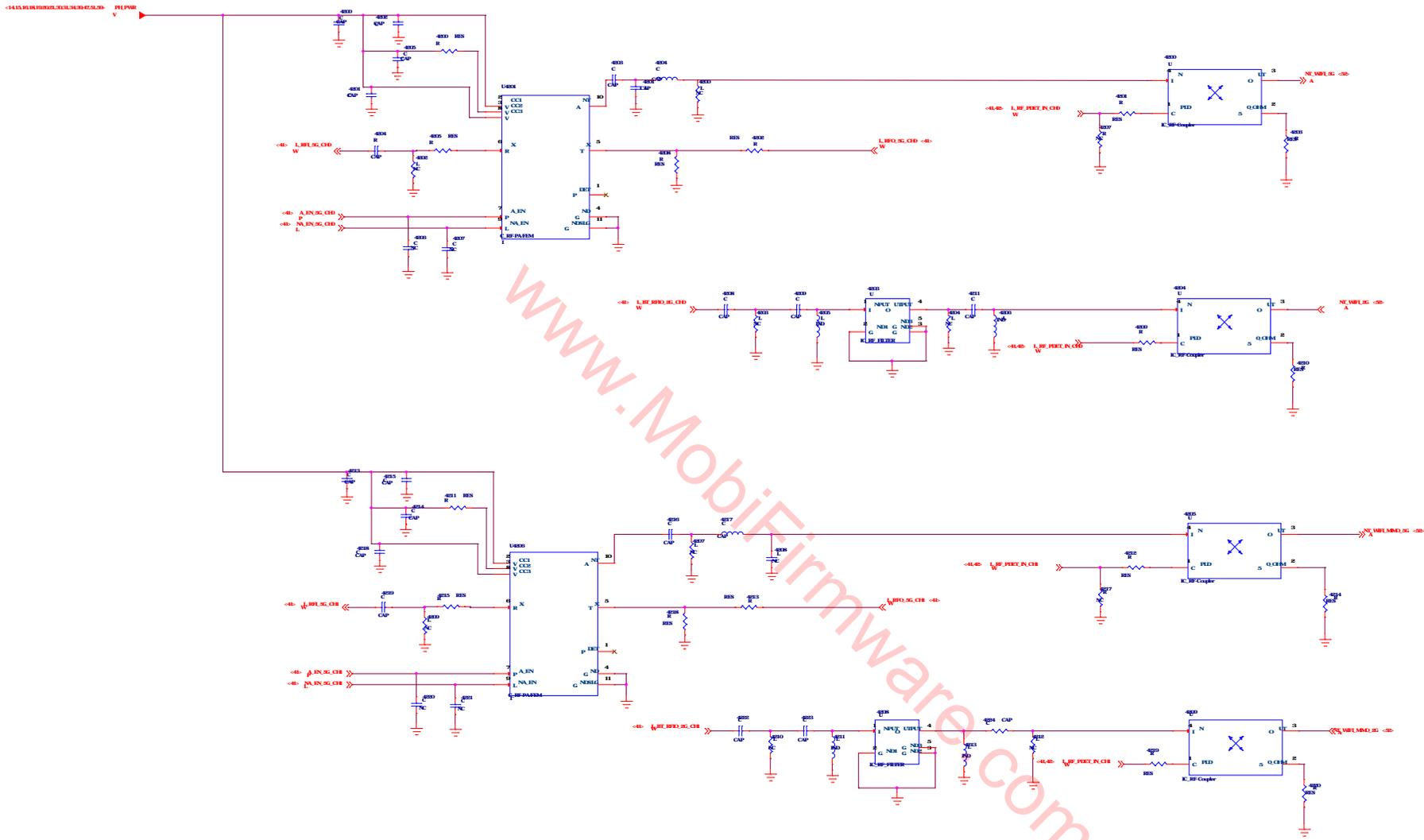


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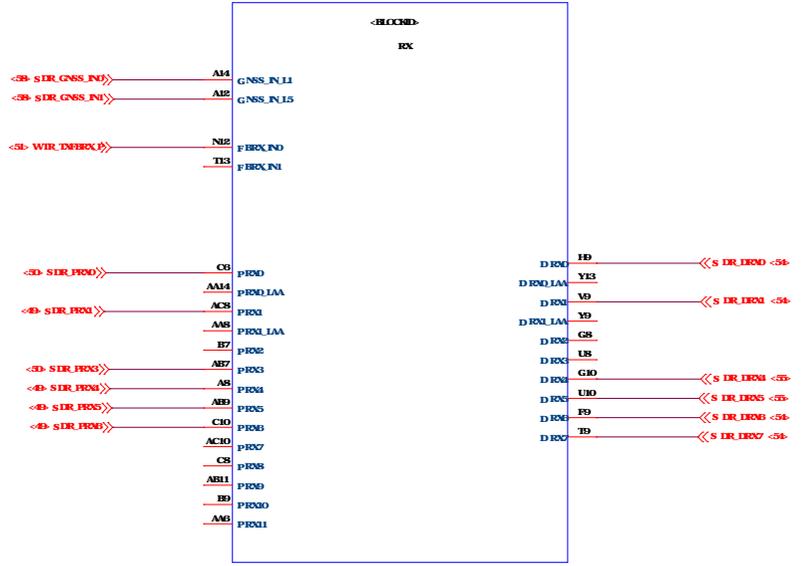
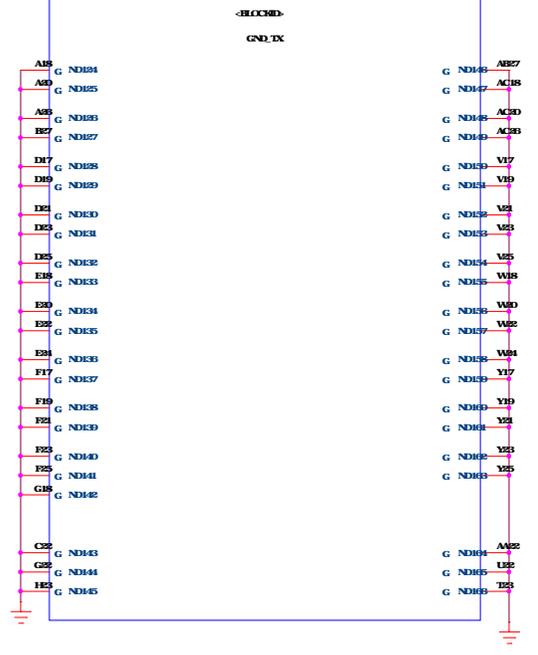
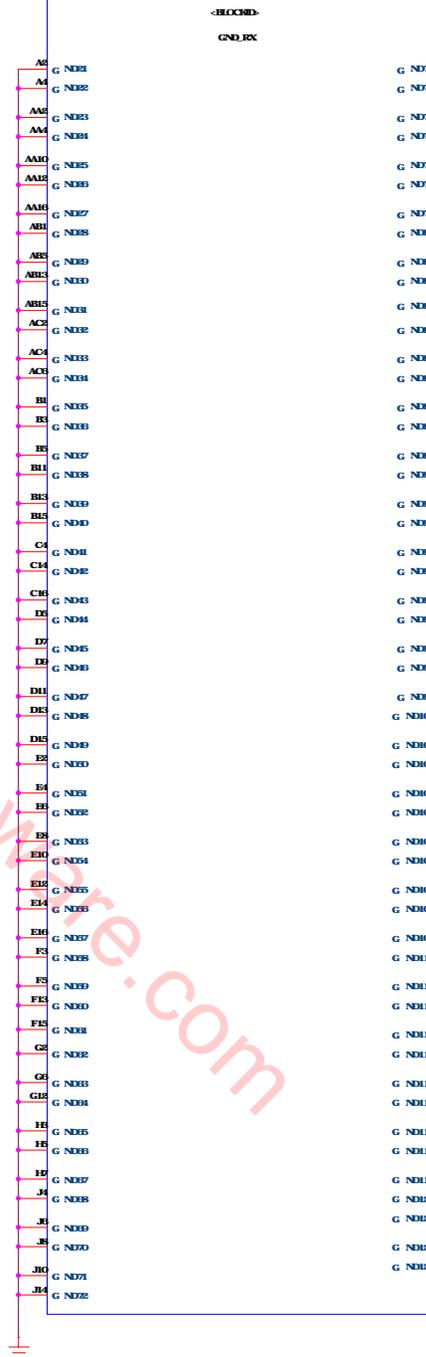
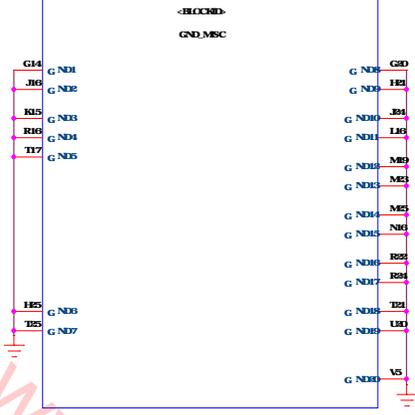
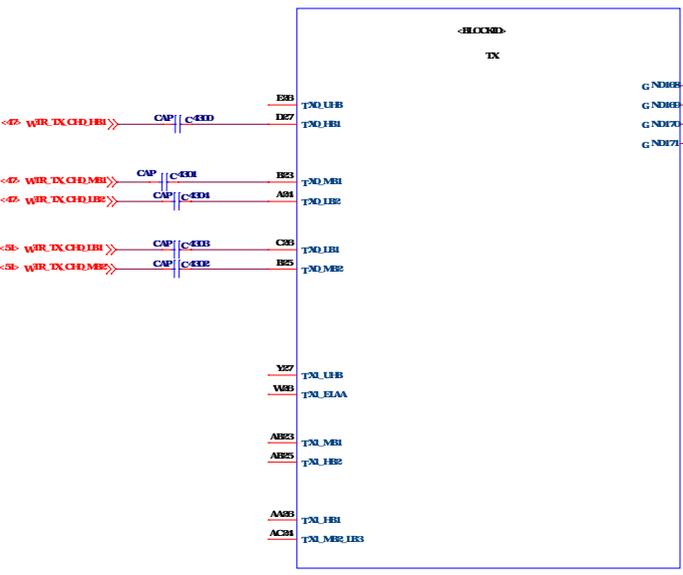


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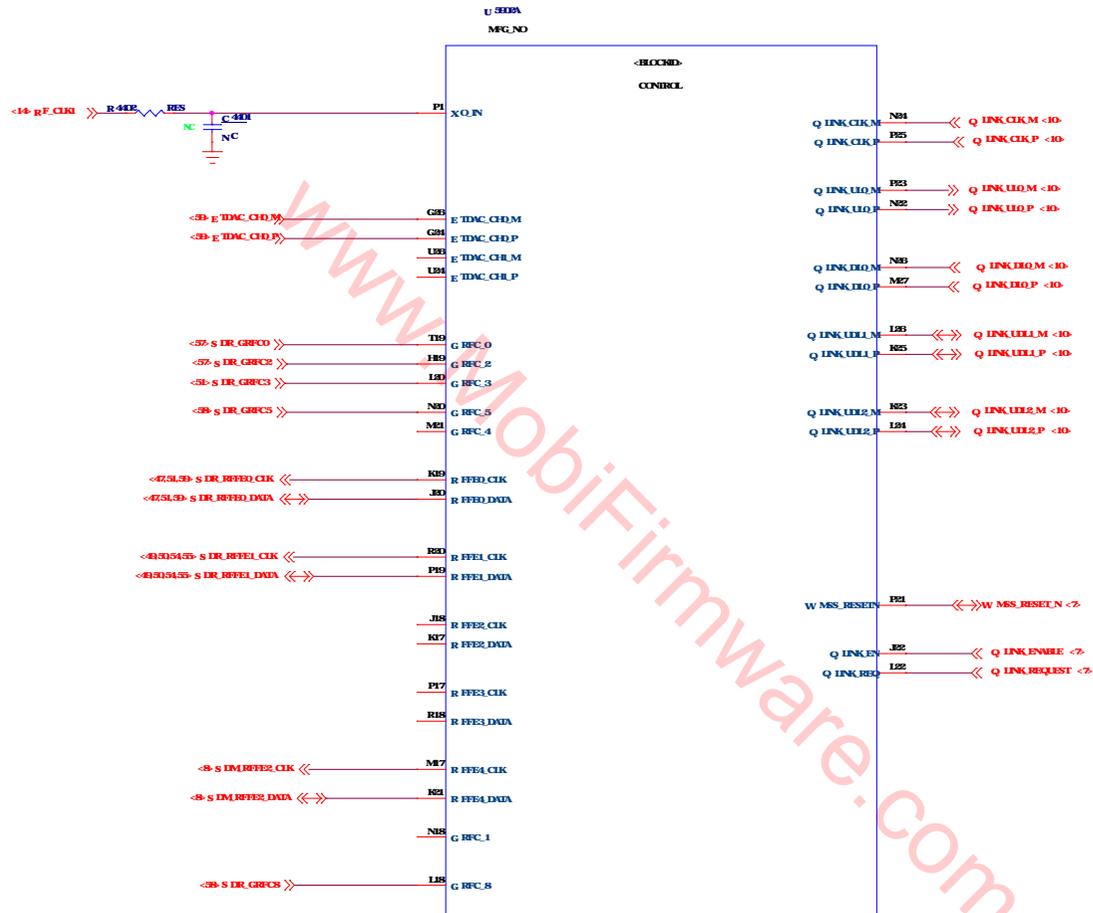
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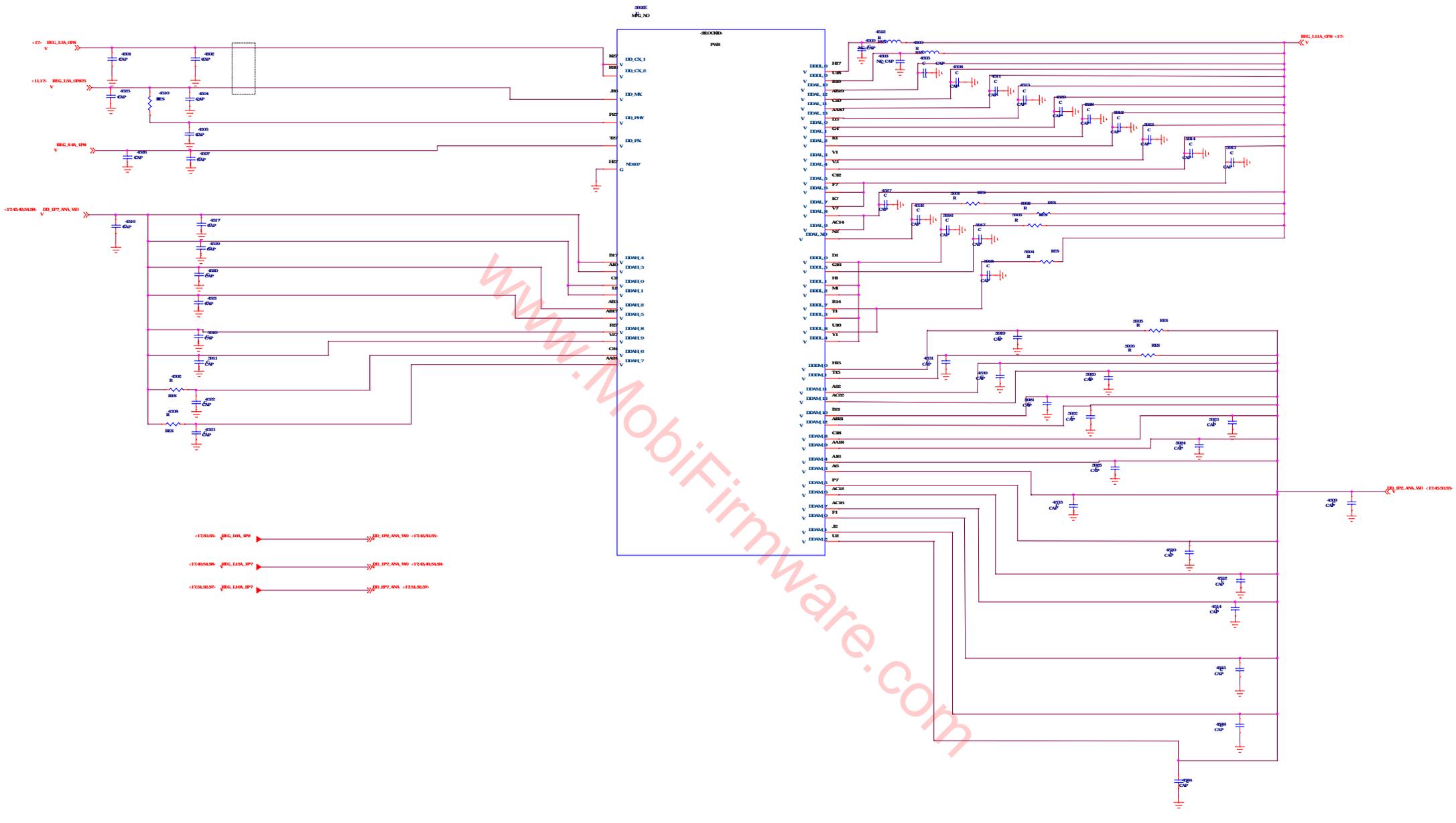
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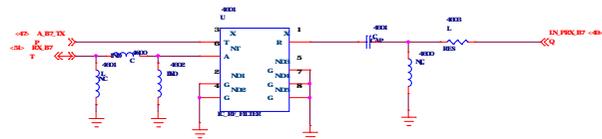
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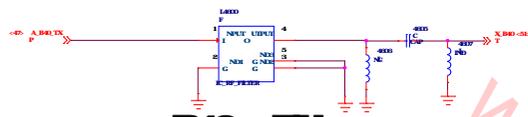
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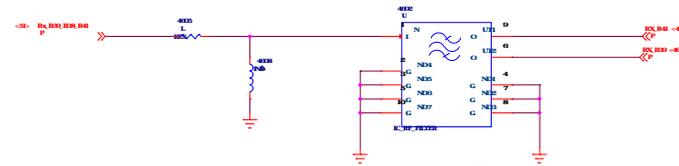
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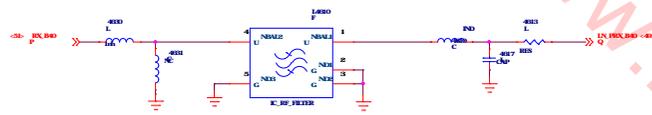
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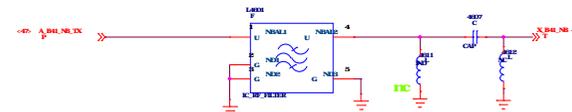
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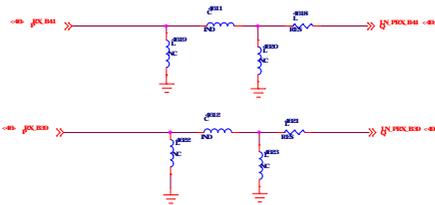
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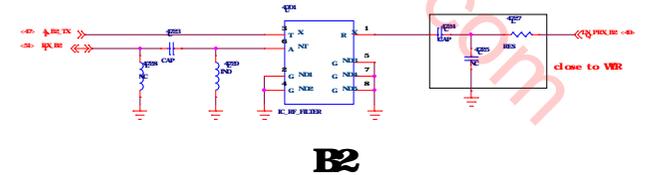
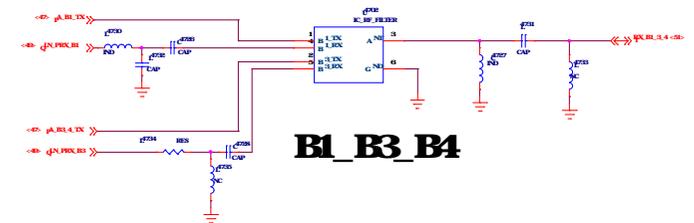
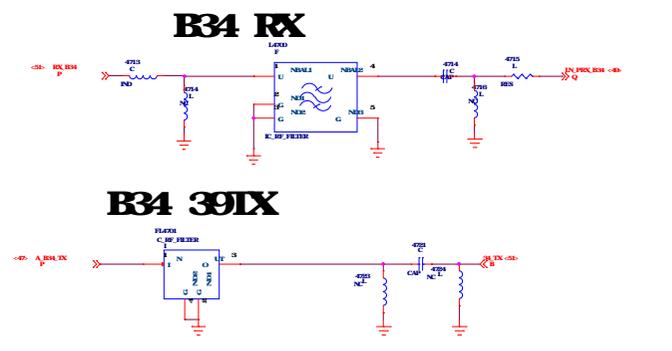
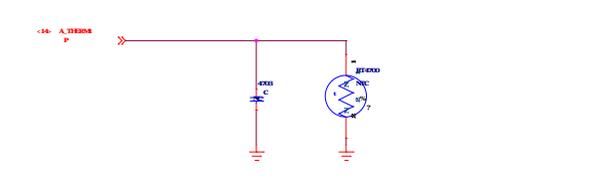
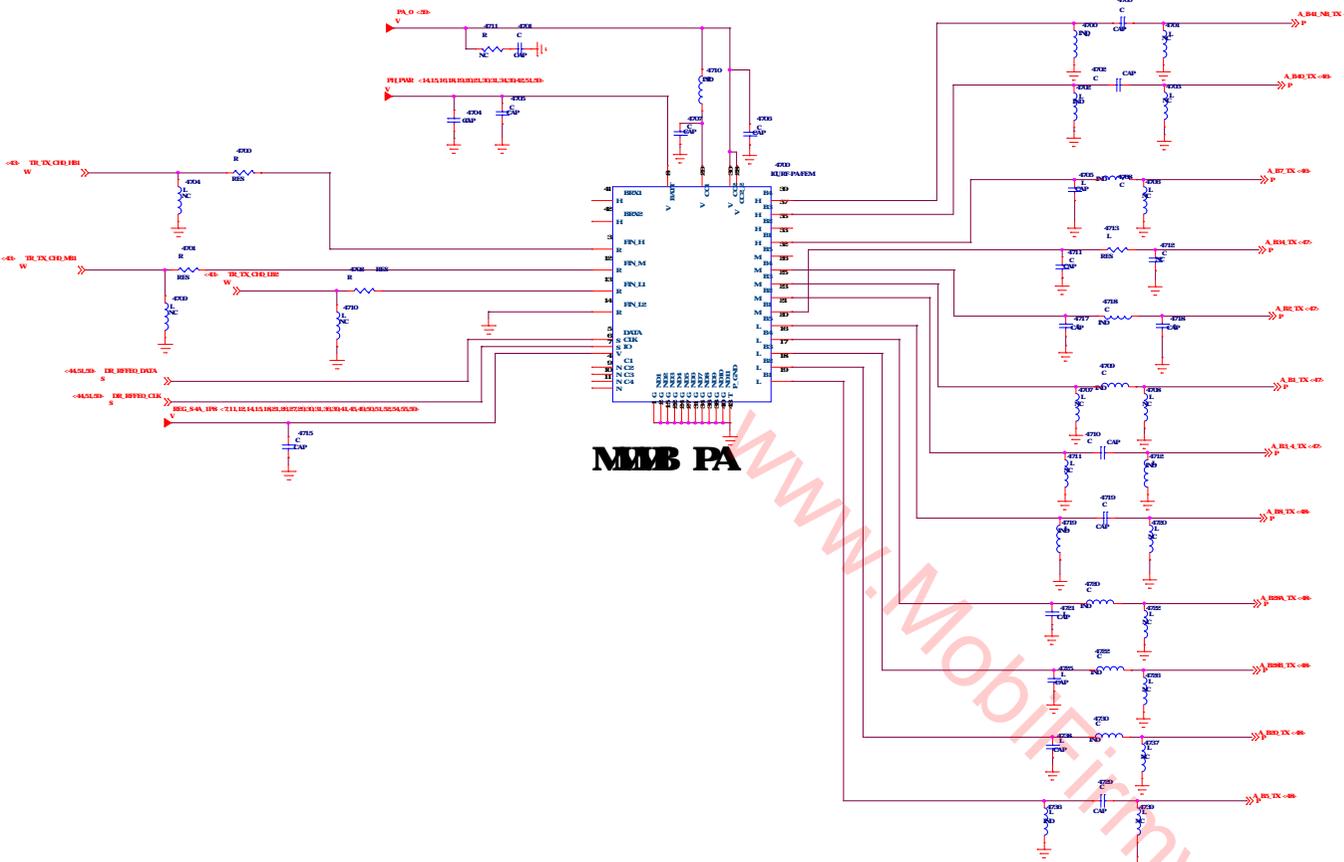
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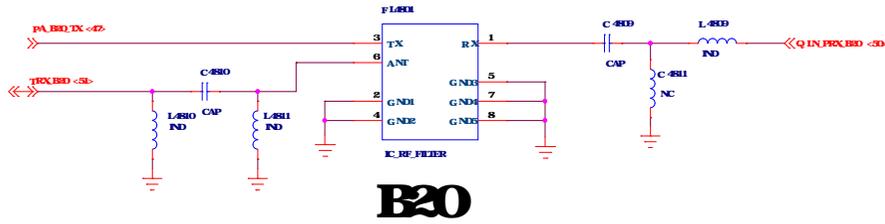
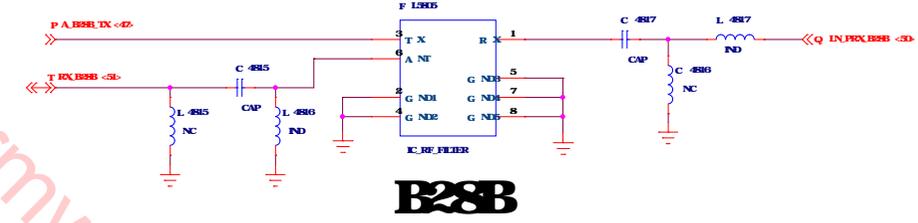
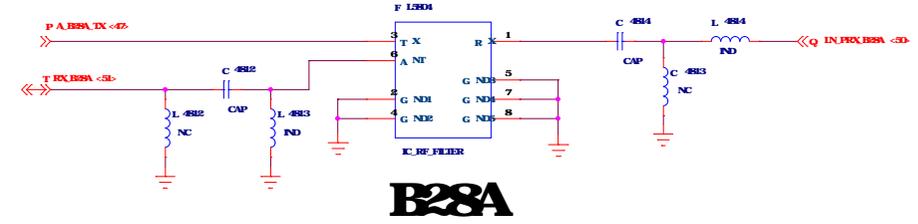
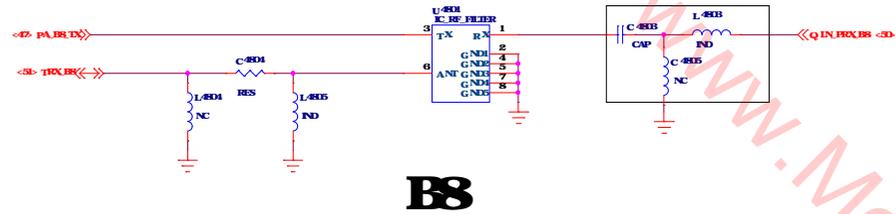
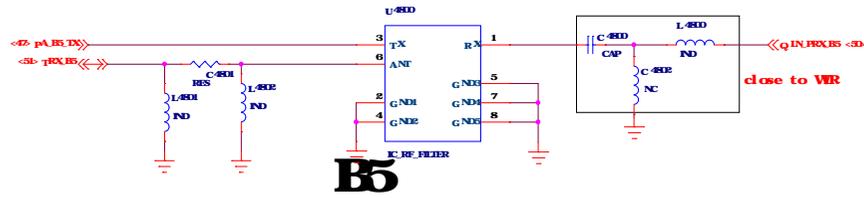
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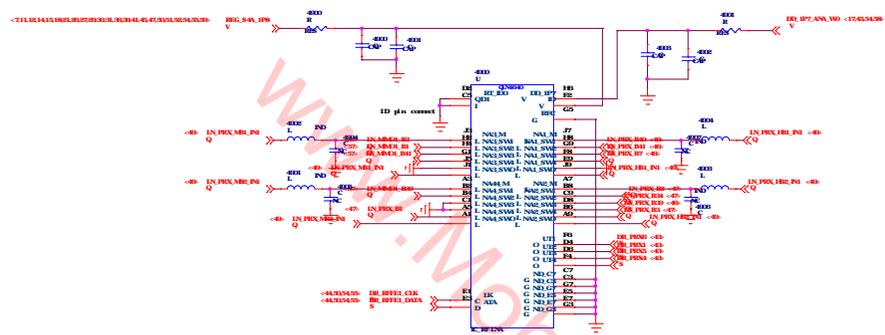
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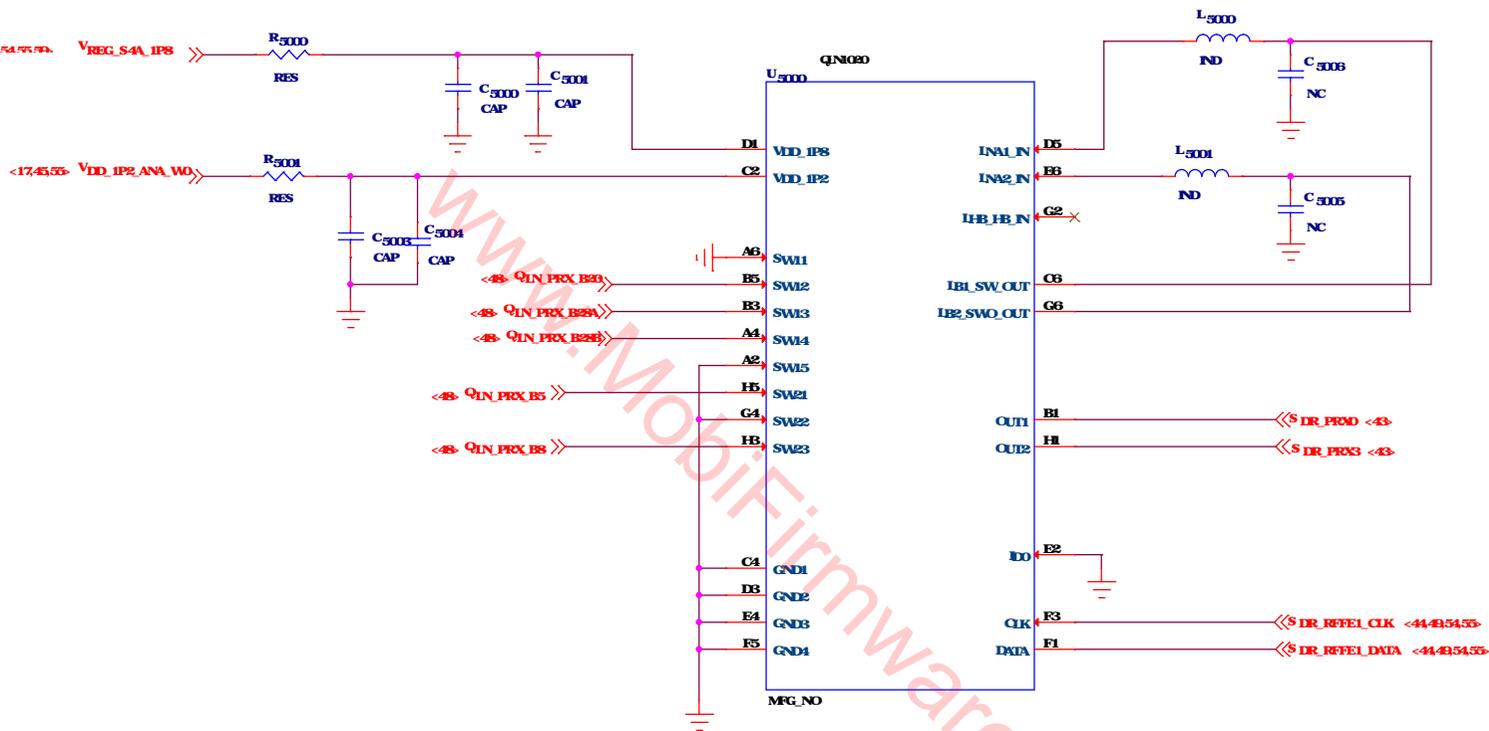
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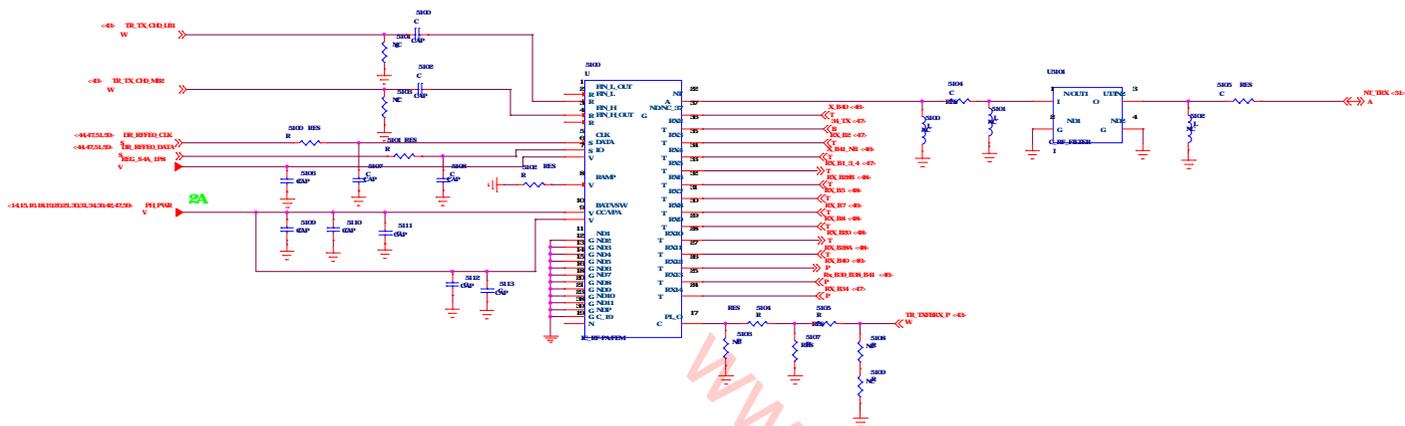
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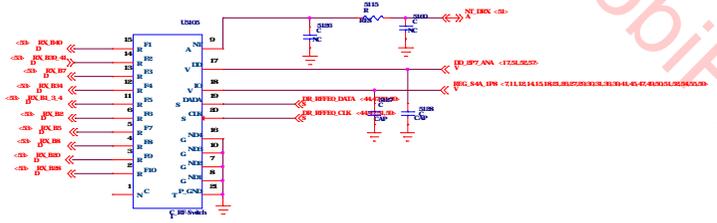
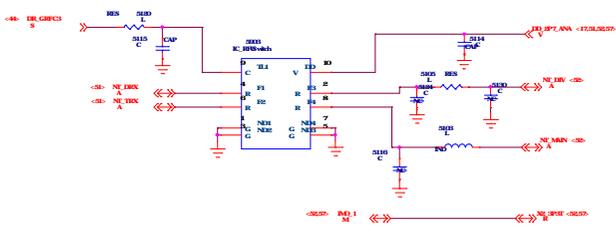


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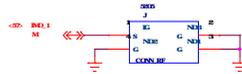
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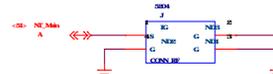


J3201 Del et al © DV2 O 2018 11 29

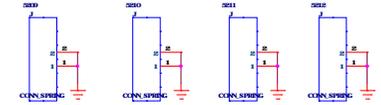
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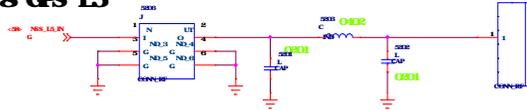
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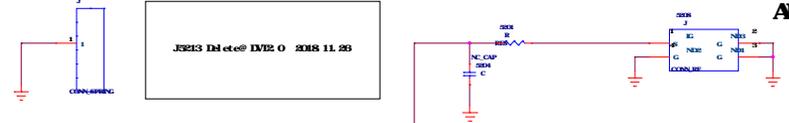


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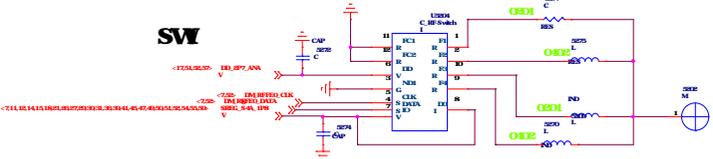
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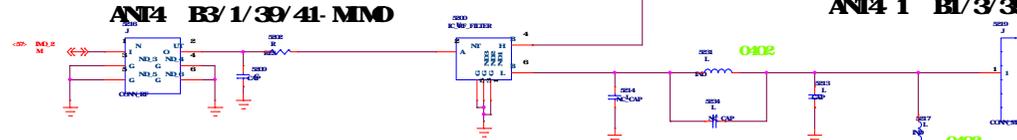
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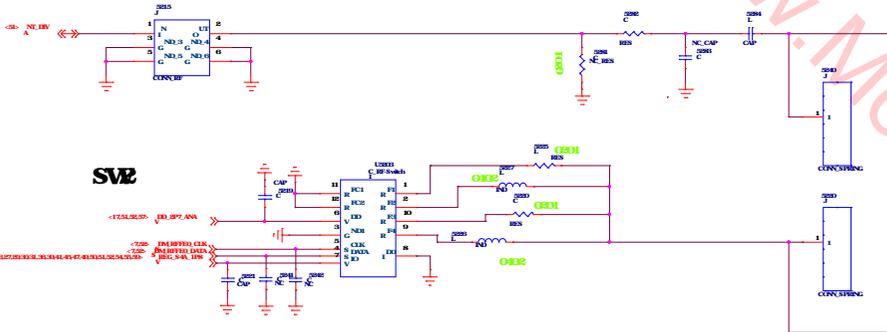
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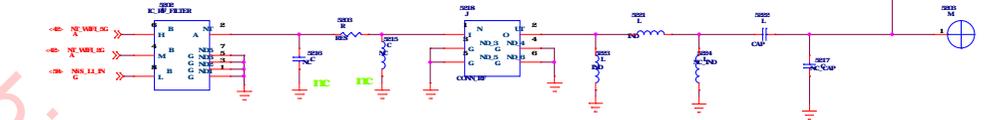


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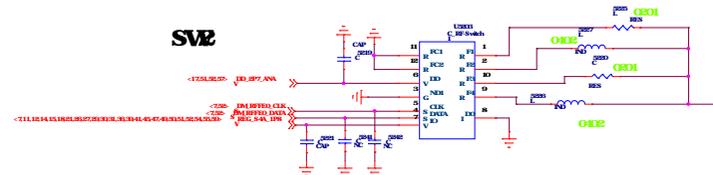
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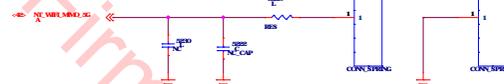
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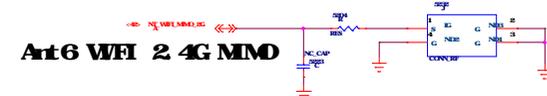
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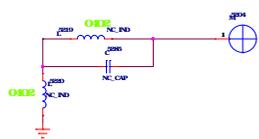
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J3225 Del et al © DV2 O 2018 11 29

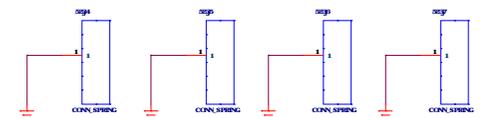
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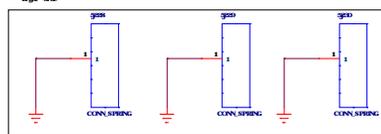
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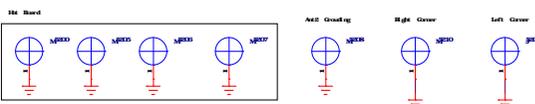
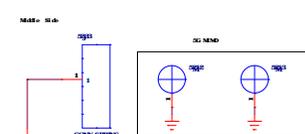
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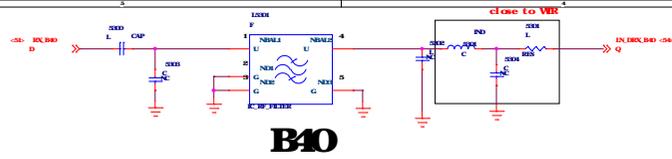
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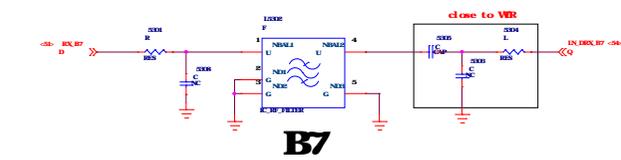
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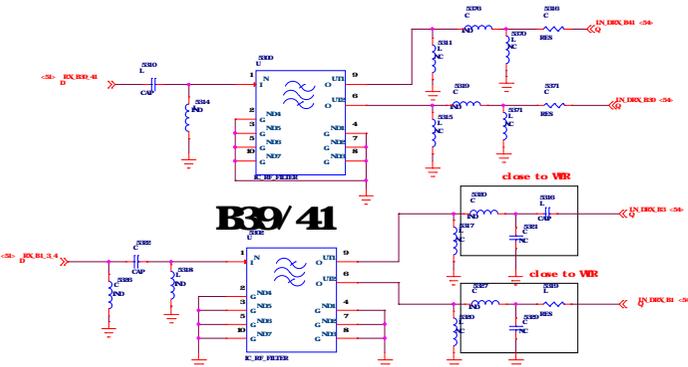
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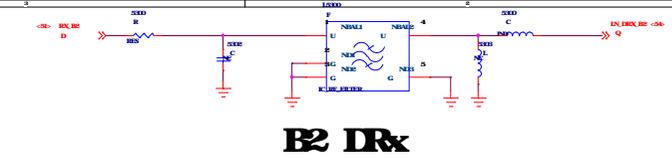


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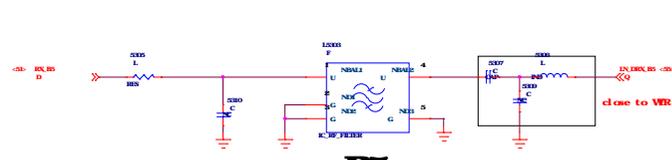


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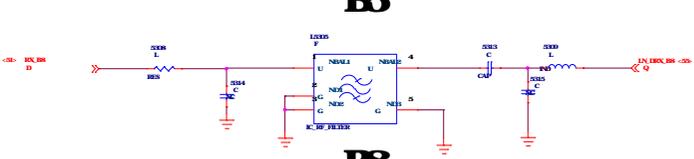
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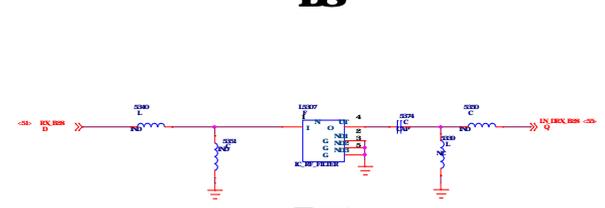
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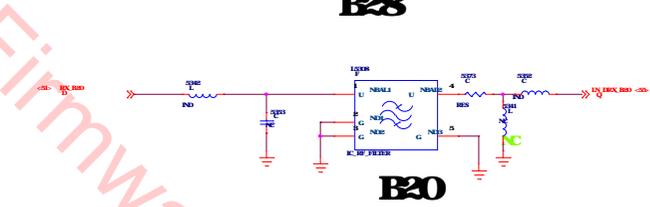
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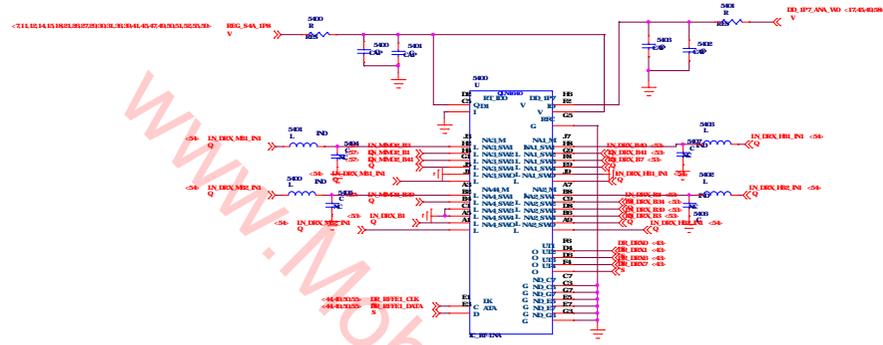


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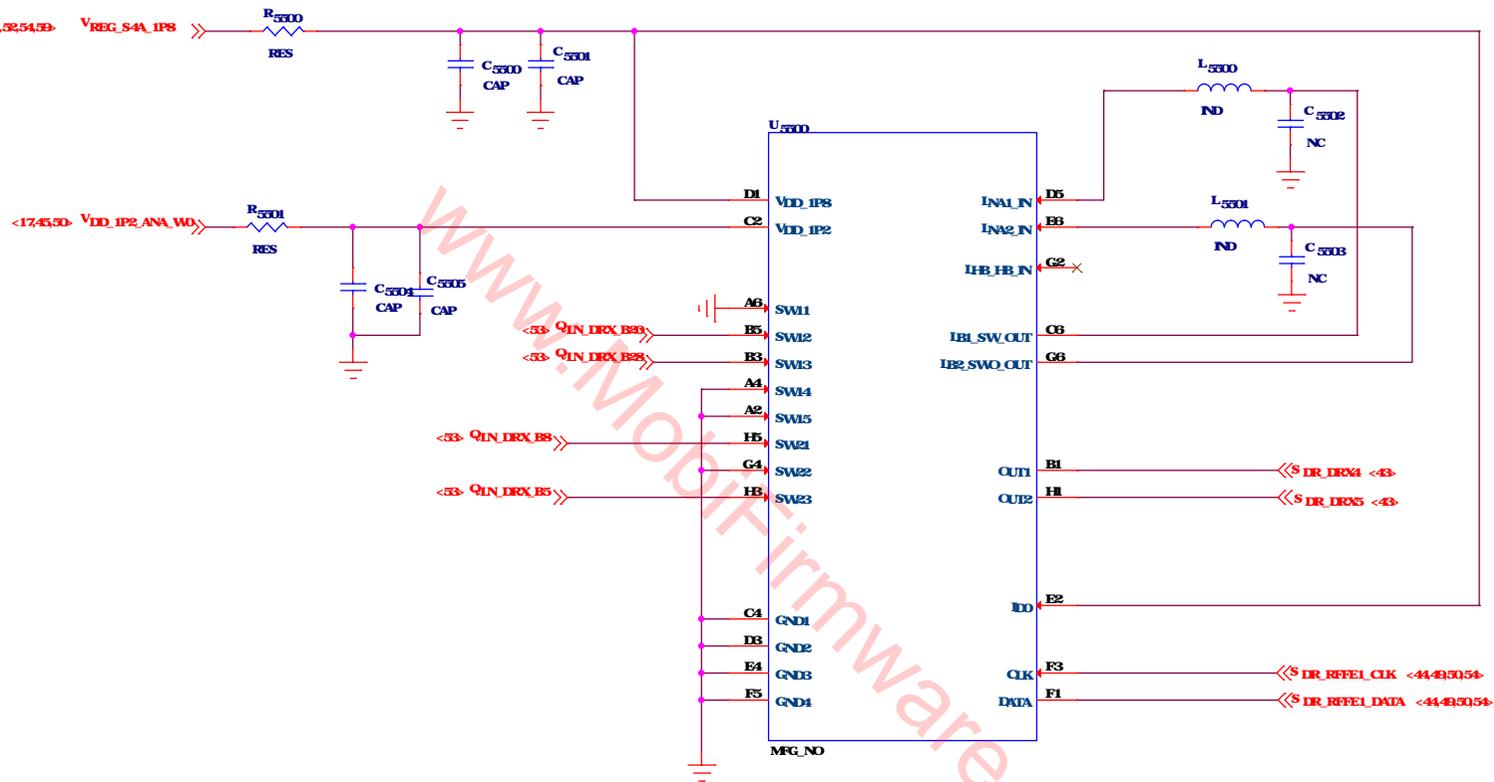
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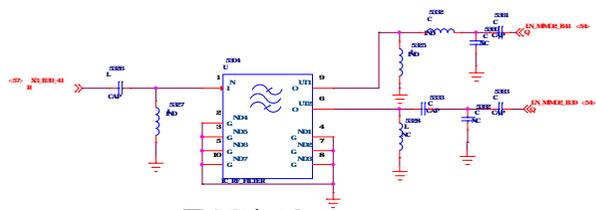
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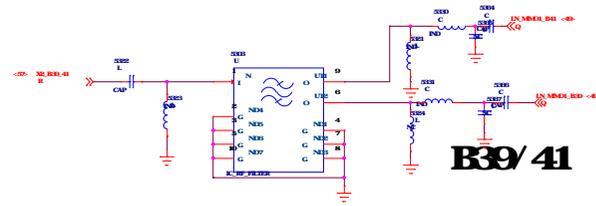
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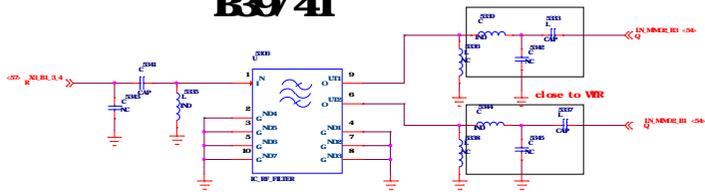
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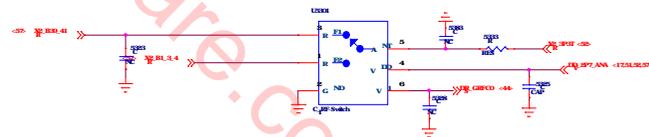
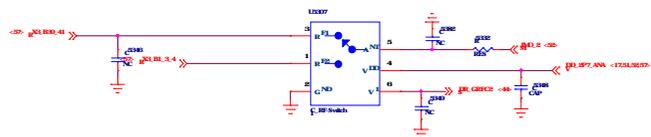
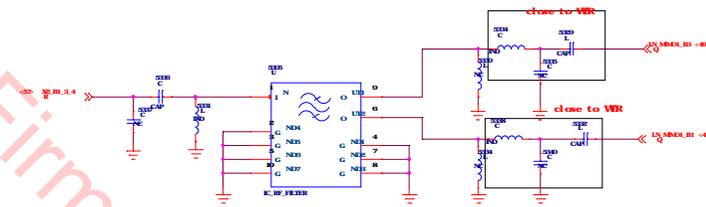
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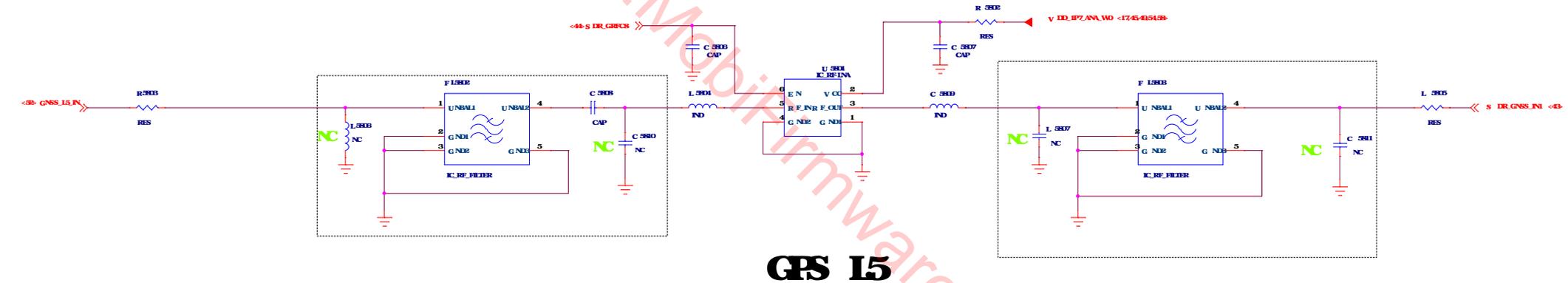
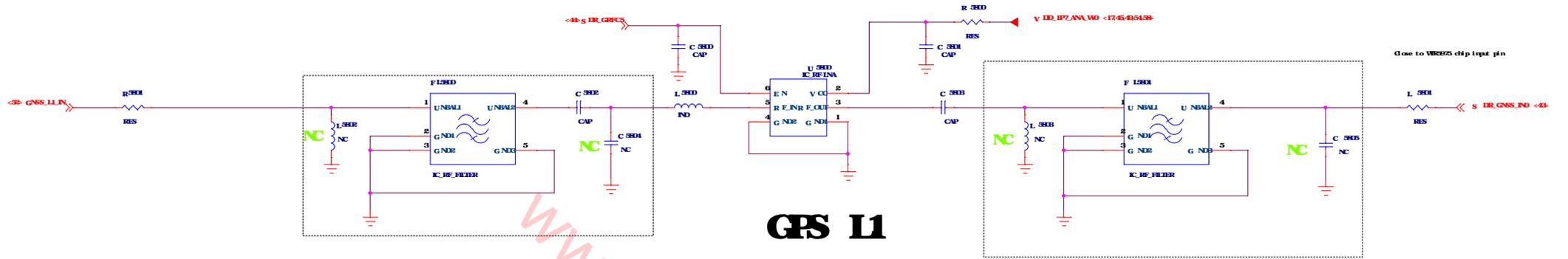
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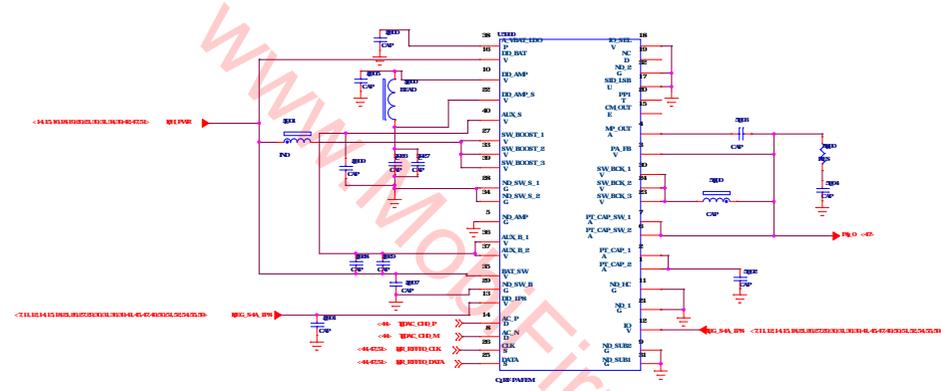
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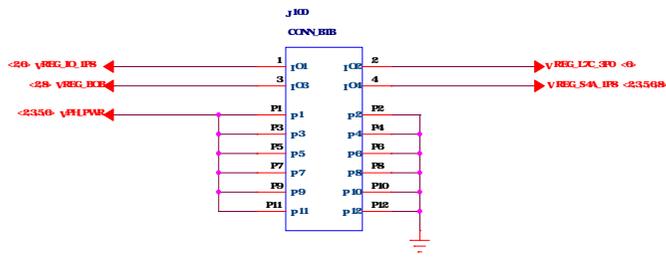
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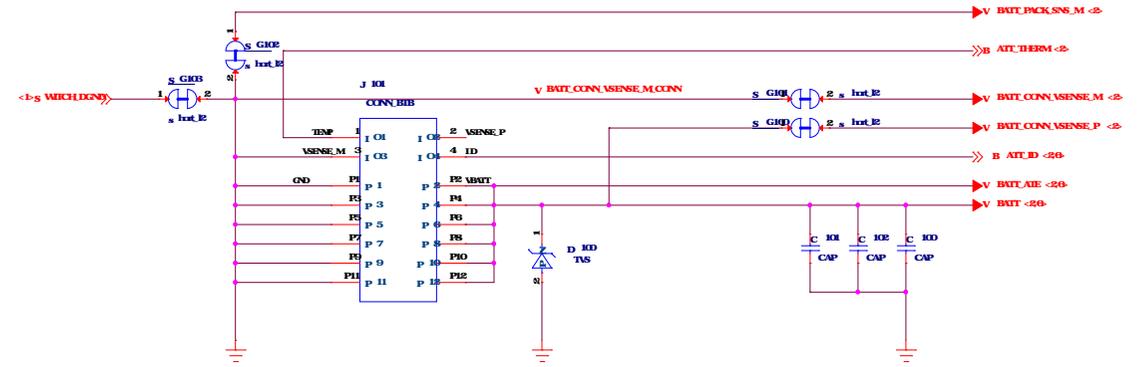
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App	<Doc>	
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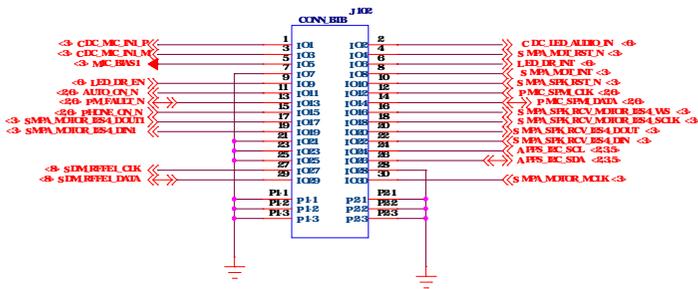




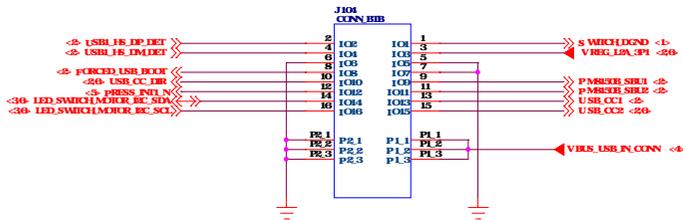
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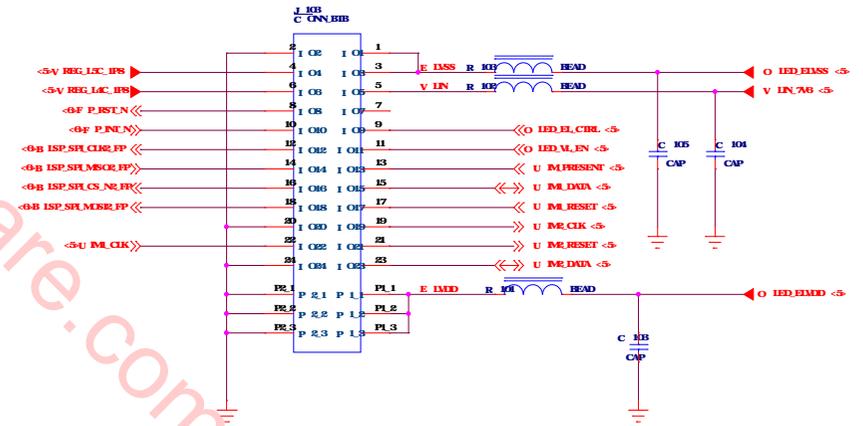
**BATT\_BIB**



**MIN\_TO\_SUBBOARD**



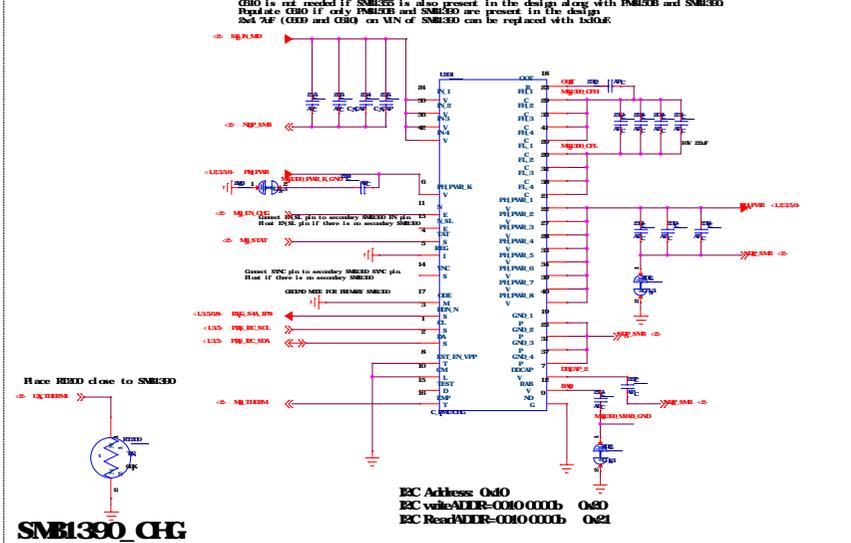
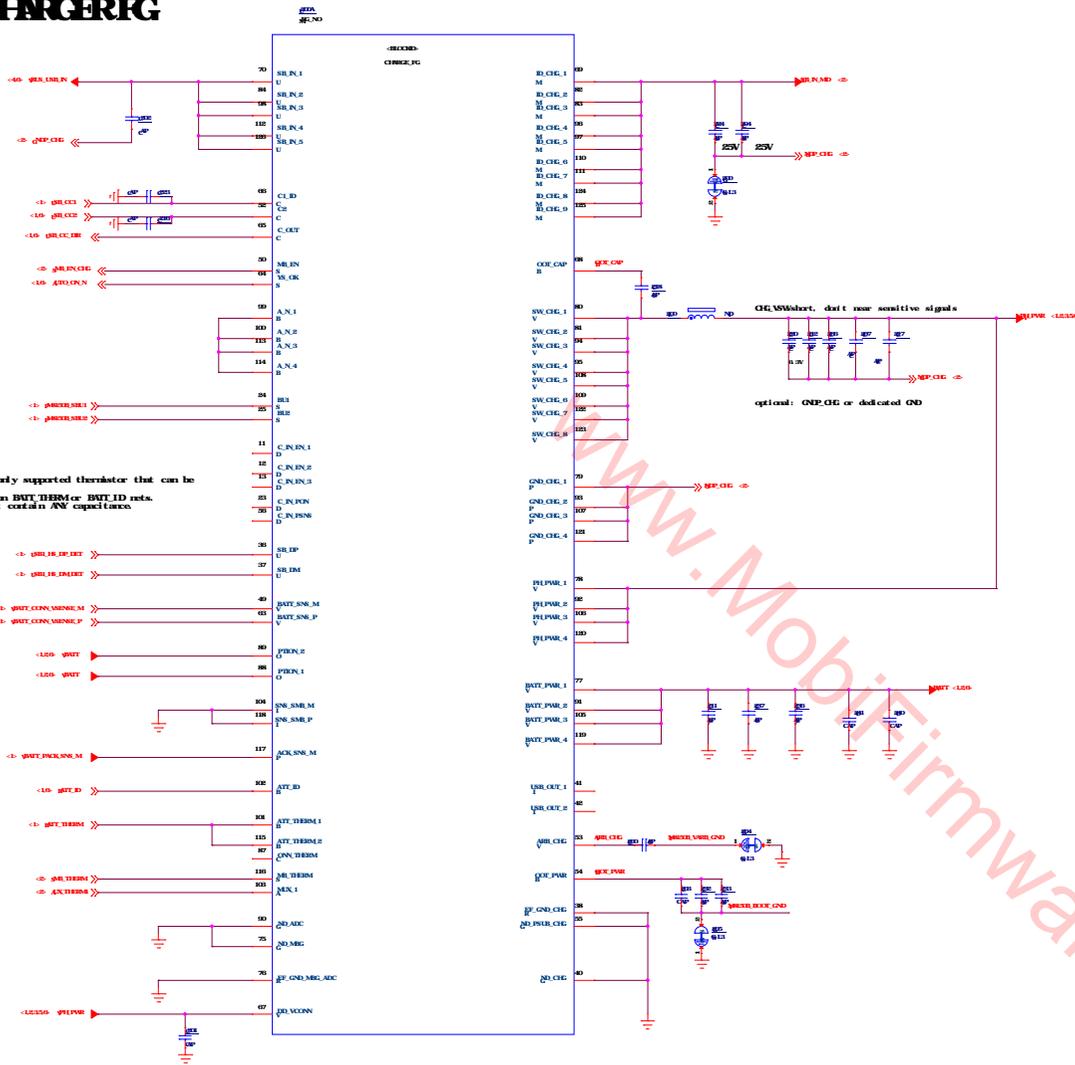
**USB\_BRANCH\_BIB**



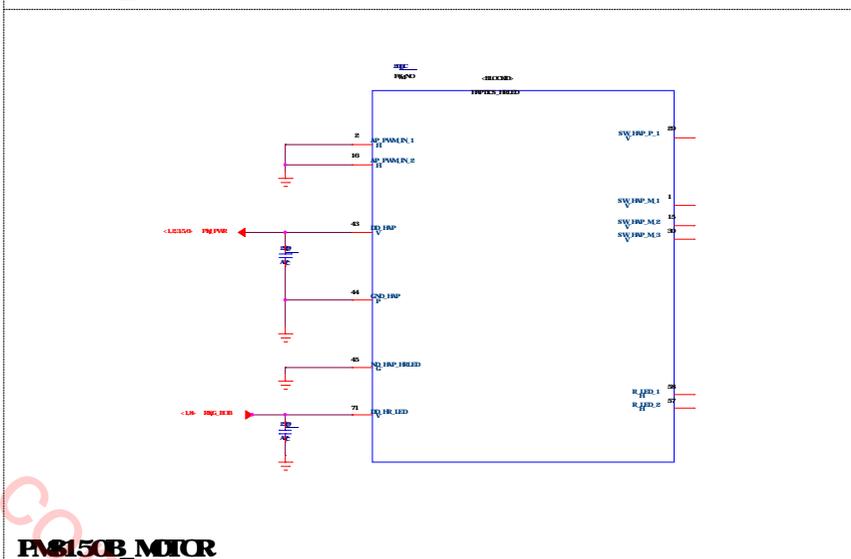
**OLED\_BRANCH\_BIB**

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Document Number		Sheet 1 of 10	
Title		Rev	
Author		Date	

# 02.PM150B.CHARGER.FG

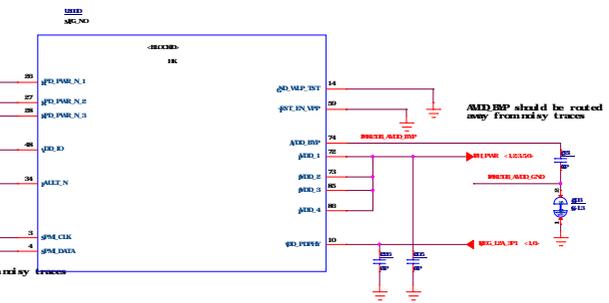


## SMI330\_CHG



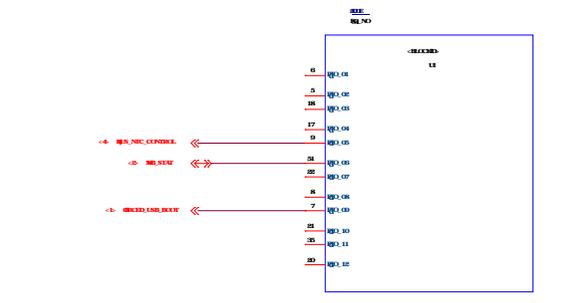
## PM150B\_MOTOR

# PM150B\_CHG

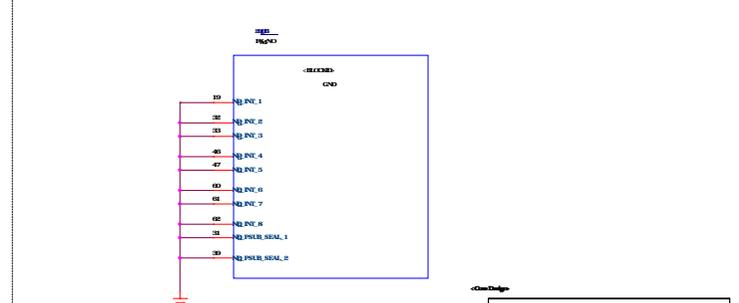


# PM150B\_SYS

Note: CHG11 of PM150B is for internal usage. Keep it float.



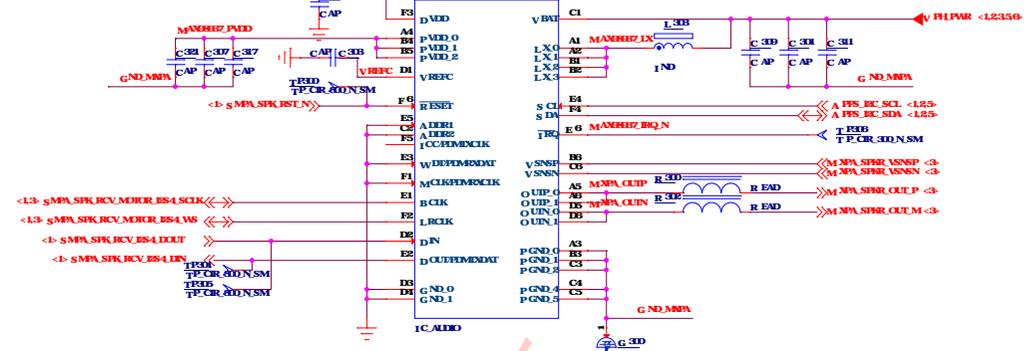
# PM150B\_GPIO



# PM150B\_GND

Pin	Signal	Notes
1	SW_FMP_P1	
2	SW_FMP_M1	
3	SW_FMP_M2	
4	SW_FMP_M3	
5	SW_FMP_M4	
6	R_HD1	
7	R_HD2	

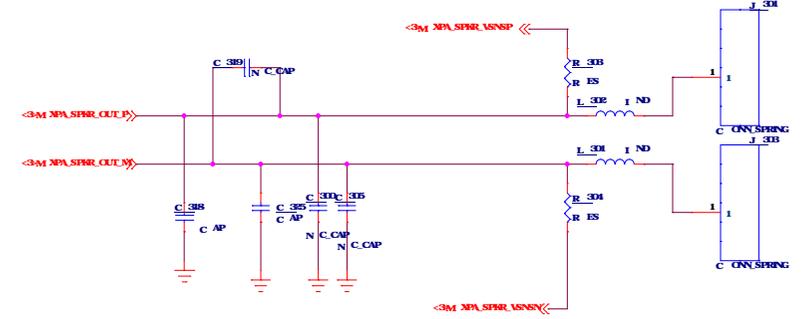
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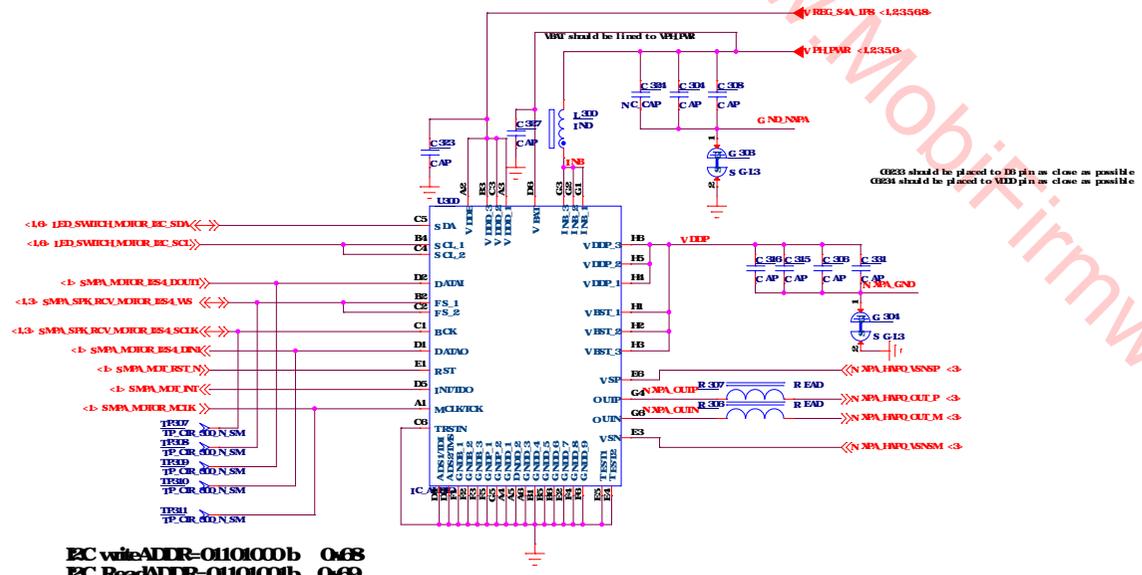
- <L3> SMA\_SPK\_RCV\_MCHOR\_BS4\_SCLK <<>
- <L3> SMA\_SPK\_RCV\_MCHOR\_BS4\_VS <<>
- <L3> SMA\_SPK\_RCV\_BS4\_DOUT <<>
- <L3> SMA\_SPK\_RCV\_BS4\_DIN <<>
- <L3> SMA\_SPK\_OUT\_M <<>
- <L3> SMA\_SPK\_OUT\_M <<>
- <L3> SMA\_SPK\_OUT\_M <<>

EC ADDR=0111010x  
 EC write ADDR=01110100b 0x74  
 EC read ADDR=01110101b 0x75

**SPEAKER SMART PA**



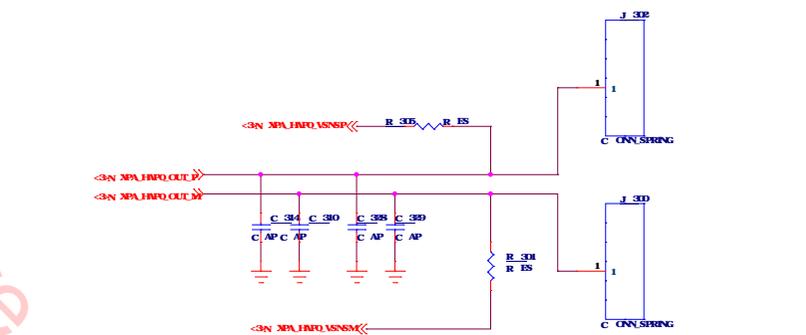
**SPEAKER**



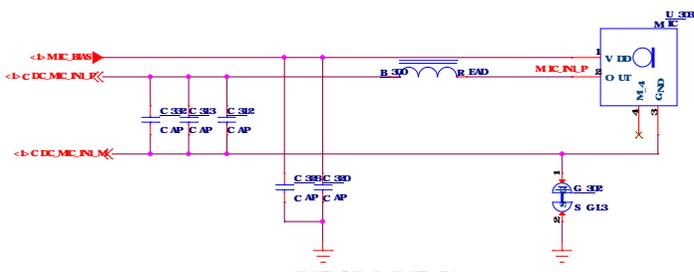
- <L6> LED\_SWITCH(MCHOR\_EC\_SDA) <<>
- <L6> LED\_SWITCH(MCHOR\_EC\_SCL) <<>
- <L3> SMA\_MCHOR\_BS4\_DOUT <<>
- <L3> SMA\_SPK\_RCV\_MCHOR\_BS4\_VS <<>
- <L3> SMA\_SPK\_RCV\_MCHOR\_BS4\_SCLK <<>
- <L3> SMA\_MCHOR\_BS4\_DIN <<>
- <L3> SMA\_MCHOUT <<>
- <L3> SMA\_MCHOUT <<>

EC write ADDR=01101000b 0x68  
 EC Read ADDR=01101001b 0x69

**VIBRATOR SMART PA**



**VIBRATOR**

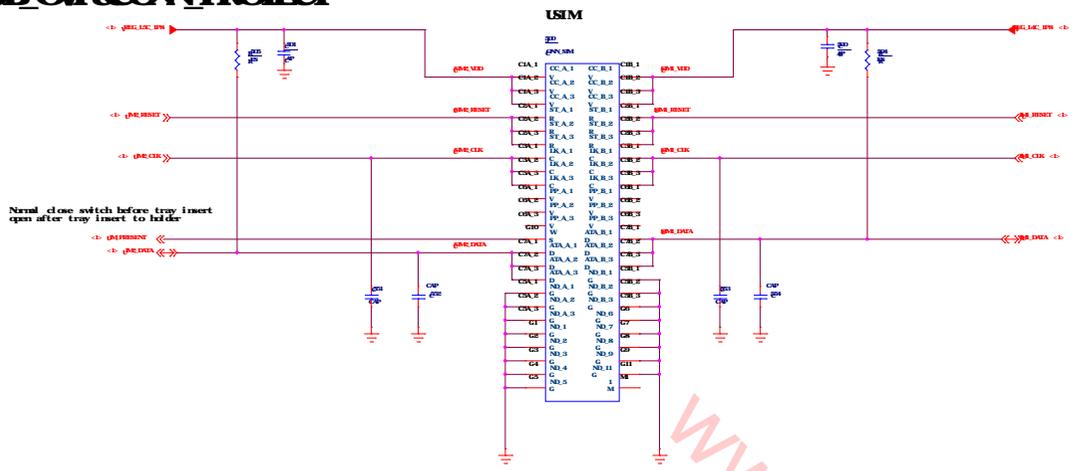


**MAN MIC**

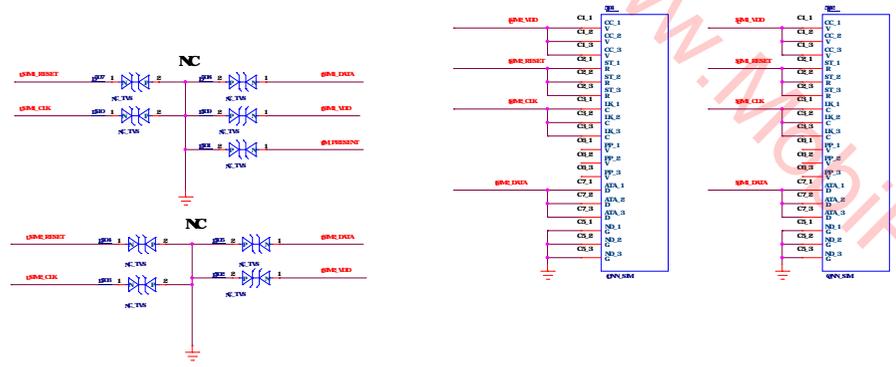
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Date:	2014/05/25 10:00	Sheet:	3 of 30



# OBUS2\_OBP&CON\_PROJECT

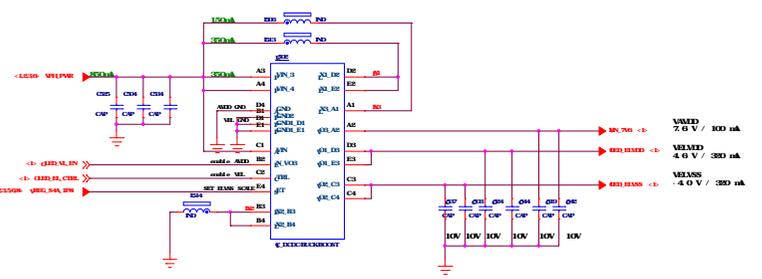


Reserved for NFC

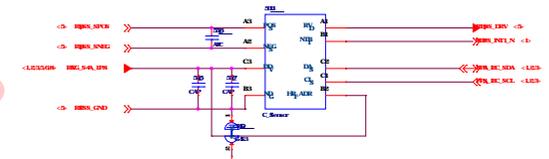


## SIM CARD

## NFC



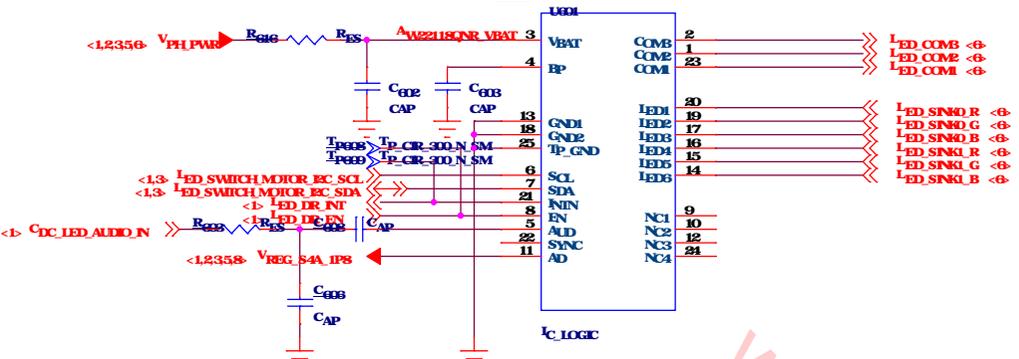
## CLED BOOST



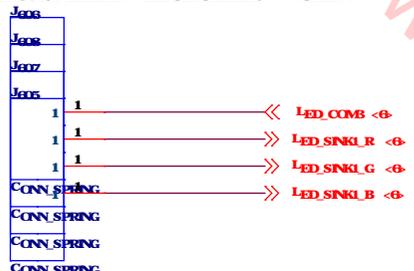
## PRESS SENSOR

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Doc Name	OBUS2_OBP&CON_PROJECT
Doc Date	2023-08-01
Doc Ver	1.0
Doc Rev	1.0
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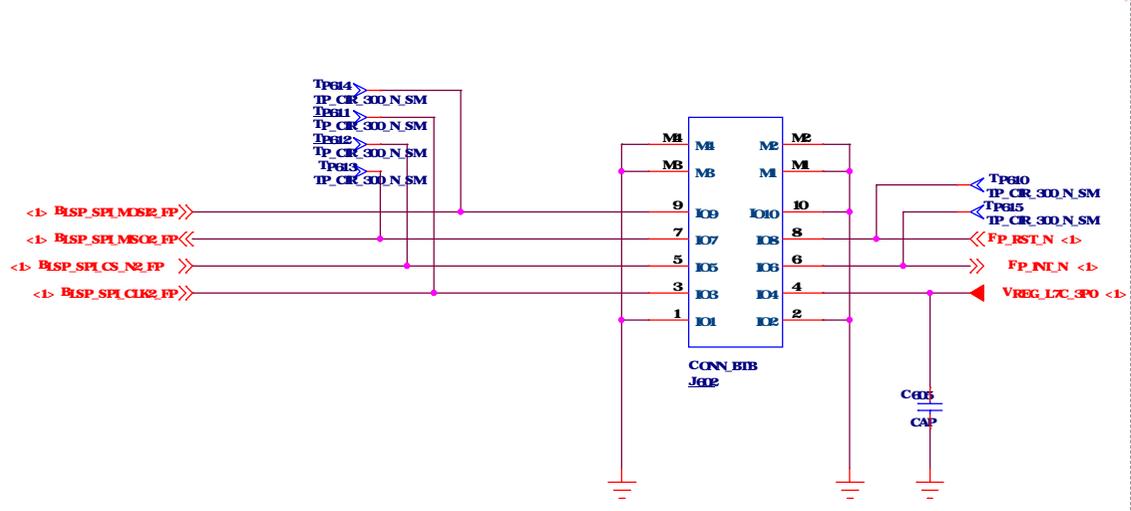
# LED IC DRIVER TP



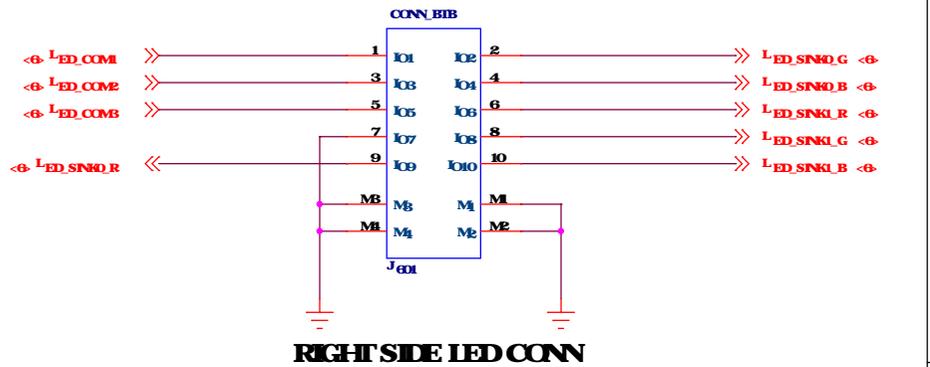
**IC Address: 0x6B**  
**IC write ADDR= 11010110b 0xD6**  
**IC Read ADDR= 11010111b 0xD7**



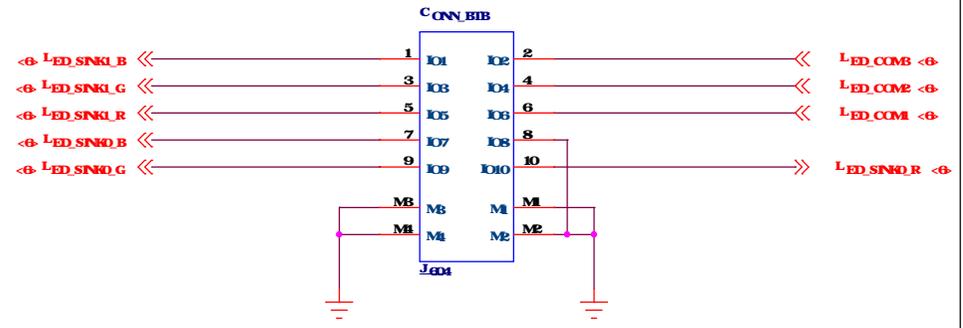
# LED IC Driver Logic LED SPRING



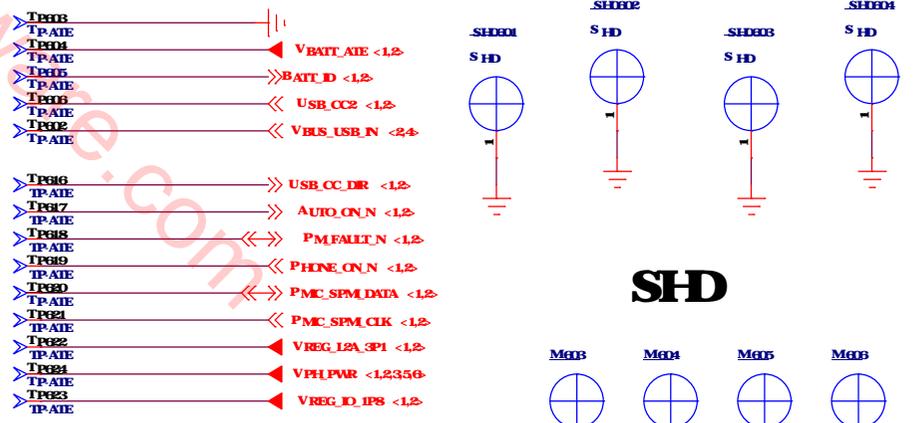
# UEP CON



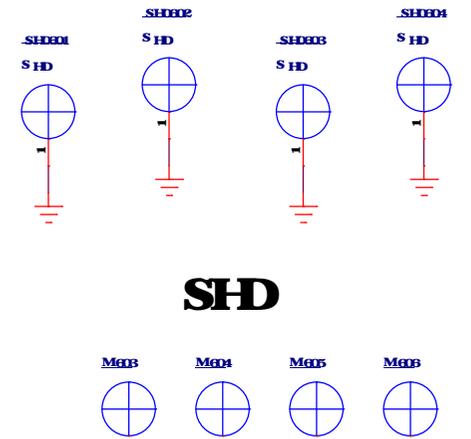
**RIGHT SIDE LED CONN**



**LEFT SIDE LED CONN**



# AIE\_TP



# SHD

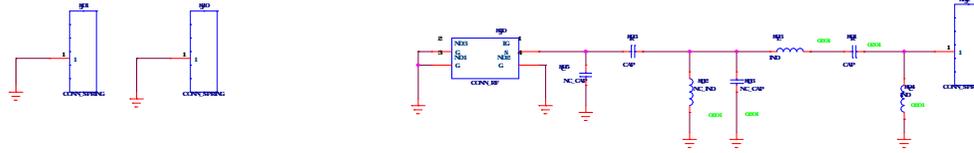
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Date Friday, January 25, 2019	Sheet 6 of 10	Rev <Rev Code>

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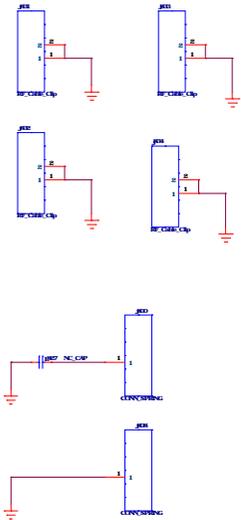
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Date:	Friday, January 25, 2019	Sheet	7 of 10

# Q8 Antenna Circuit

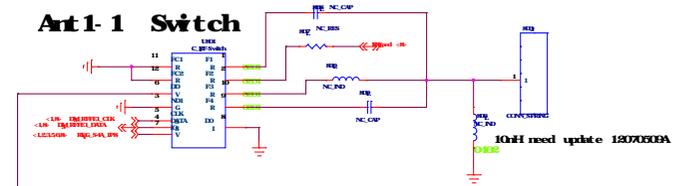
## Ant3 B3/1/39/41-Rx



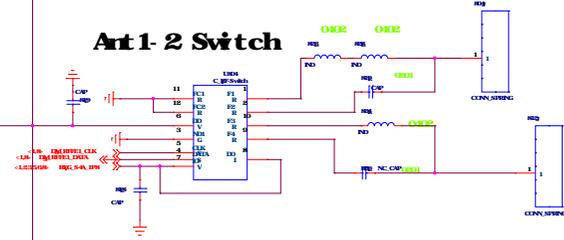
## Coaxial Cable



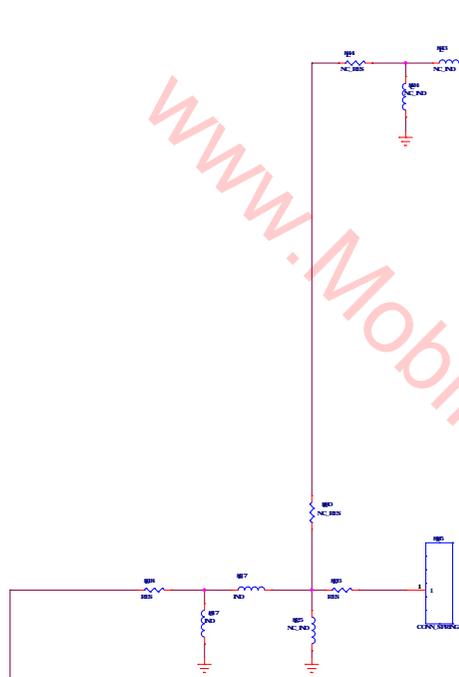
## Ant1-1 Switch



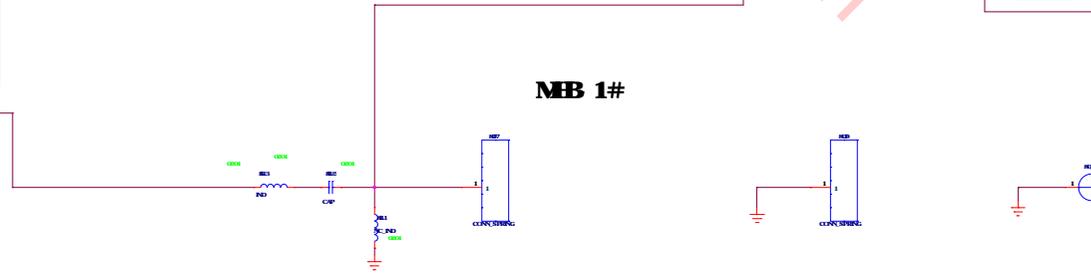
## Ant1-2 Switch



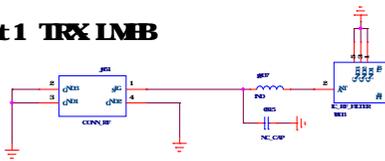
## IB 1#



## NB 1#



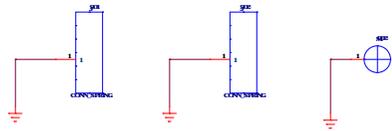
## Ant 1 TR& LMB



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# Antenna Grounding

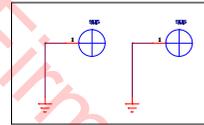
## Bottom



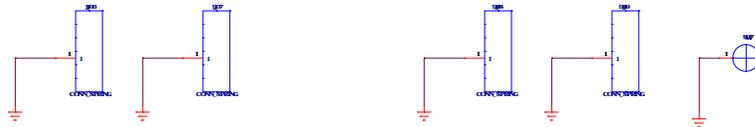
## Top



## Type C Grounding



## Ant 1- LB Switch Top Side



## Revision History

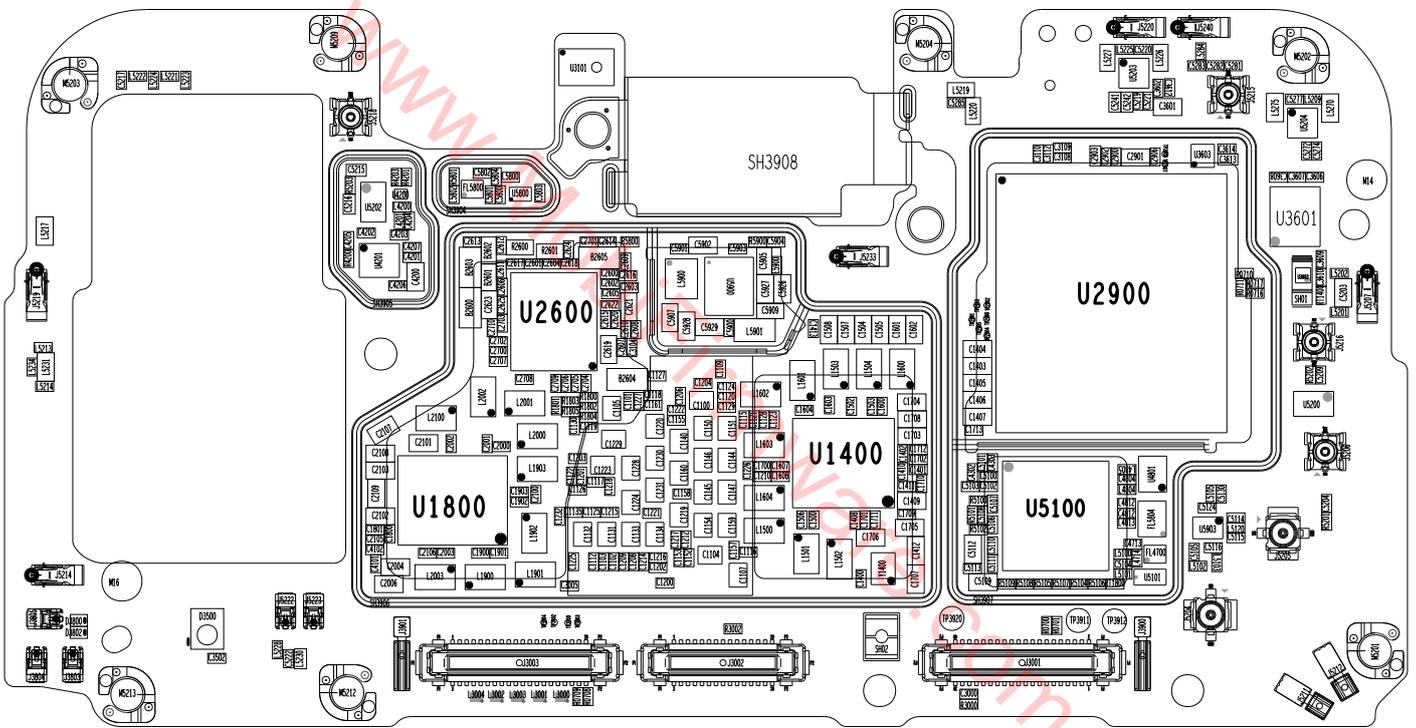
REV	DESCRIPTION
V0	First release

## Board Change History

SCHEMATIC MIN	HWVERSION	REVISION	DESCRIPTION OF CHANGE



ART FILM - 32\_REF\_BOTTOM



ART FILM - 32\_REF\_BOTTOM