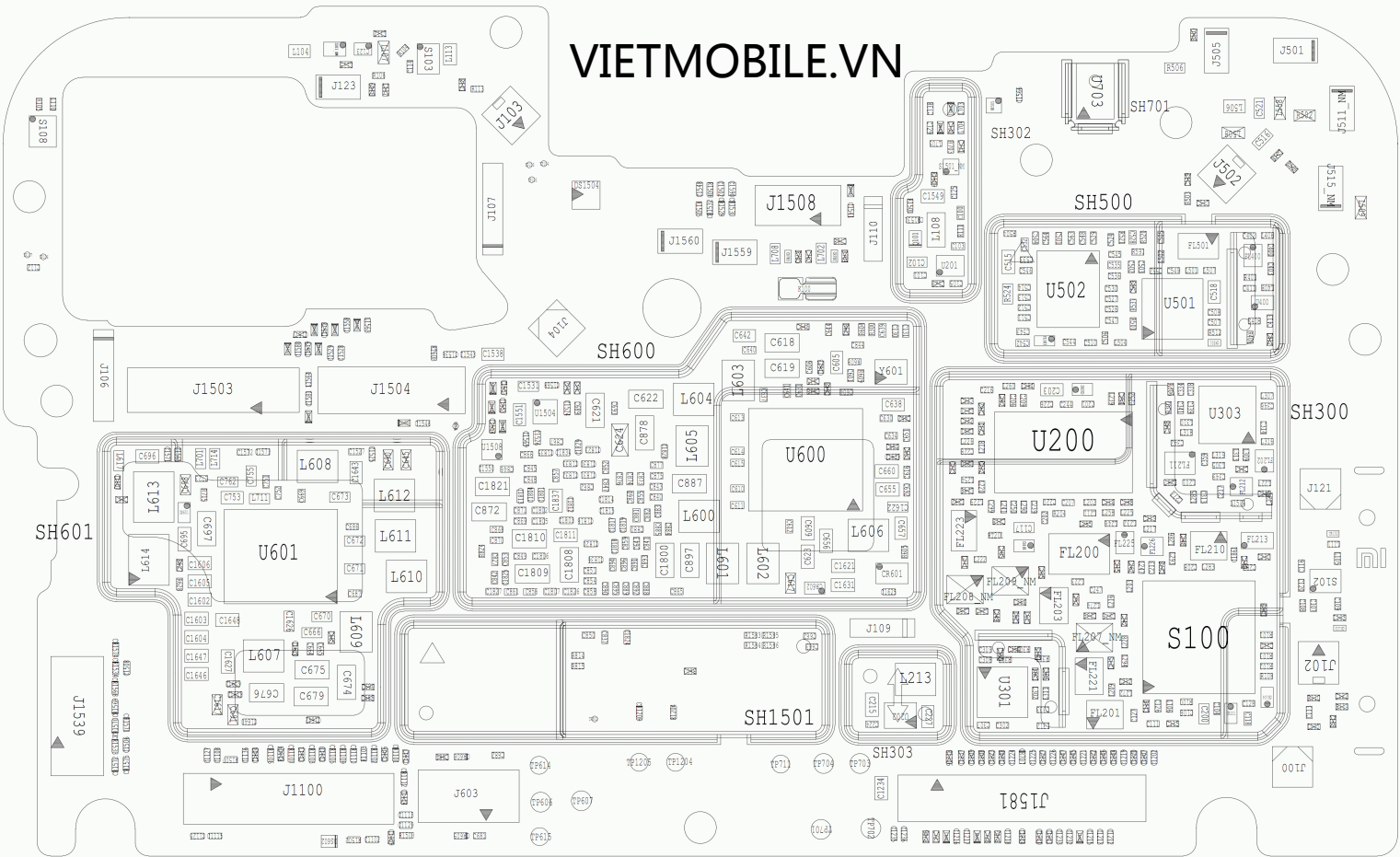
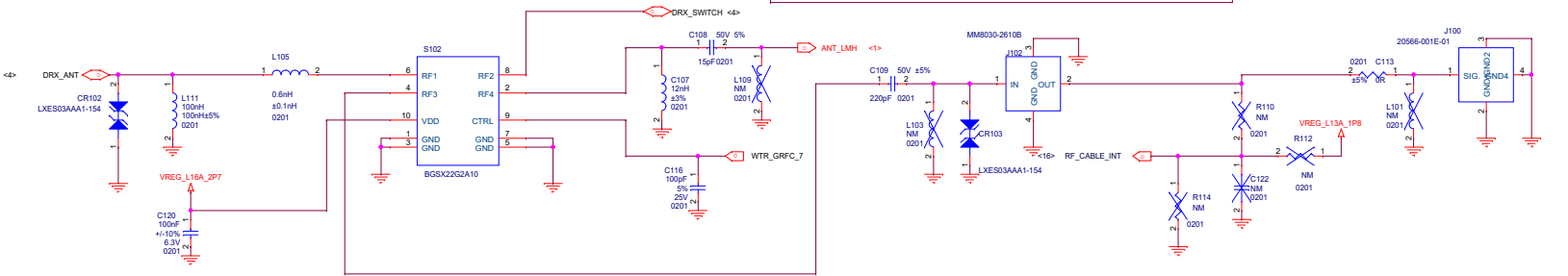
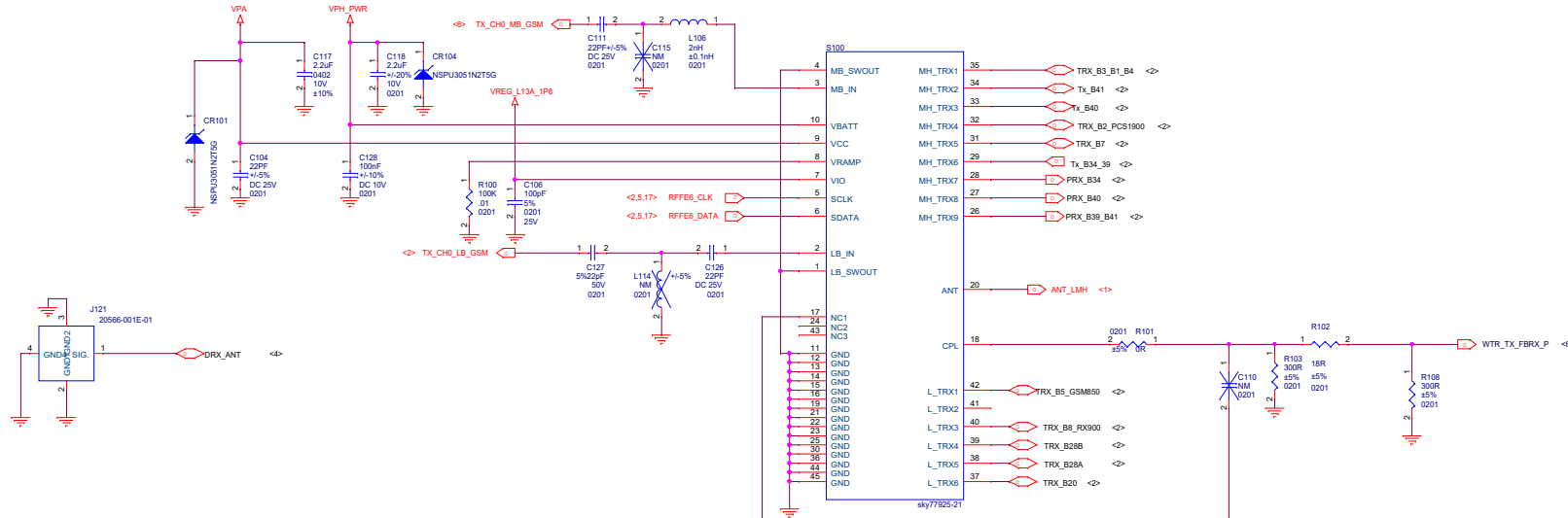
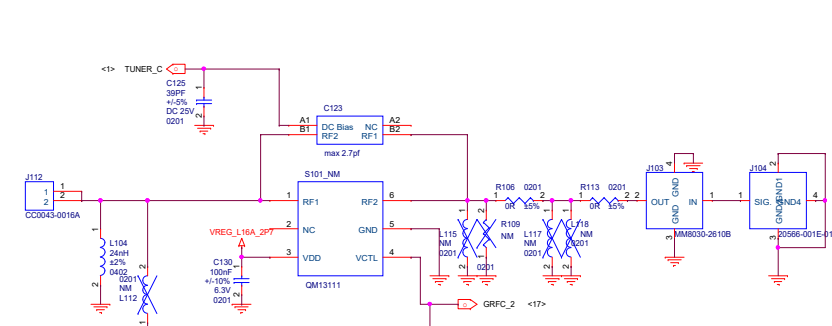


VIETMOBILE.VN

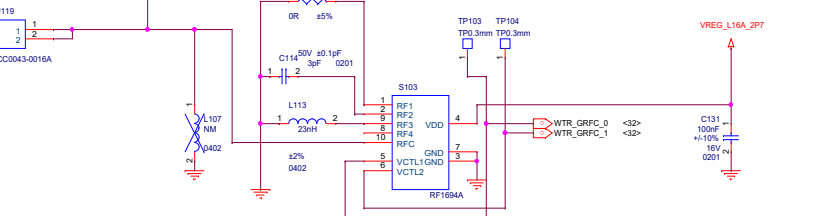




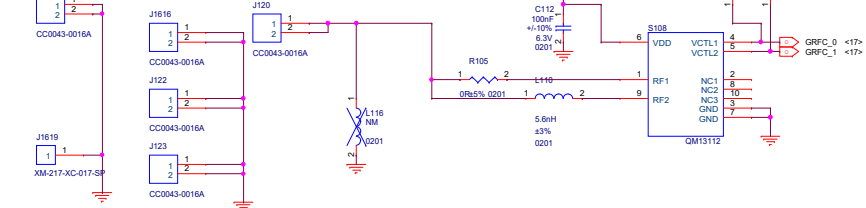
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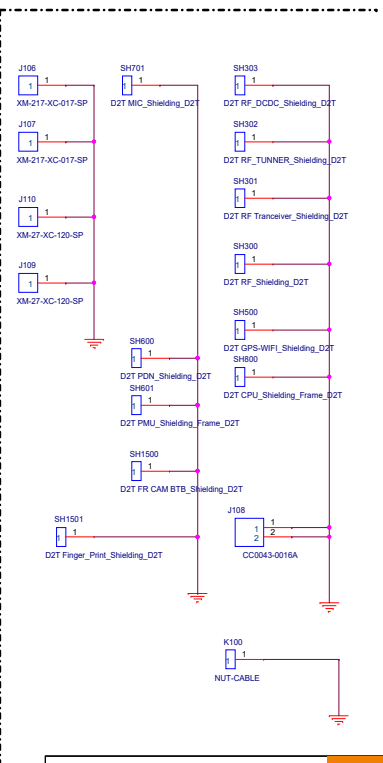
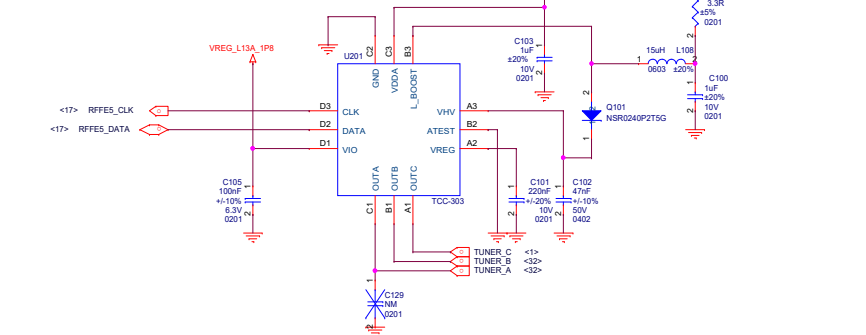
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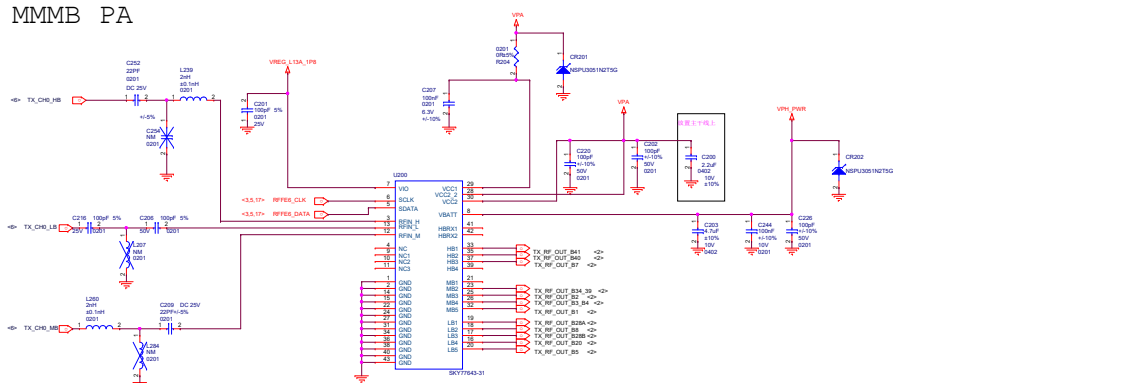


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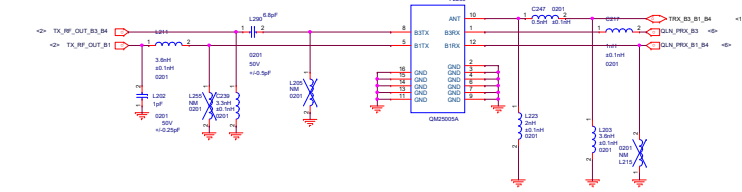


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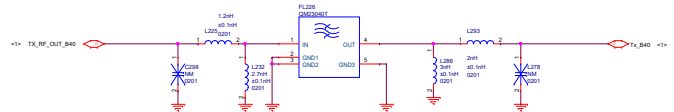




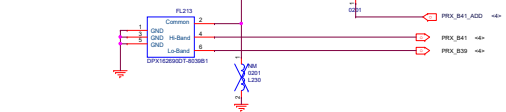
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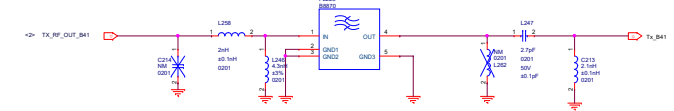
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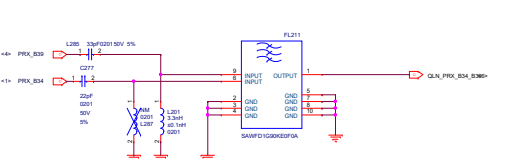
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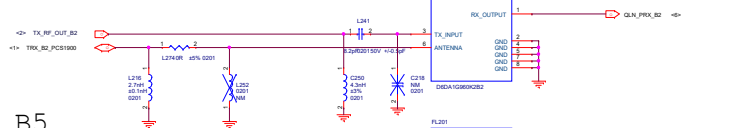
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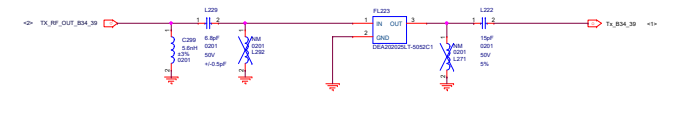
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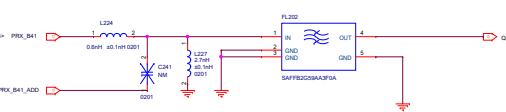
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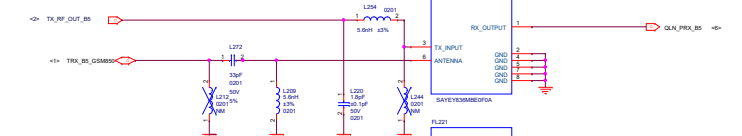
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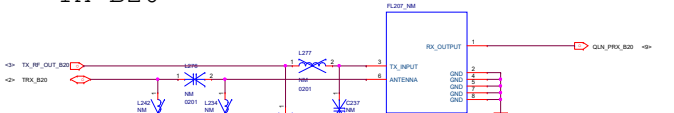
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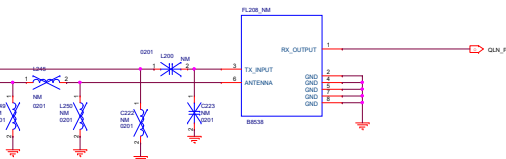
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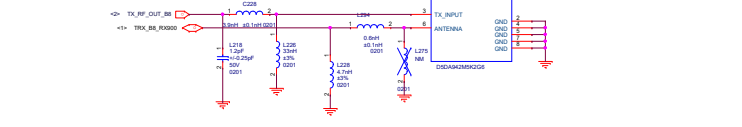
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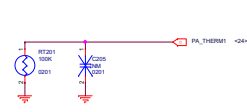
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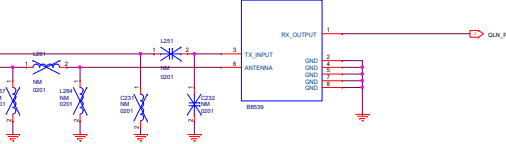
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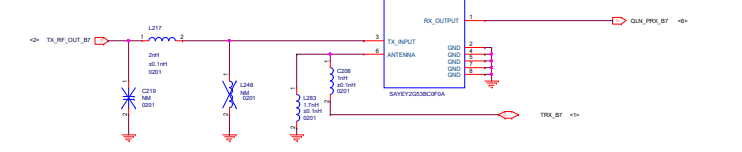
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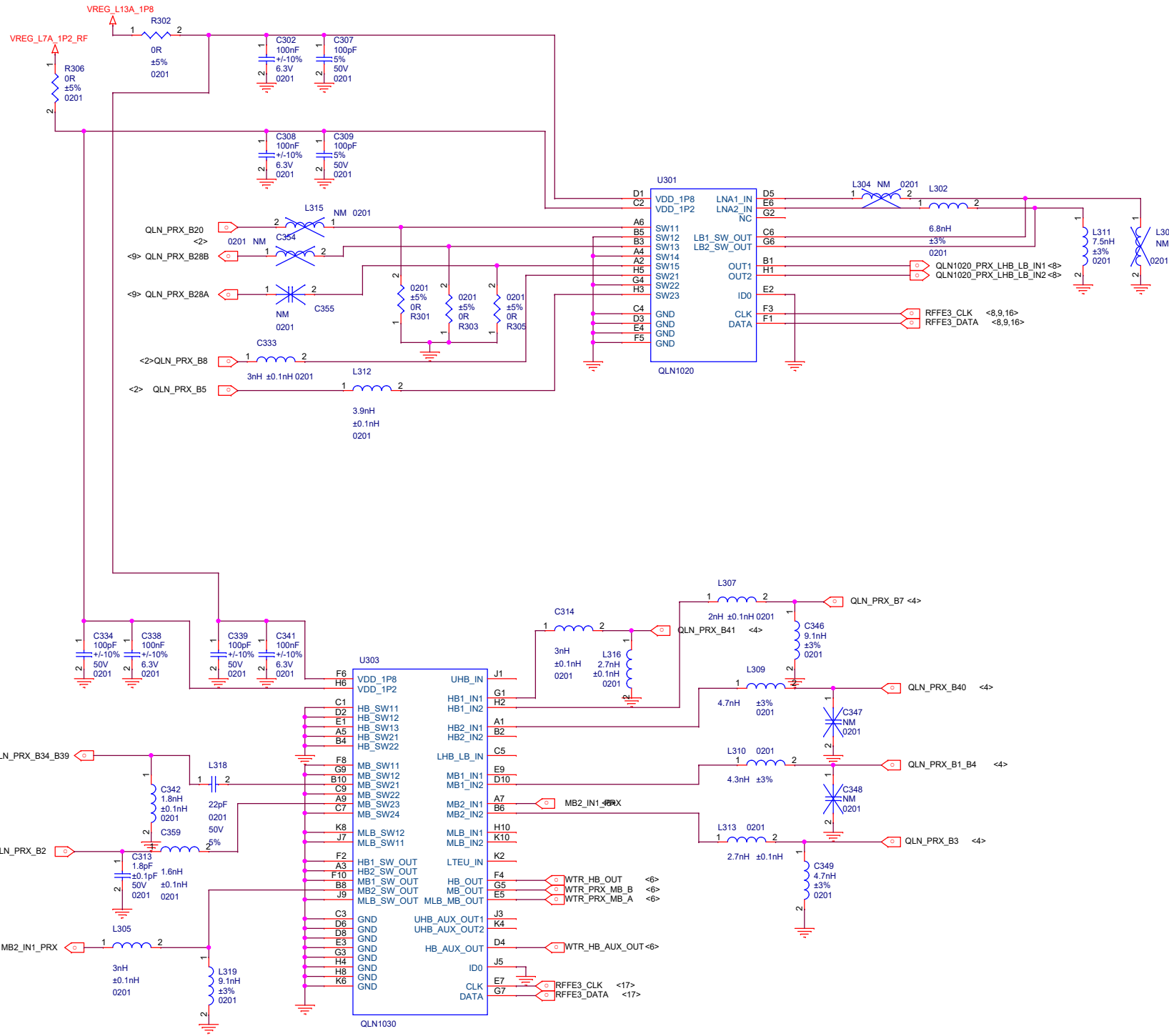



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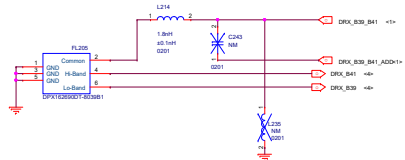
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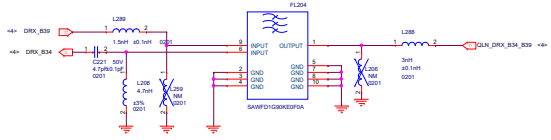


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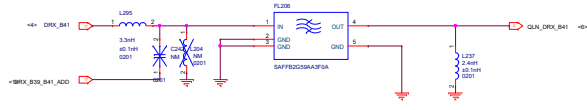
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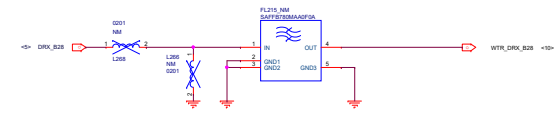
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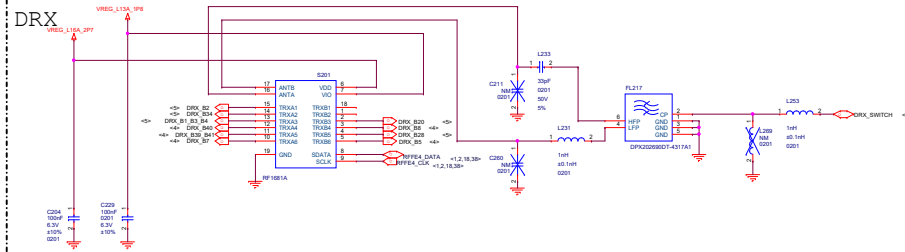
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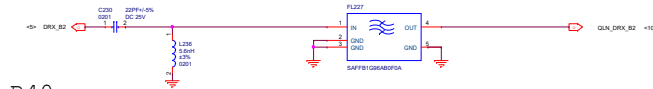
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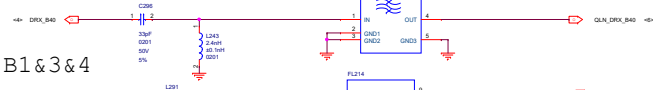
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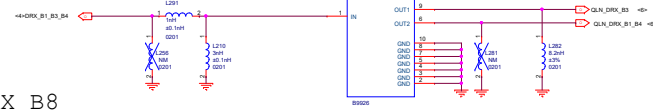
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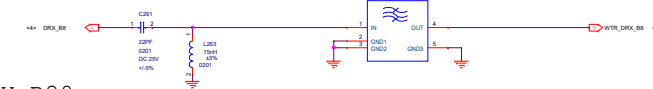
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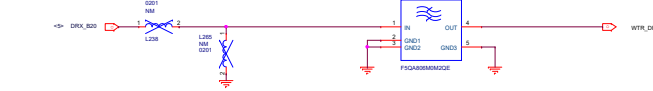
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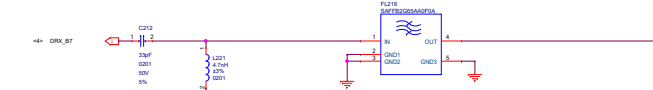
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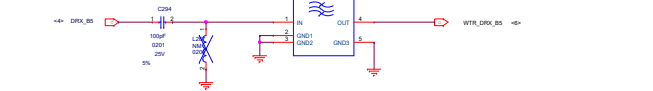
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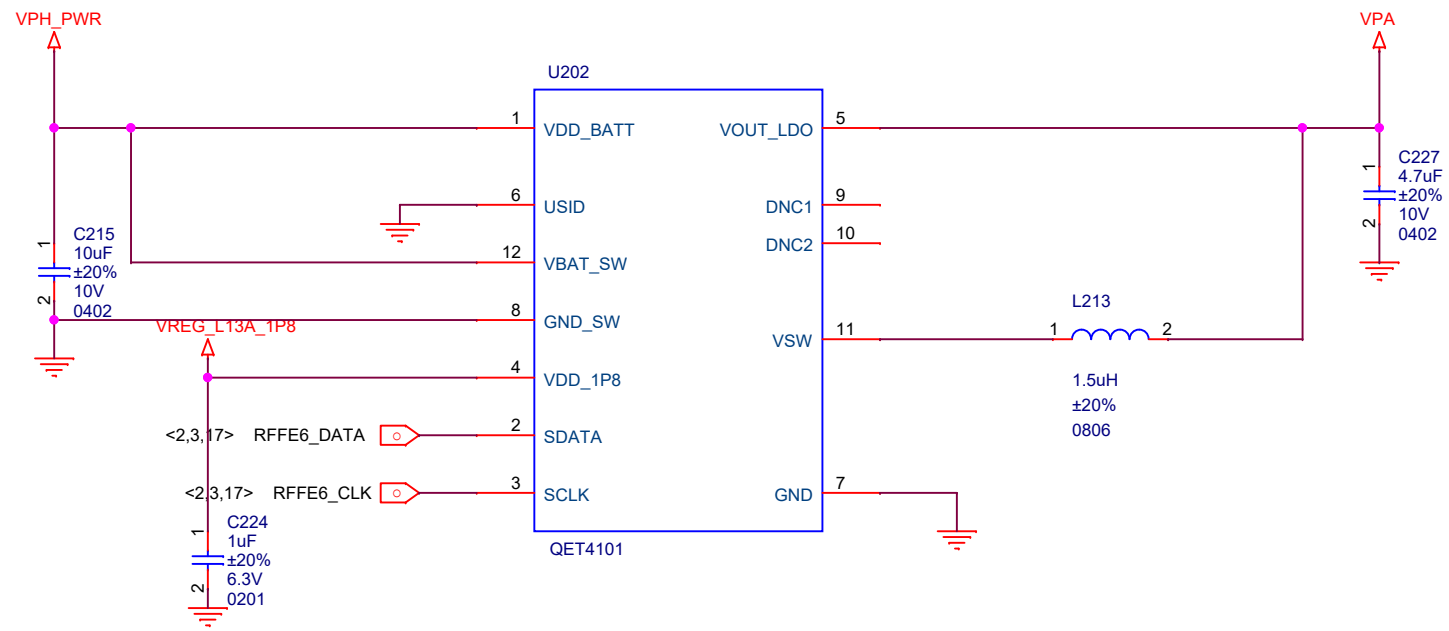


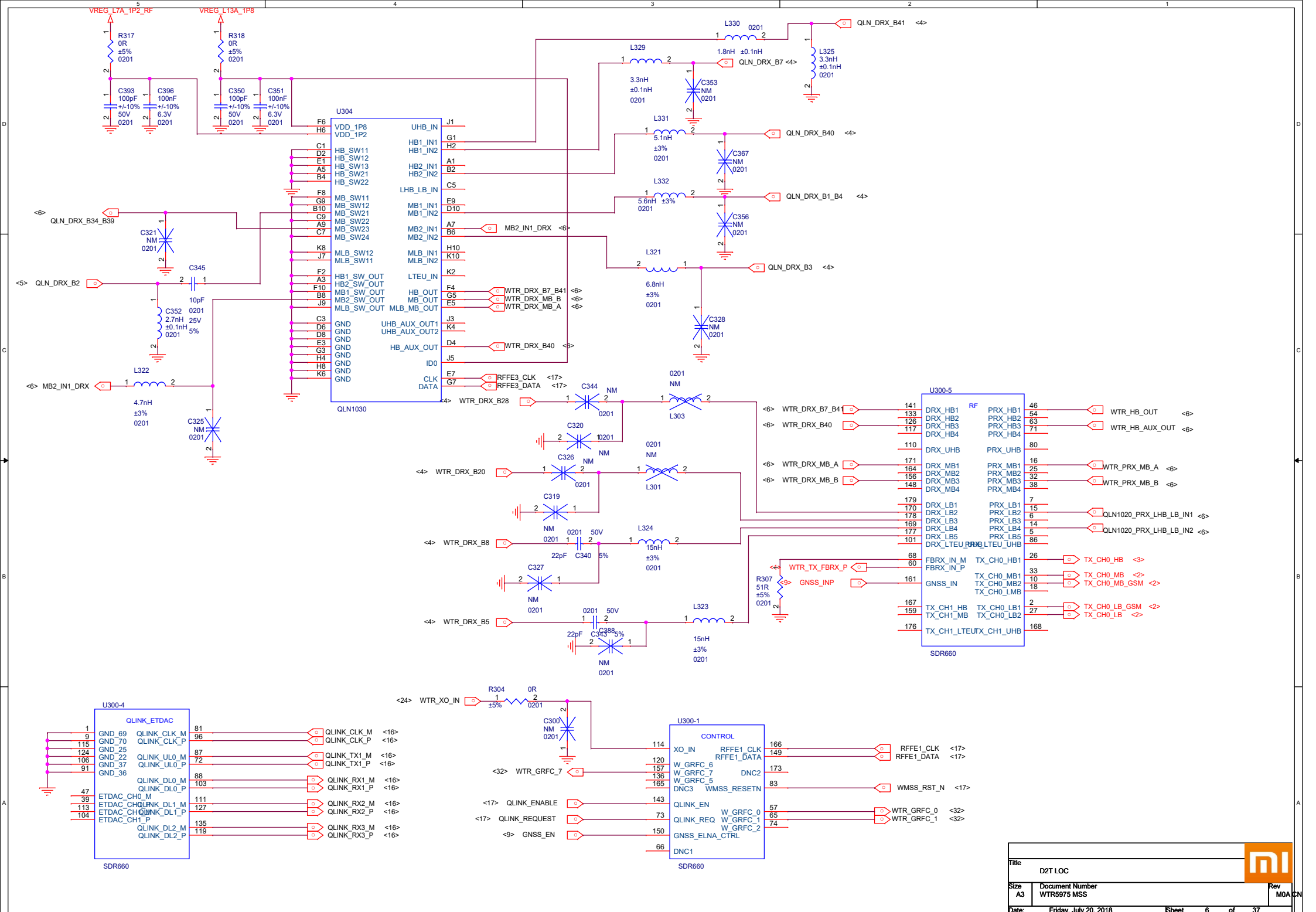
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DRX B5



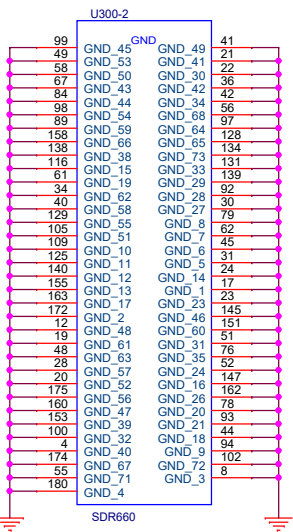
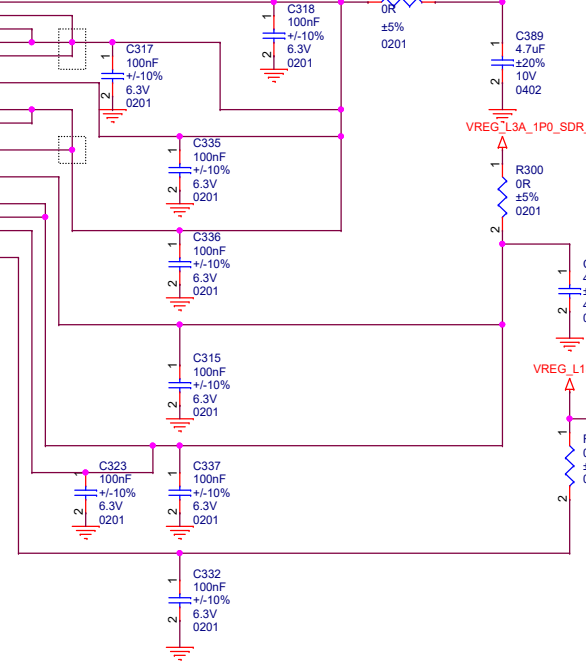
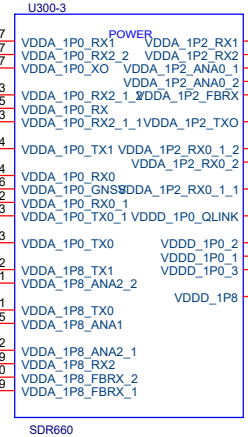
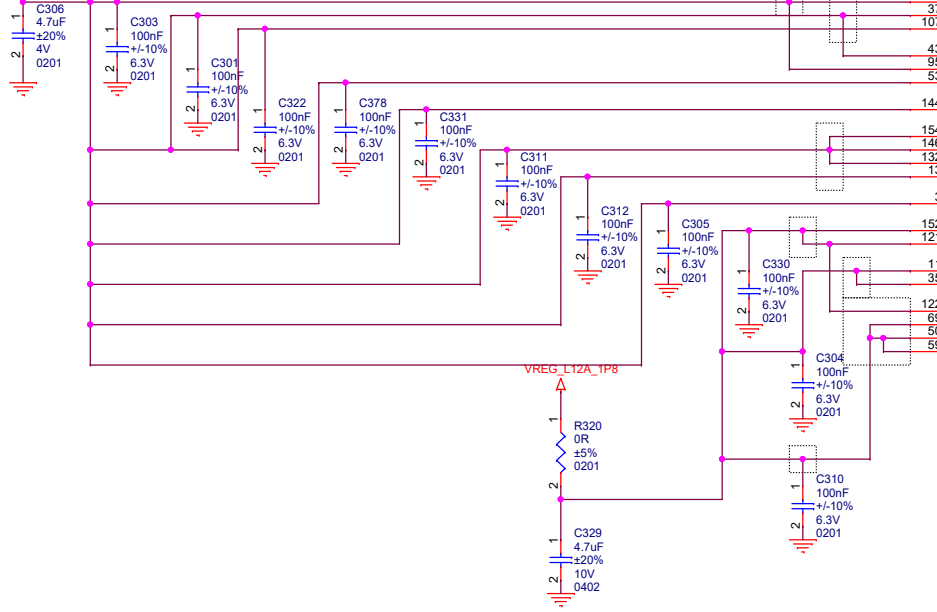


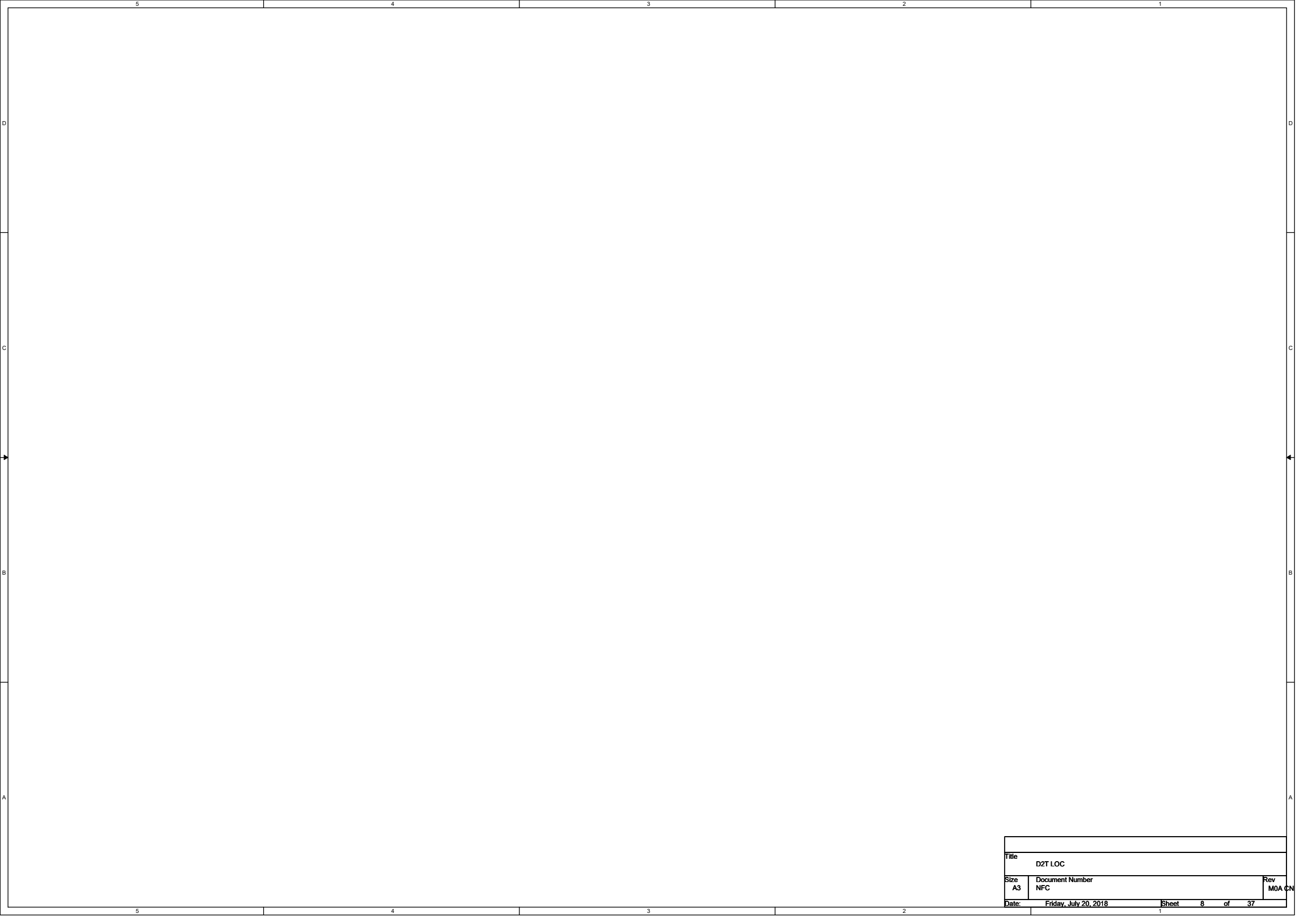


VREG_L2A_1P0_SDR_AVDD

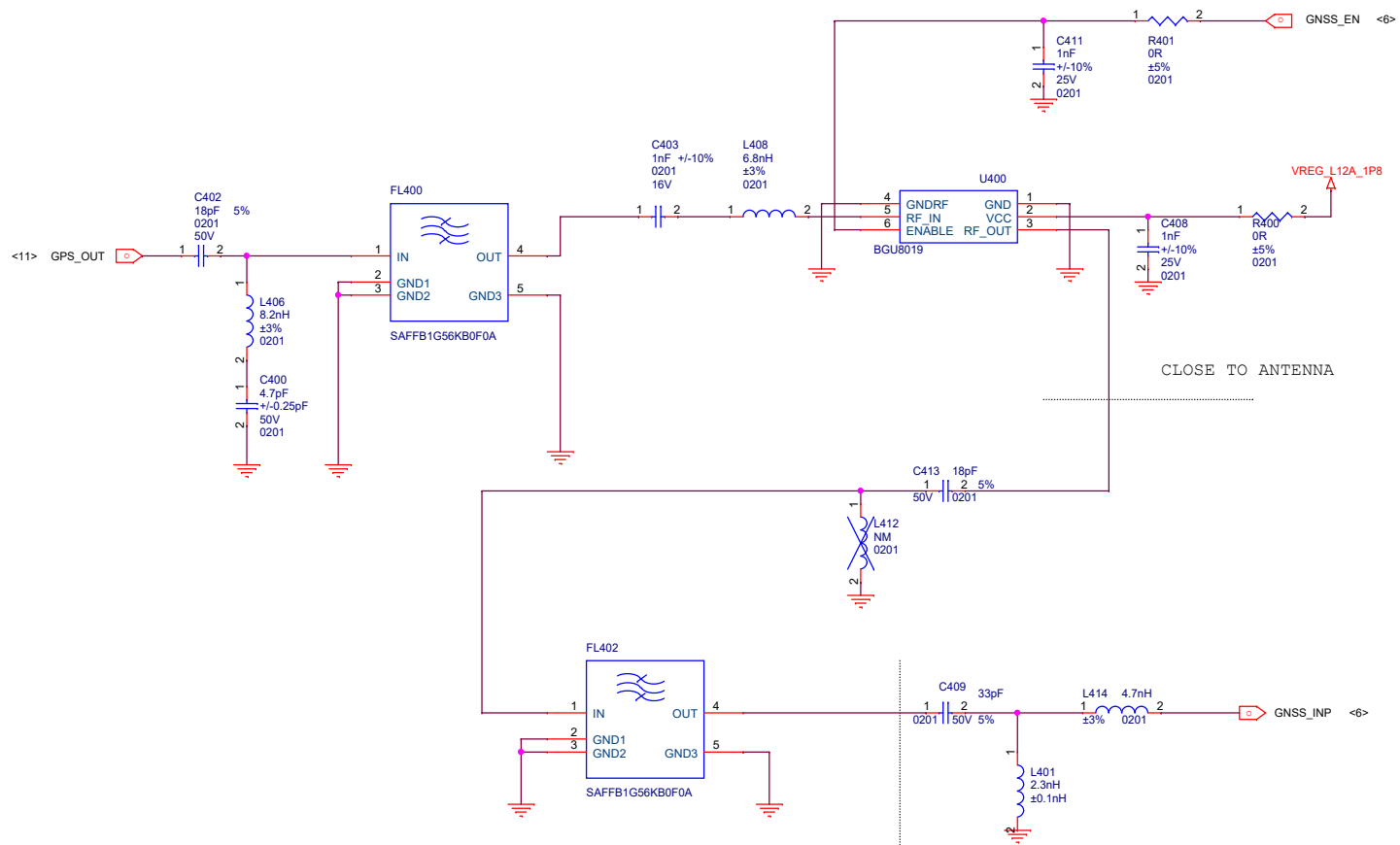


CAD NOTE: Dotted box represent SDR660 power supply star routing

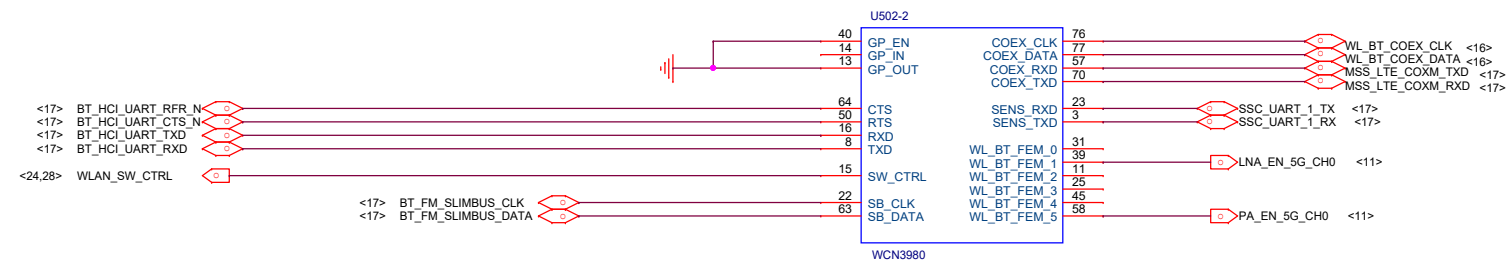
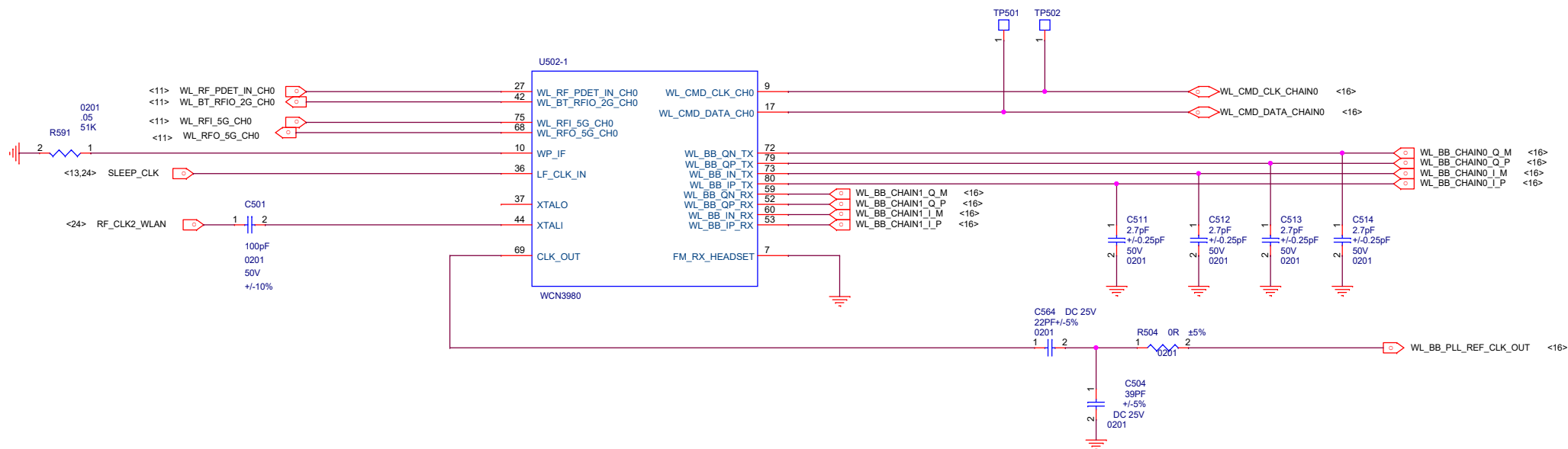




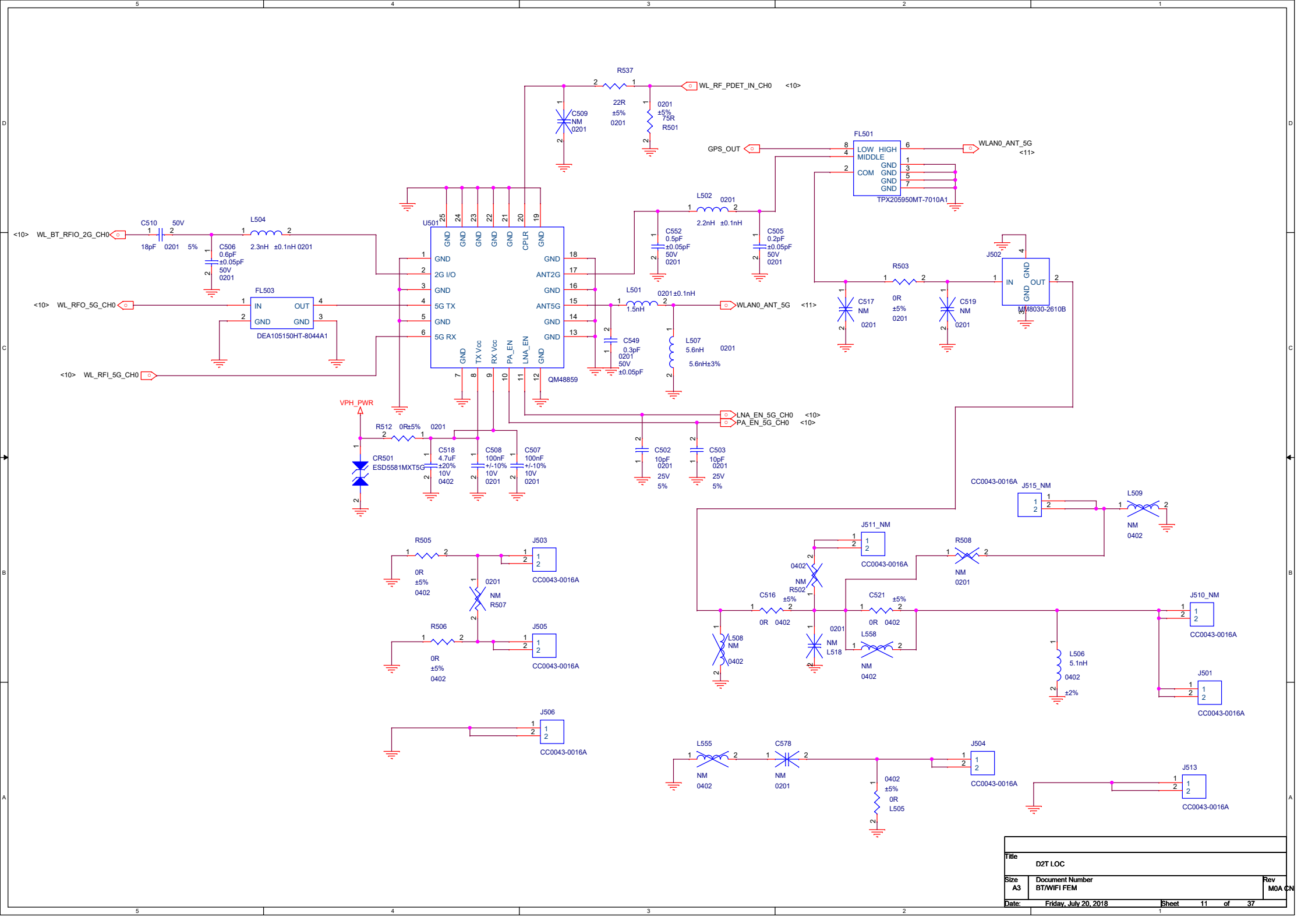
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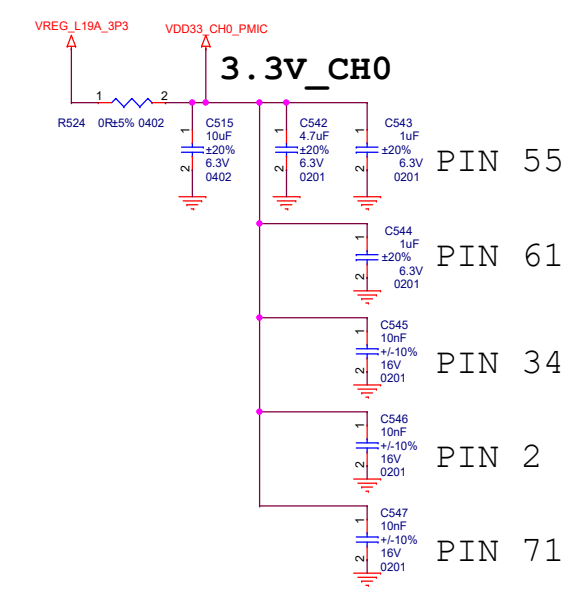
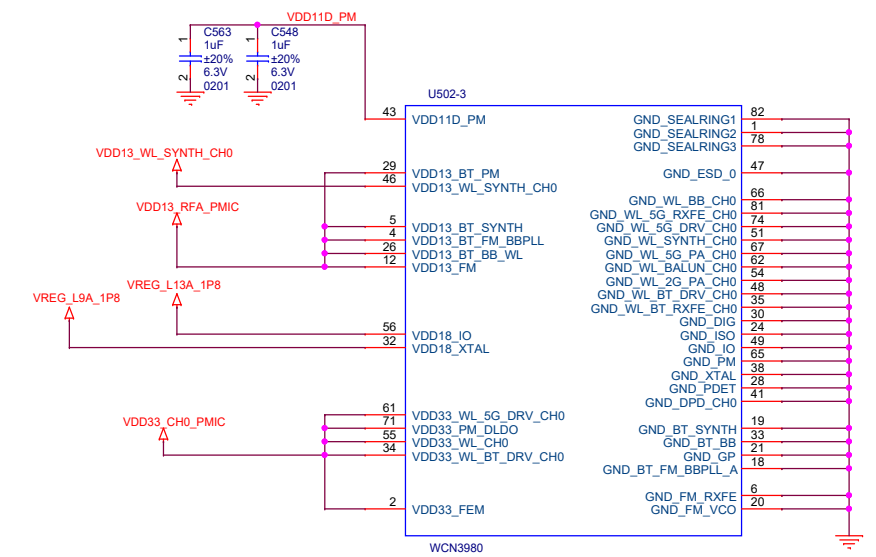
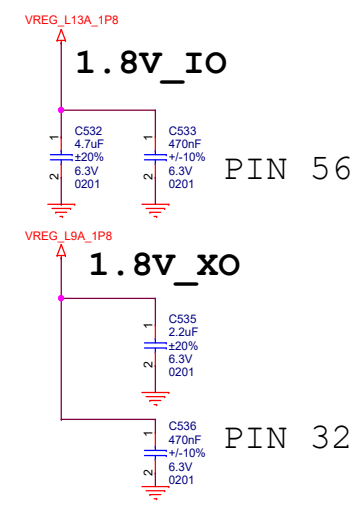
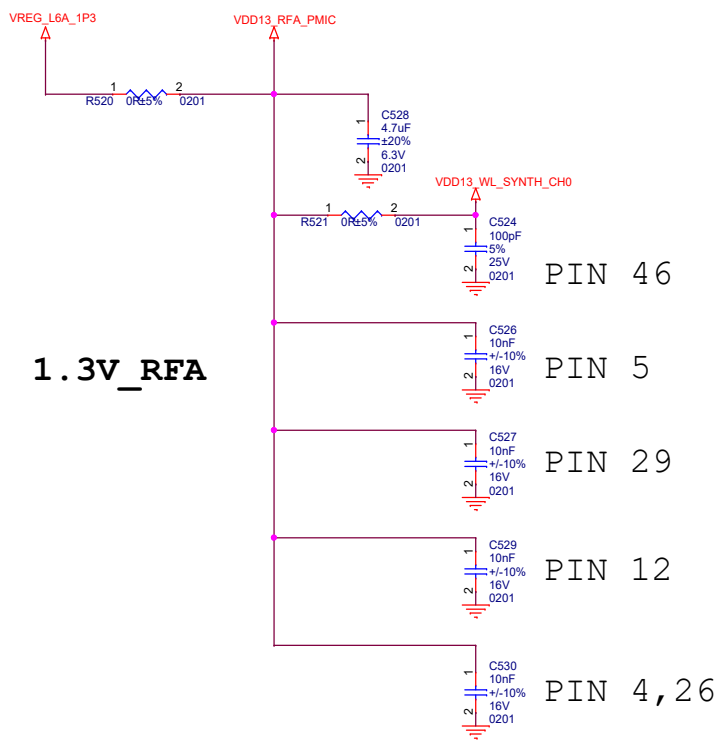
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Size	Document Number	Rev
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Size	Document Number	Rev
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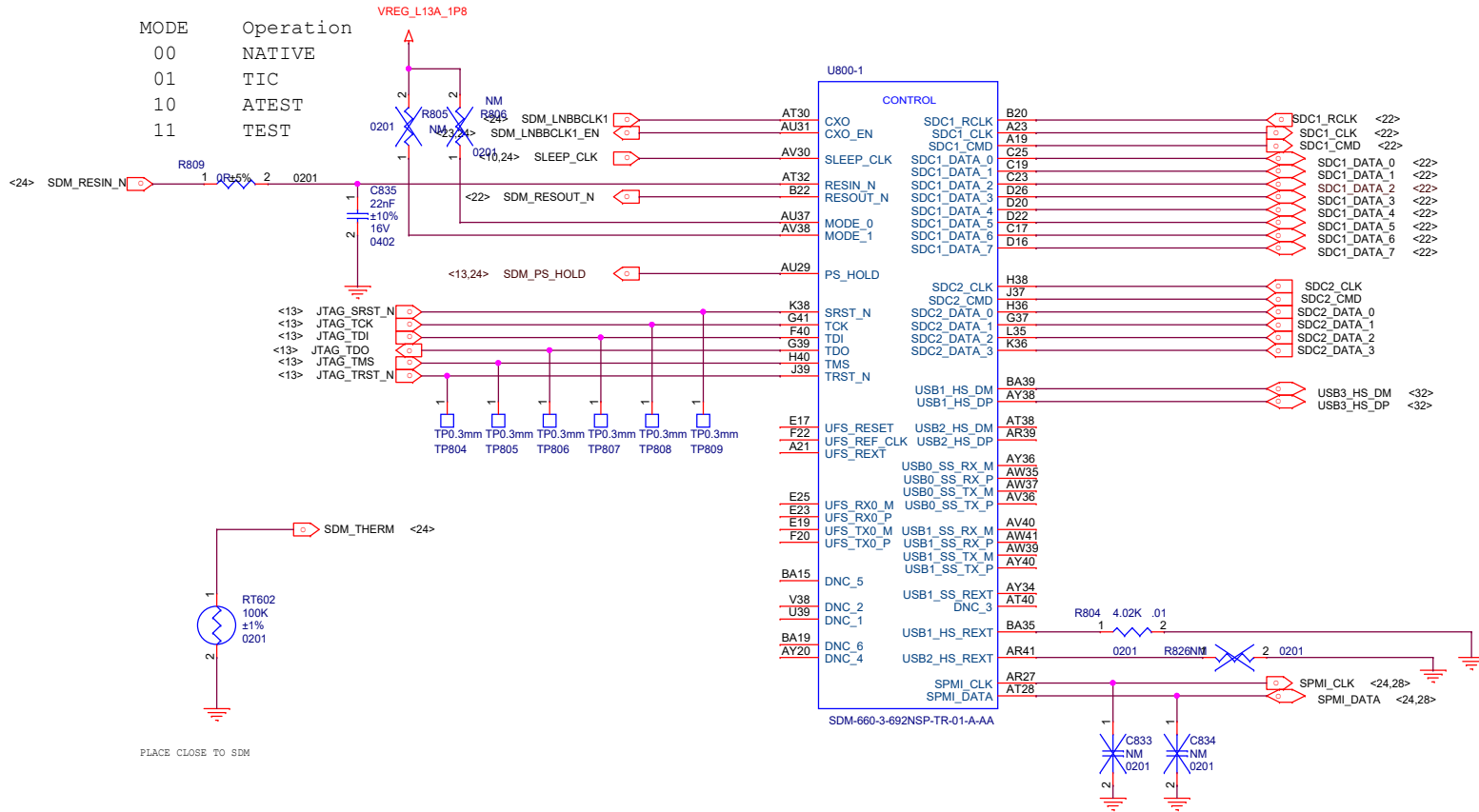


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Size	Document Number	Rev
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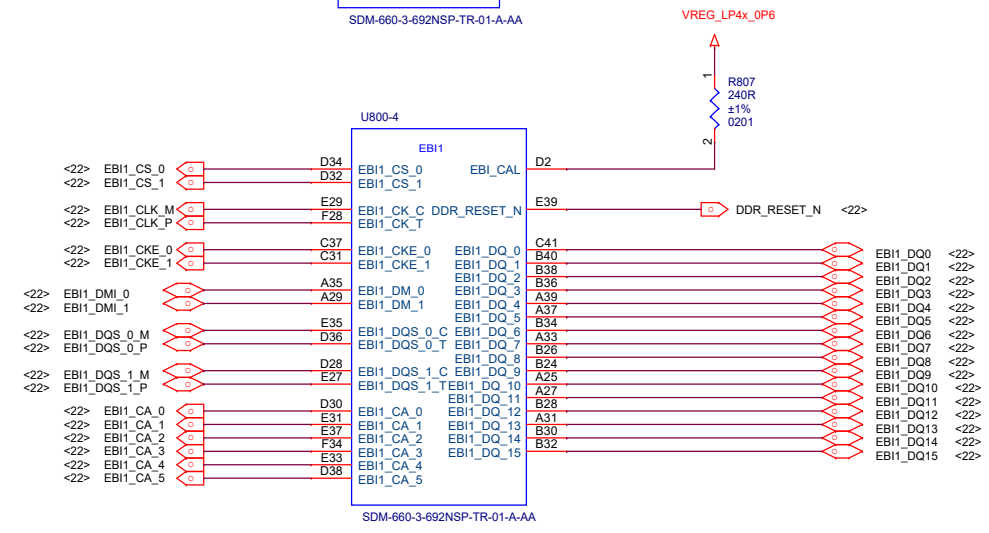
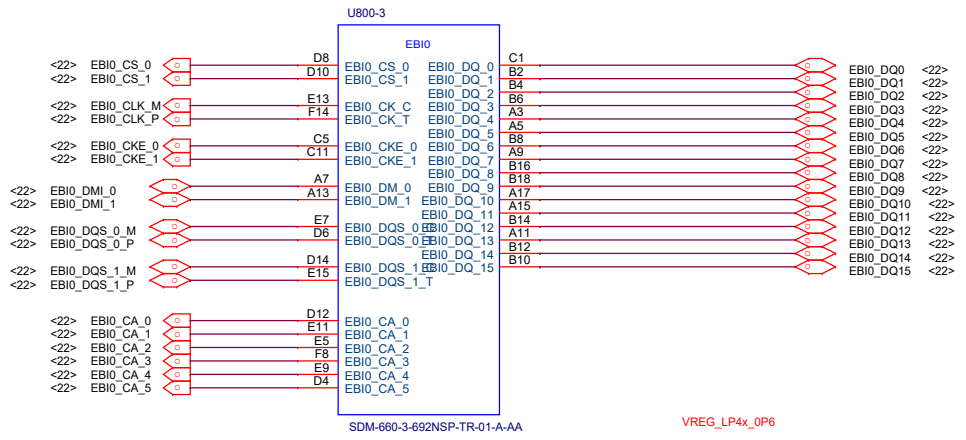
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MODE	Operation
00	NATIVE
01	TIC
10	ATEST
11	TEST



PLACE CLOSE TO SDM

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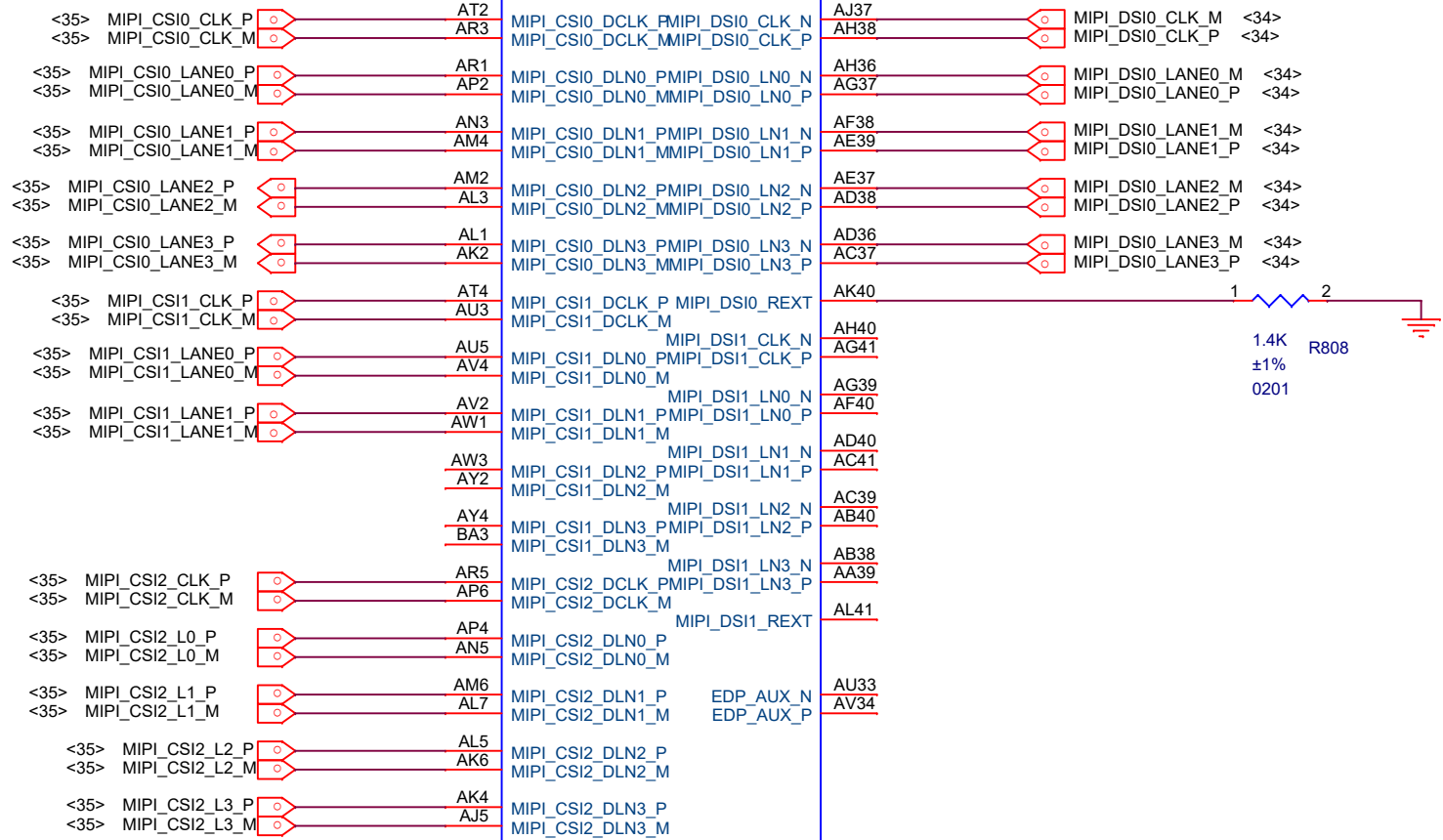
REAR CAMERA

AUX CAMERA

FRONT CAMERA

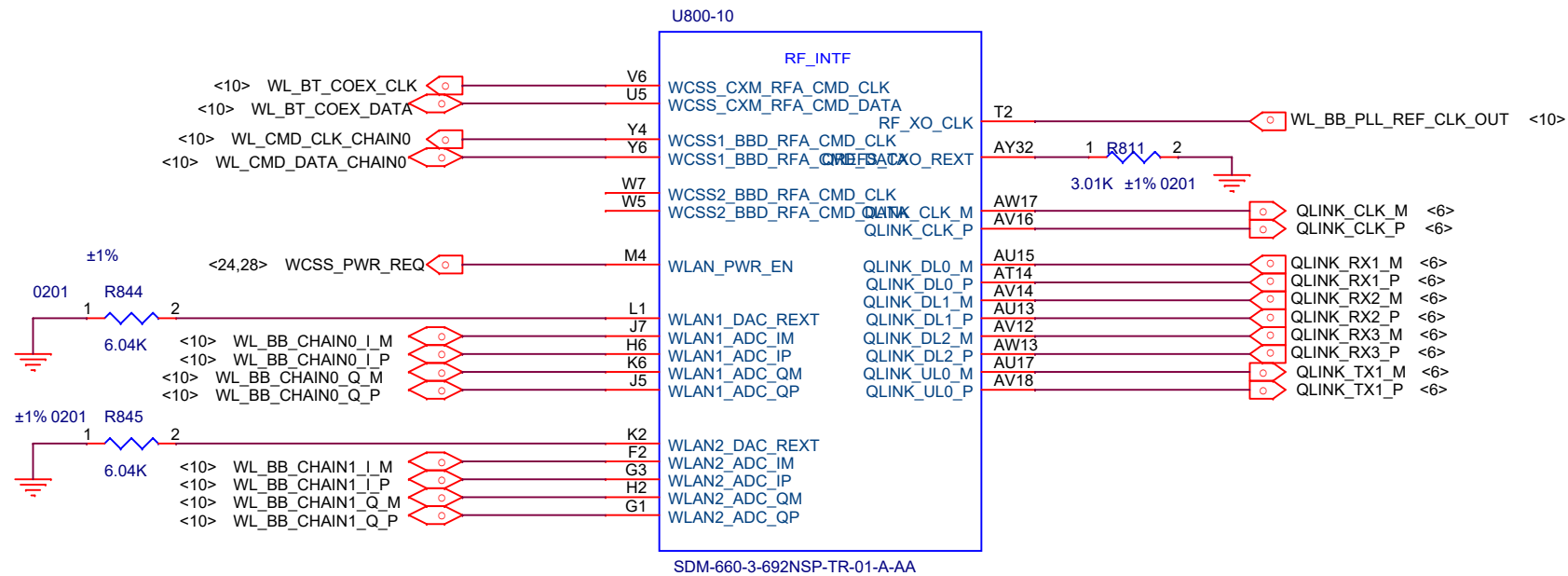
U800-7

MIPI

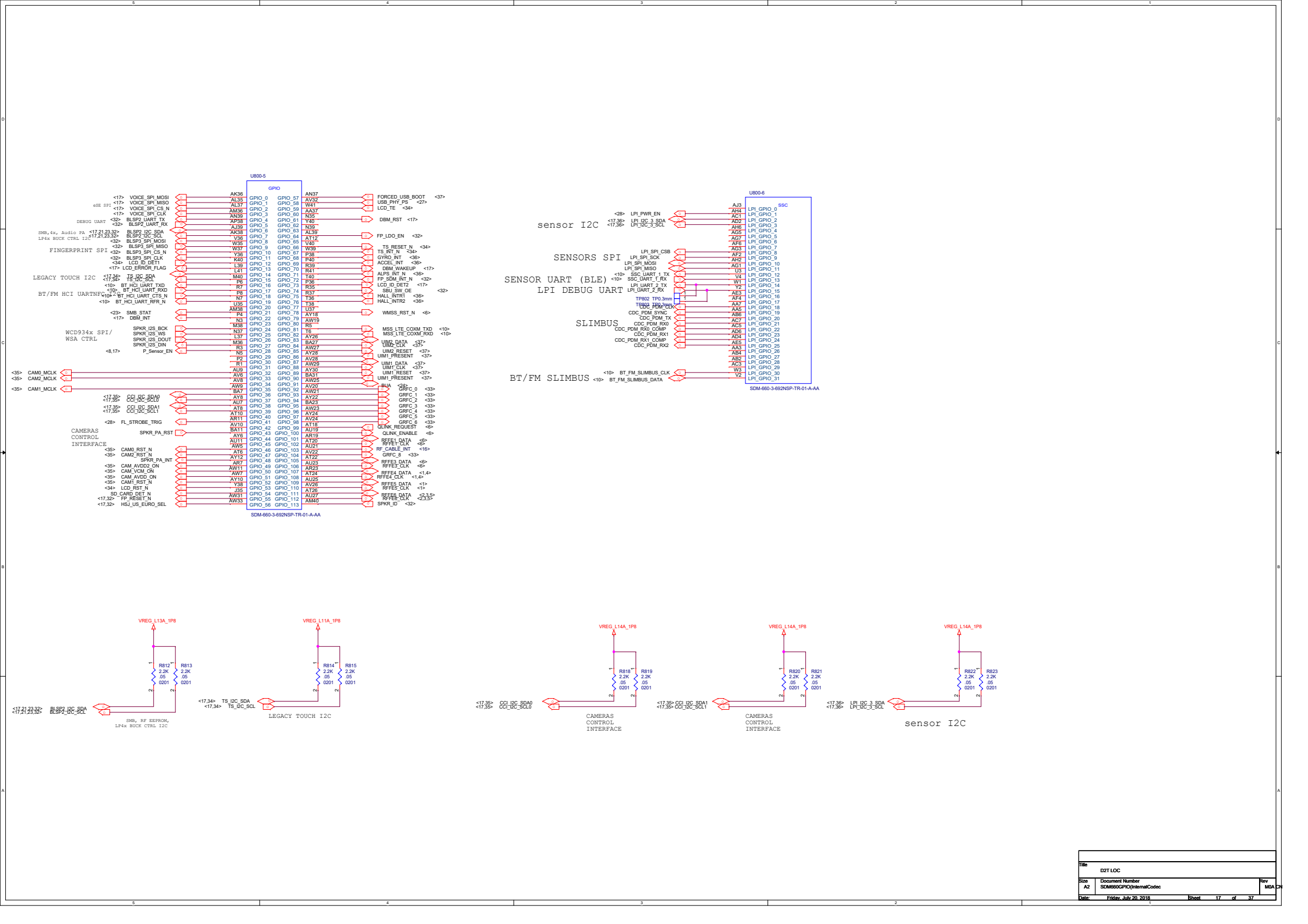


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D2T LOC		
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U800-6

GPIO

AK36	GPIO 0	AK37
AL55	GPIO 1	AV32
AL37	GPIO 2	W41
AN36	GPIO 3	AN37
AN39	GPIO 4	N35
AP38	GPIO 5	Y40
AL39	GPIO 6	N38
AK38	GPIO 7	AL39
V36	GPIO 8	AL32
V35	GPIO 9	V40
W37	GPIO 10	W39
V36	GPIO 11	P38
K40	GPIO 12	P40
L39	GPIO 13	R39
L41	GPIO 14	R41
M40	GPIO 15	T40
P6	GPIO 16	P36
RP	GPIO 17	R35
P8	GPIO 18	R37
N7	GPIO 19	T36
L35	GPIO 20	T38
AM38	GPIO 21	U37
P4	GPIO 22	Y18
N3	GPIO 23	AW19
M38	GPIO 24	R5
N37	GPIO 25	R5
L37	GPIO 26	AV26
M36	GPIO 27	BA27
R3	GPIO 28	AW27
N5	GPIO 29	AV28
R2	GPIO 30	AV28
R1	GPIO 31	AW29
AL9	GPIO 32	AV30
AV6	GPIO 33	BA31
AV8	GPIO 34	AV25
BA7	GPIO 35	AW21
AV6	GPIO 36	AV22
AU7	GPIO 37	BA23
AT8	GPIO 38	AV23
AT9	GPIO 39	AV24
AR11	GPIO 40	AV24
AV10	GPIO 41	AT18
BA11	GPIO 42	AU19
AV10	GPIO 43	GPIO 100
AV6	GPIO 44	GPIO 101
AU11	GPIO 45	GPIO 102
AV5	GPIO 46	GPIO 103
AT6	GPIO 47	GPIO 104
AY12	GPIO 48	GPIO 105
AP7	GPIO 49	GPIO 106
AW11	GPIO 50	GPIO 107
AW7	GPIO 51	GPIO 108
AV10	GPIO 52	GPIO 109
V36	GPIO 53	GPIO 110
J5	GPIO 54	GPIO 111
AW31	GPIO 55	GPIO 112
AW33	GPIO 56	GPIO 113

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sensor I2C

SENSORS SPI

SENSOR UART (BLE)

LPI DEBUG UART

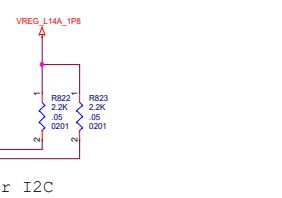
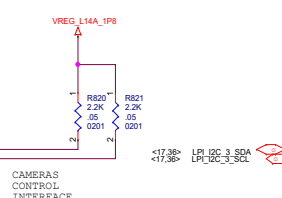
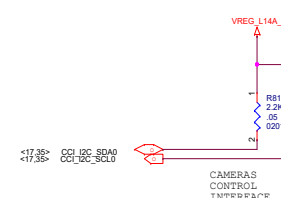
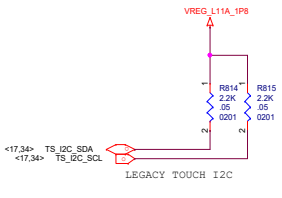
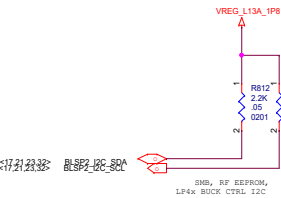
SLIMBUS

BT/FM SLIMBUS

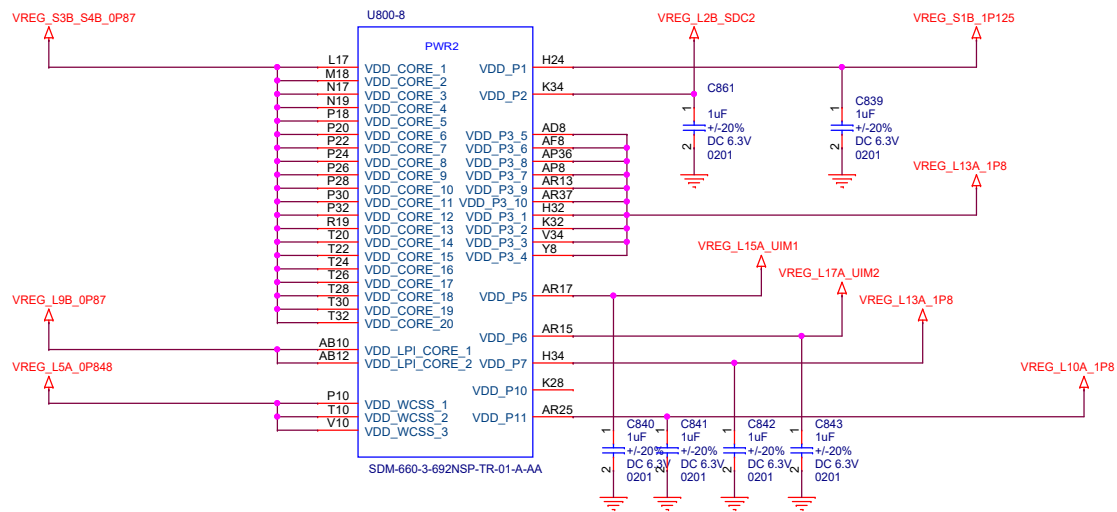
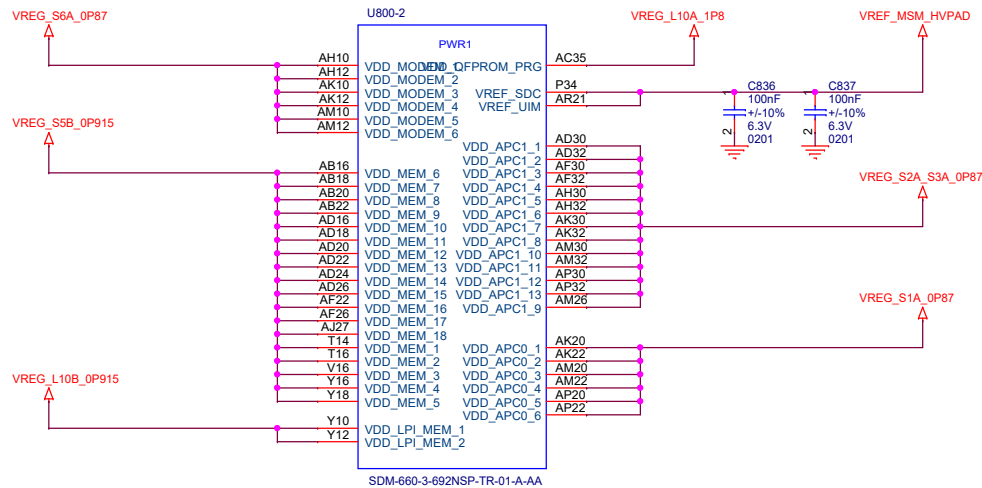
SSC

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AC1	LPI_GPIO_1
AZ0	LPI_GPIO_2
AG5	LPI_GPIO_3
AG5	LPI_GPIO_4
AG7	LPI_GPIO_5
AF6	LPI_GPIO_6
AC53	LPI_GPIO_7
AF2	LPI_GPIO_8
AP2	LPI_GPIO_9
U3	LPI_GPIO_10
V6	LPI_GPIO_11
W1	LPI_GPIO_12
Y2	LPI_GPIO_13
Y2	LPI_GPIO_14
AE3	LPI_GPIO_15
AF4	LPI_GPIO_16
AA7	LPI_GPIO_17
AA5	LPI_GPIO_18
AC7	LPI_GPIO_19
AC5	LPI_GPIO_20
AA6	LPI_GPIO_21
AD4	LPI_GPIO_22
AA6	LPI_GPIO_23
AA5	LPI_GPIO_24
AA3	LPI_GPIO_25
AB2	LPI_GPIO_26
AB3	LPI_GPIO_27
AC3	LPI_GPIO_28
W3	LPI_GPIO_29
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V2	LPI_GPIO_31

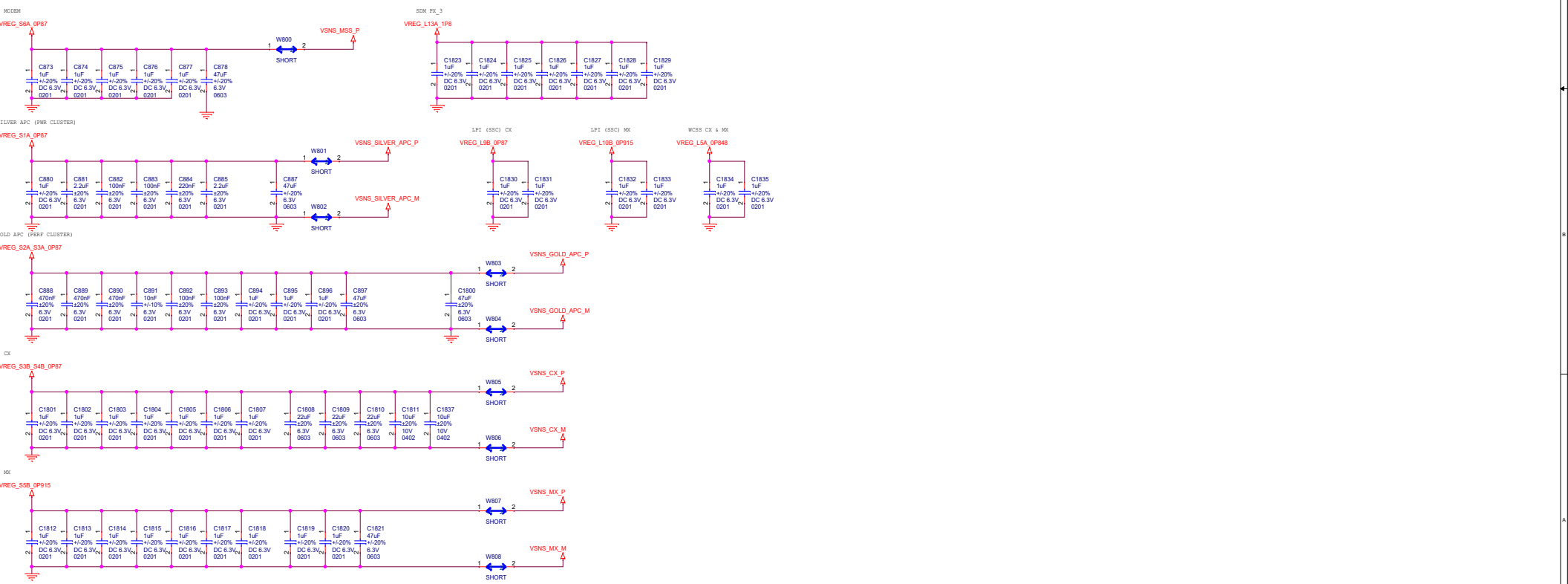
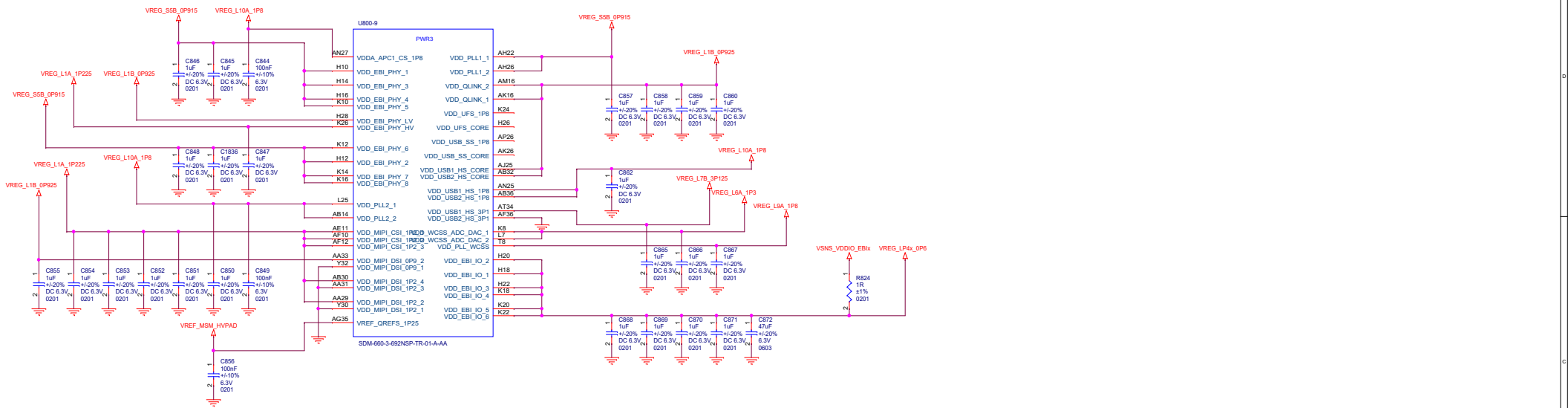
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A2	SDM660GPIO(InternalCode)	MDA
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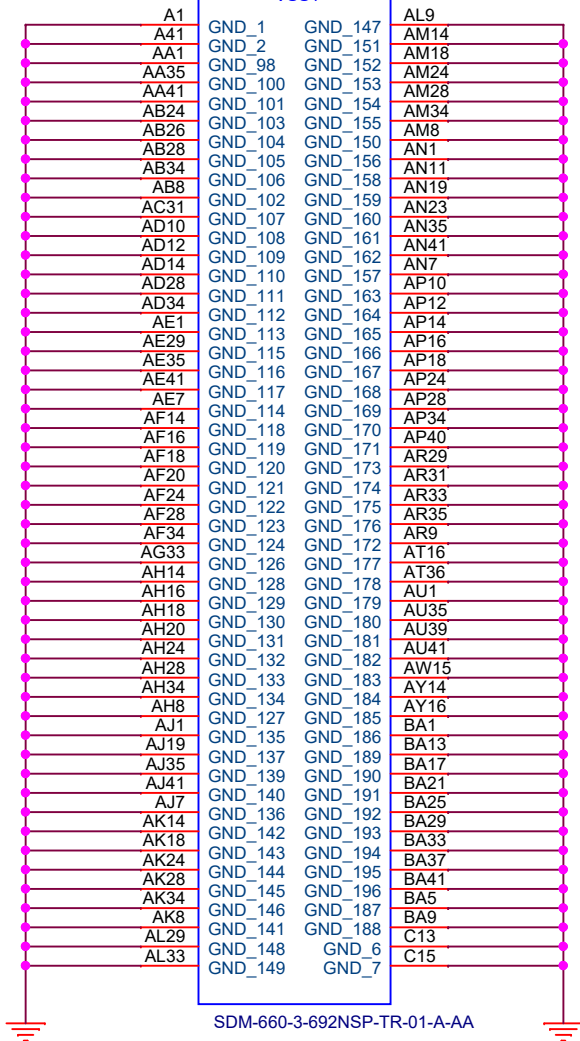
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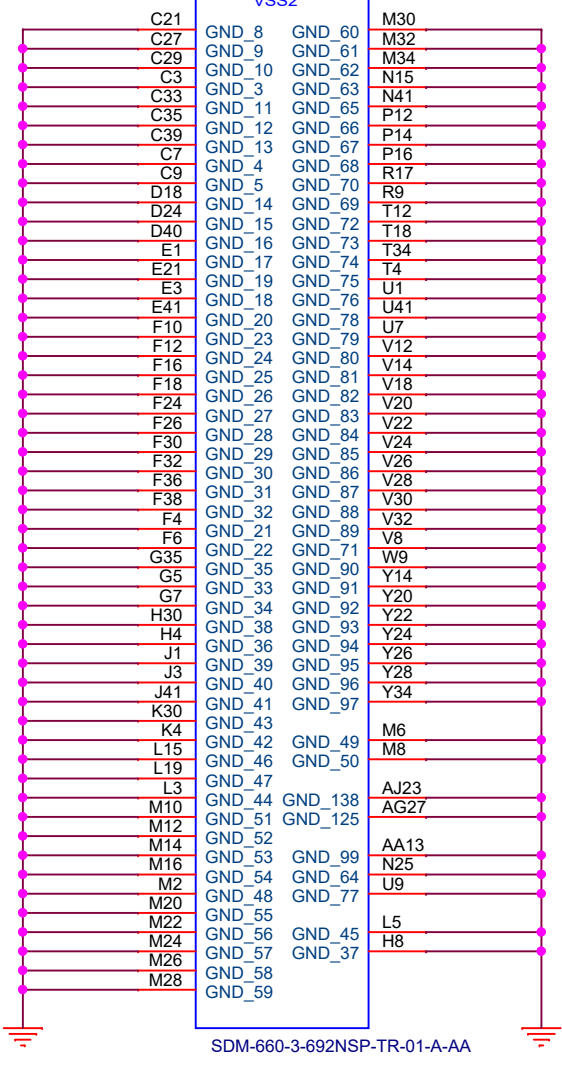
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SDM-660-3-692NSP-TR-01-A-AA

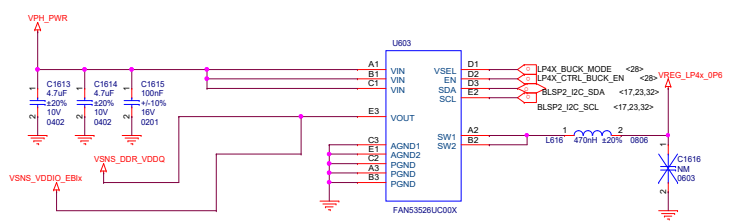
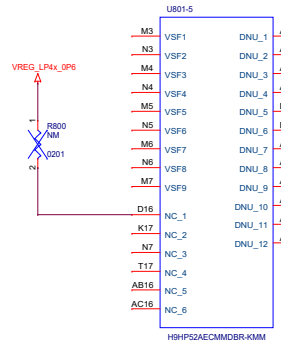
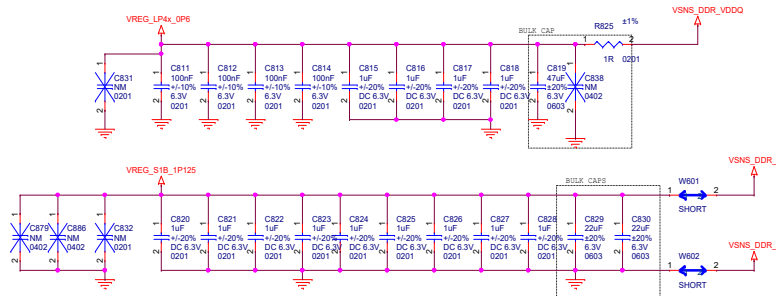
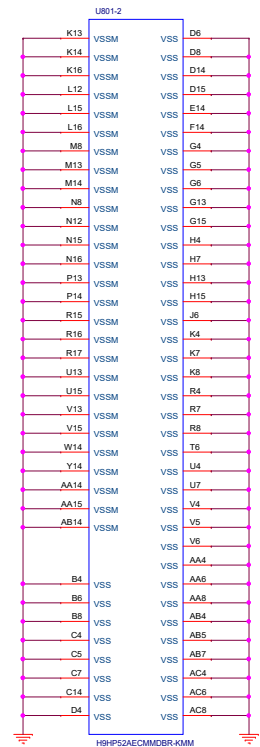
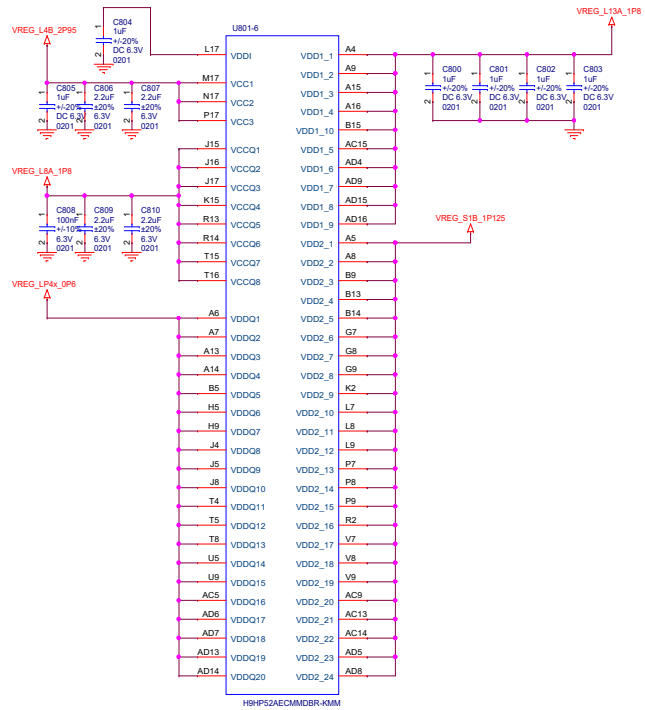
U800-12

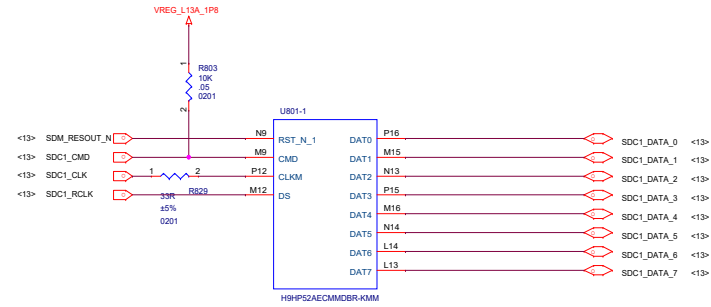
VSS2



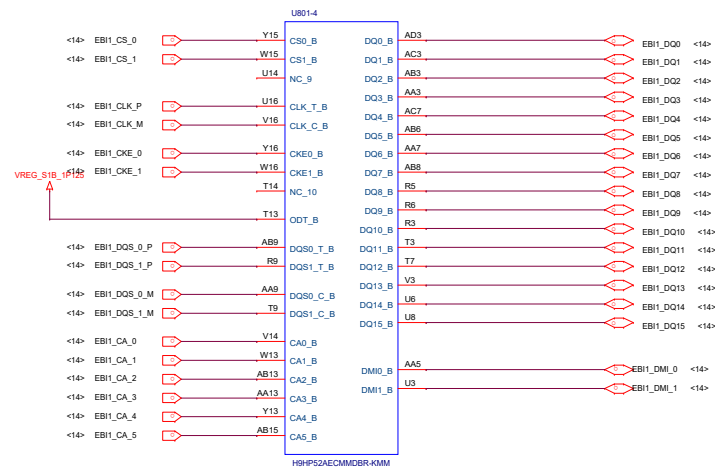
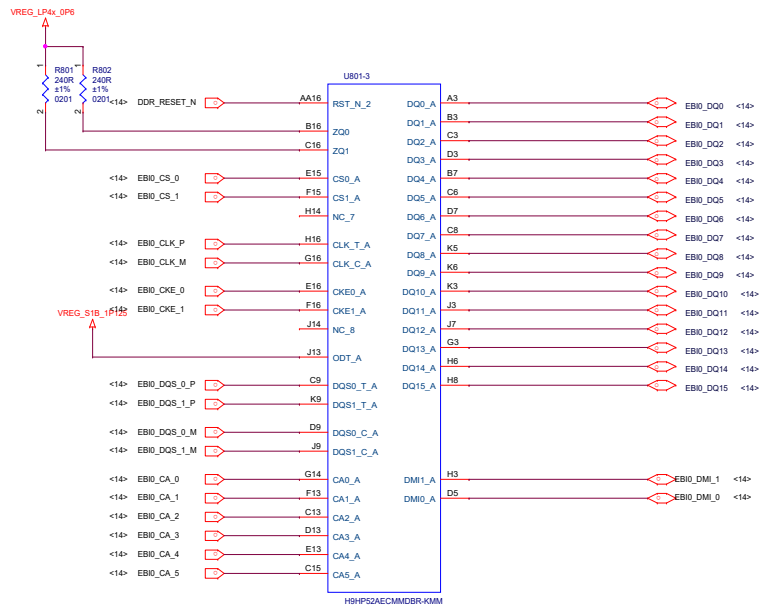
SDM-660-3-692NSP-TR-01-A-AA

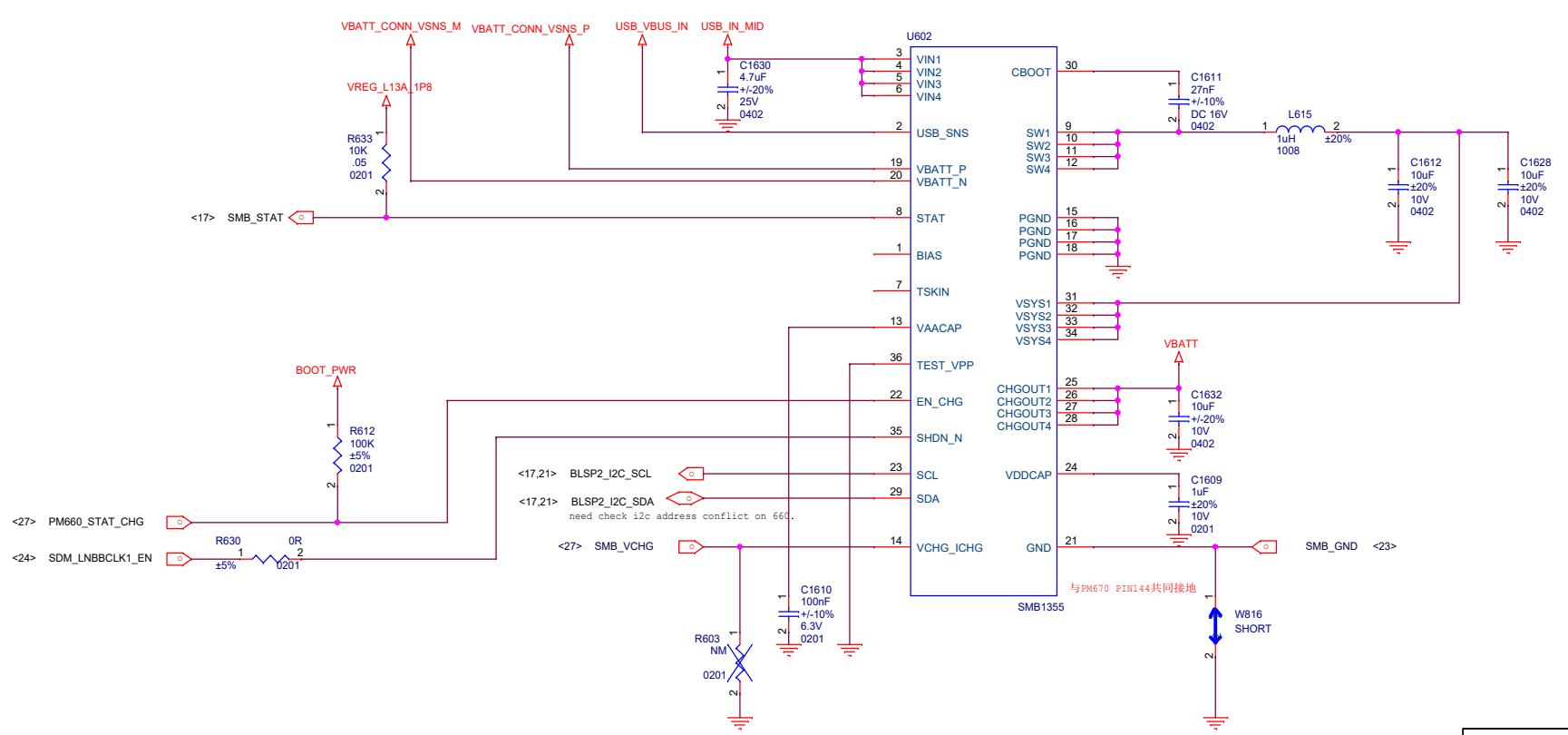
Title		
D2T LOC		
Size	Document Number	Rev
A4	SDM660 Ground	M0A
Date:	Friday, July 20, 2018	Sheet 20 of 37



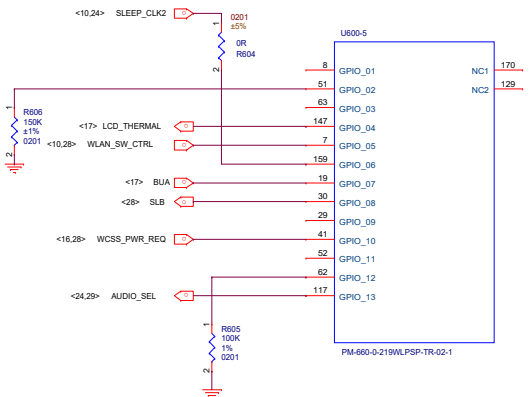
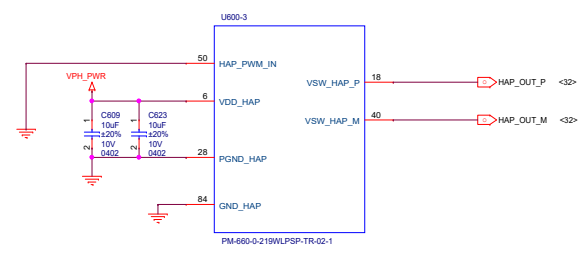
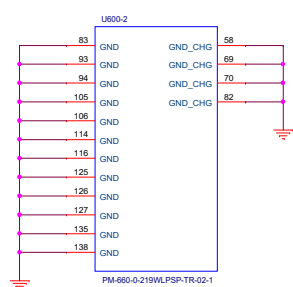
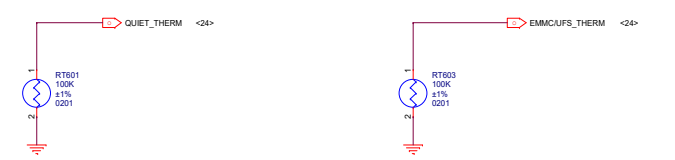
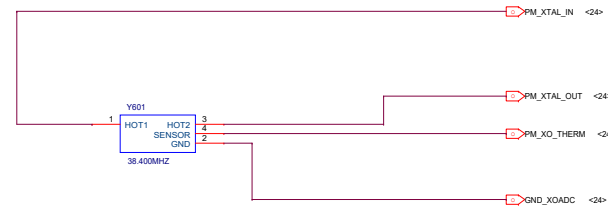
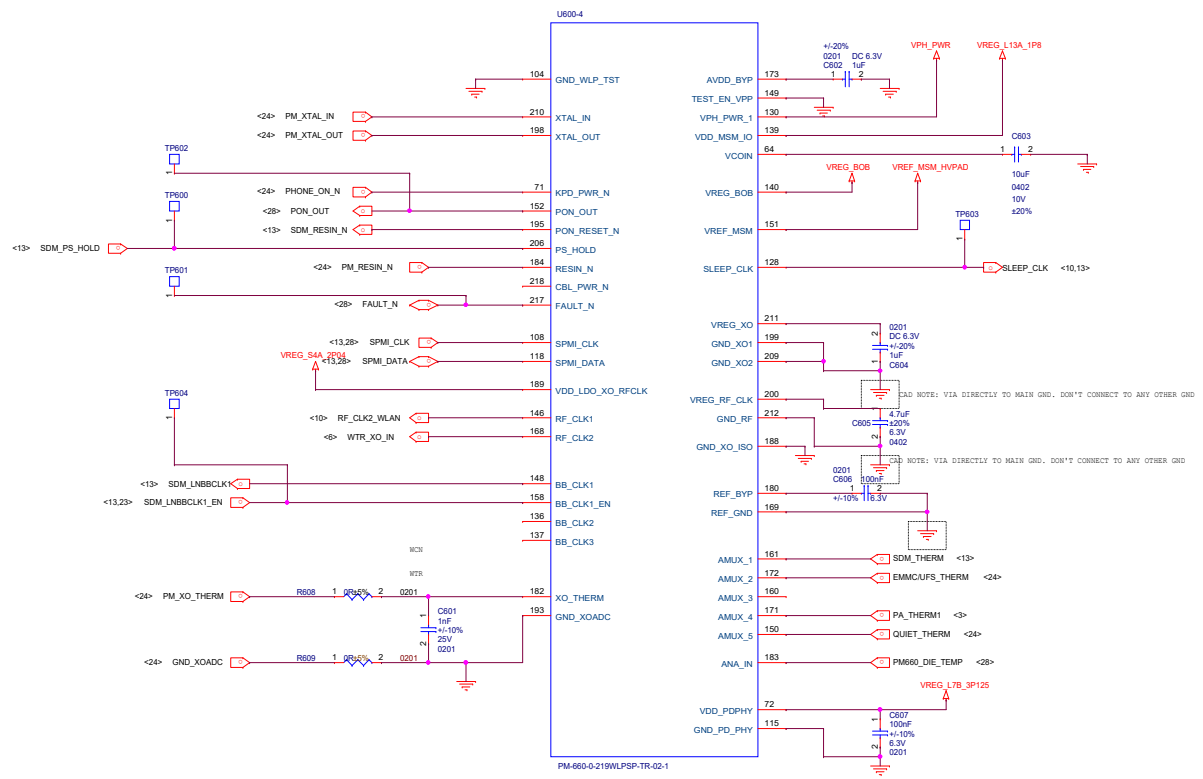


LPDDR4 / 4x

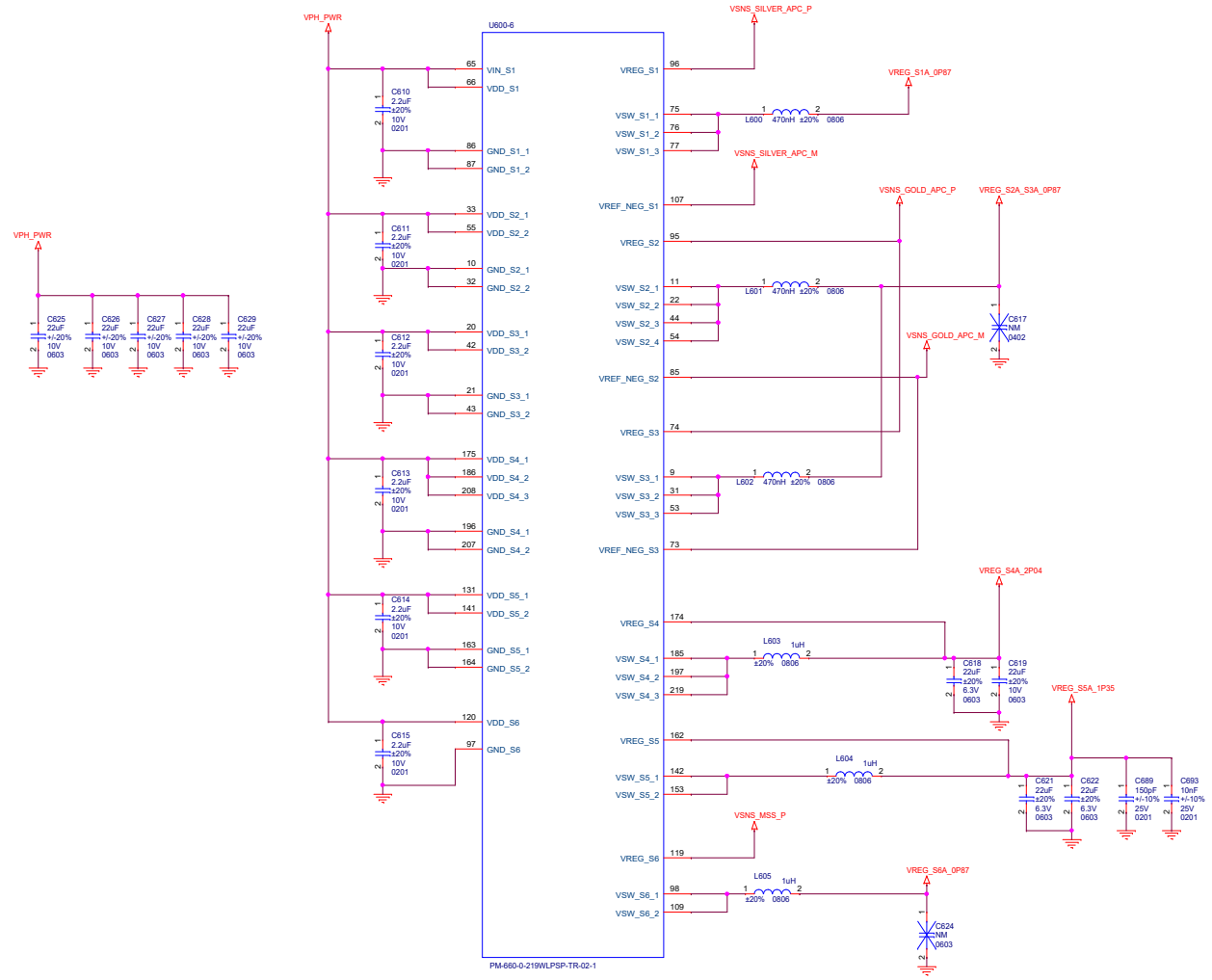




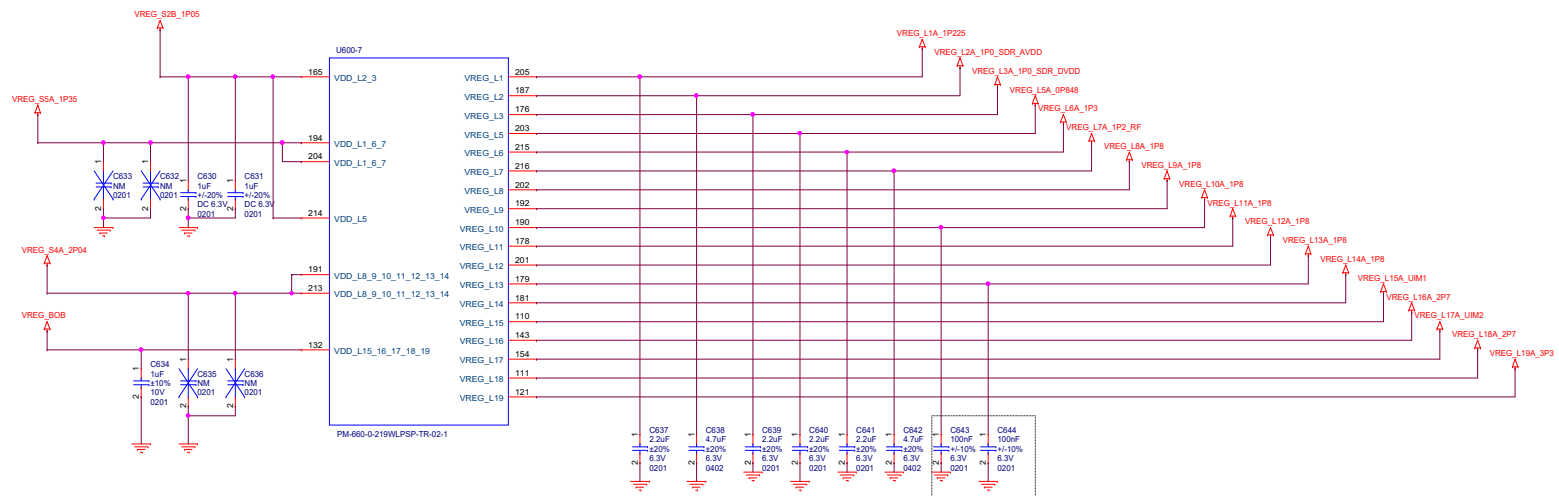
Title		
D2T LOC		
Size	Document Number	Rev
A3	SMB1351	MOA CH
Date:	Friday, July 20, 2018	Sheet 23 of 37



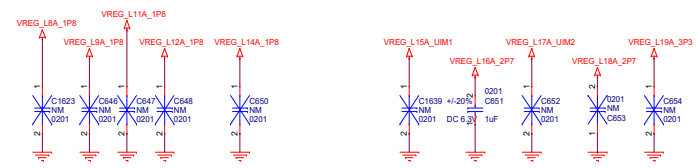
Title			
DZY LOC			
Size	Document Number	Rev	
A2	PM660 CLK Control_GND	MJA/CN	
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File		
02T LOC		
Size	Document Number	Rev
A2	PM660 Buck Converter	MDA
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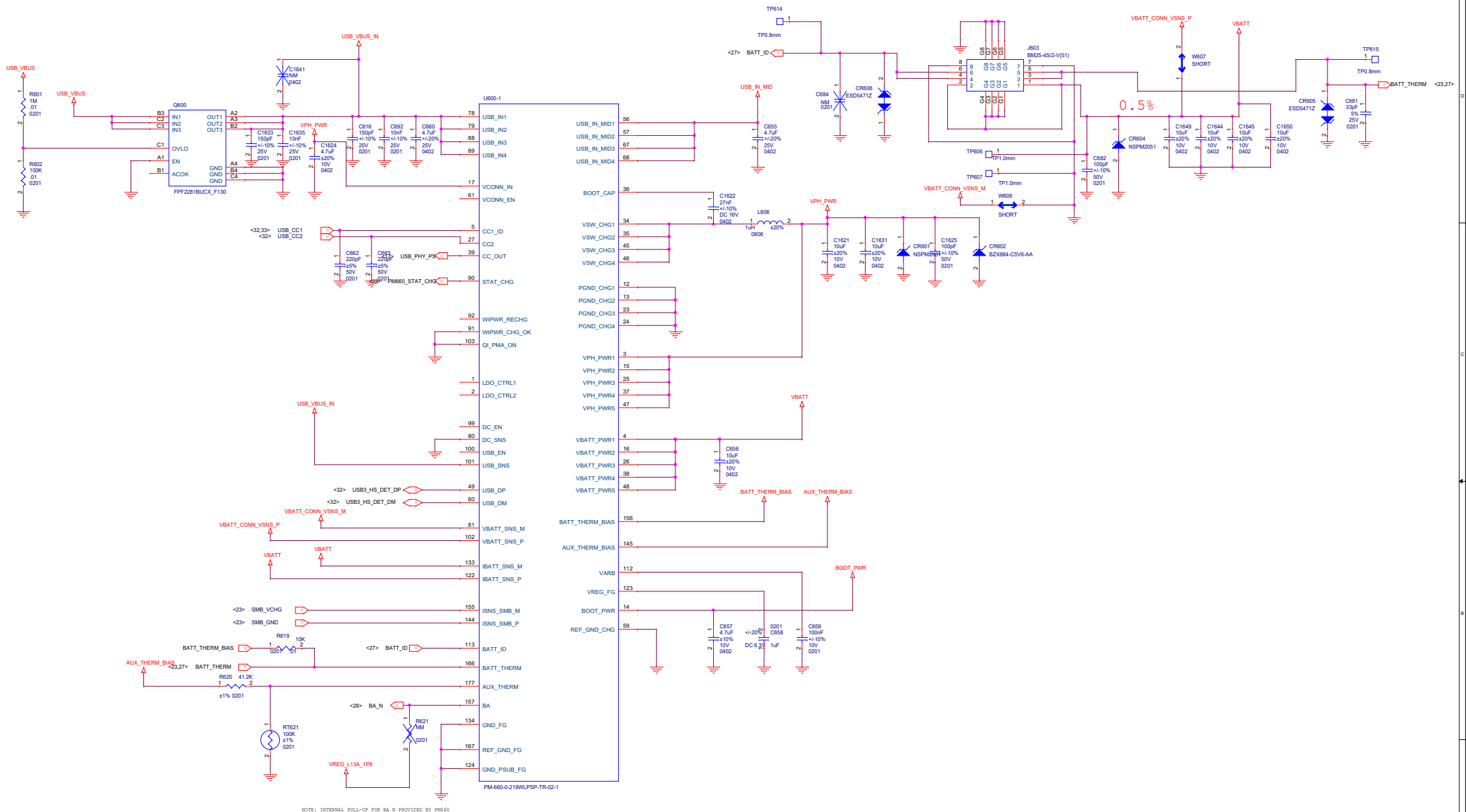


L004 USED INTERNAL TO PMIC NEED LOCAL DECAP

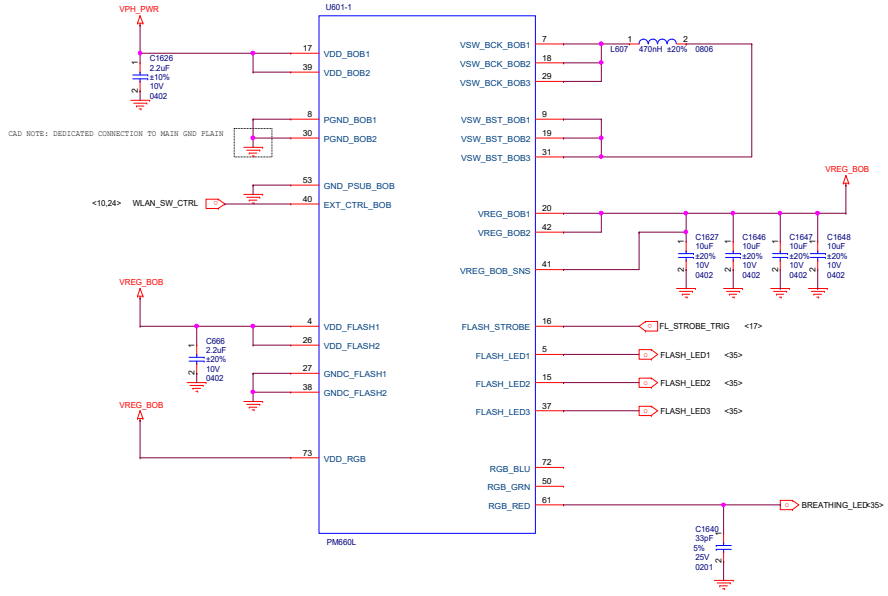
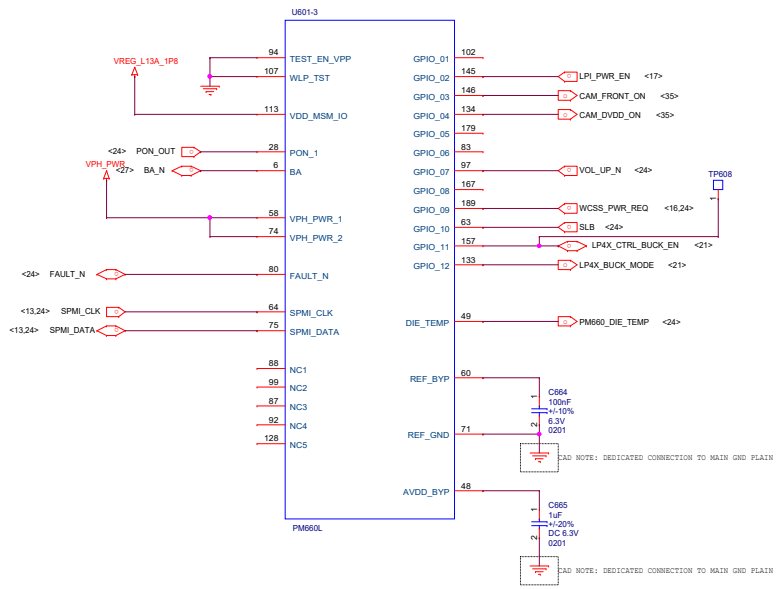


PRELDO CAPLESS LDOs
 For CAPLESS LDOs: If decaps on the load side do not
 add up to LDO spec, then install the cap close to the PMIC

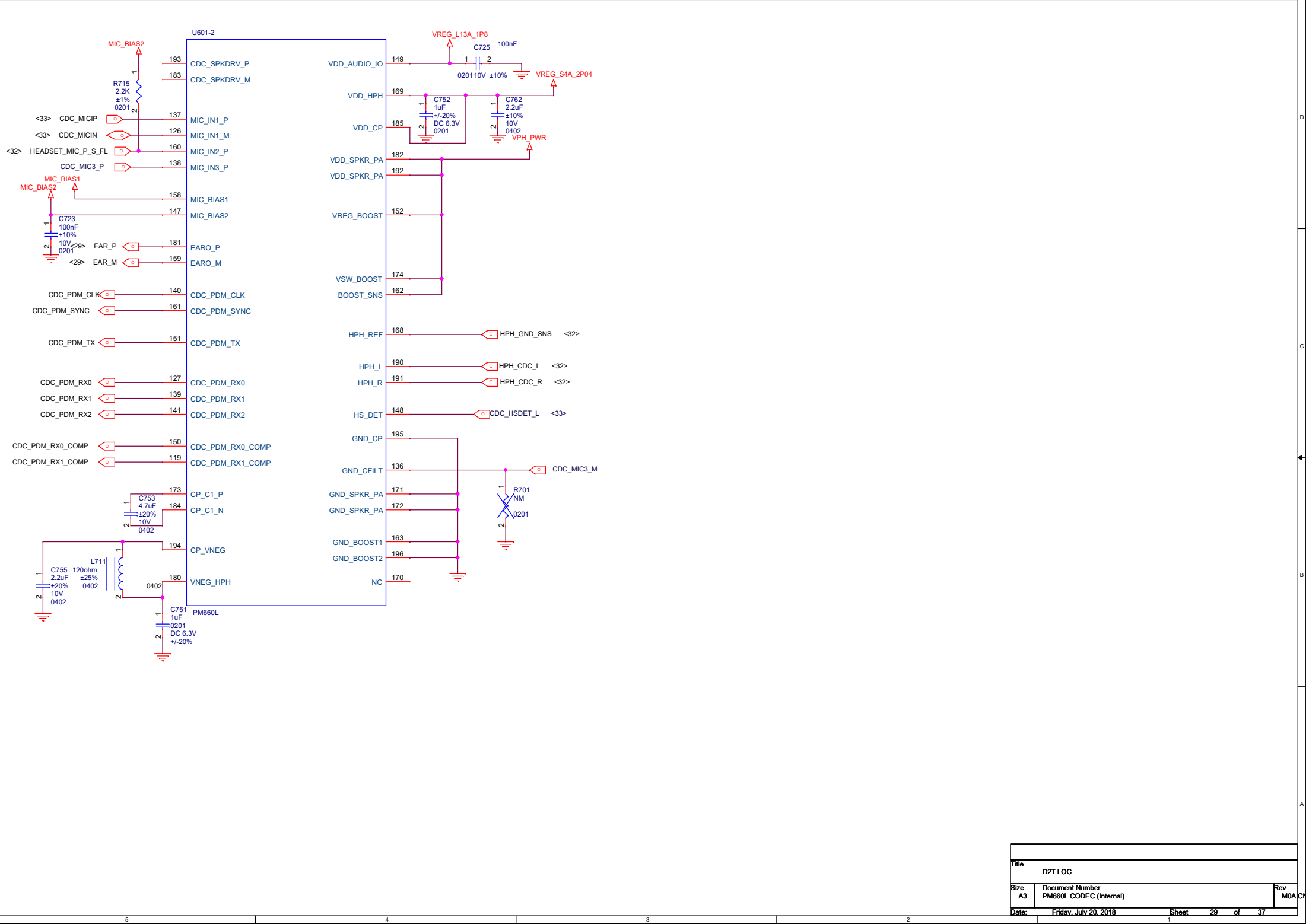
Title		
D27 LOC		
Size	Document Number	Rev
A2	PM660 LDO Circuits	M0A
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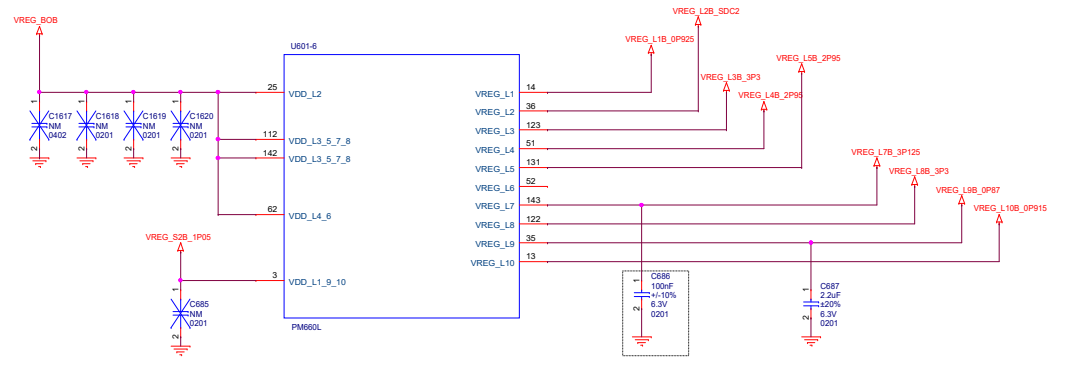
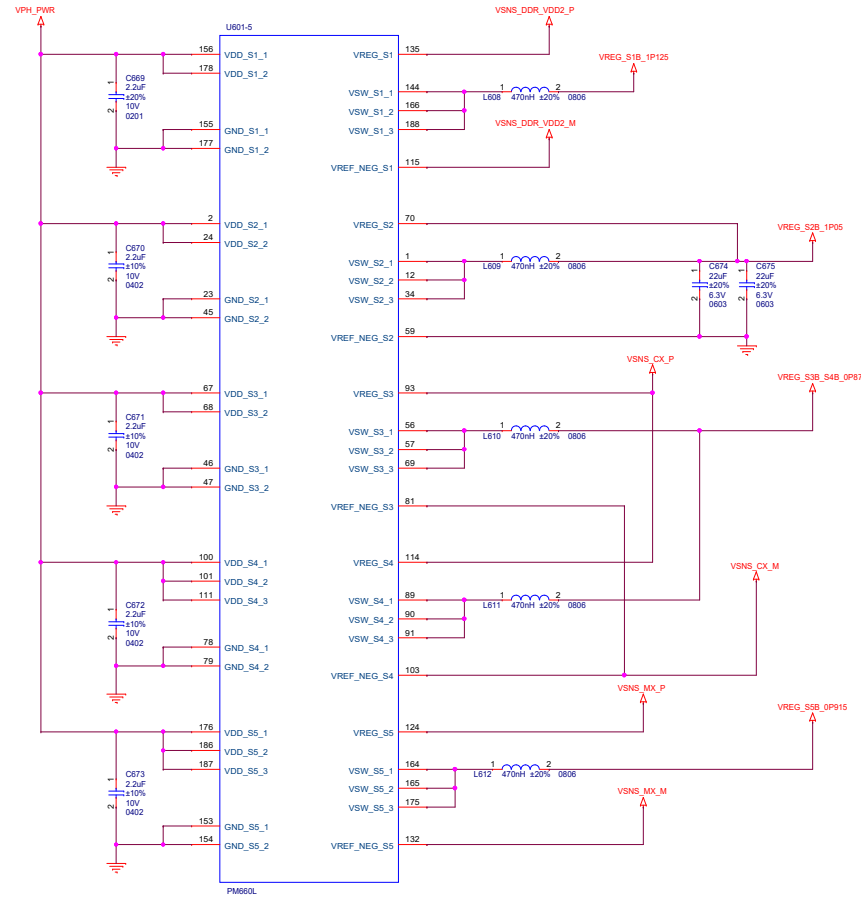
NOTE: INTERNAL PULL-UP FOR BA_N PROVIDED BY PM660



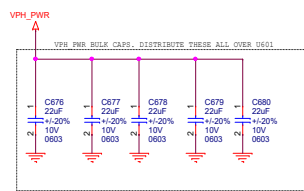
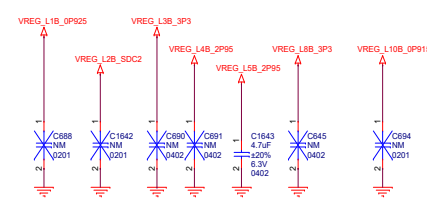
Title		
DZ7 LOC		
Size	Document Number	Rev
A2	PM660L.ControlsandBobFlash	M0A
Date	Fileav_JUL20_2018	Sheet 28 of 37



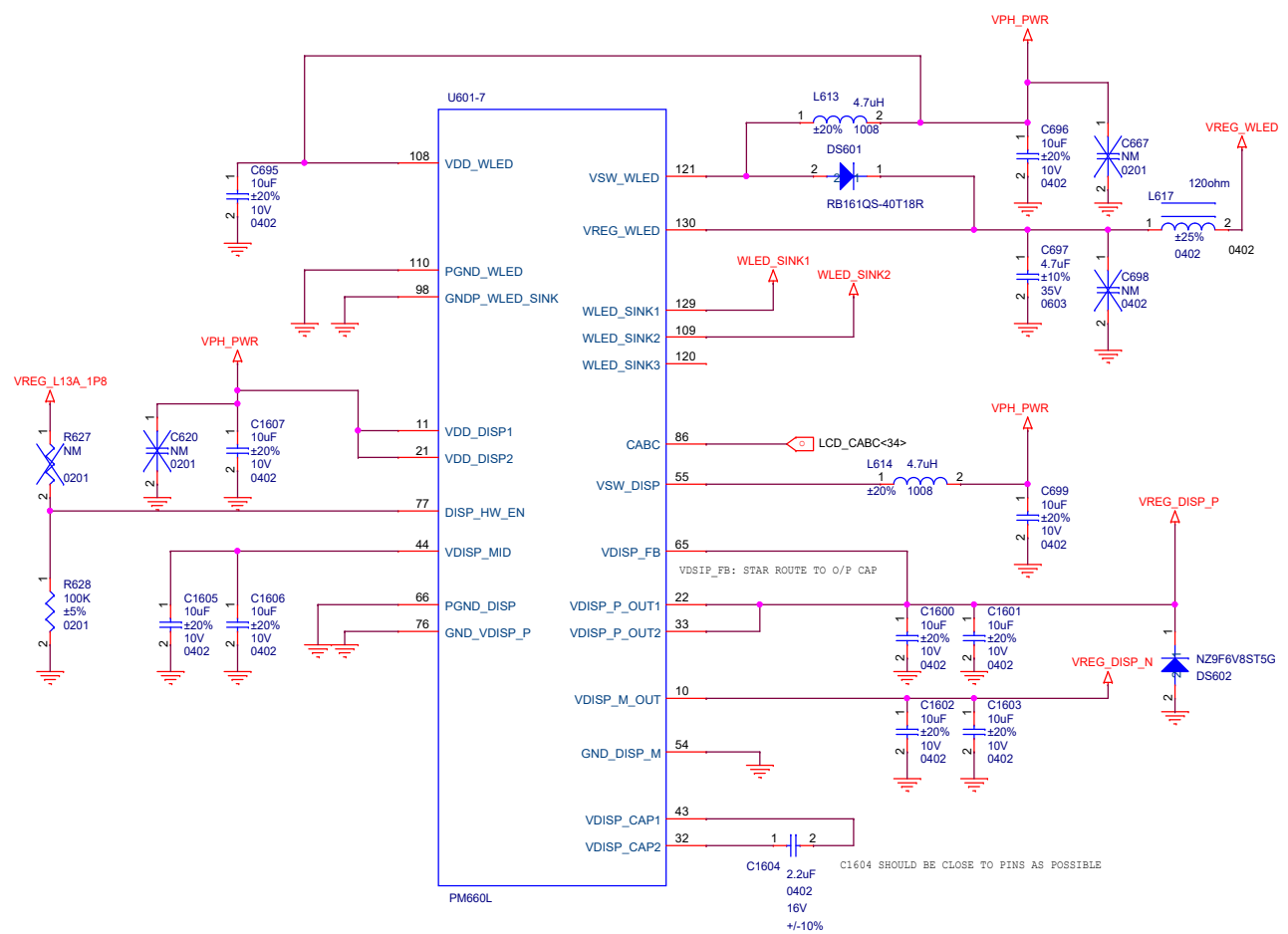
Title		D2T LOC
Size	Document Number	Rev
A3	PM660L CODEC (Internal)	MOA CH
Date:	Friday, July 20, 2018	Sheet 29 of 37



IF LDO USED INTERNAL TO PMIC, NEED LOCAL DECAP



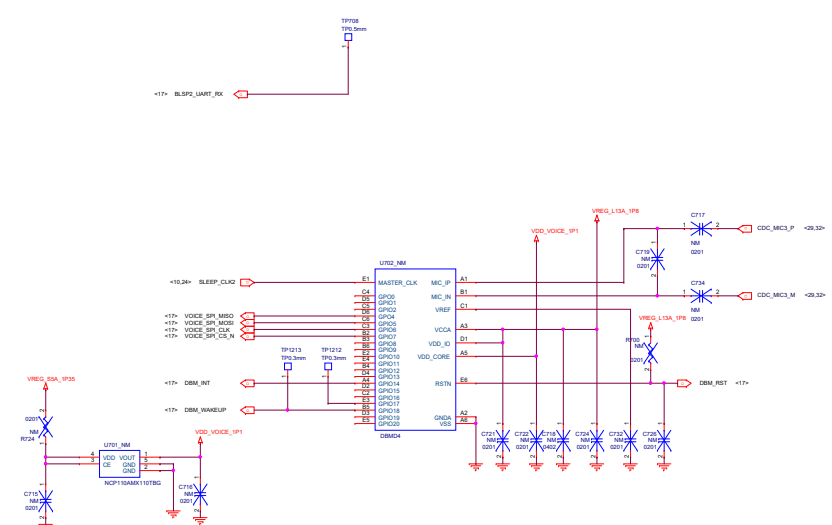
Title			D27 LOC		
Size	A2	Document Number	PM660L Buck and LDO	Rev	MDA
Date	Fri May 20 2018	Sheet	30	of	37



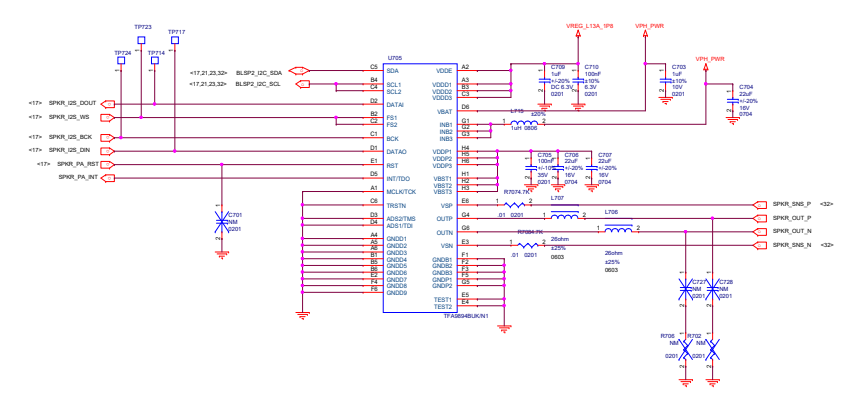
C1604 2.2uF 0402 16V +/-10%
 C1604 SHOULD BE CLOSE TO PINS AS POSSIBLE

Title		D2T LOC
Size	Document Number	Rev
A3	PM660L Display (LCD)	MOA CH
Date:	Friday, July 20, 2018	Sheet 31 of 37

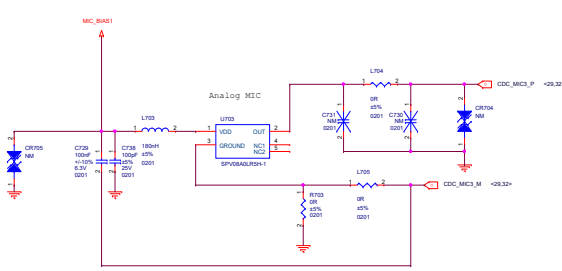
HEADSET



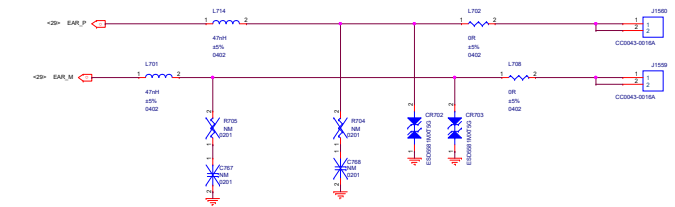
Speaker PA



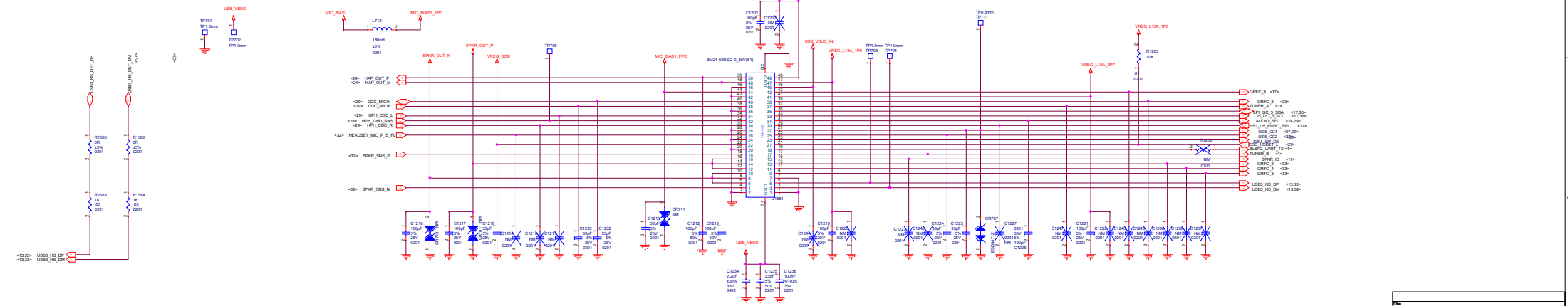
AUX Mic

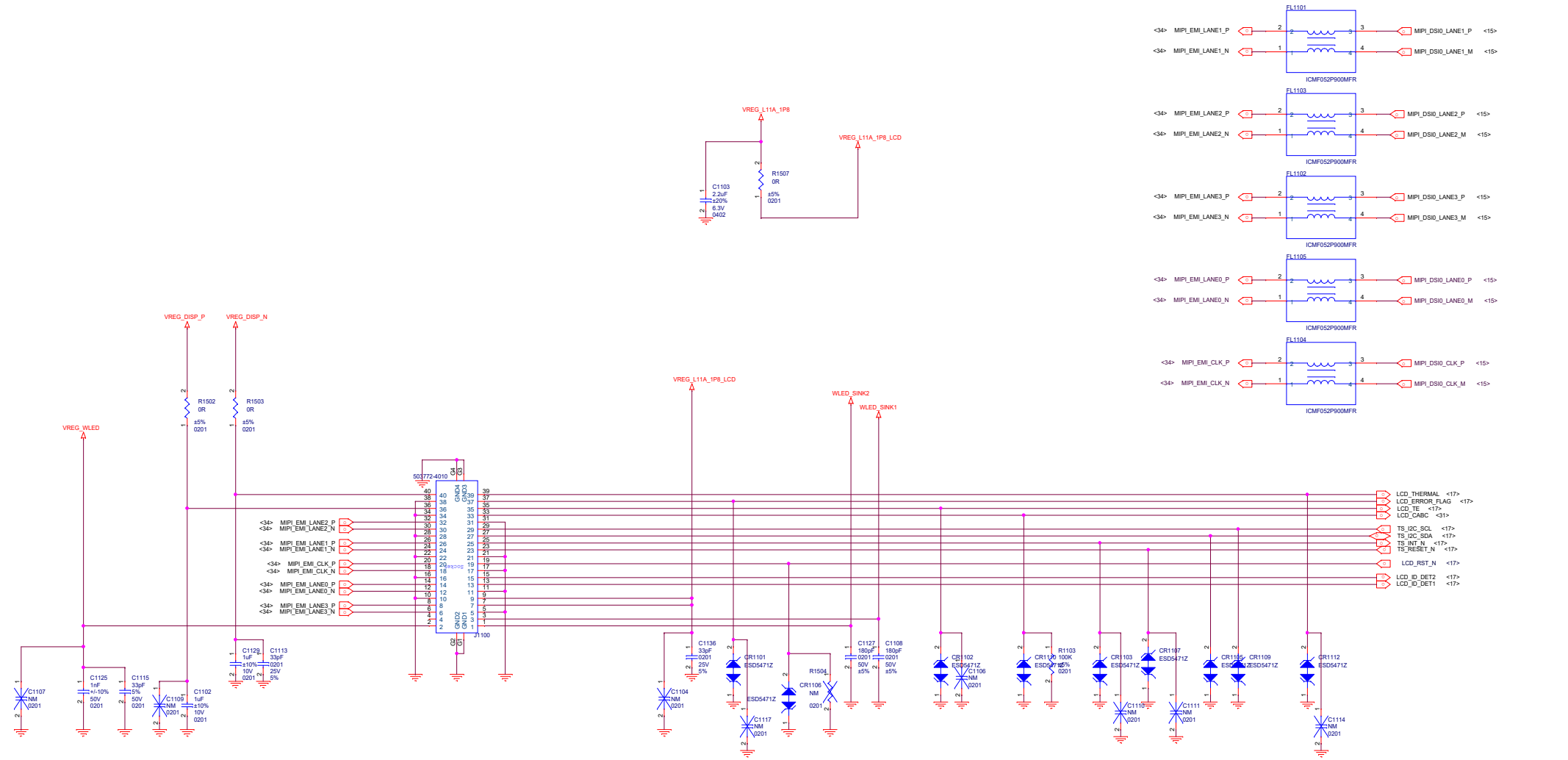


Receiver

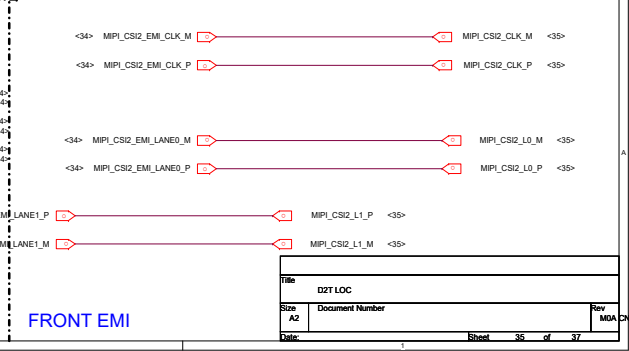
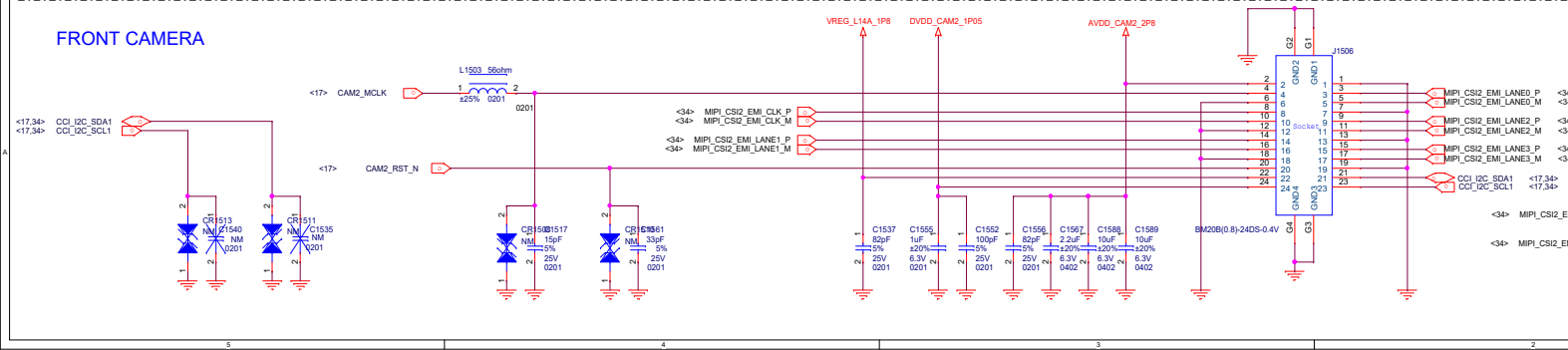
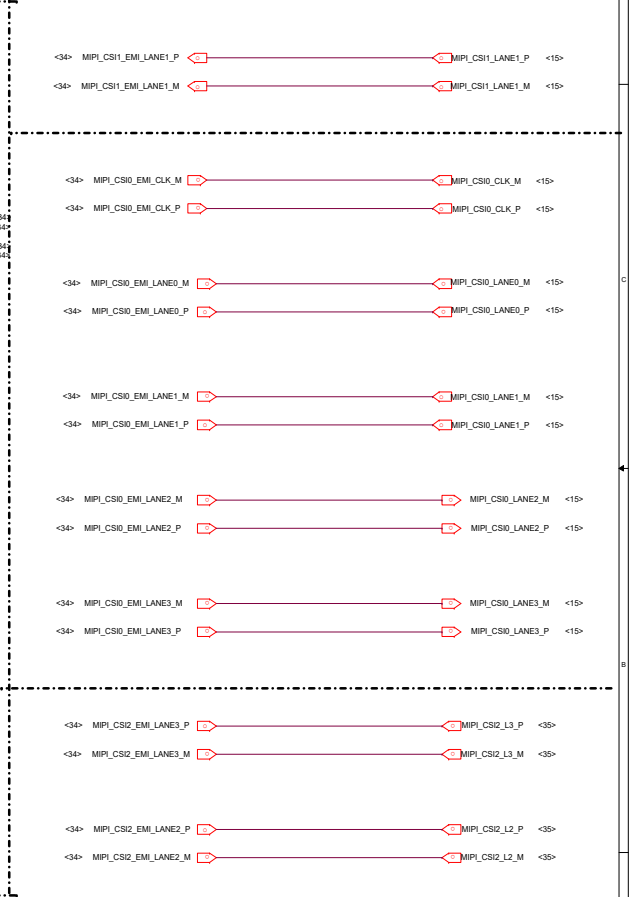
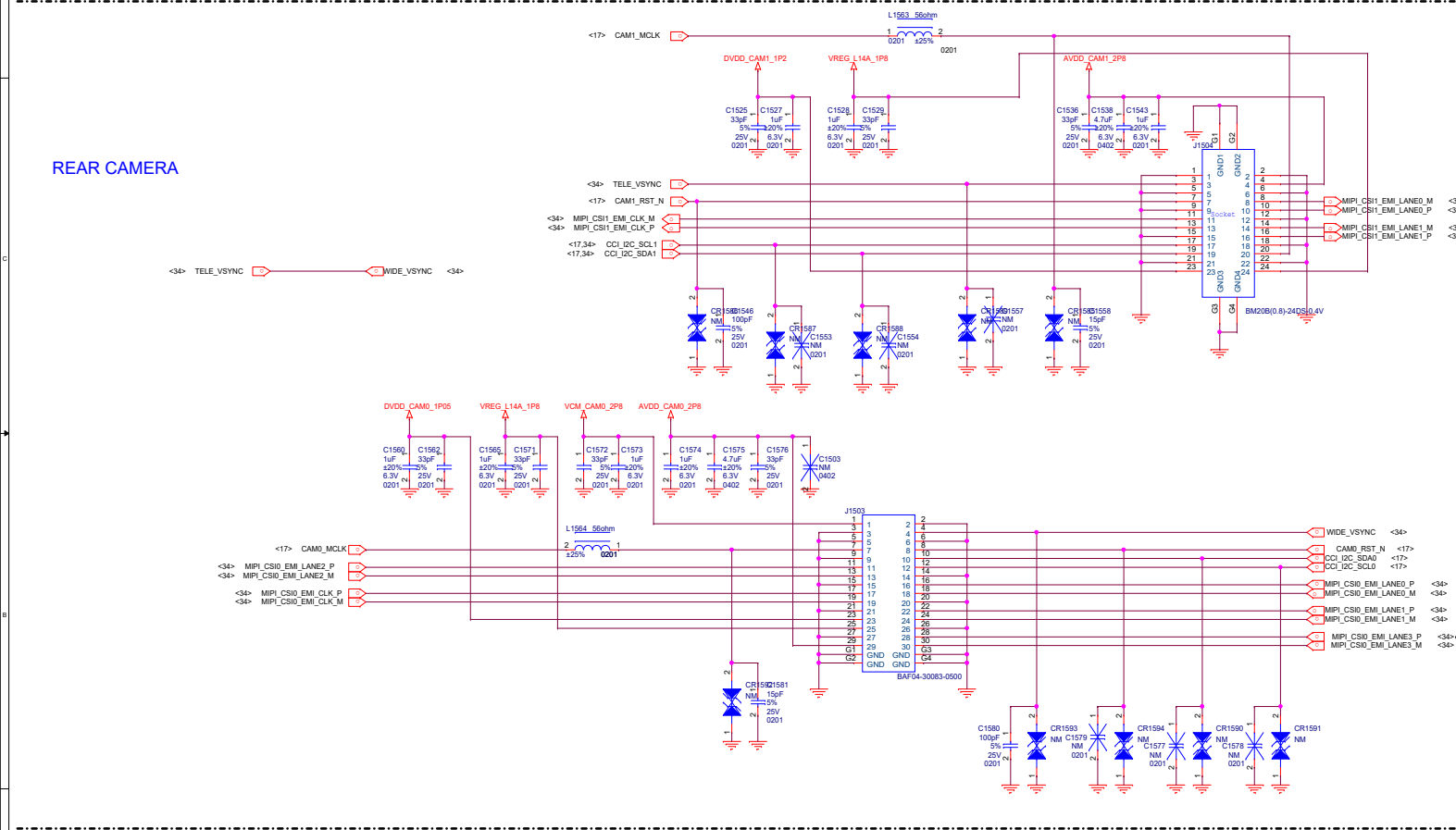
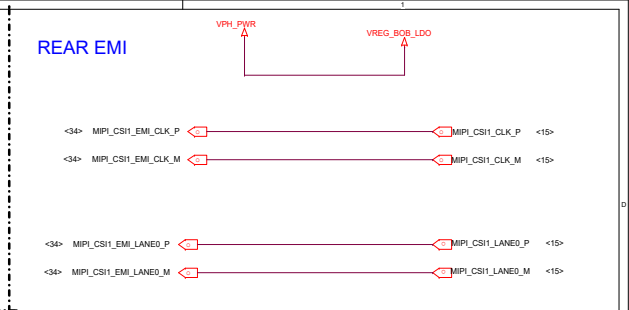
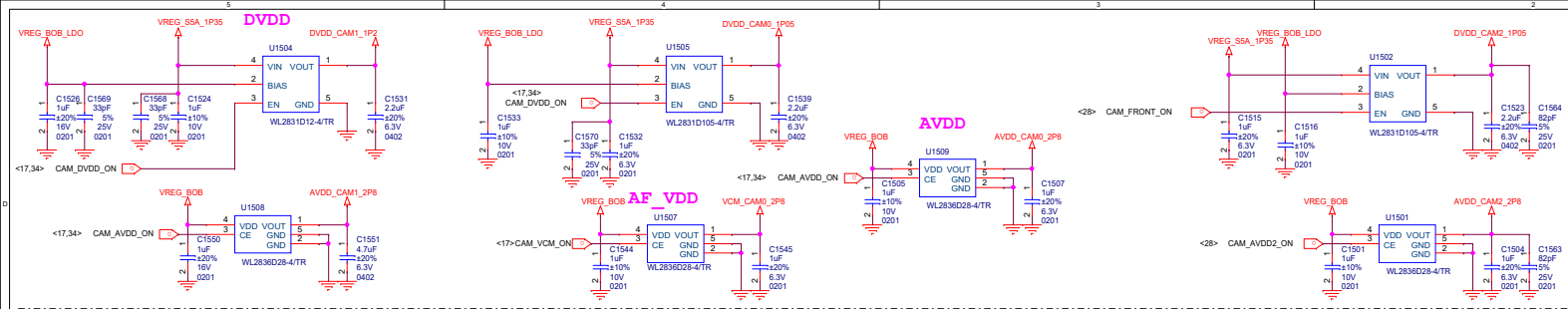


Bottom fpc Connector



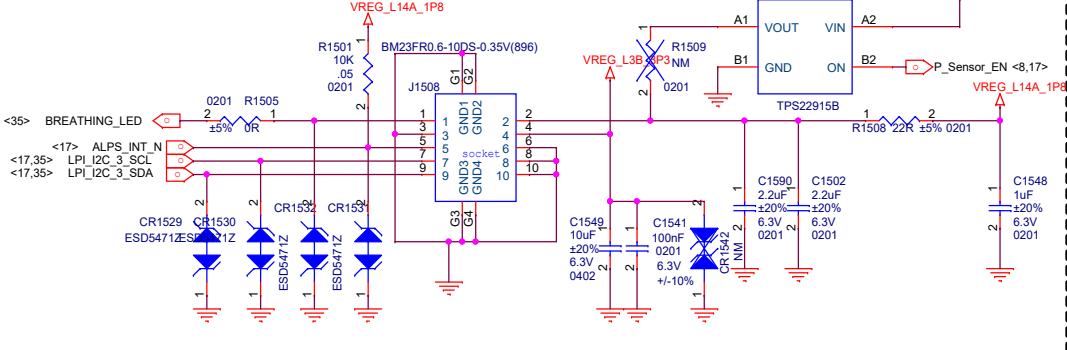


Title		D27 LOC	
Size	Document Number	Rev	
A2	LCD	MOA	
Date:	Friday, July 20, 2018	Sheet	34 of 37

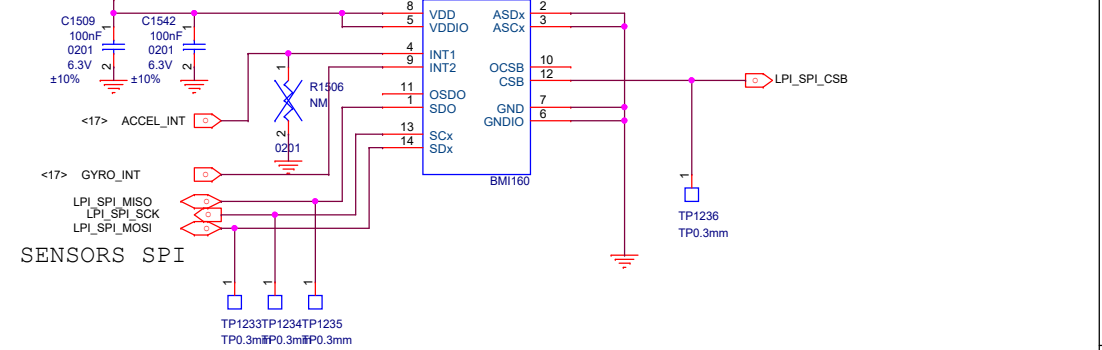


Title		DZ7 LOC	
Size	A2	Document Number	
Date:		Sheet	35 of 37

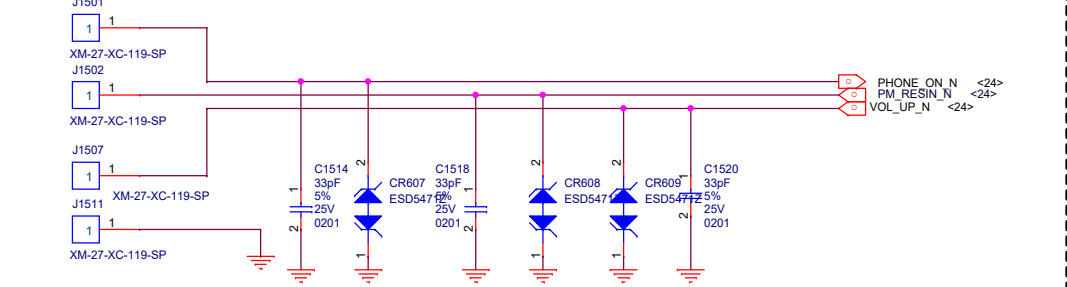
P-sensor



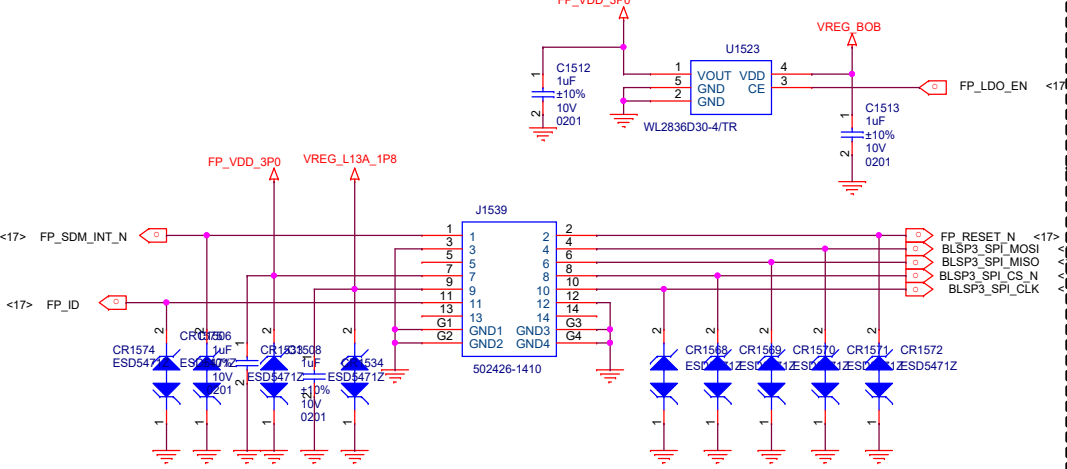
G+G



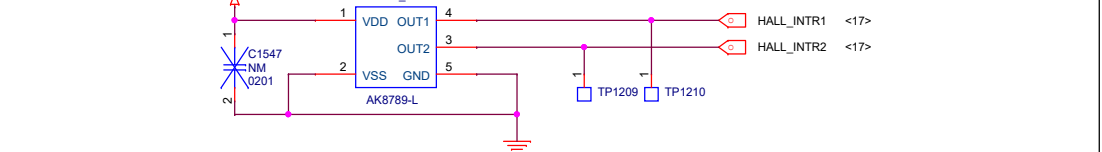
Sidekey



Fingerprint

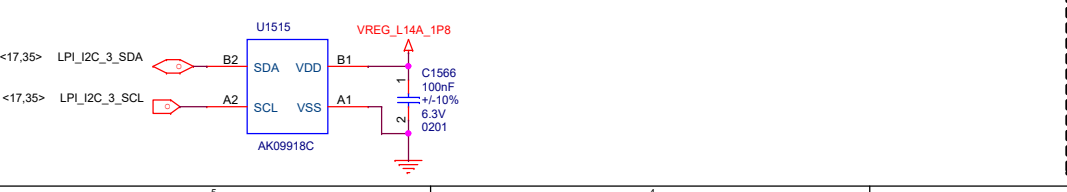


HALL

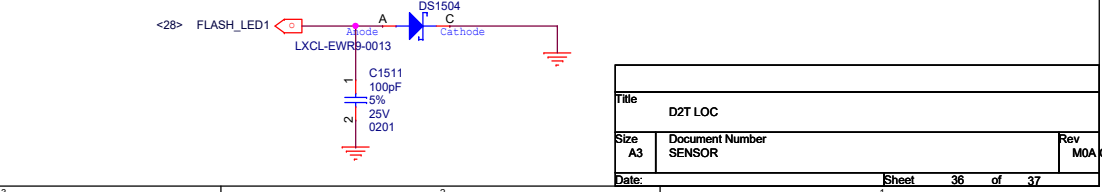


INFARED

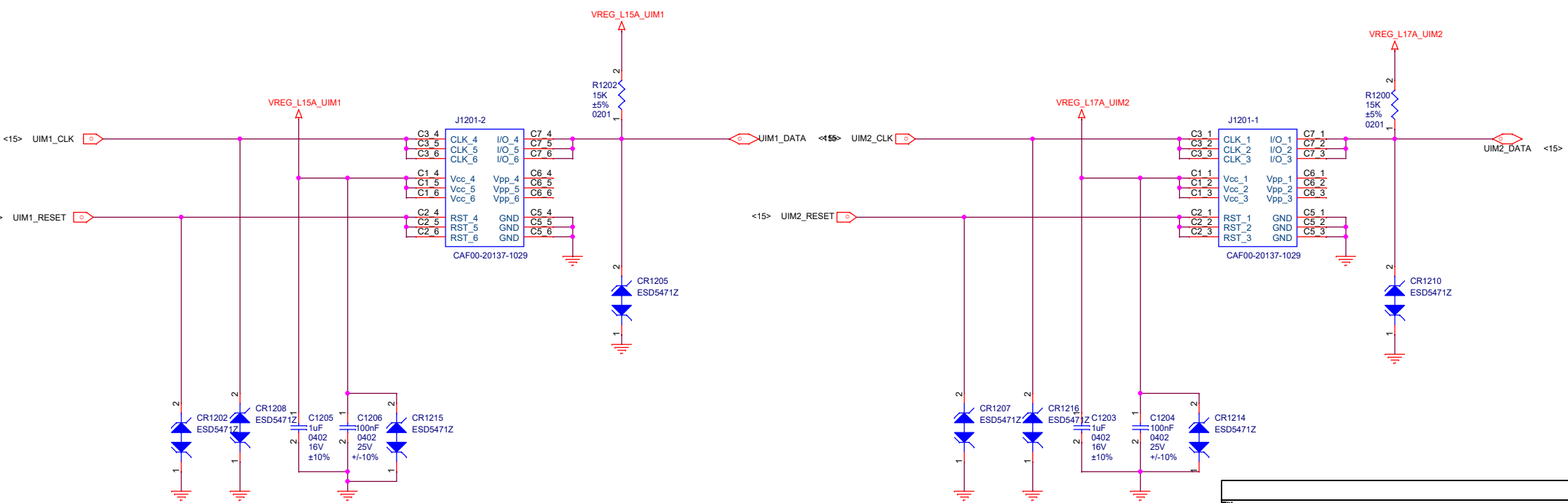
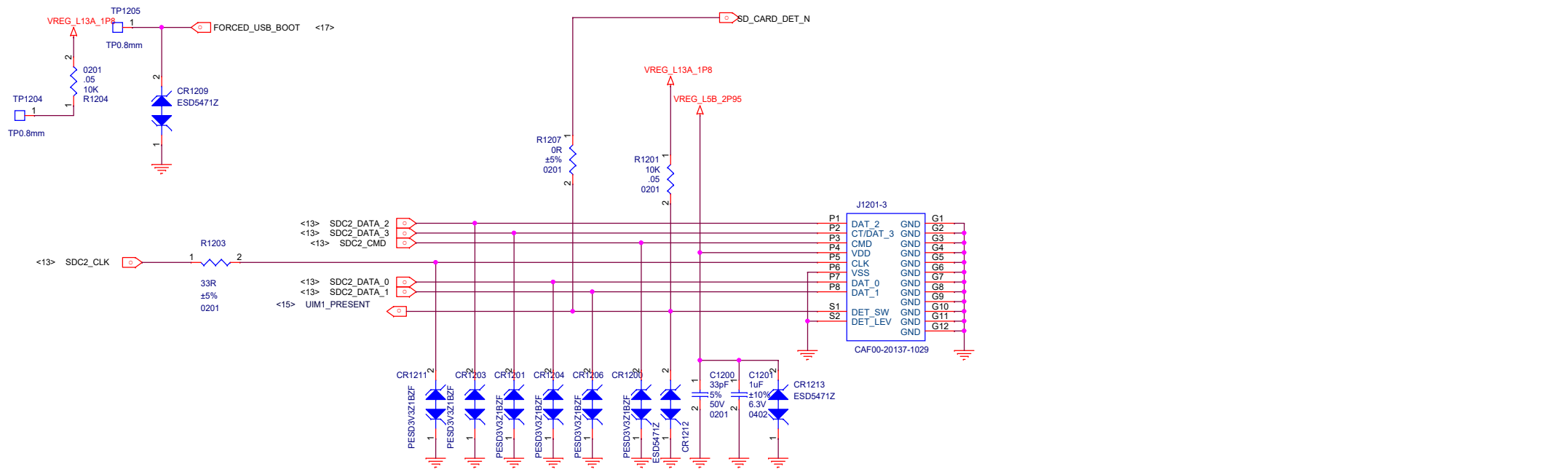
COMPASS



FLASH



Title		D2T LOC
Size	Document Number	Rev
A3	SENSOR	MOA CN
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Title		
D2T LOC		
Size	Document Number	Rev
A3	SIM CONNECTOR	MOA CH
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