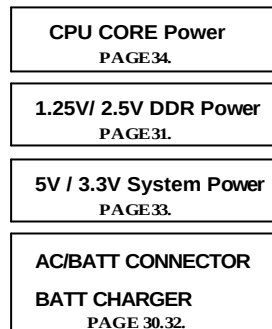
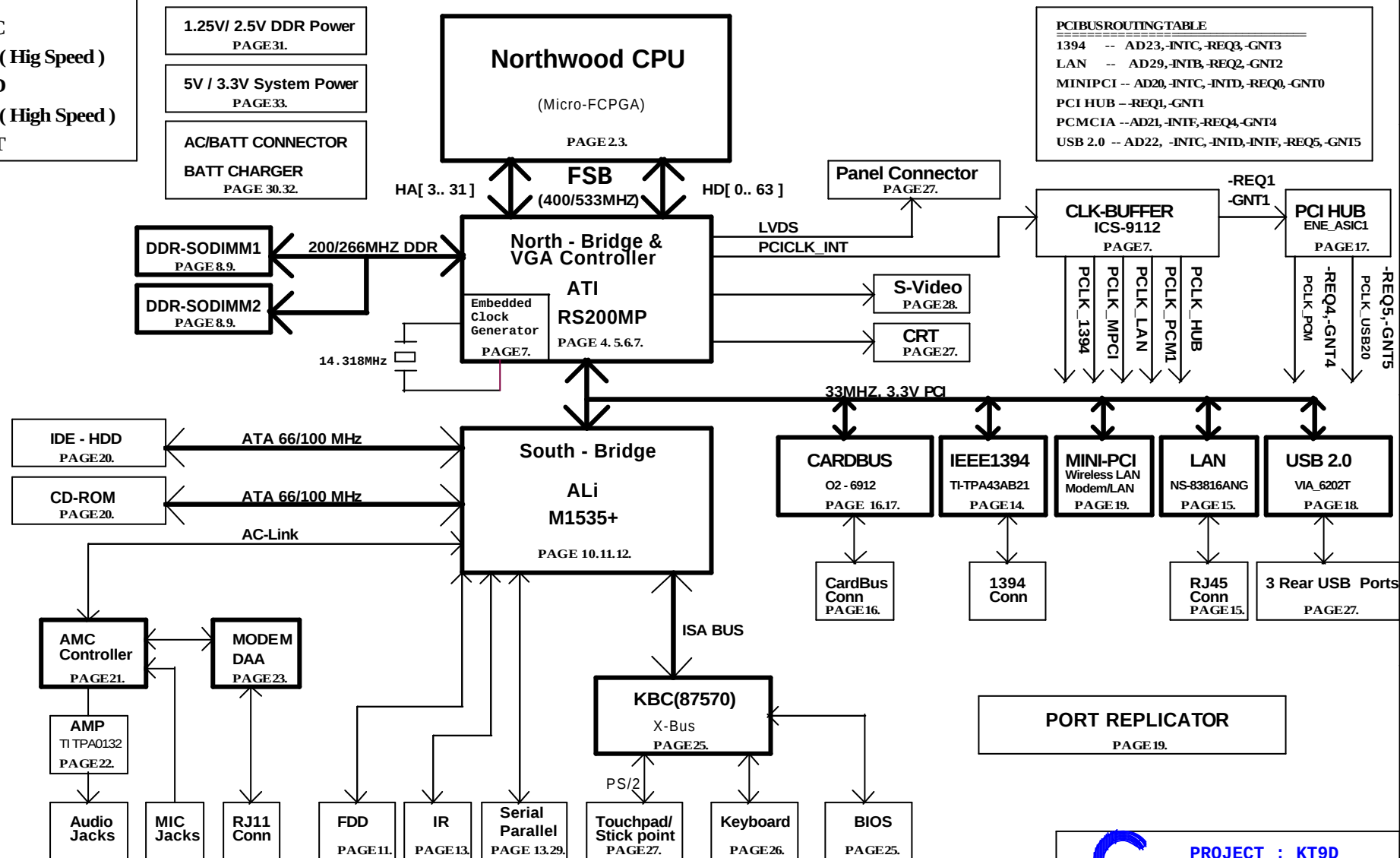


STACK LAYERS

L1 : TOP
L2 : VCC
L3 : IN1 (Hig Speed)
L4 : GND
L5 : IN2 (High Speed)
L6 : BOT

Power - Source Control**KT9D**

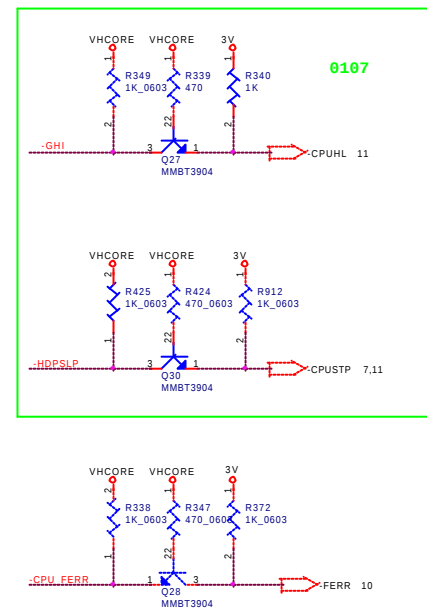
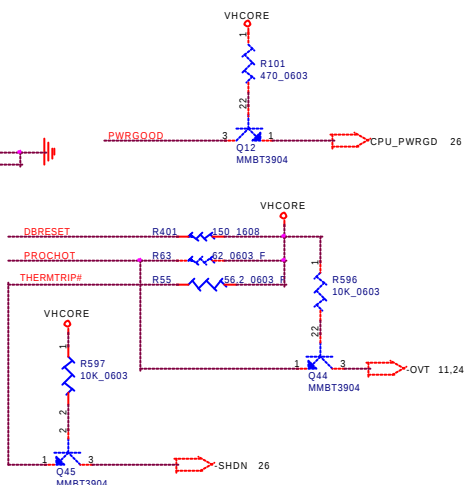
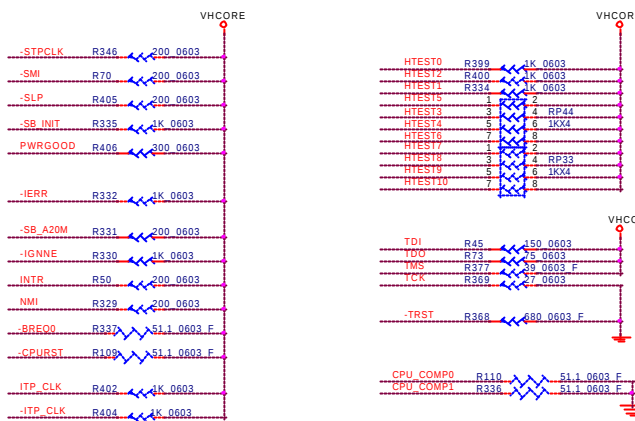
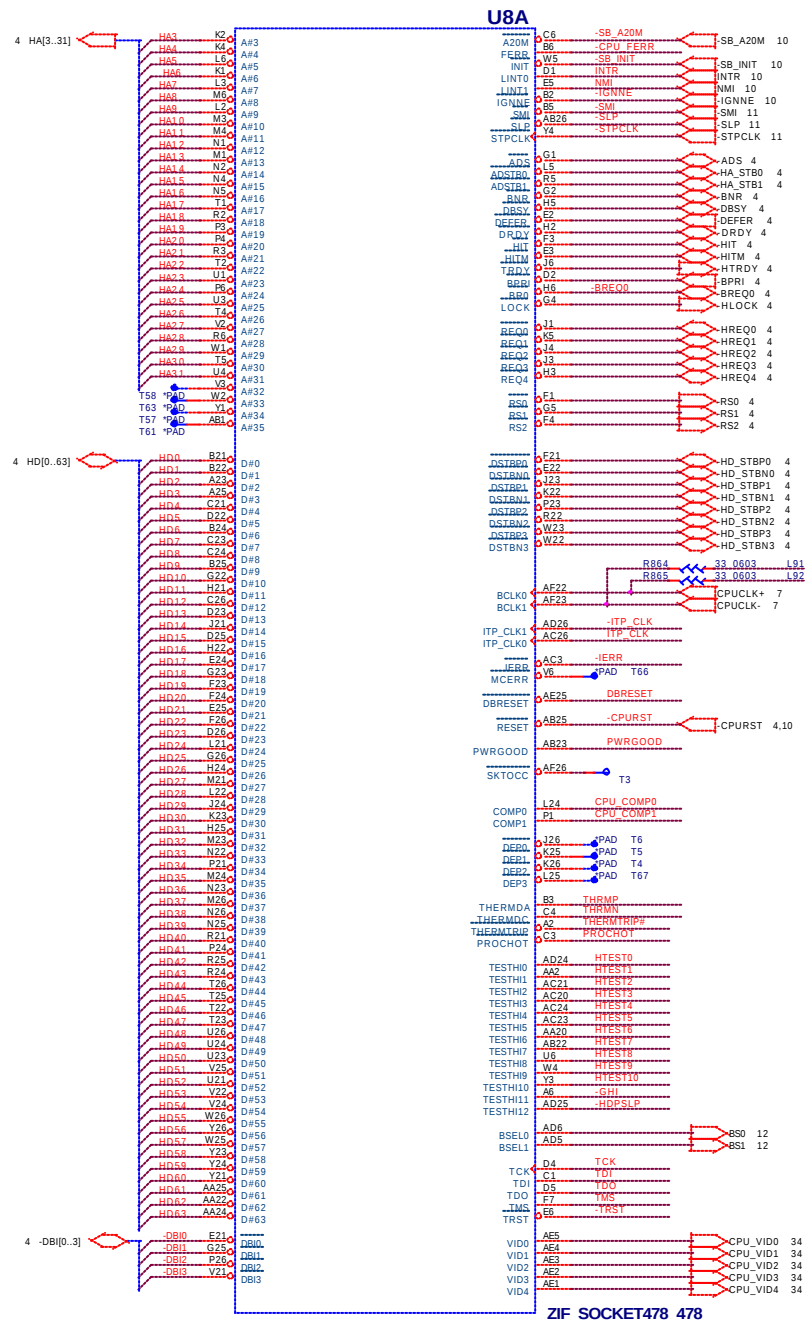
P/N: DAKT9DMB6A0



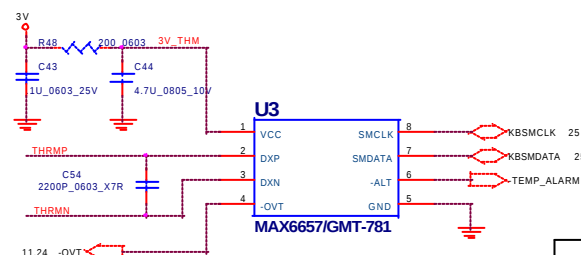
PROJECT : KT9D
Quanta Computer Inc.

NORTHWOOD CPU -A

02

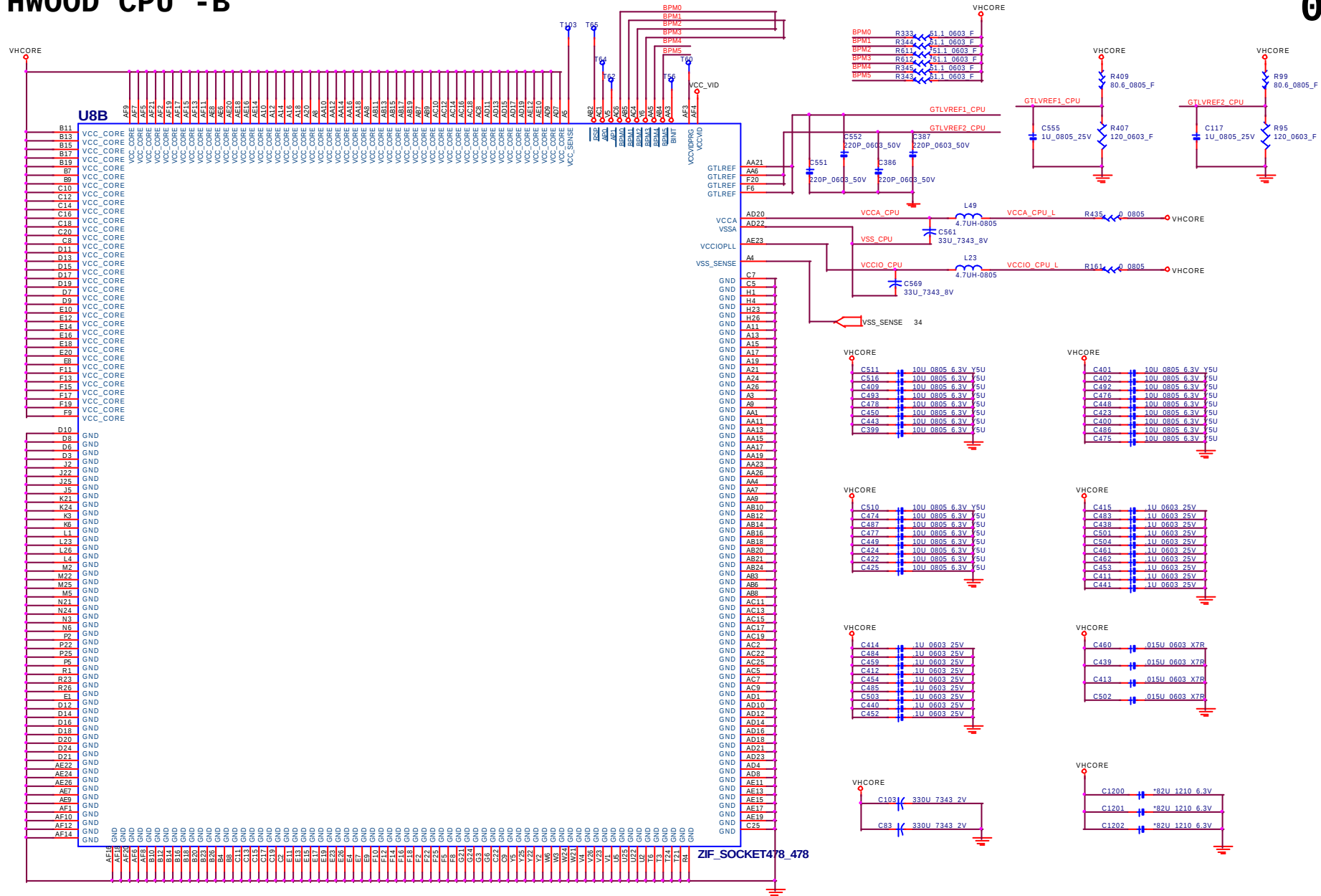


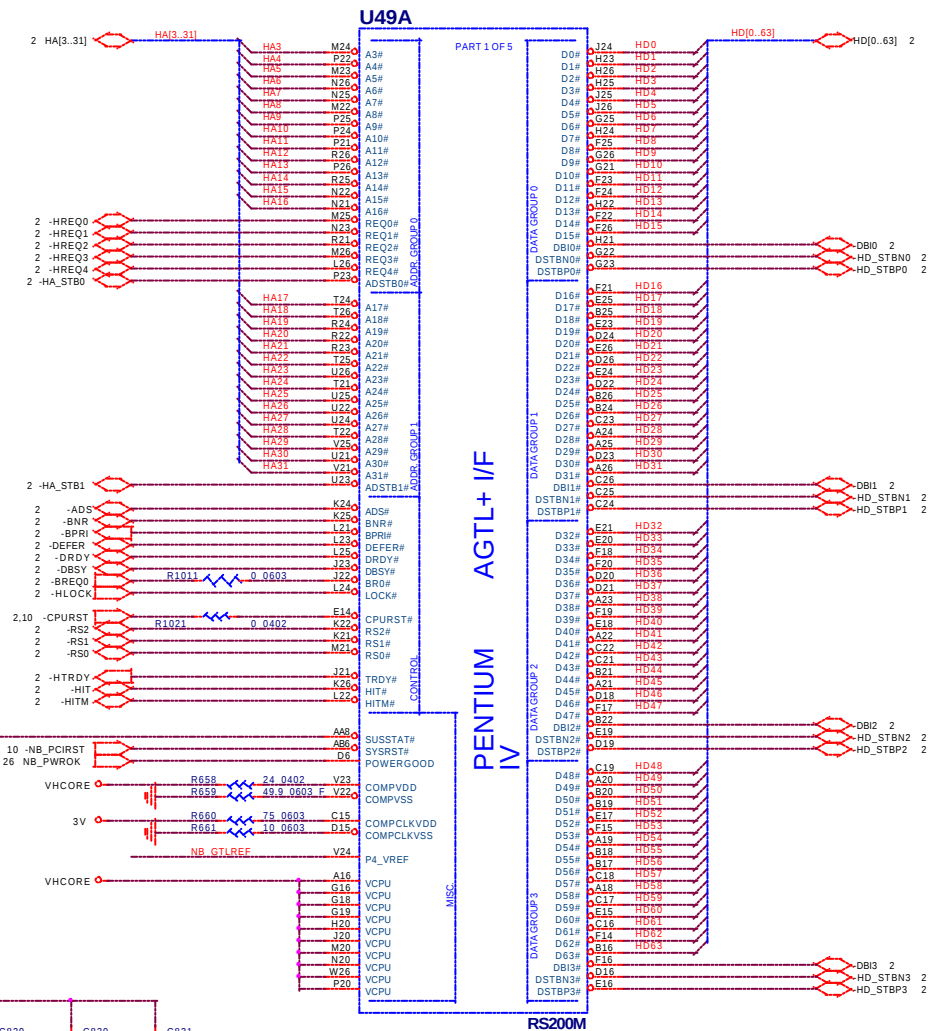
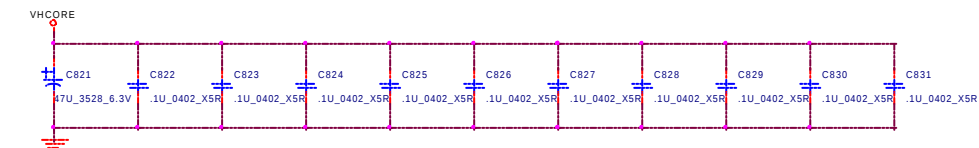
H/W MONITOR



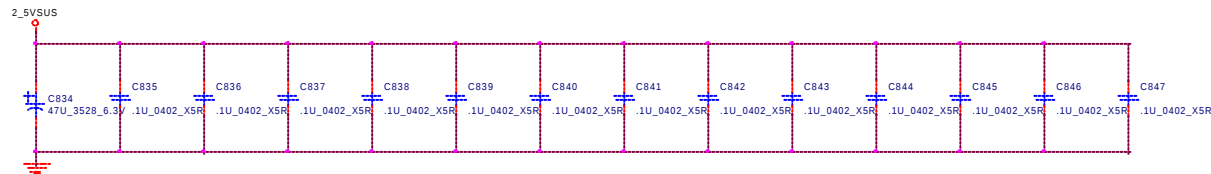
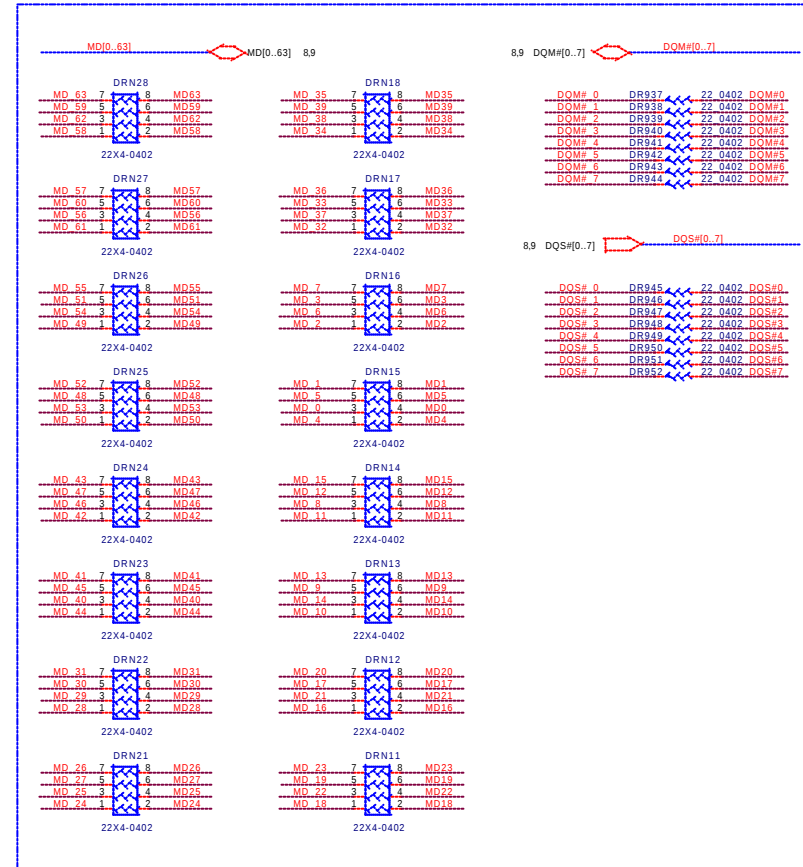
NORTHWOOD CPU -B

03





05

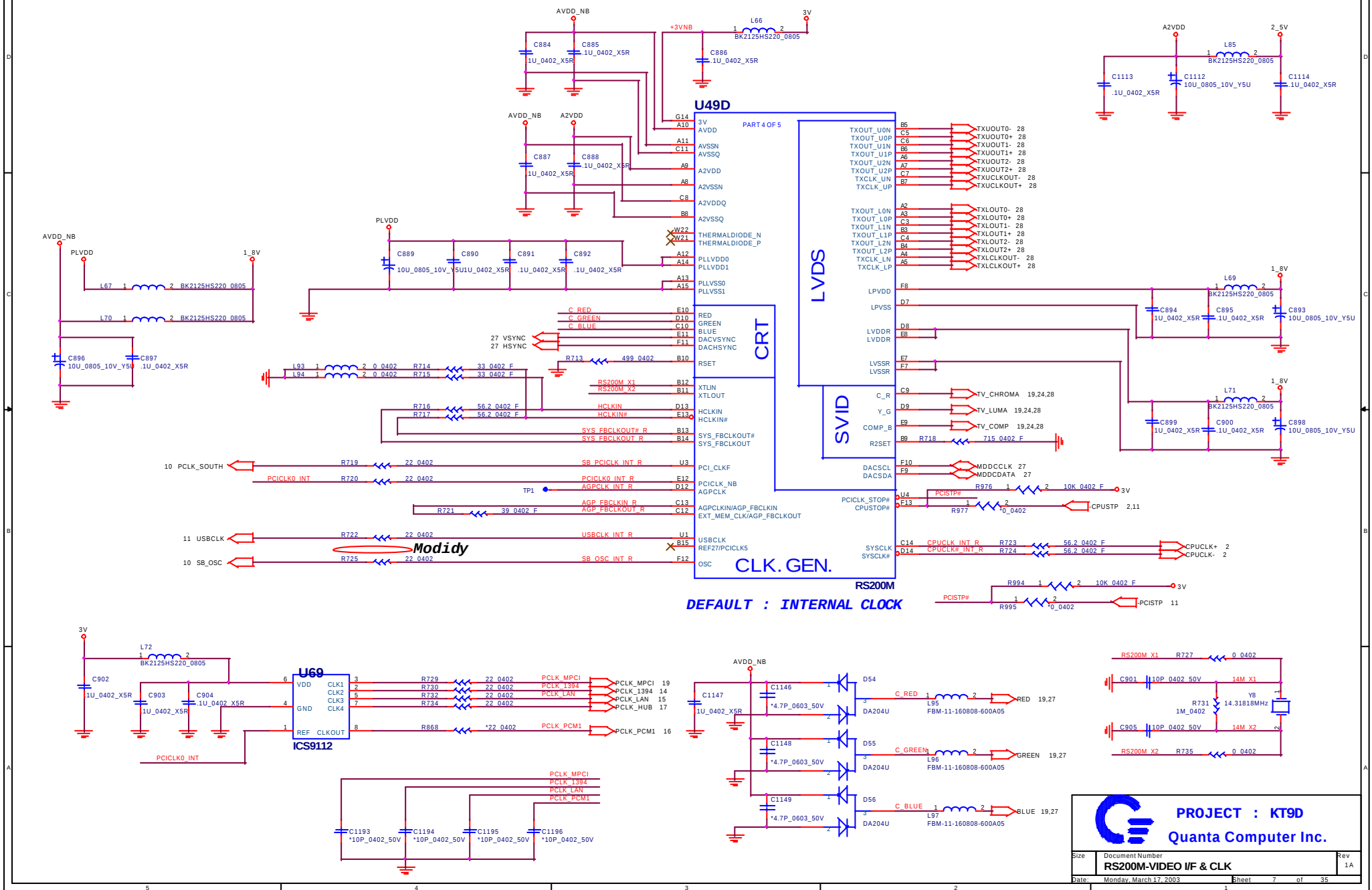


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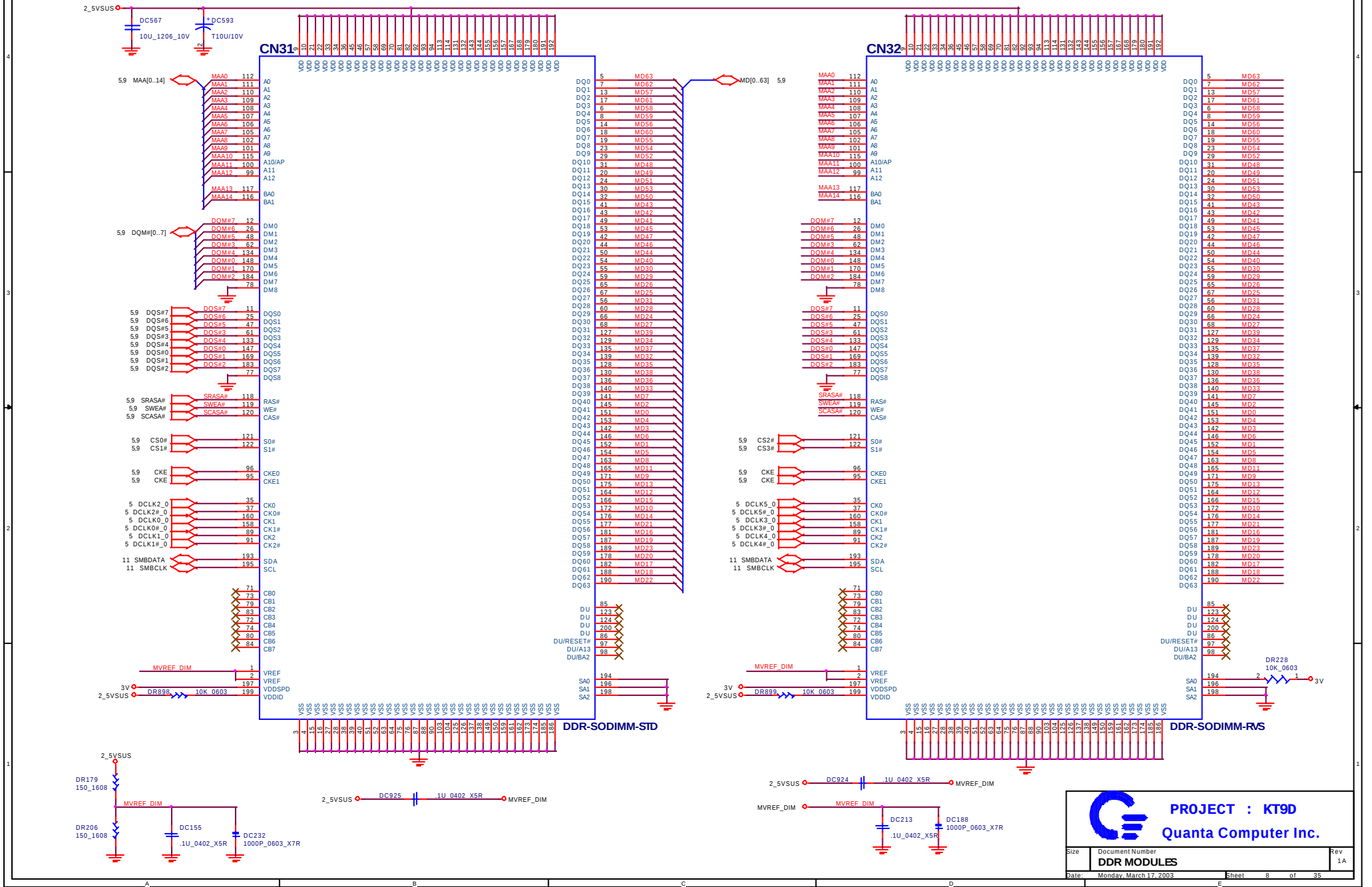
Size	Document Number	Rev
	RS200M-PCI/AGP I/F	1A
Date:	Monday, March 17, 2003	Sheet 6 of 35

07



DDR - Modules

08



PROJECT : KT9D

Quanta Computer Inc.

Size	Document Number	Rev
	DDR MODULES	1A
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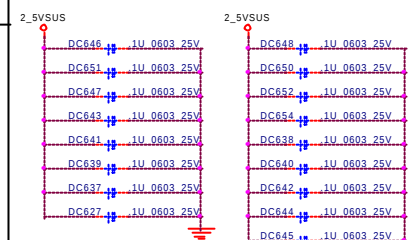
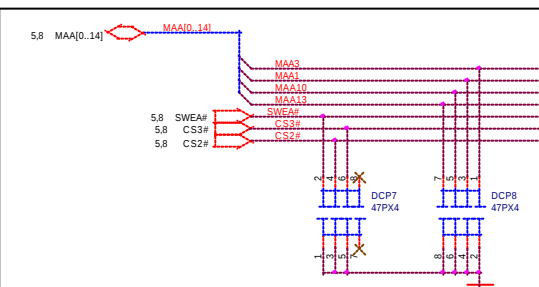
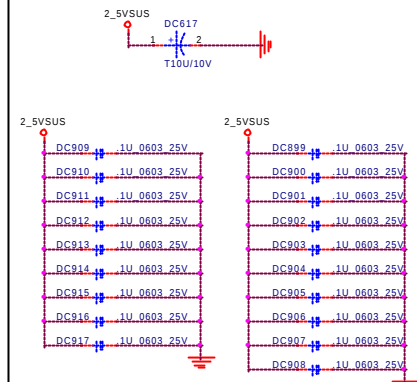
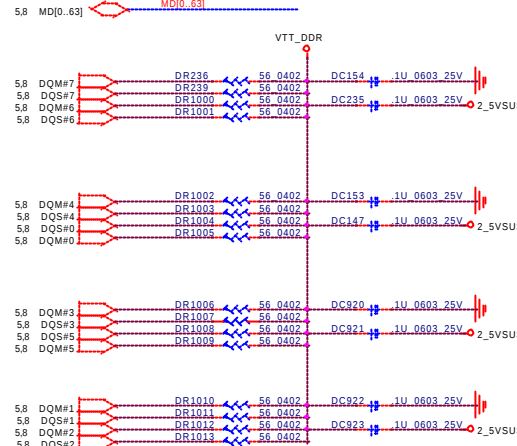
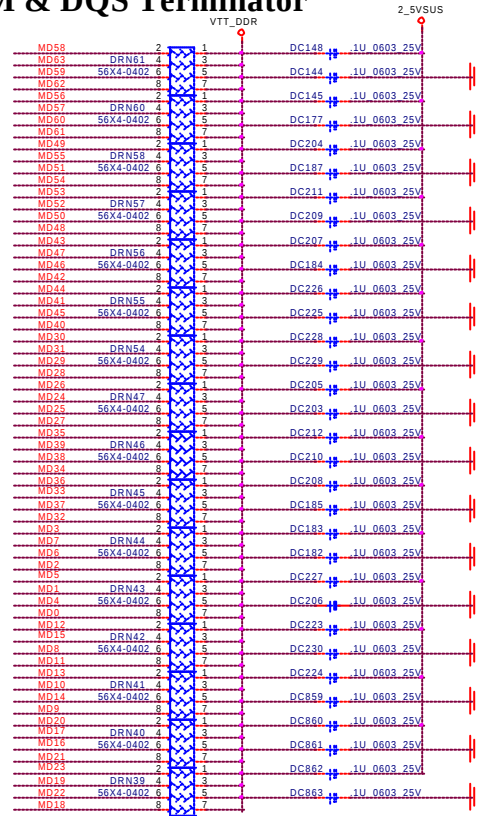
DDR TERMINATOR



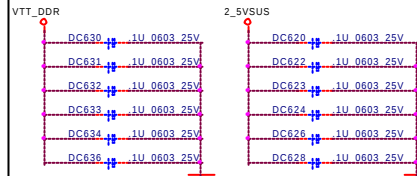
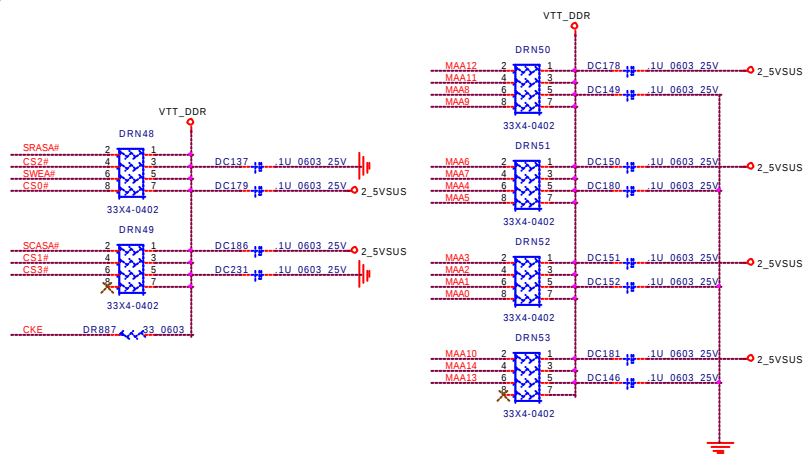
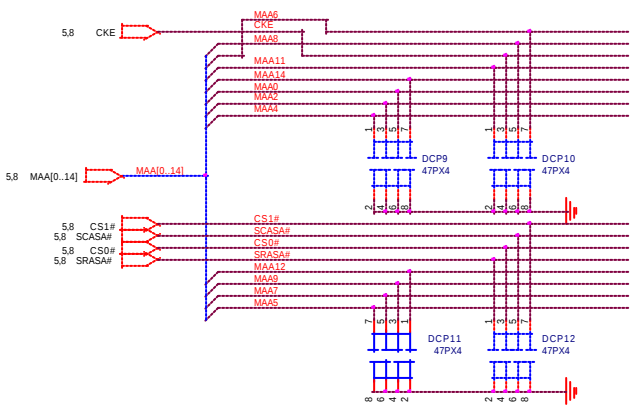
Could swap pin for layout .

09

MD & DQM & DQS Terminator



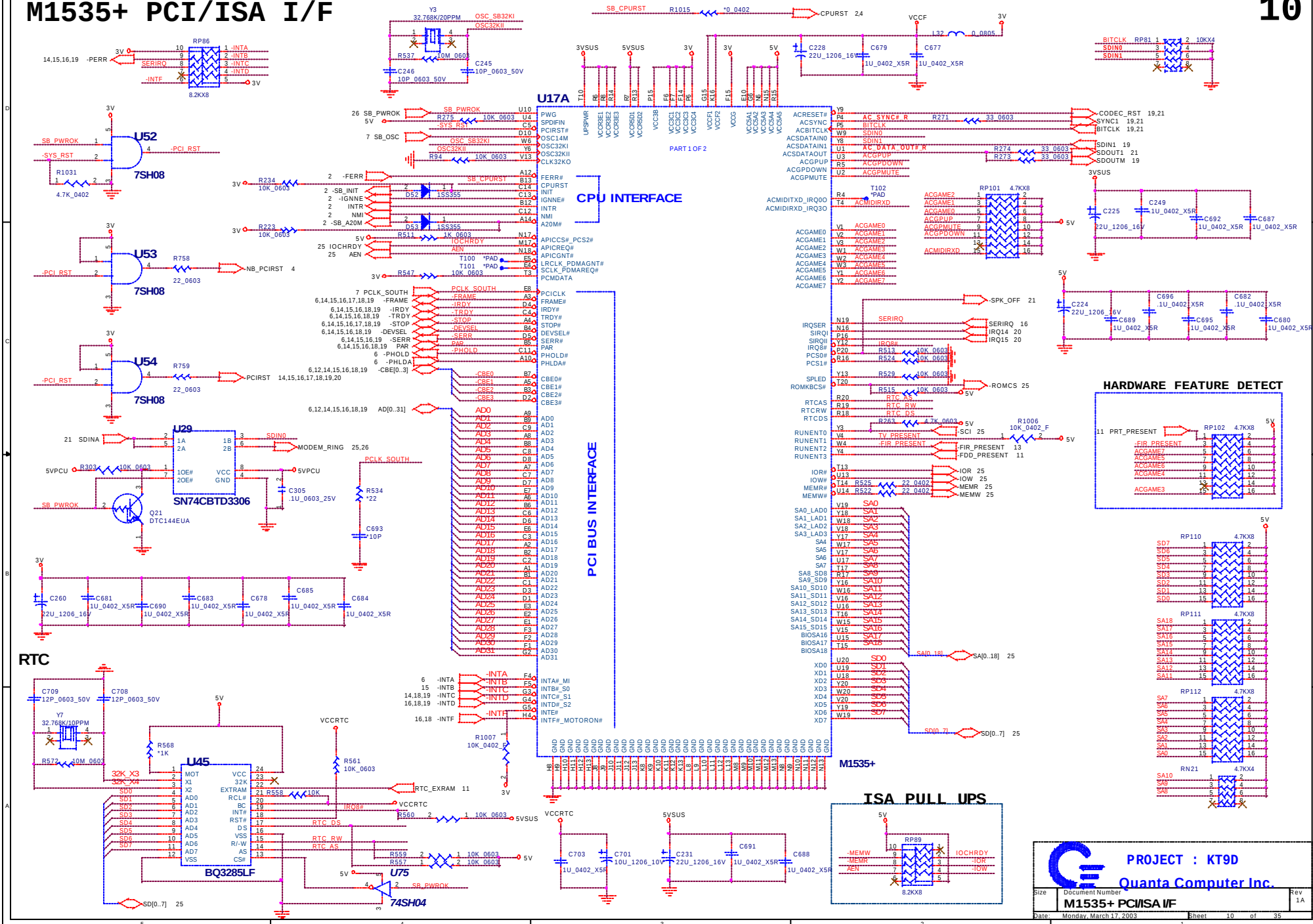
MA Terminator



PROJECT : KT9D
Quanta Computer Inc.

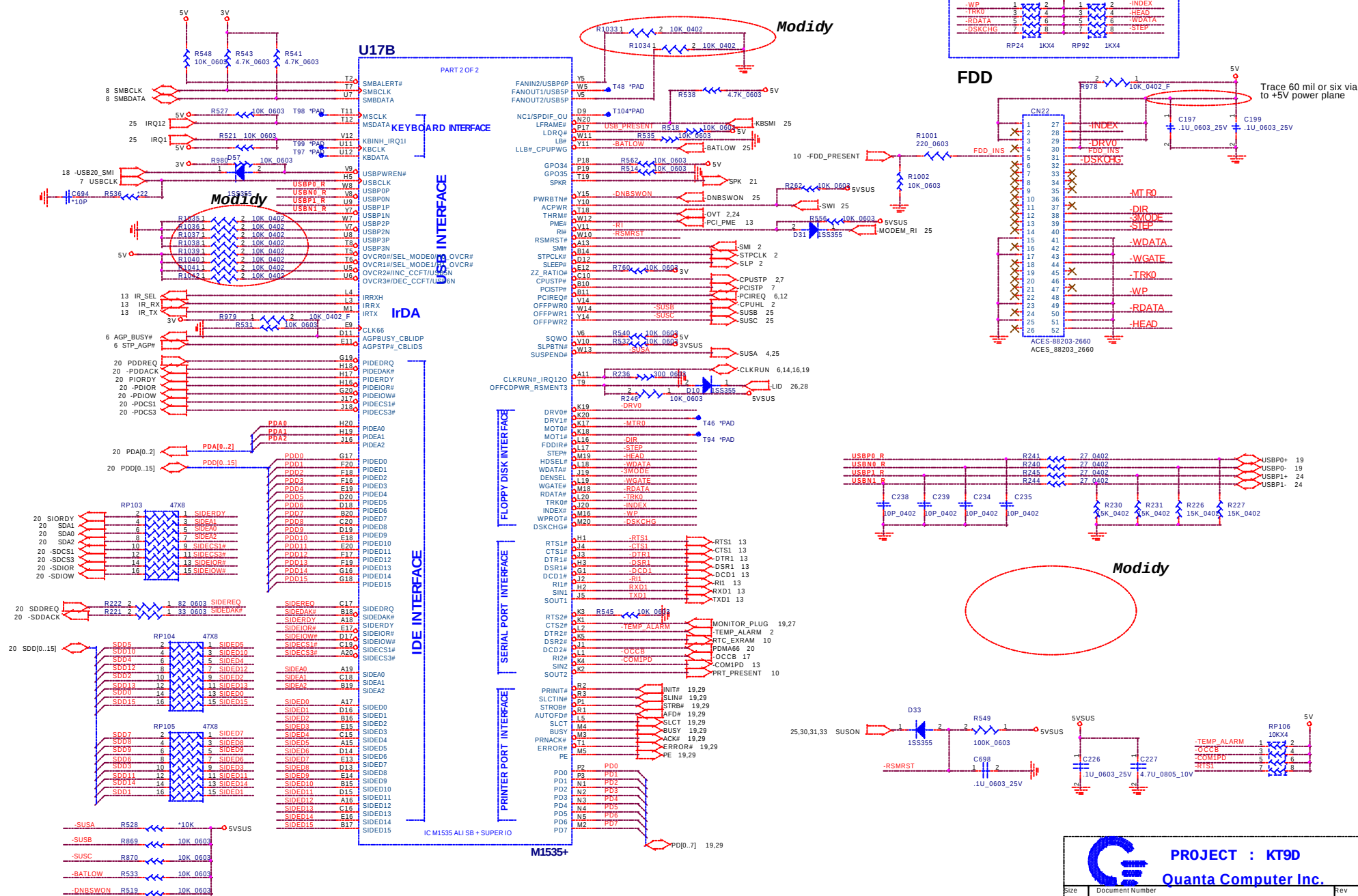
M1535+ PCI/ISA I/F

10



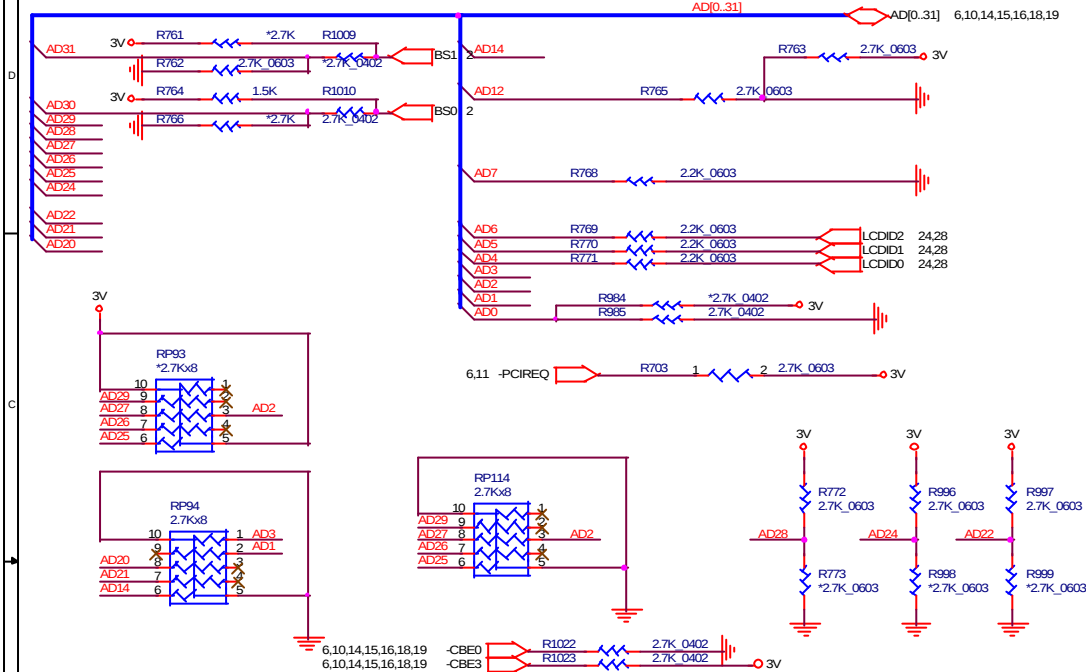
M1535+_USB, IDE, FDD INTERFACE

11



N.B & S.B H/W STRAPPING

NORTH BRIDGE H/W STRAPPING



AD[31..30] : CLK SPEED DEFAULT: 00	00: 100 MHz 01: 133 MHz 10: 166MHz 11: 66 MHz	AD15 : RESERVED
AD29: RS200M FULL CONFIGURATION DEFAULT: 0	0: FULL CONFIGURATION 1: USES DEFAULT VALUES	AD14 : AGTL+ VDDQ DEFAULT: 0
AD28: SPREAD SPECTRUM ENABLE DEFAULT: 0	0: DISABLE 1: ENABLE	AD13: RESERVED
AD27: MCERR OBSERVATION DEFAULT: 0	0: DISABLE 1: ENABLE	AD[12] : FLAT PANEL ID MSB
AD26: QUICK START SELECT DEFAULT : 0	0: DISABLE 1: ENABLE	AD10 : RESERVED
AD25: INIT OBSERVATION DEFAULT : 0	0: DISABLE 1: ENABLE	AD09 : RESERVED
AD24: IOQ ENABLE DEFAULT : 1	0: DISABLE 1: ENABLE	AD[7..4] : FLAT PANEL ID
AD23 : RESERVED		AD3: PC166 MODE DEFAULT: 0
AD22: 27MHz/33 MHz CLOCK SELECTION DEFAULT : 1	0: 33 MHz CLOCK OUTPUT 1: 27 MHz CLOCK INPUT	AD2: CAL DEFAULTS FOR CPU DEFAULT: 0 (FOR BRING UP)
AD21 : RESERVED	0: PLL Bypass mode disable(Normal mode) 1: PLL Bypass mode enable(Debug only)	AD1: FrcShortReset DEFAULT: 0
AD20 : PCICLK EXPANSION DEFAULT : 0	1: PCI_REQ#3, PCI_GNT#3 USED AS PCICLK 0: PCI_REQ#3, PCI_GNT#3 USED AS REQ/GNT	AD0: PCI/AGP CONFIG. DEFAULT : 0
CBE#3: PROD TEST DEFAULT : 1	0: SHORT TIMERS 1: NORMAL OPERATION	-PCI_REQ: INTERNAL CLOCK GEN. DEFAULT : 1

12

SOUTH BRIDGE H/W STRAPPING

	DTR2#	RTS2#	RTS1#	CLK320	SPLED	SQWO
PULL HIGH	INTERNAL KEYBOARD PS2 MODE DEFAULT	ENABLE INTERNAL KEYBOARD	SIO BASE ADDRESS 0X3F0h	32K CLOCK TEST MODE ENABLE	AT POWER MODE	PENTIUM II MODE DEFAULT
PULL LOW	INTERNAL KEYBOARD AT MODE	DISABLE INTERNAL KEYBOARD DEFAULT	SIO BASE ADDRESS 0X370h DEFAULT	32K CLOCK TEST MODE DISABLE DEFAULT	ATX POWER MODE DEFAULT	PENTIUM / K7 MODE

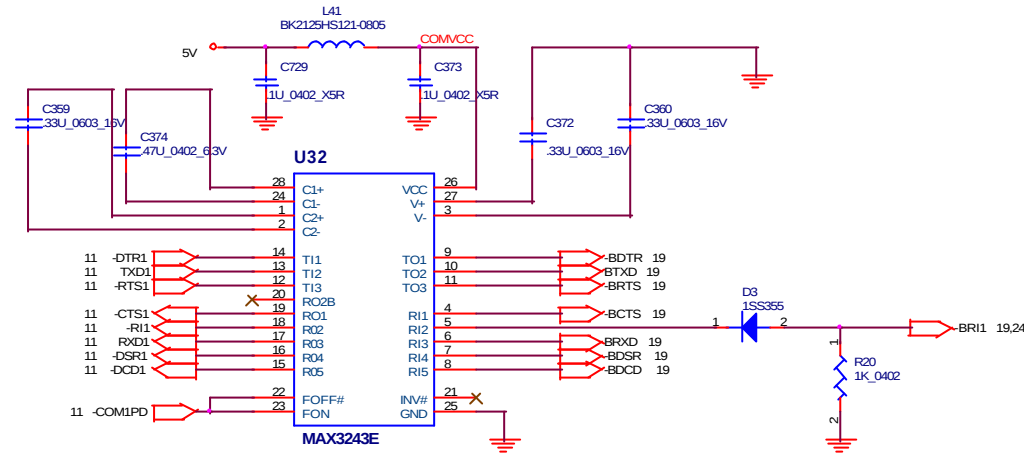
PCS1#	PCS0#	GPO34	LB#	GPO30[AEN]	GPO29	ROMCS#	GPO35
GP124 & GP030 FUNCTIONS DEFAULT	DISABLE 4MB FLASH ROM MODE DEFAULT	CHIP TEST MODE DISABLE DEFAULT	ACTIVE HIGH TO POWER OFF DEFAULT	L, L - USB TEST MODE L, H - AUDIO/MODEM TEST MODE H, L - SUPER IO TEST MODE H, H - NORMAL OPERATION DEFAULT : H, H		FLASH ROM MODE SELECTION L, L - RESERVED L, H - ENABLE 8Mb Flash ROM SIZE H, L - DISABLE ALL FUNC. IN THIS CONFIG. H, H - ENABLE LPC FLASH ROM DEFAULT : H, L	



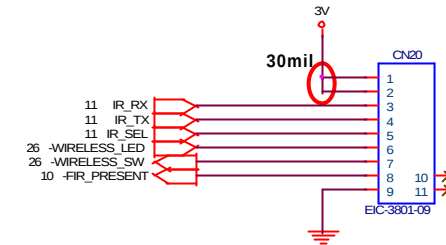
PROJECT : KT9D
Quanta Computer Inc.

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	N.B & S.B H/W STRAPPING	1A
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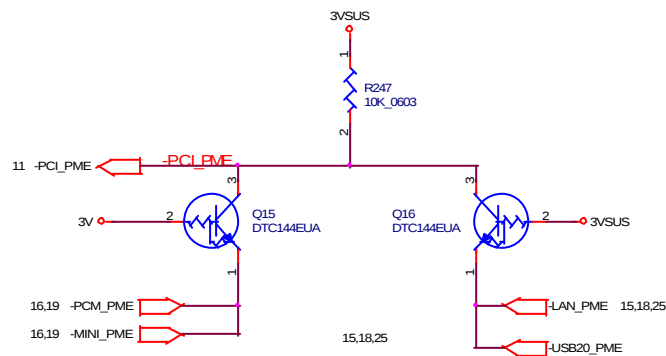
SERIAL PORT



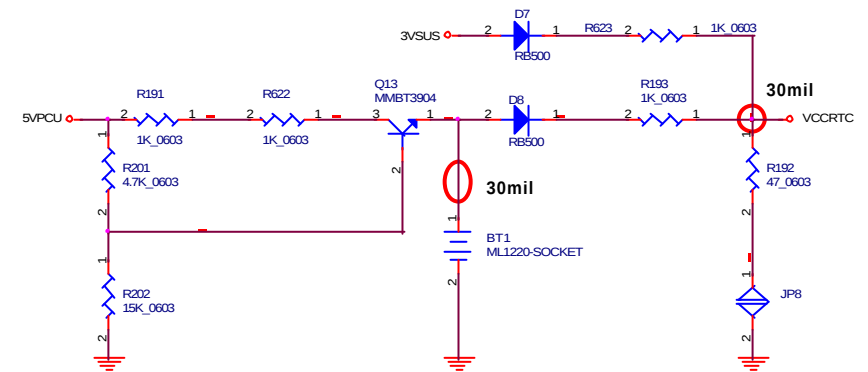
FIR CON



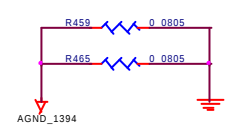
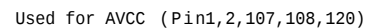
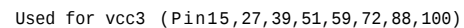
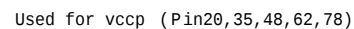
PME CONTROL



RTC POWER CIRCUIT

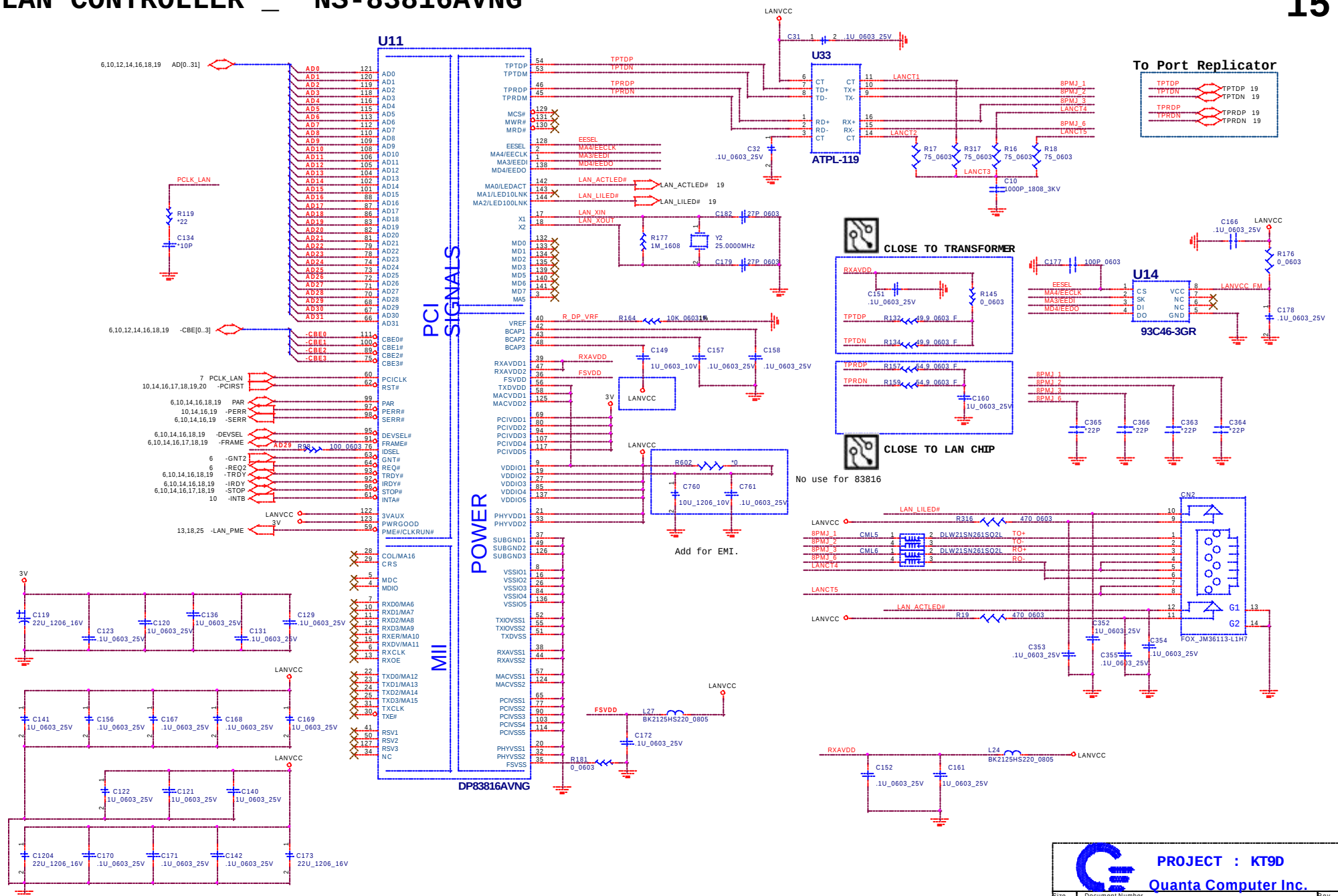


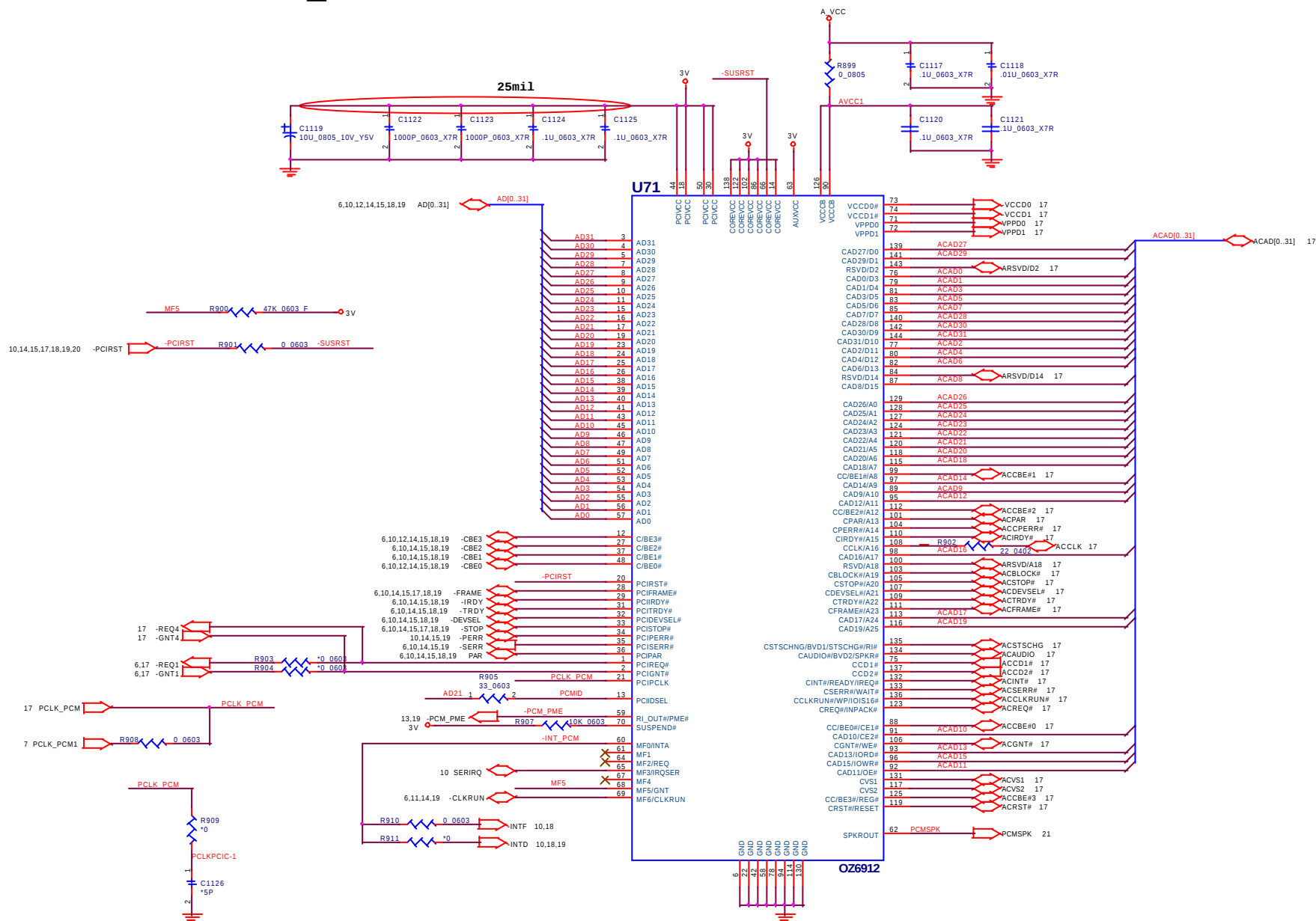
14



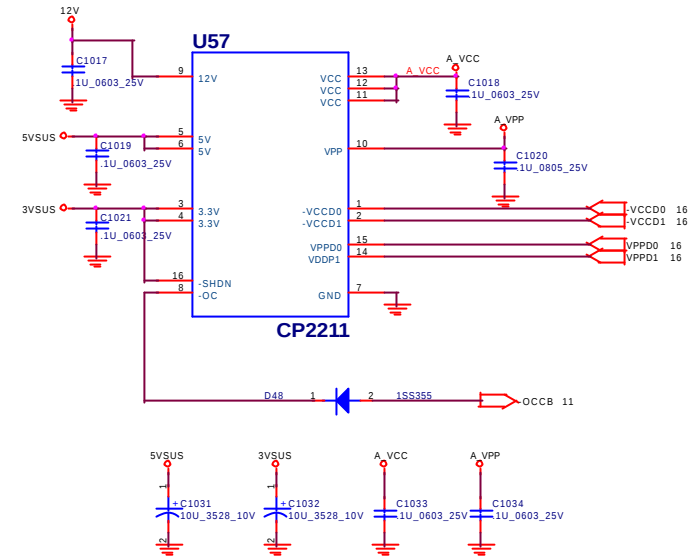
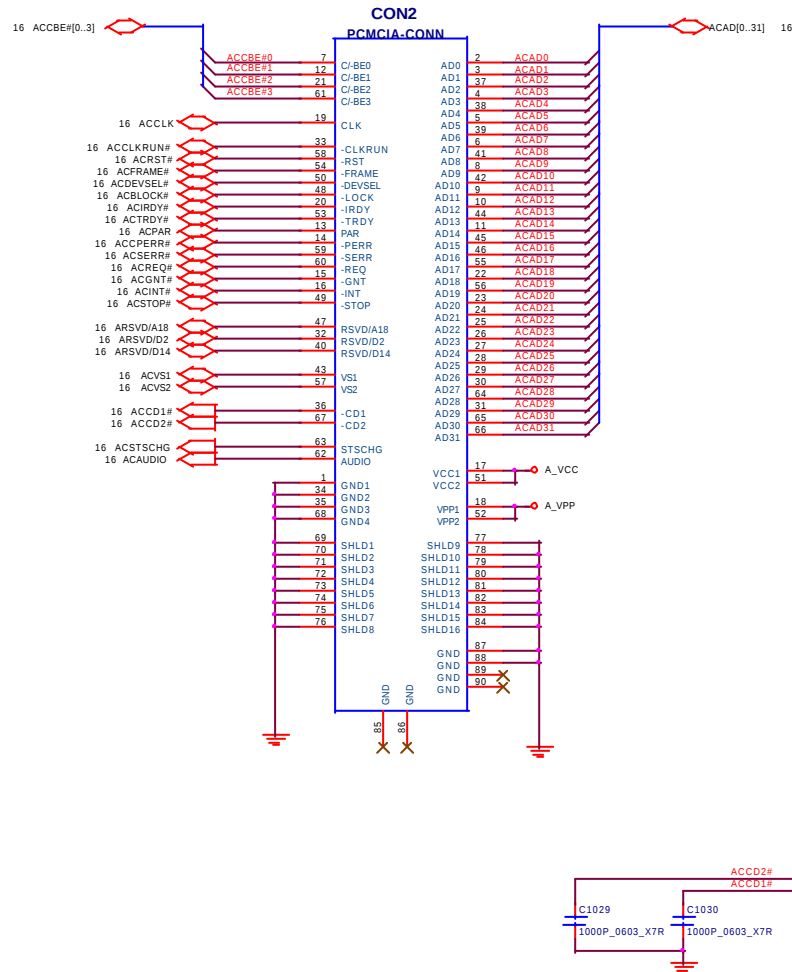
LAN CONTROLLER NS-83816AVNG

15

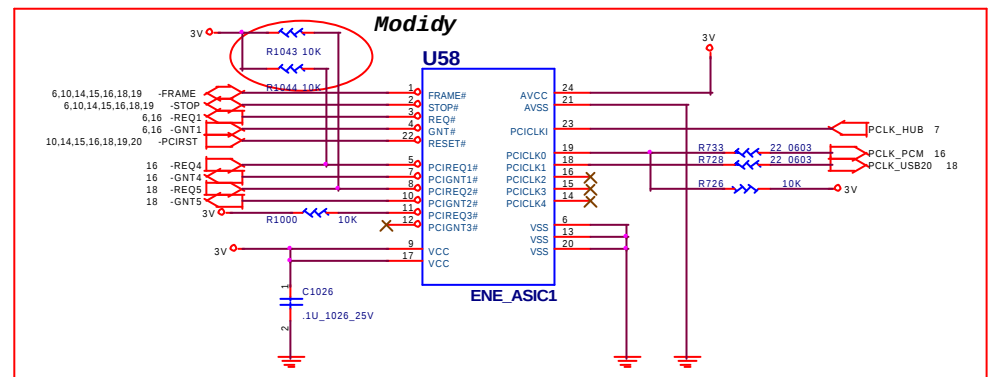


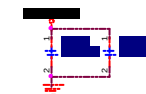
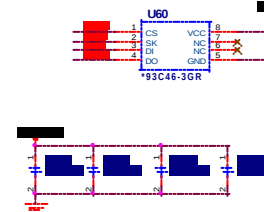
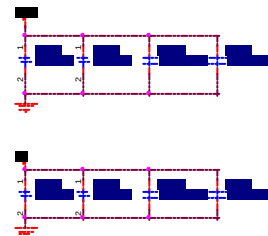
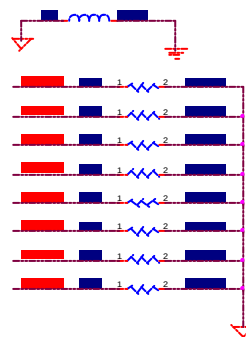
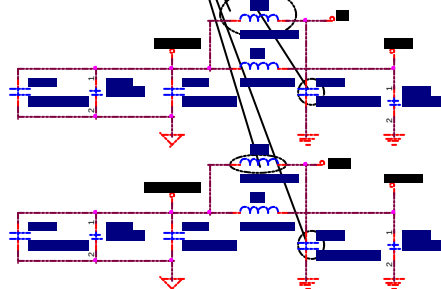
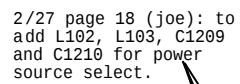
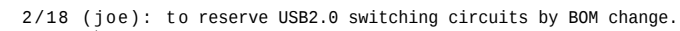


CARDBUS POWER SWITCH



PCI HUB_ENE-ASIC1



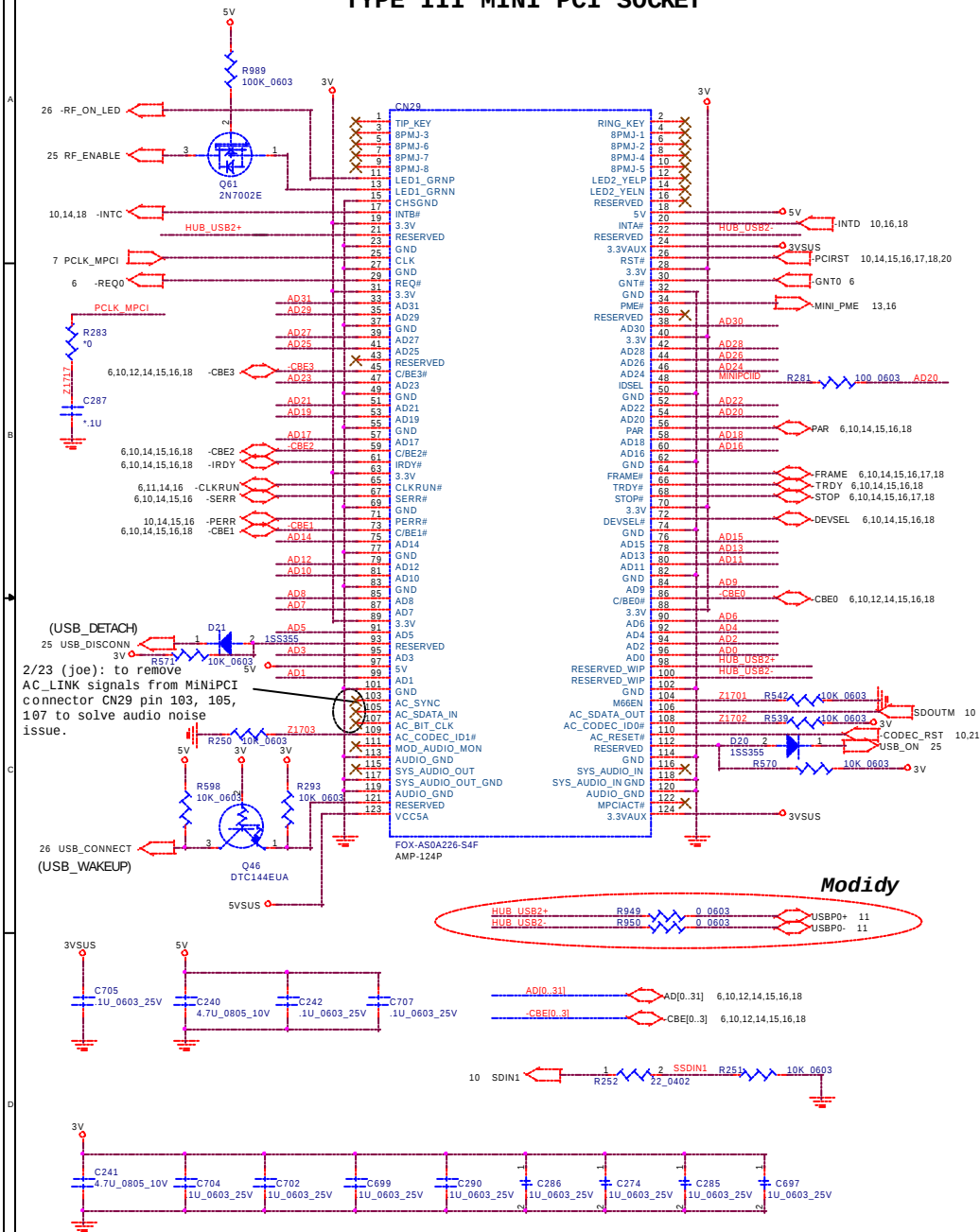


Loic	NC	NO
0	ON	OFF
1	OFF	ON

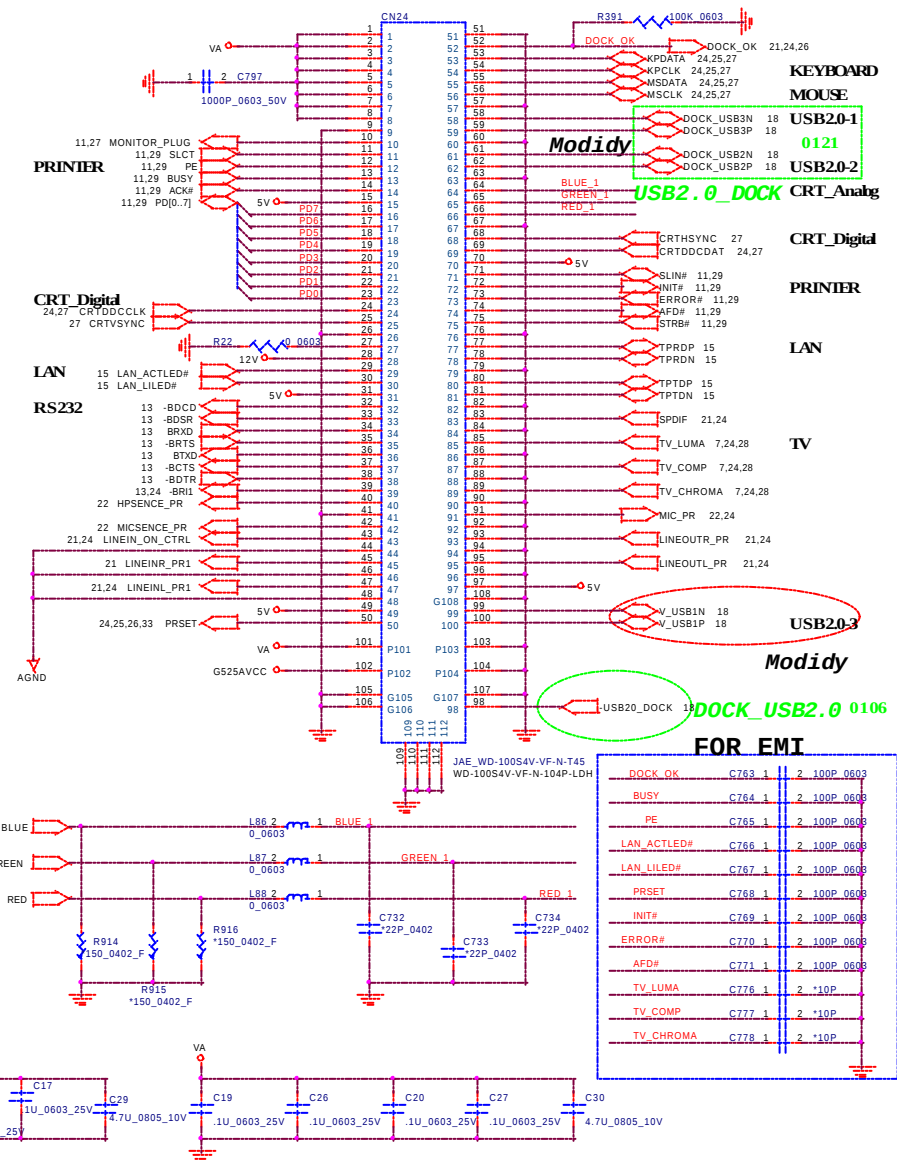
MINI PCI CON. & PR CON.

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TYPE III MINI PCI SOCKET



PORT REPLICATOR Connector

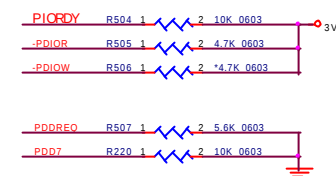
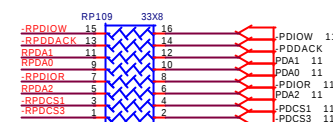
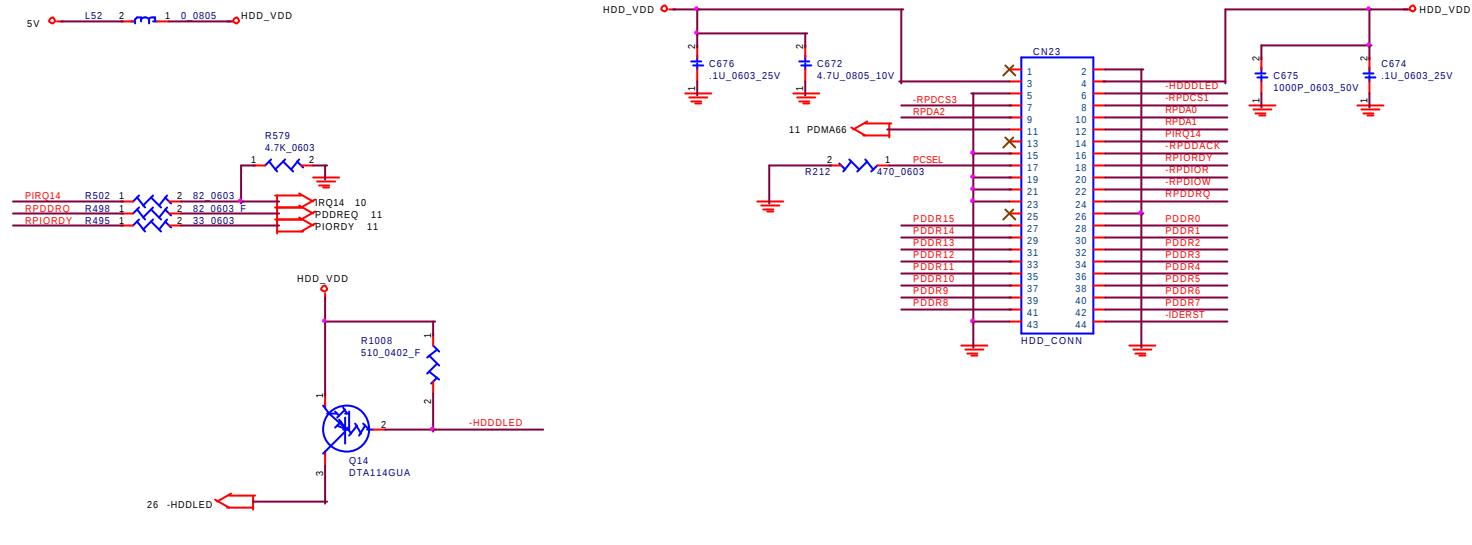


IDE INTERFACE

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HDD CONNECTOR

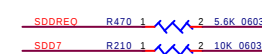
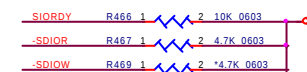
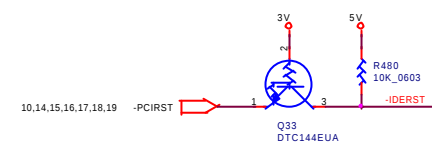
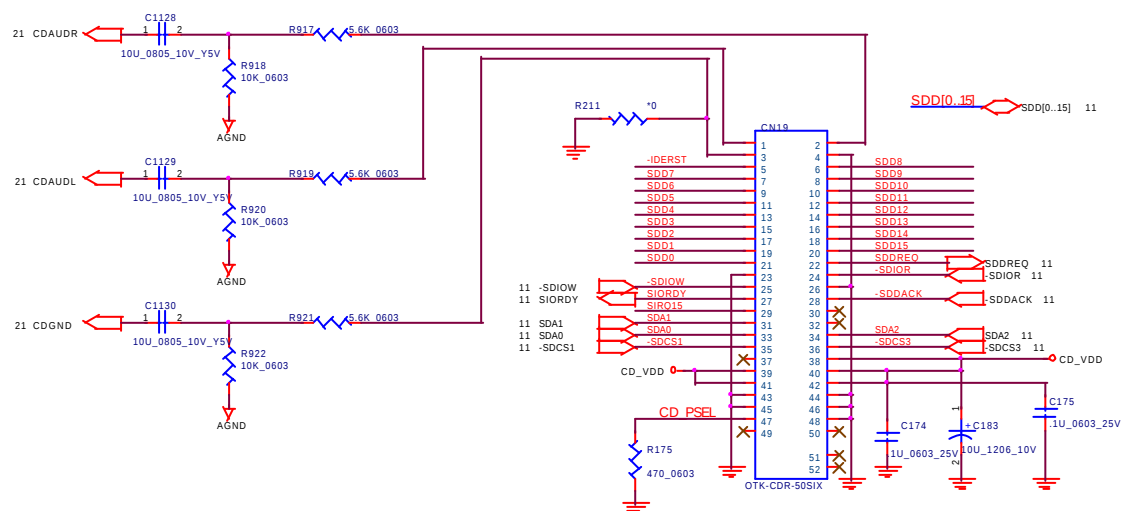
40MIL



CD-ROM CONNECTOR

80MIL

5V L26 2 1 0.0805 CD_VDD

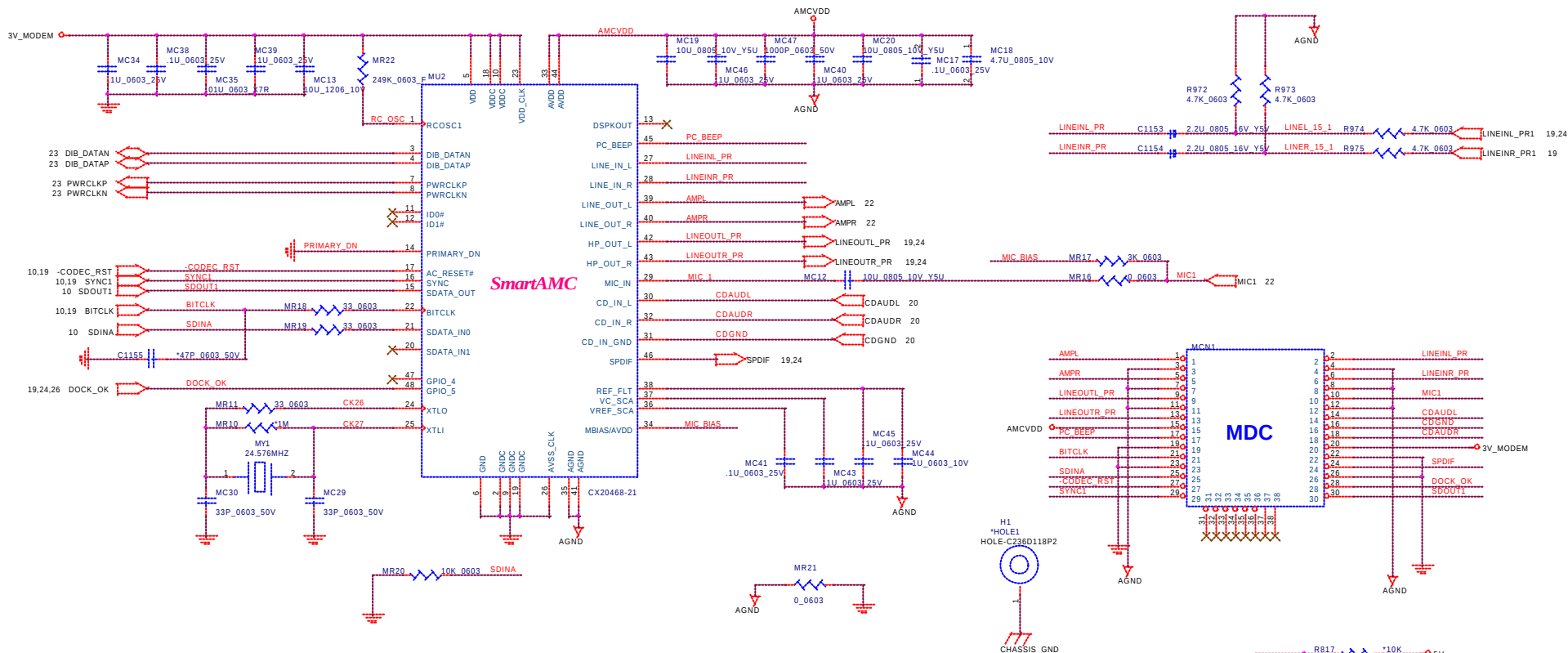


PROJECT : KT9D

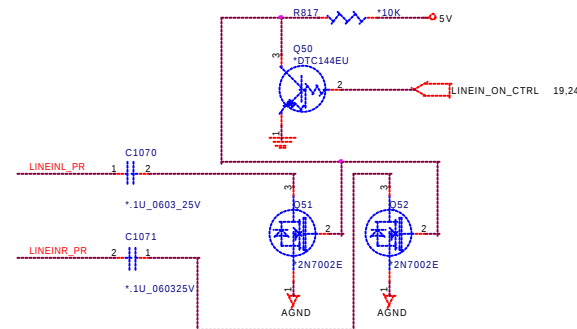
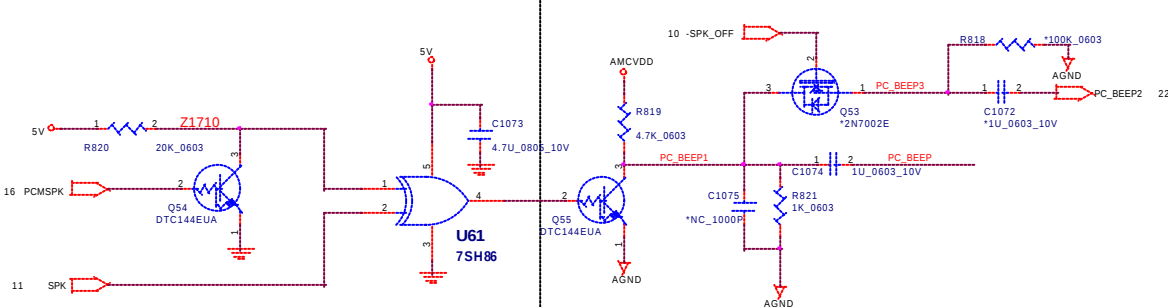
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The AMC20493-001 modem is used for mother board family MBAMC20493-006.



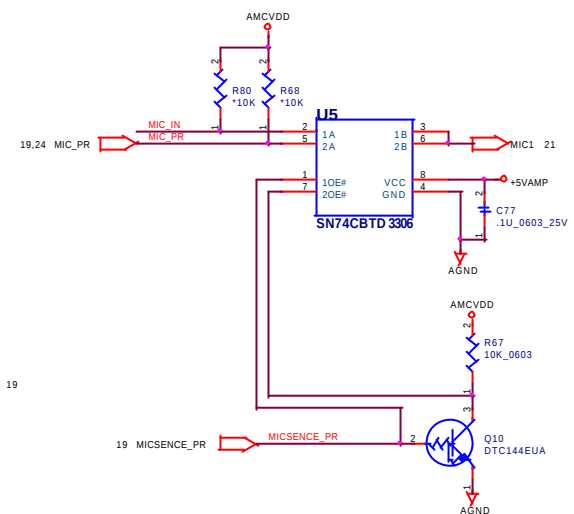
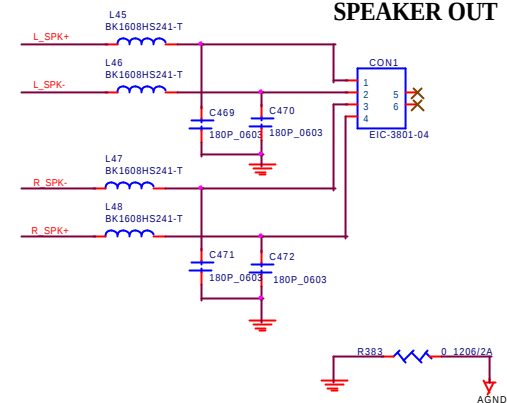
PC SPEAKER



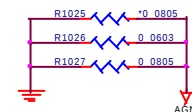
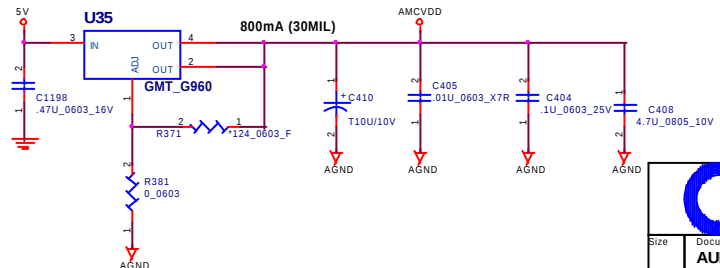
PROJECT : KT9D
Quanta Computer Inc.

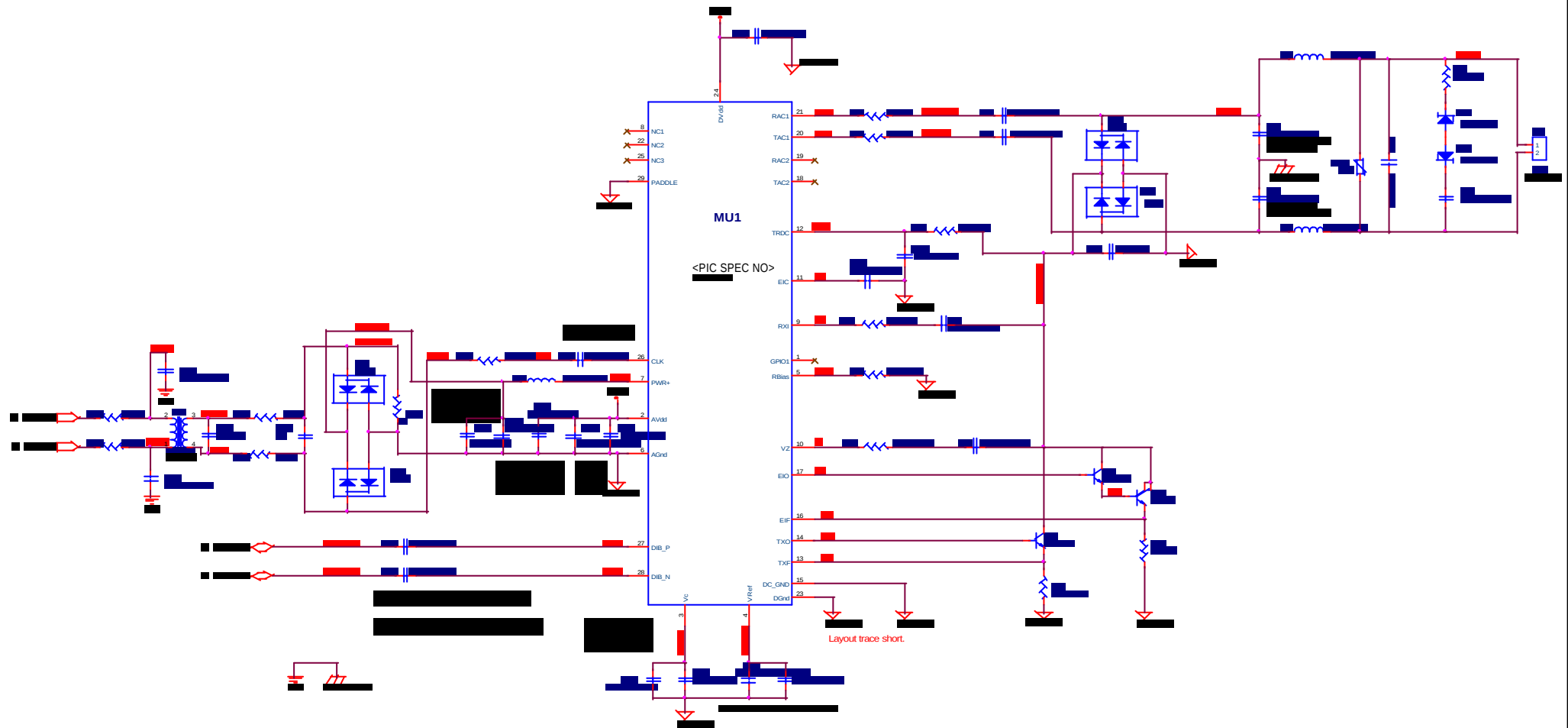
Size Document Number
AMC97 CODEC
Date Monday, March 17, 2003 Sheet 21 of 35 Rev 1A

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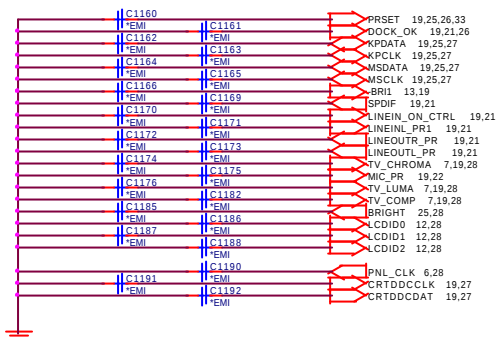


Gain0	Gain1	Av
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

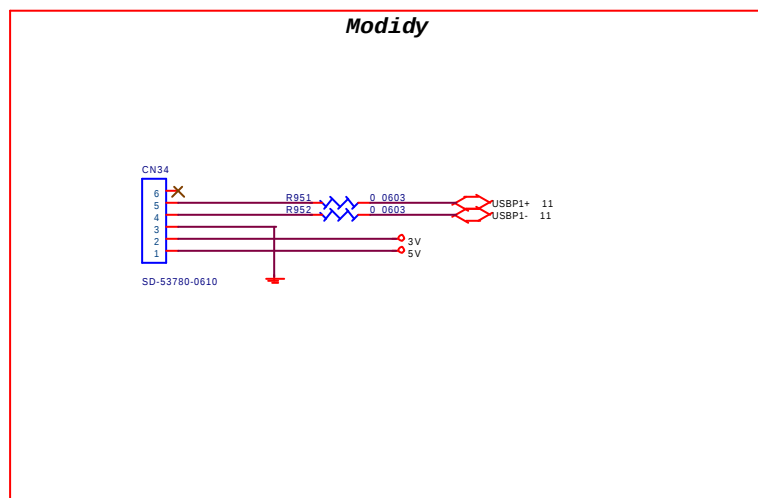




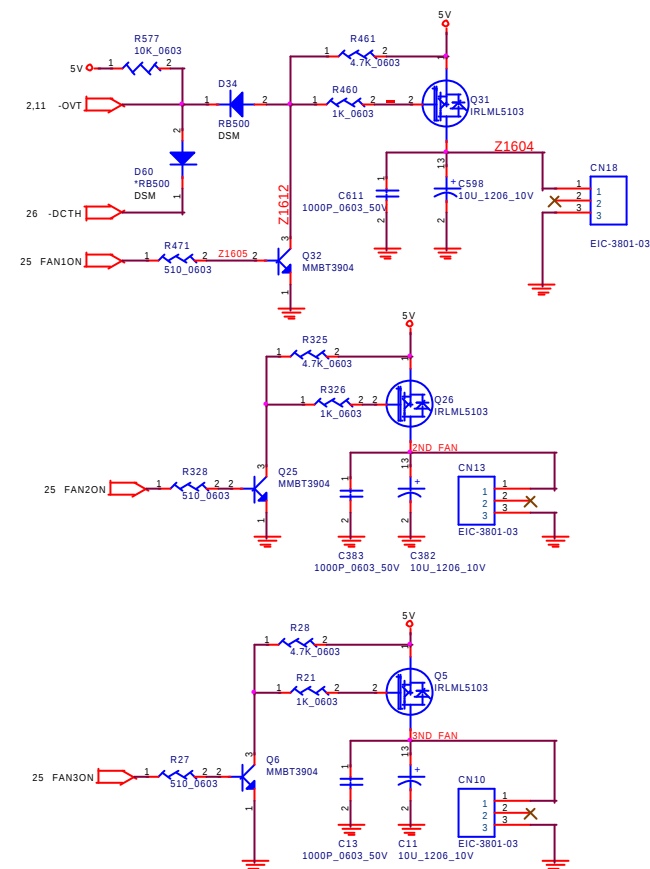
EMI



CARD READER (Option)



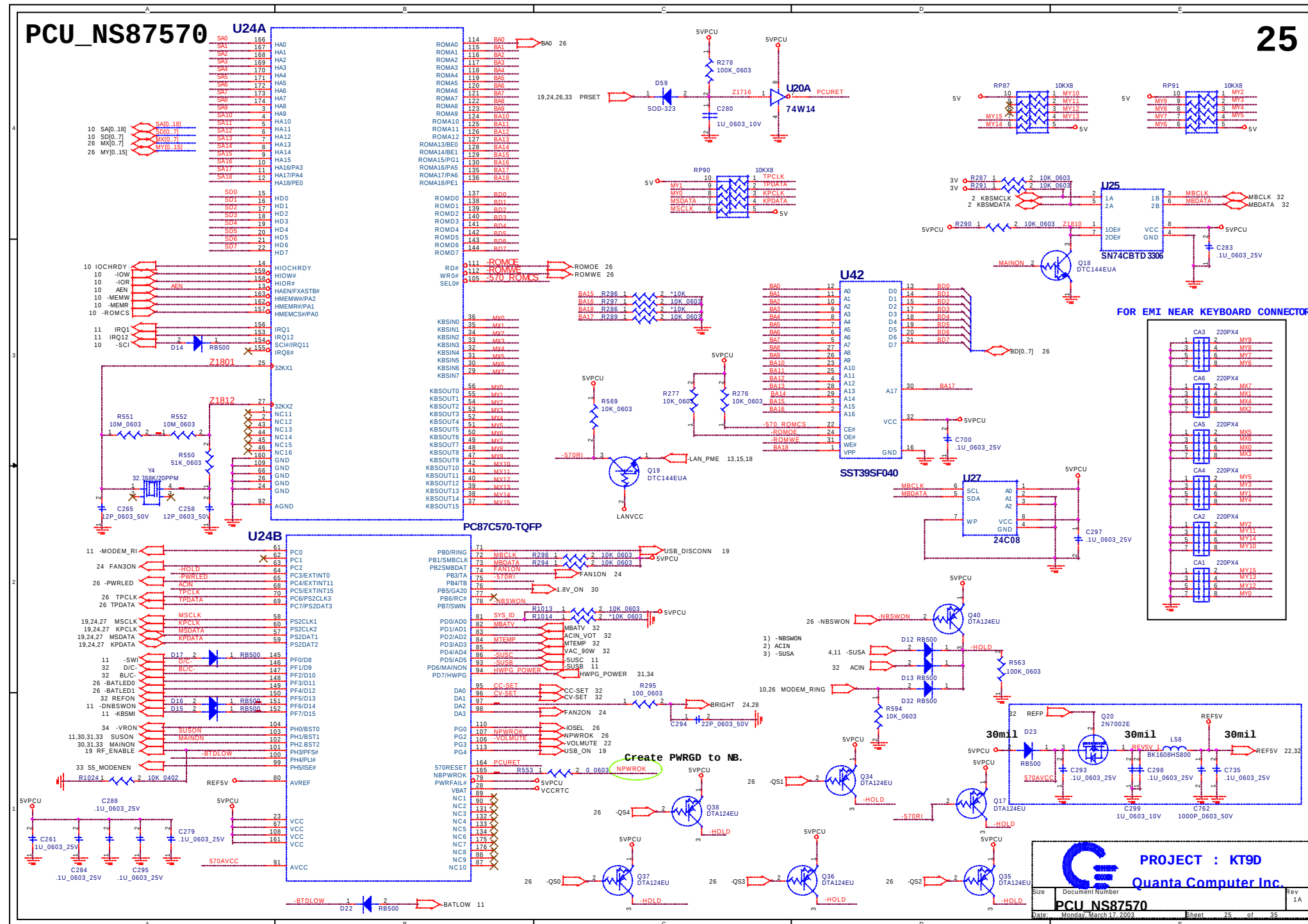
FAN CONTROL



PCU_NS87570

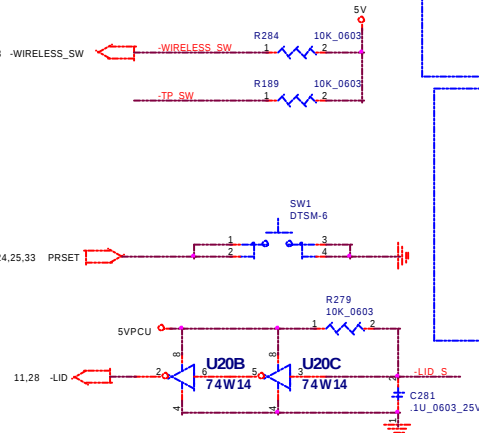
U24A

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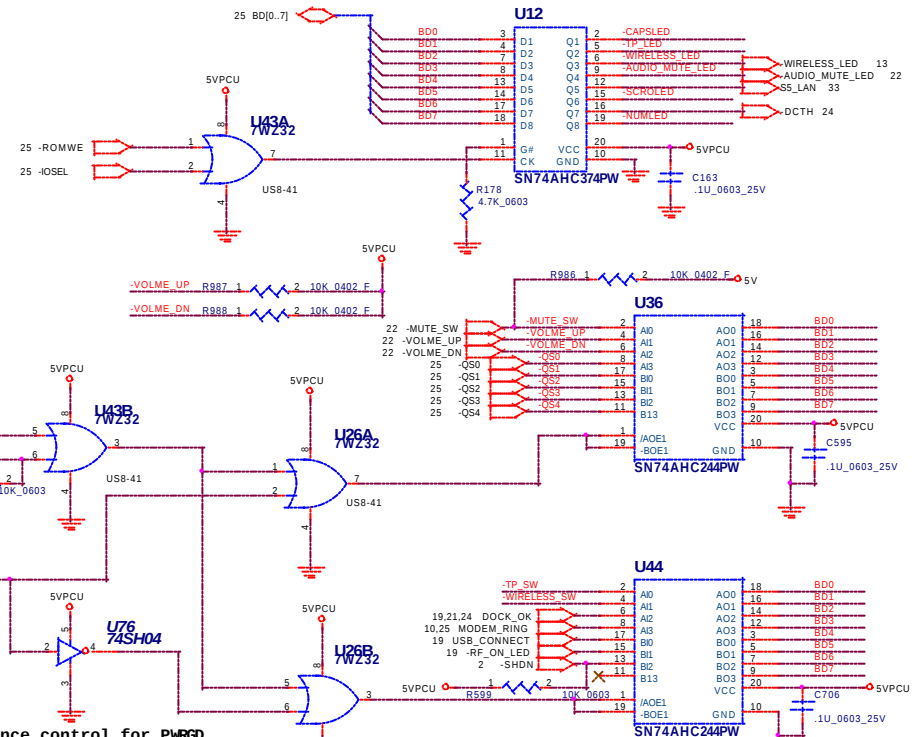
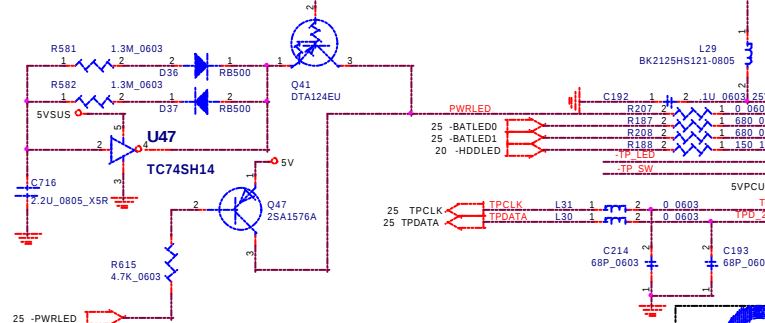
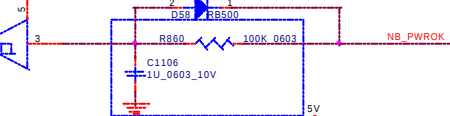
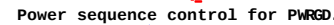


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-NBSWON	C805	100P 0603
-QS0	C806	100P 0603
-QS4	C807	100P 0603
-NUMLED	C808	100P 0603
-QS2	C809	100P 0603
-CAPSLED	C810	100P 0603
-SCROLED	C811	100P 0603
-QS1	C812	100P 0603
-QS3	C813	100P 0603
-LID S	C814	100P 0603



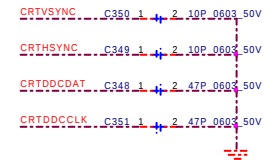
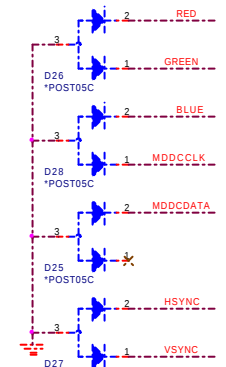
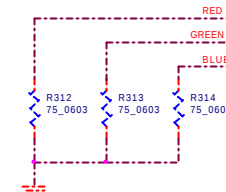
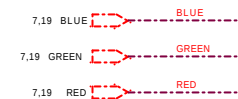
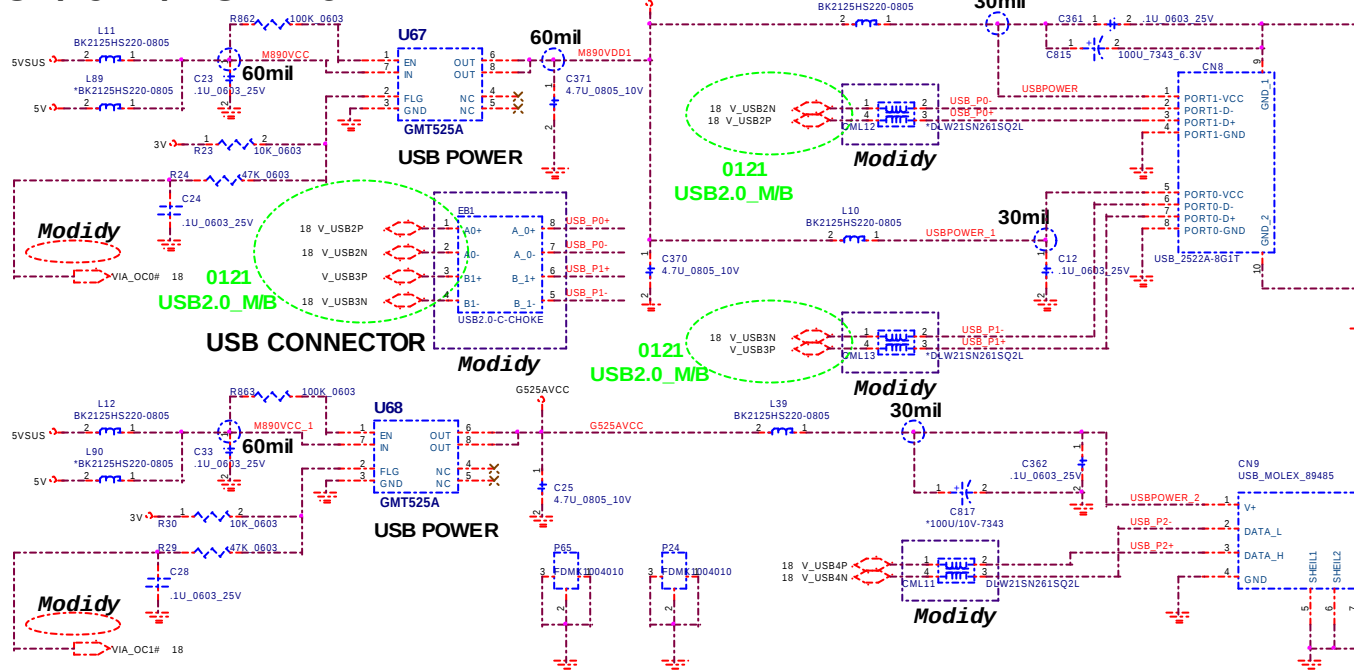
For_ATI_PWG function



		PROJECT : KT9D	
		Quanta Computer Inc.	
Size	Document Number	Rev	
	LED & SWITCH & KBC & TOUCHPAD I/F	1A	
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USB/CRT/PS2 PORT

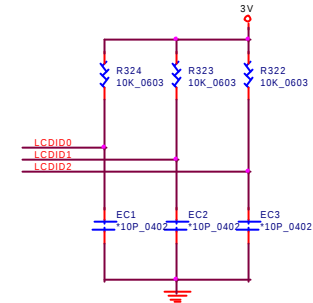
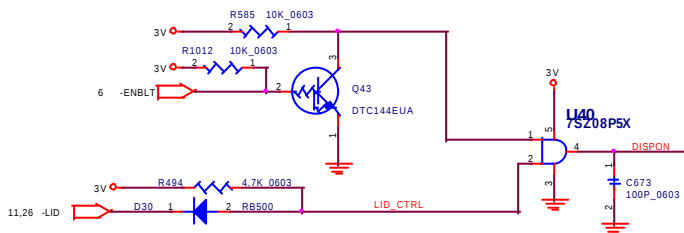
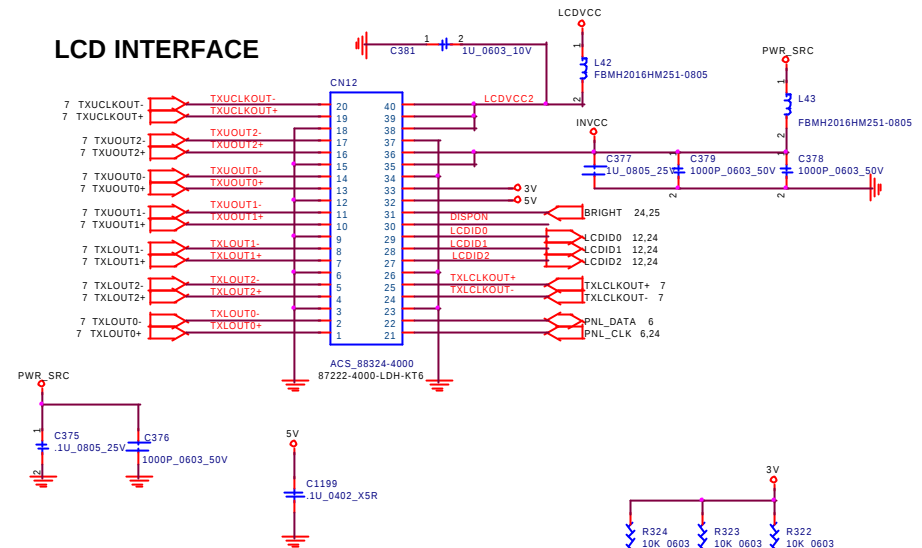
27



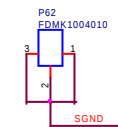
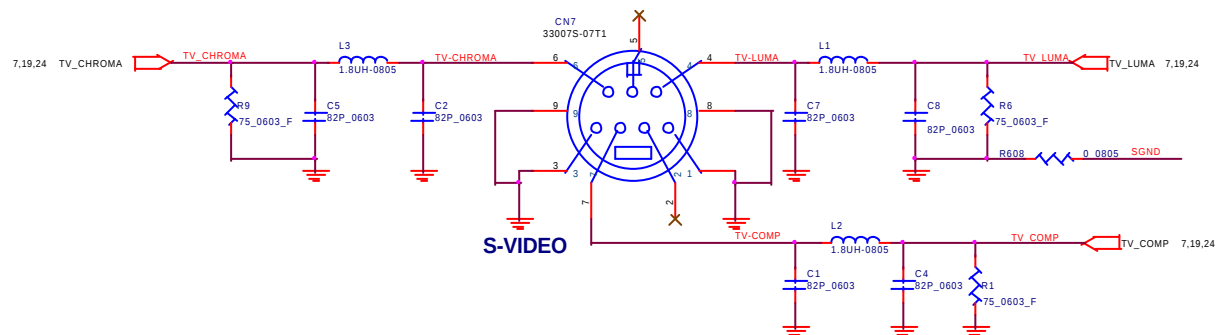
CRT PORT

PS/2 PORT

LCD INTERFACE



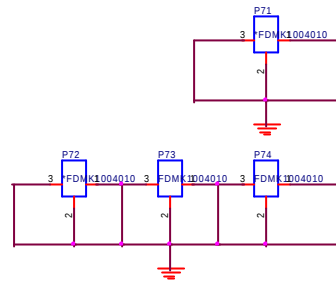
S-VIDEO



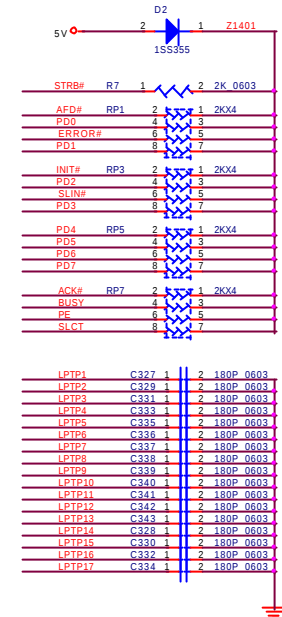
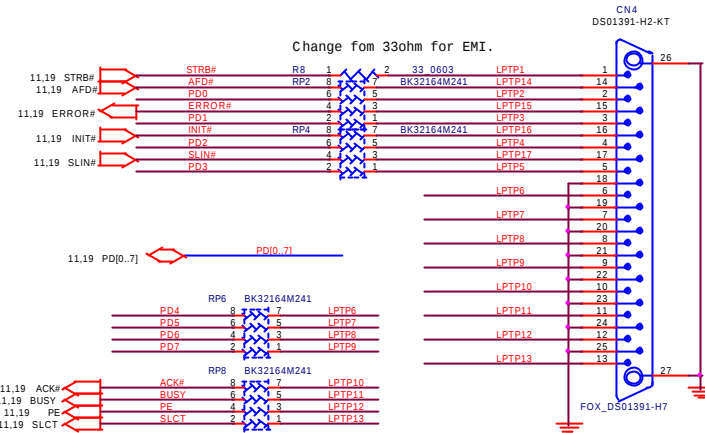
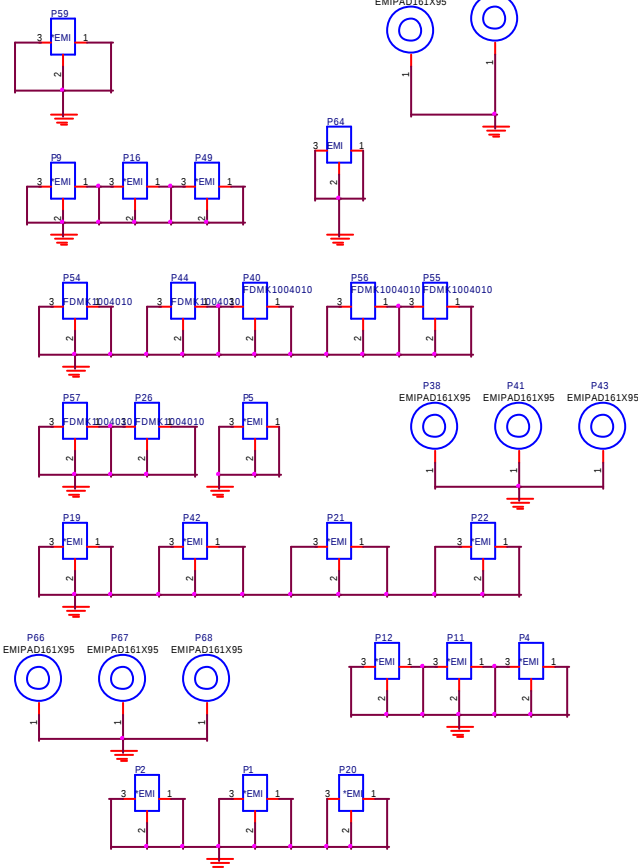
Parallel PORT & SCREW HOLE

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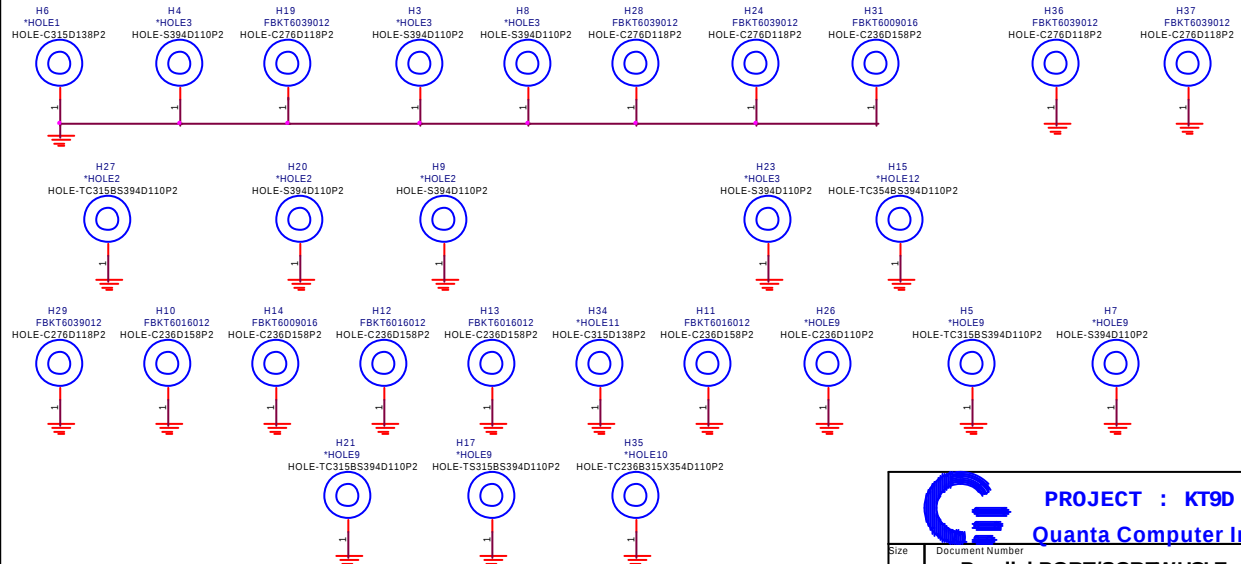
PARALLEL PORT INTERFACE



EMI FENSES

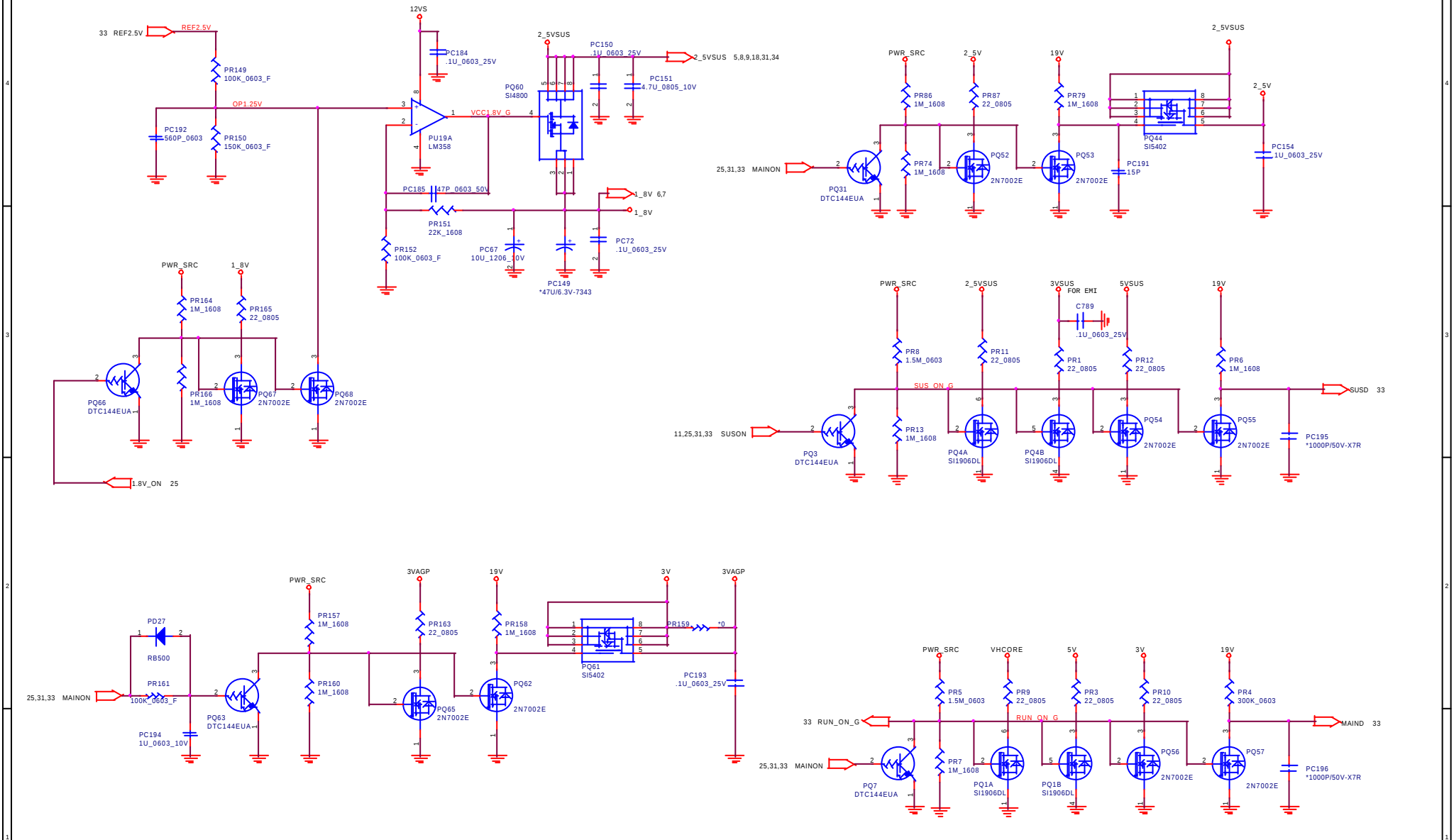


SCREW HOLES



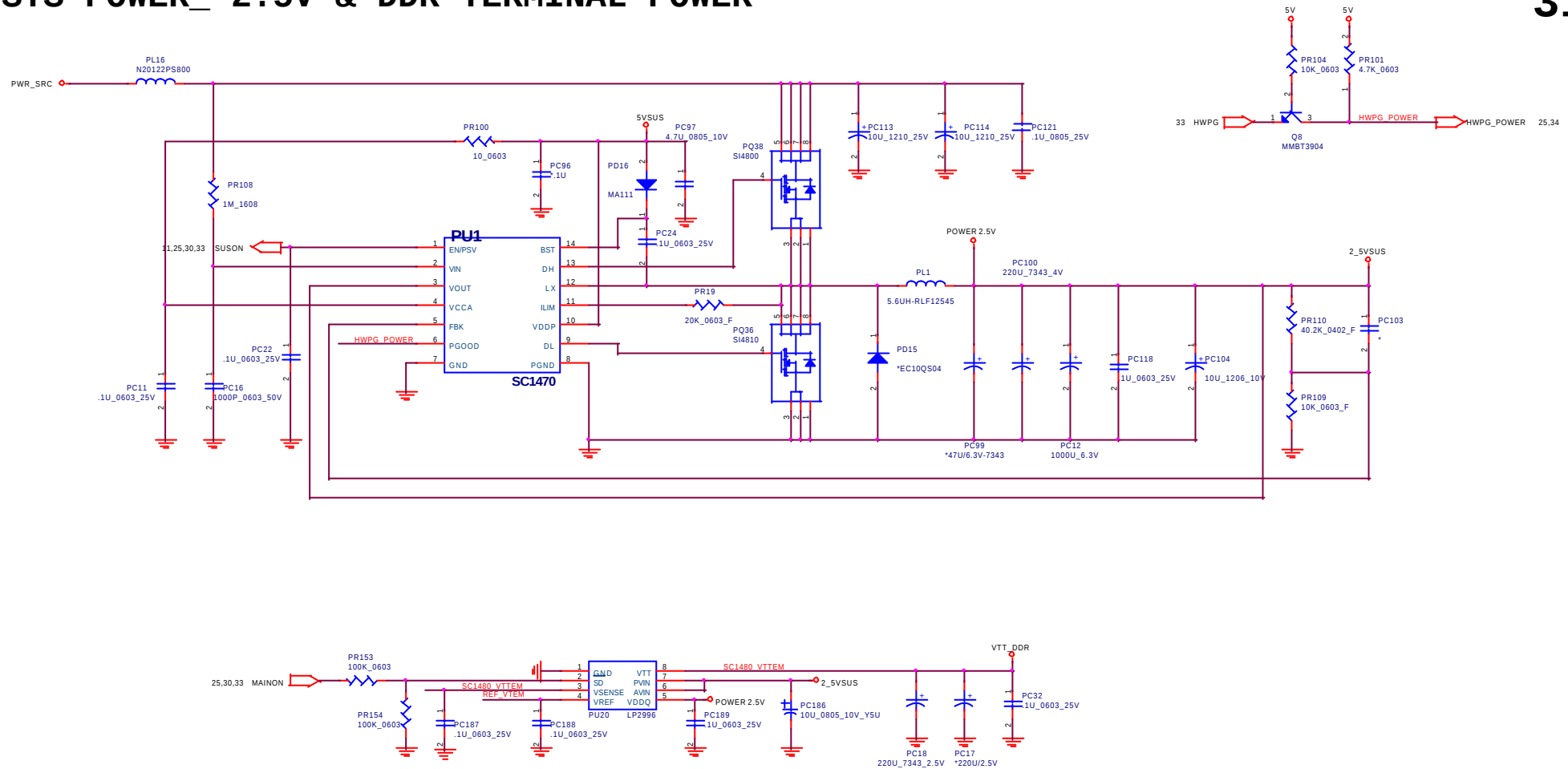
DISCHARGE CIRCUIT

30



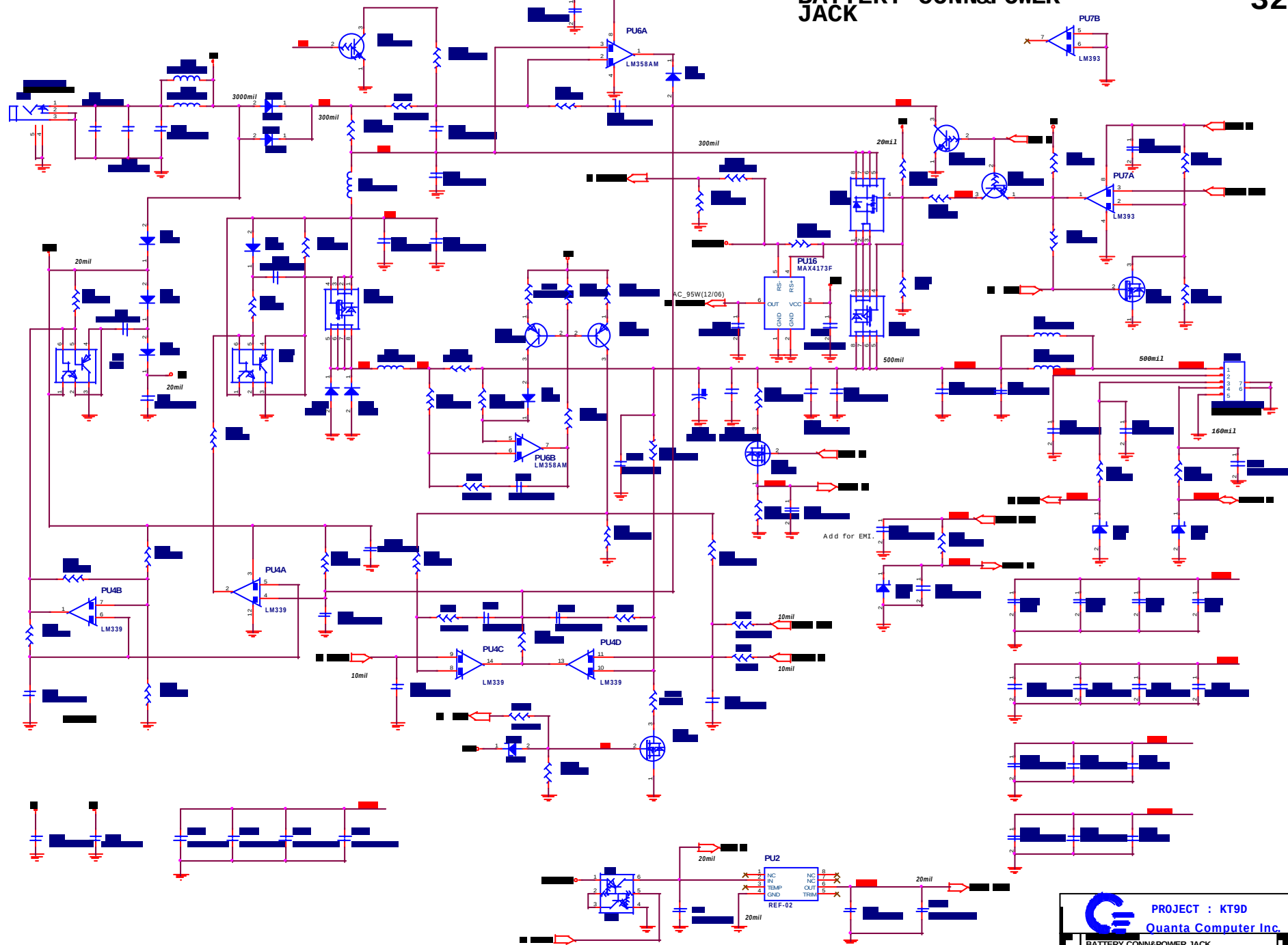
SYS POWER_ 2.5V & DDR TERMINAL POWER

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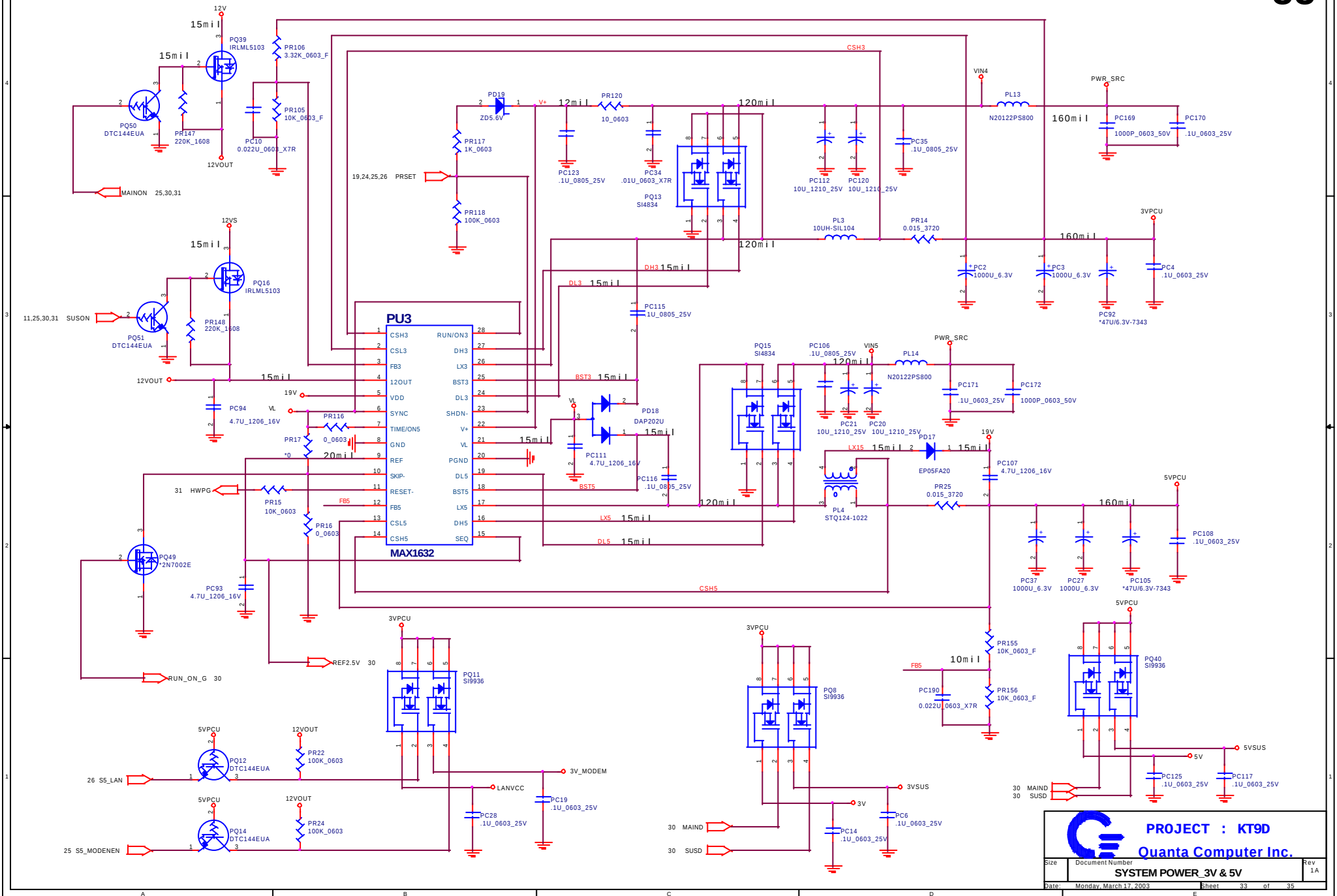
BATTERY CONN&POWER JACK

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SYSTEM POWER_3V & 5V

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[illegible]