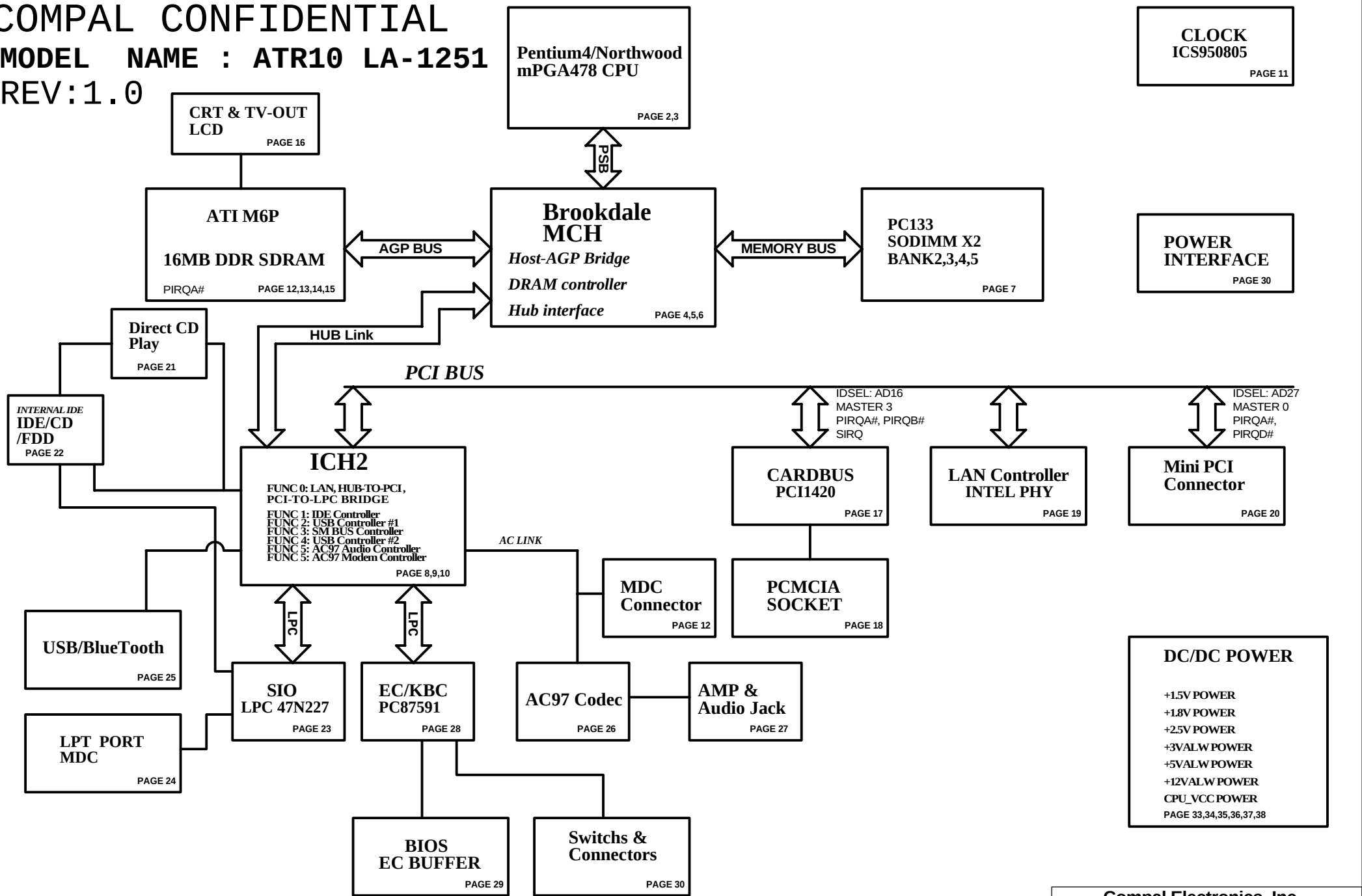
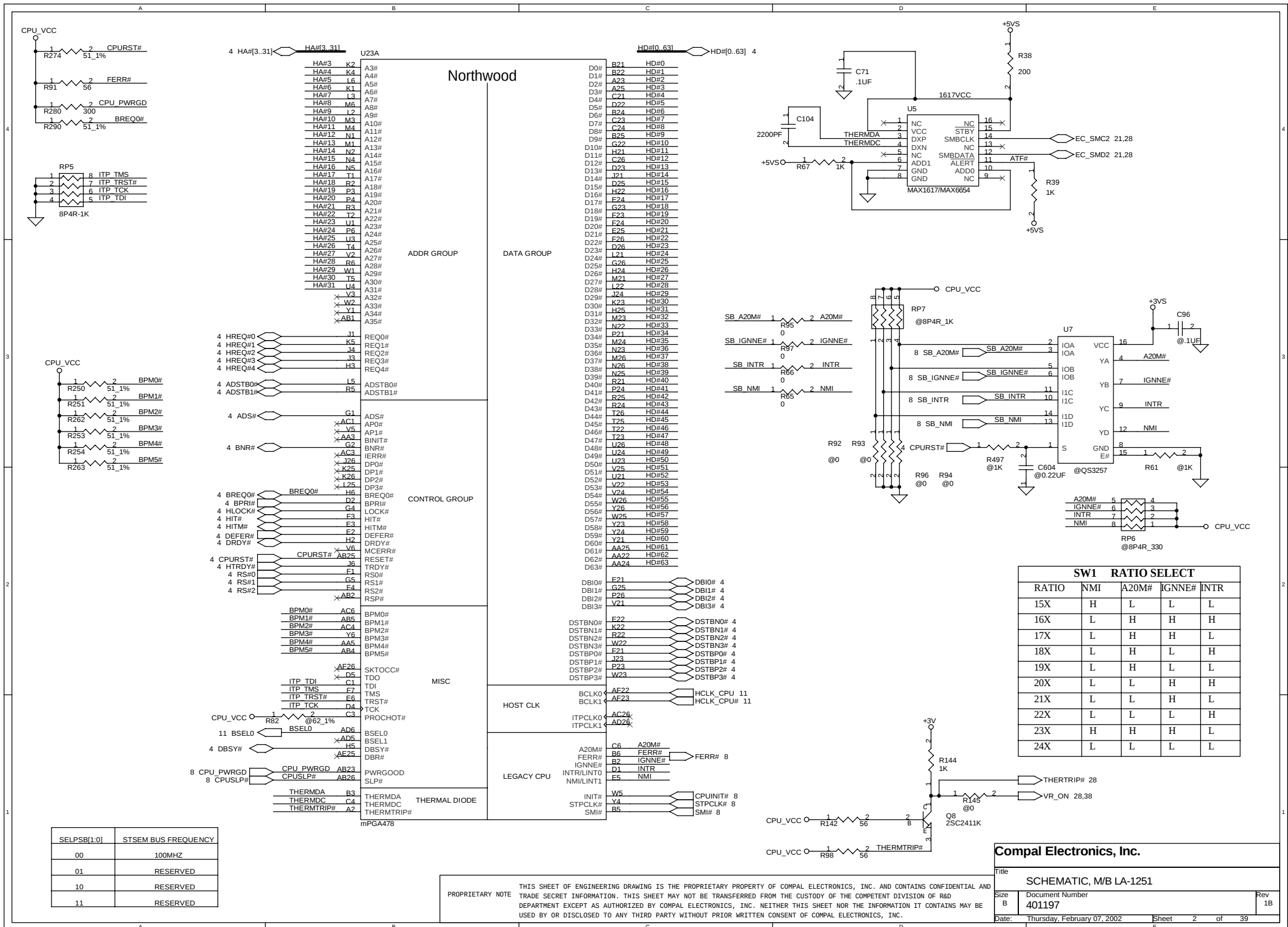


COMPAL CONFIDENTIAL
MODEL NAME : ATR10 LA-1251
REV:1.0



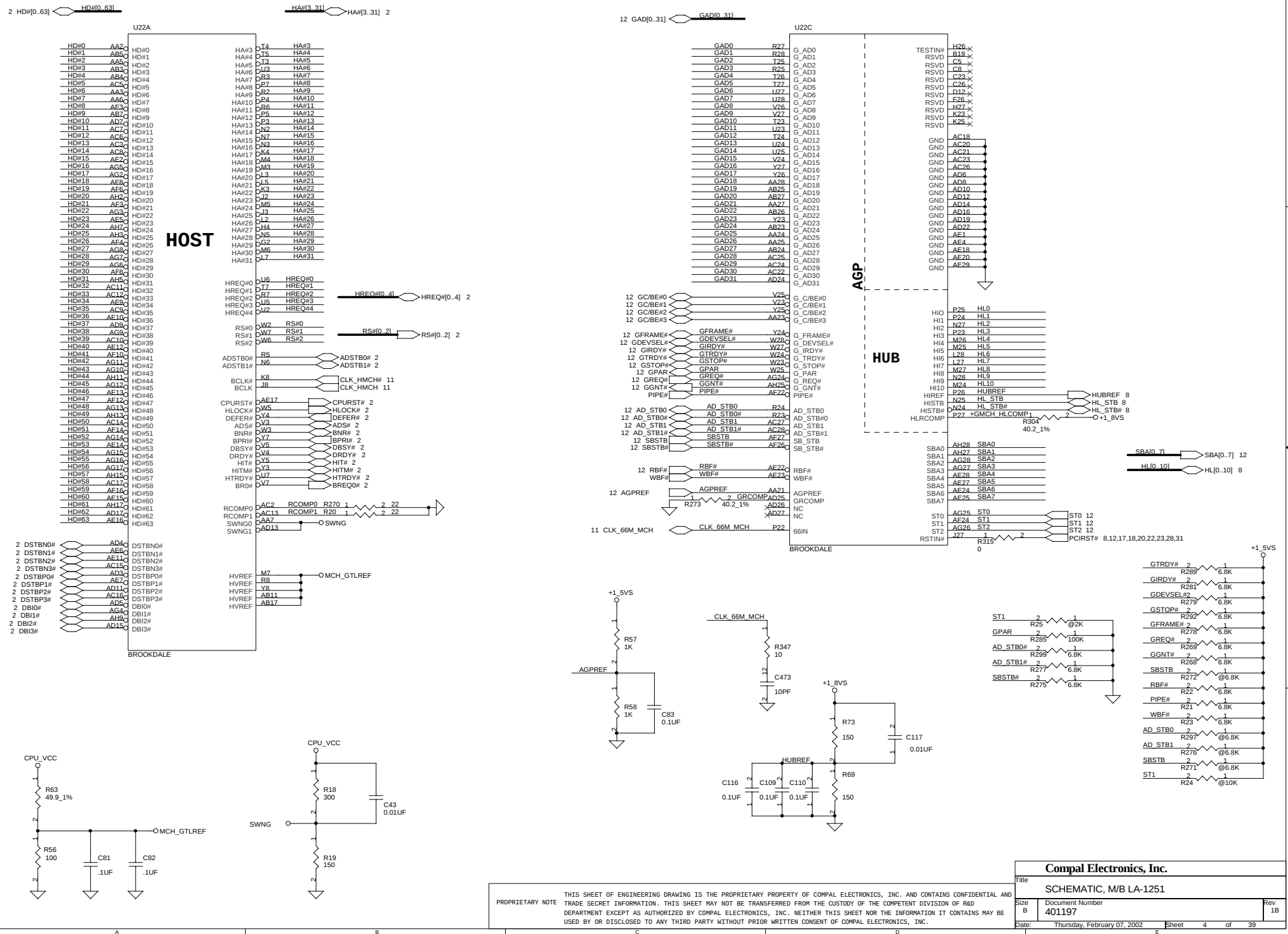
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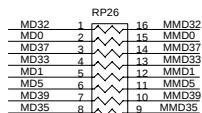
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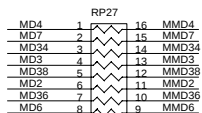


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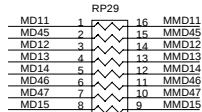


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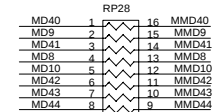
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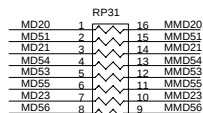
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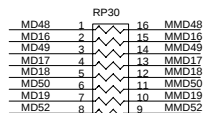
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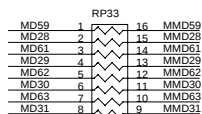
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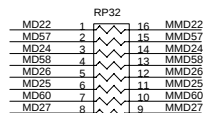
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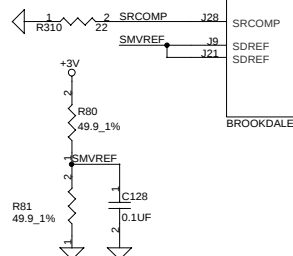
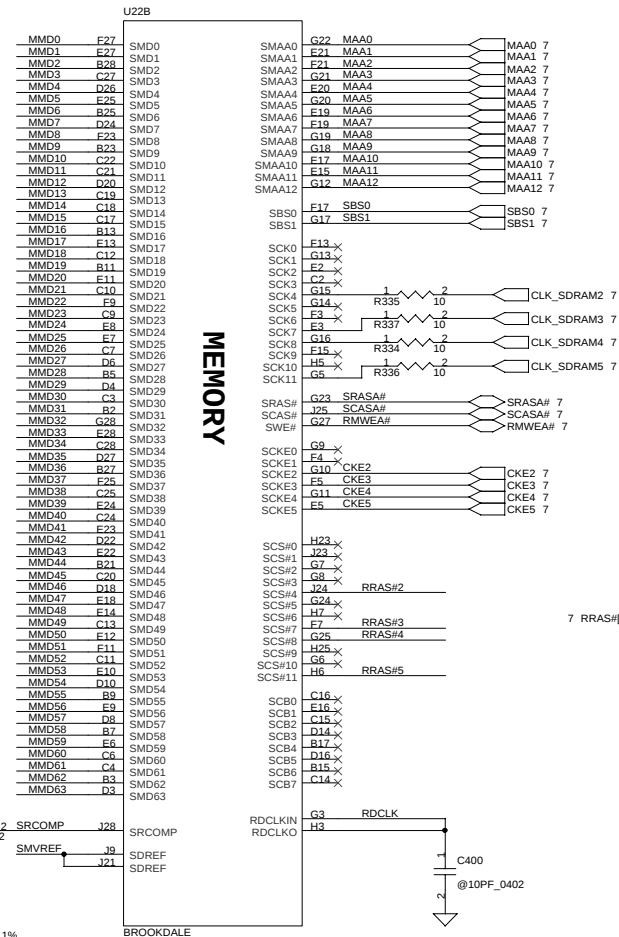
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16P8R-22



16P8R-22



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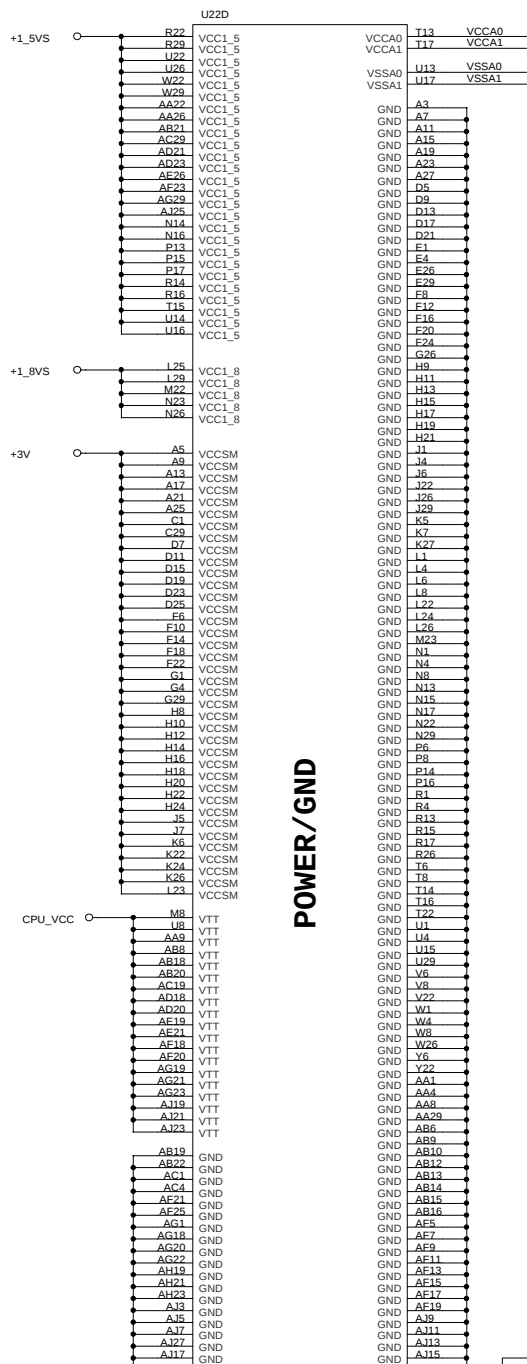
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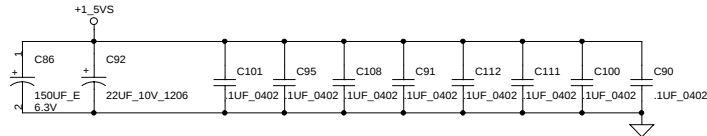
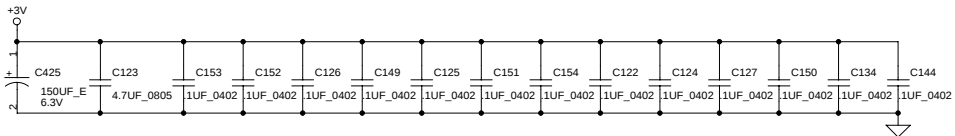
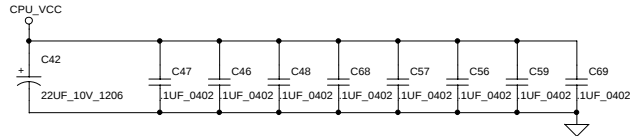
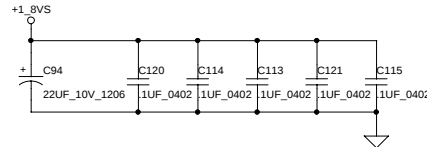
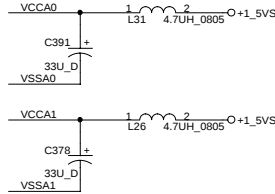
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MCH-3/3(Power)



POWER/GND



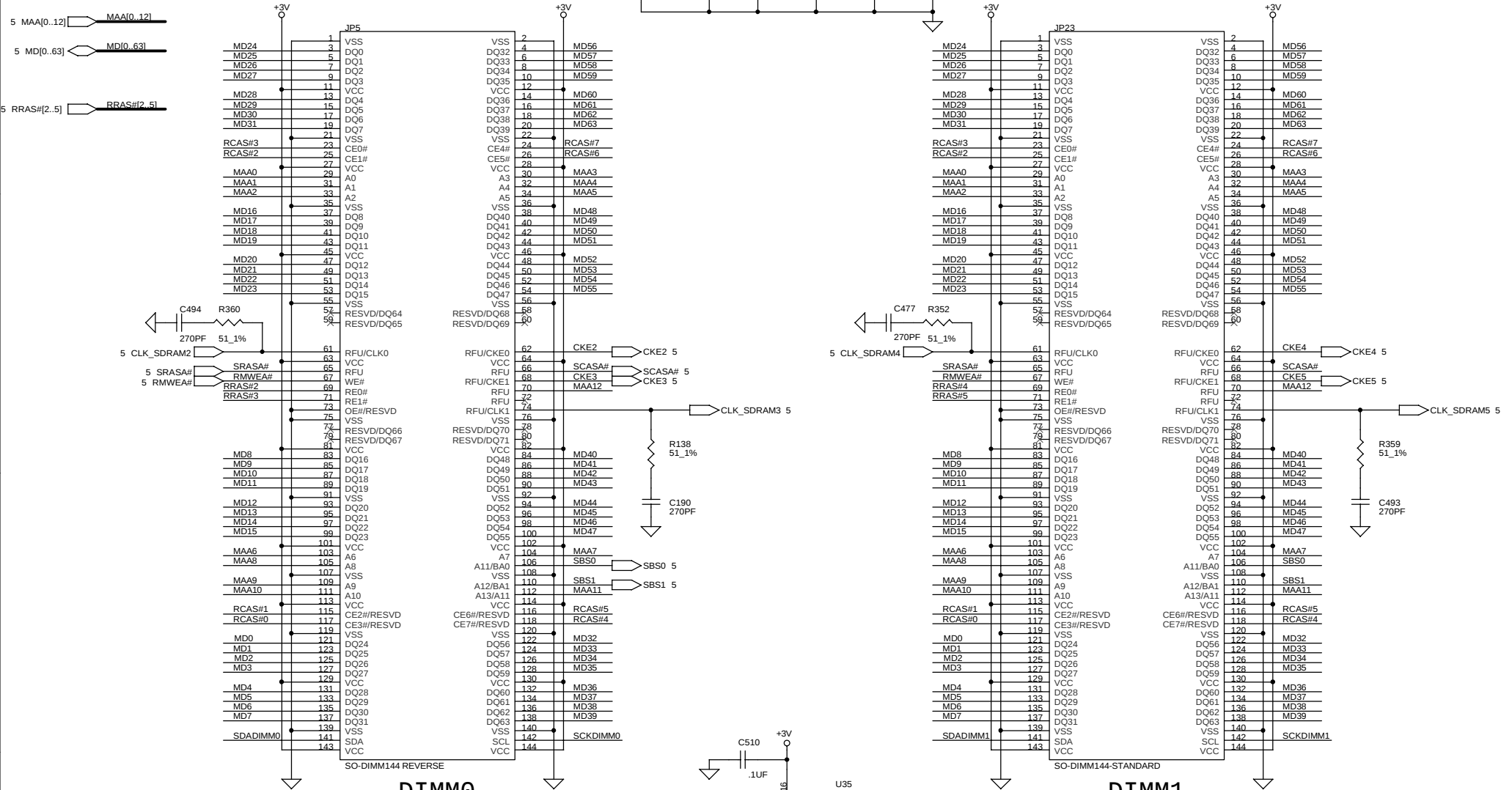
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SO-DIMM 144 PINS RAM MODULE CONN.

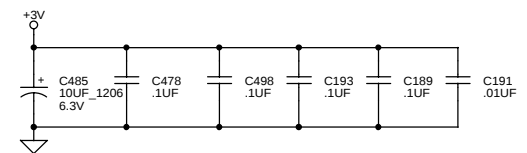
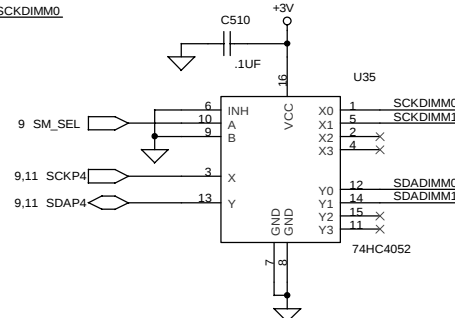
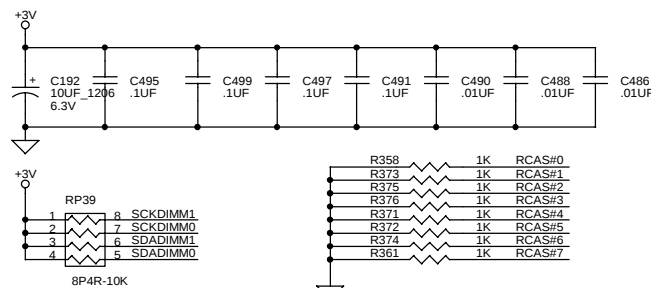
BANK2/3

BANK4/5



DIMM0

DIMM1



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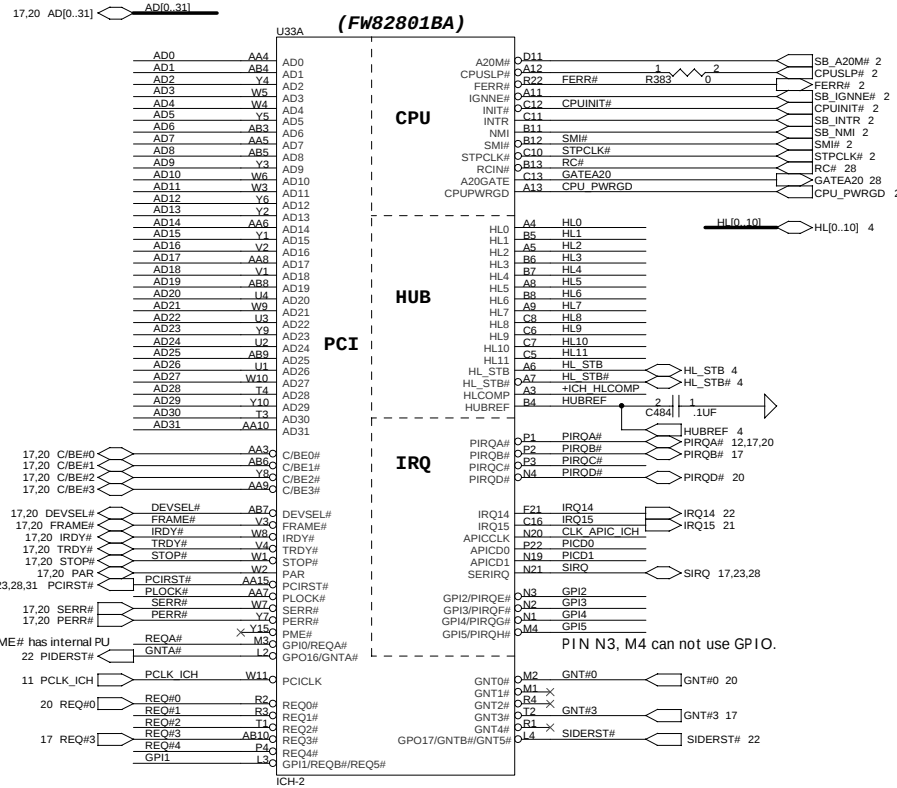
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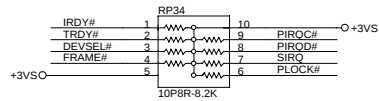
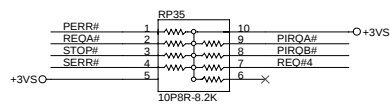
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ICH-2

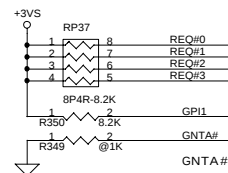
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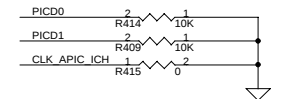
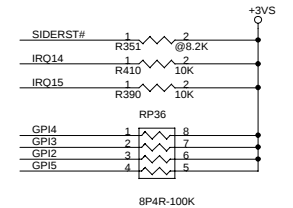
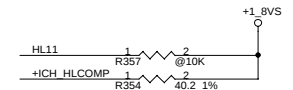
PCI Pullups



PCI REQ ASSIGNMENT	
REQ#0	WLAN
REQ#1	NC
REQ#2	NC
REQ#3	PCMCIA CONTROLLER
REQ#4	NC



GNTA# Strapping for "A16 swap override": "0" -> Enable

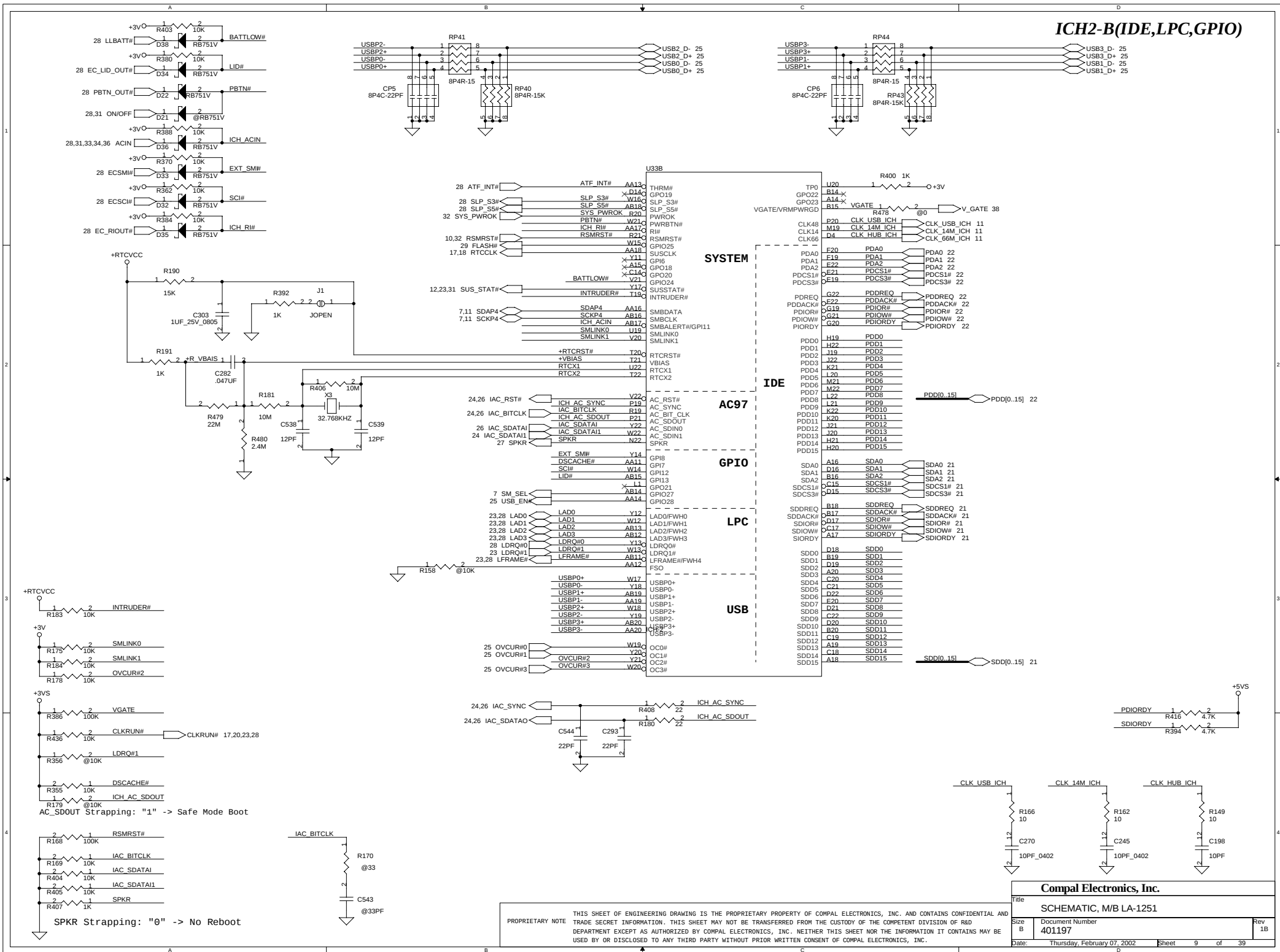


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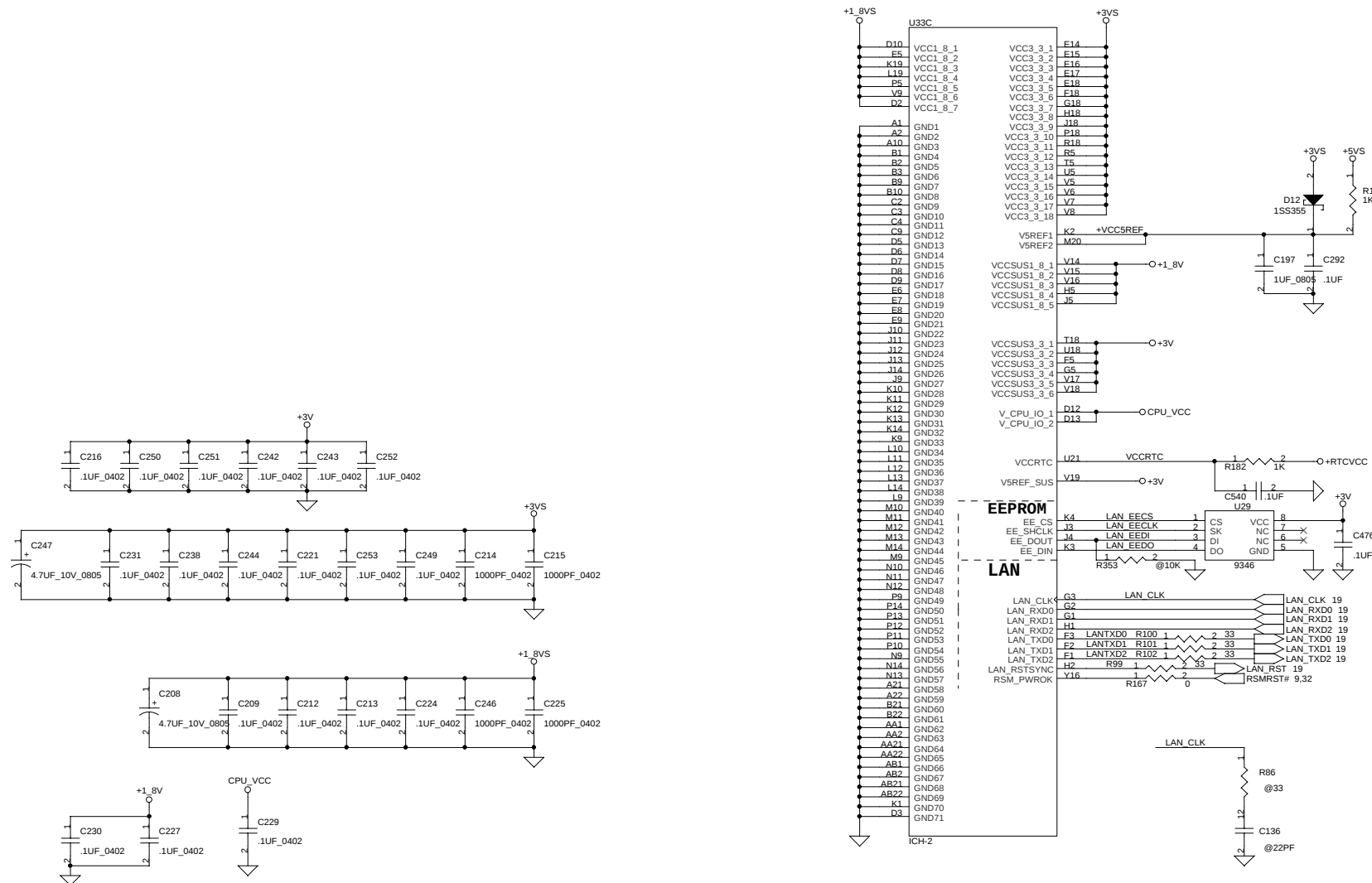
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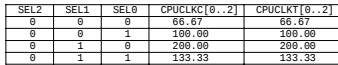
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ICH2-C(LAN,Power)

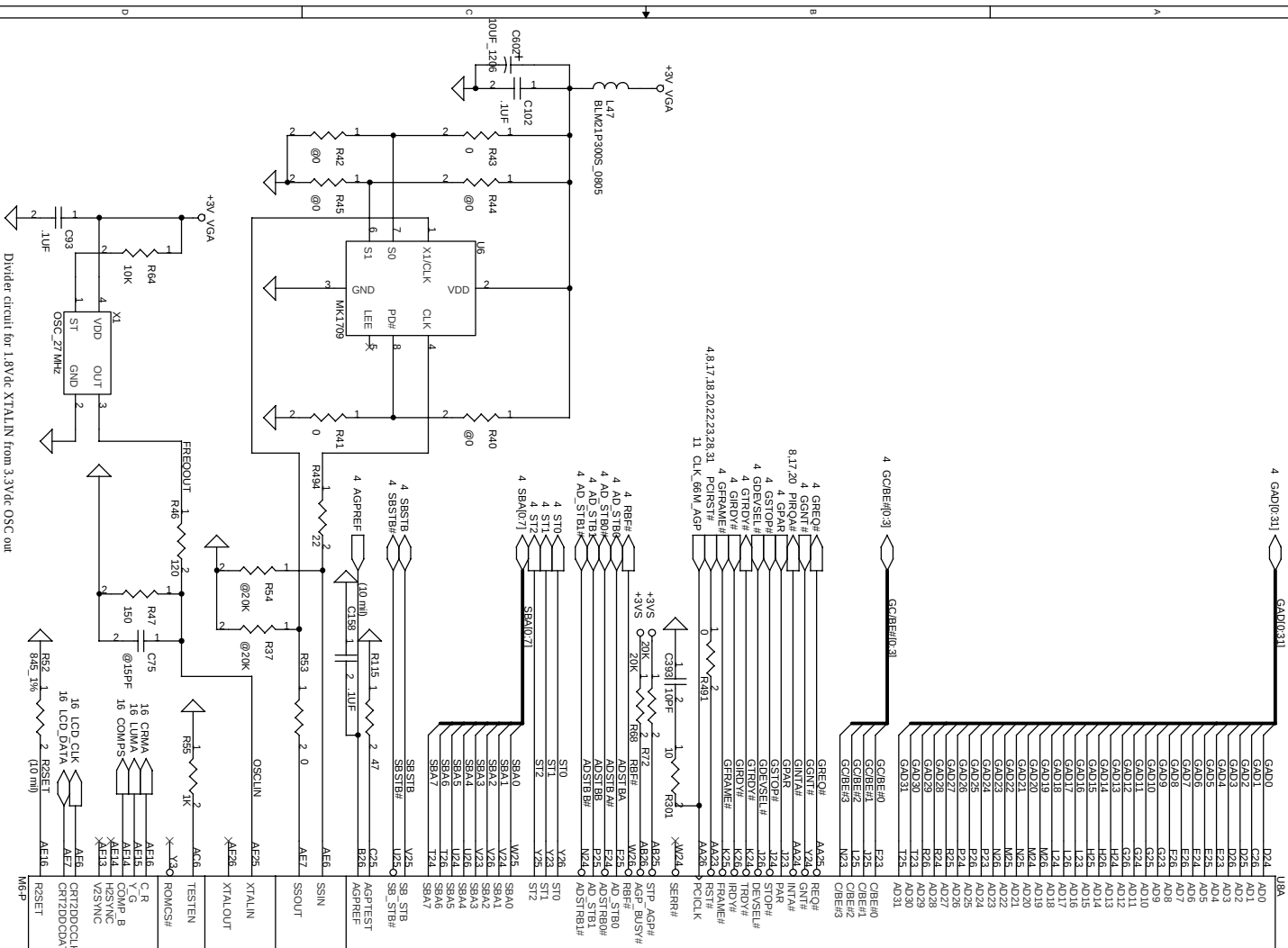


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8	7	6	5	4	3	2
---	---	---	---	---	---	---



PCI/AGP HOST BUS INTERFACE

GPIO0

R7L 1

2 @10K

Strap-G

GPIO1

R7D 1

2 @10K

Strap-H

GPIO2

R7A 1

2 @10K

Strap-H

GPIO3

R7S 1

2 @10K

Strap-K

GPIO4

W1

2 @10K

Strap-J

GPIO5

V2

2 @10K

Strap-H

GPIO6

V3

2 @10K

Strap-H

GPIO7

W3

2 @10K

Strap-H

GPIO8

W3

2 @10K

Strap-H

GPIO9

W3

2 @10K

Strap-H

GPIO10

W3

2 @10K

Strap-H

GPIO11

W3

2 @10K

Strap-H

GPIO12

W3

2 @10K

Strap-H

GPIO13

W3

2 @10K

Strap-H

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ZV PORT / EXT TMDs

ZV_L0CDPA1A0

AA4

TX0M

16

ZV_L0CDPA1A1

AA1

TX0P

16

ZV_L0CDPA1A2

AA3

TX0P

16

ZV_L0CDPA1A3

AA4

TX0P

16

ZV_L0CDPA1A4

AA3

TX0P

16

ZV_L0CDPA1A5

AA3

TX0P

16

ZV_L0CDPA1A6

AA4

TX0P

16

ZV_L0CDPA1A7

AA1

TX0P

16

ZV_L0CDPA1A8

AA3

TX0P

16

ZV_L0CDPA1A9

AA3

TX0P

16

ZV_L0CDPA1A10

AA3

TX0P

16

ZV_L0CDPA1A11

AA3

TX0P

16

ZV_L0CDPA1A12

AA2

TX0P

16

ZV_L0CDPA1A13

AA3

TX0P

16

ZV_L0CDPA1A14

AA3

TX0P

16

ZV_L0CDPA1A15

AA3

TX0P

16

ZV_L0CDPA1A16

AA3

TX0P

16

ZV_L0CDPA1A17

AA4

TX0P

16

ZV_L0CDPA1A18

AA3

TX0P

16

ZV_L0CDPA1A19

AA3

TX0P

16

ZV_L0CDPA1A20

AA3

TX0P

16

ZV_L0CDPA1A21

AA3

TX0P

16

ZV_L0CDPA1A22

AA3

TX0P

16

ZV_L0CDPA1A23

AA3

TX0P

16

ZV_L0CDPA1A24

AA3

TX0P

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ZV_L0CDPA1A25

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TX0P

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ZV_L0CDPA1A27

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TX0P

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ZV_L0CDPA1A32

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TX0P

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TX0P

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ZV_L0CDPA1A35

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TX0P

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ZV_L0CDPA1A36

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TX0P

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ZV_L0CDPA1A37

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TX0P

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TX0P

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ZV_L0CDPA1A39

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TX0P

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ZV_L0CDPA1A40

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TX0P

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ZV_L0CDPA1A41

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TX0P

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ZV_L0CDPA1A42

AA3

TX0P

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ZV_L0CDPA1A49

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TX0P

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TX0P

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ZV_L0CDPA1A84

AA3

TX0P

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ZV_L0CDPA1A85

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TX0P

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ZV_L0CDPA1A86

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TX0P

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ZV_L0CDPA1A87

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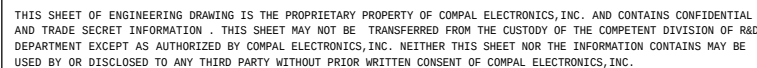
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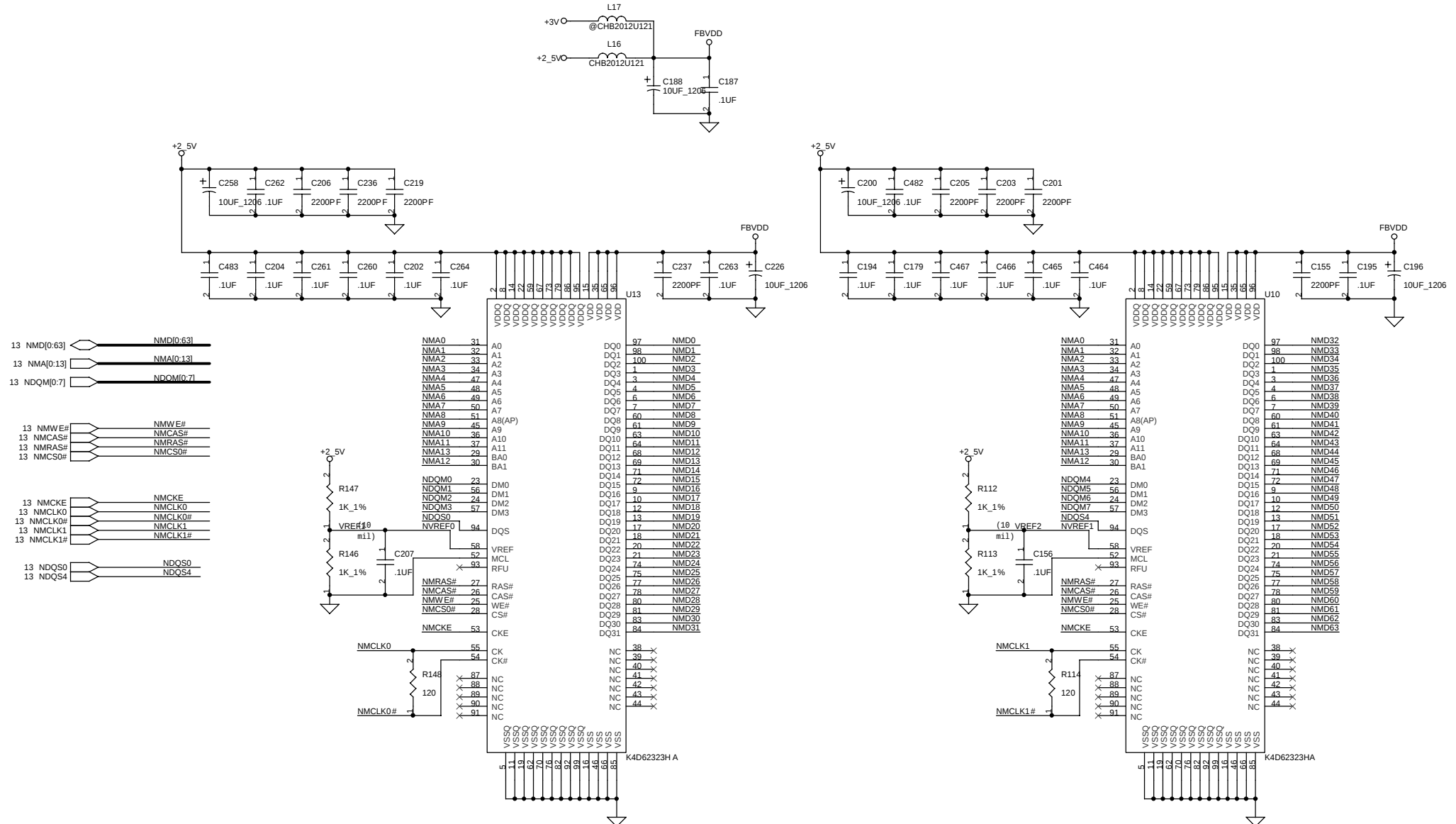
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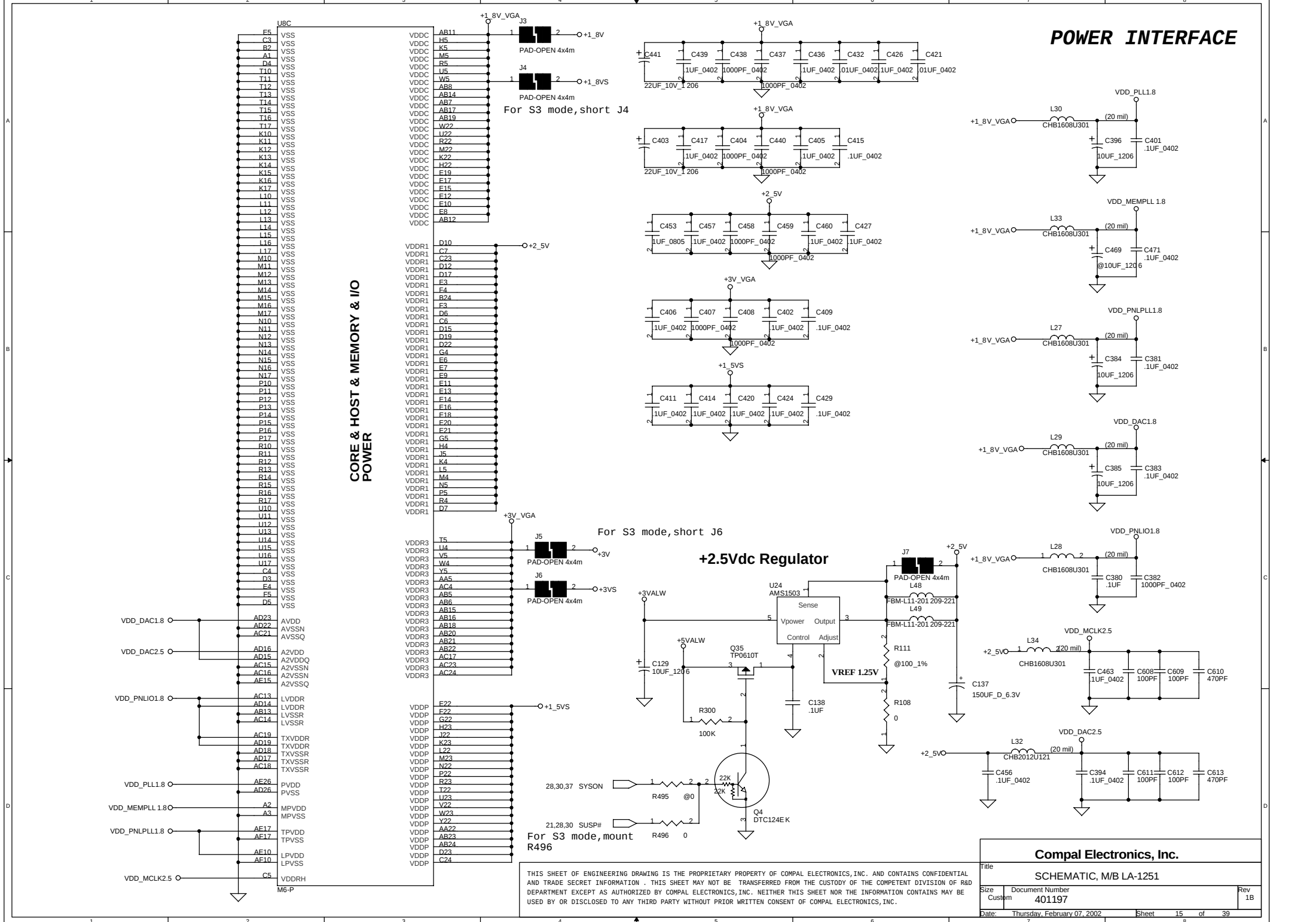


DDR SDRAM



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POWER INTERFACE

CORE & HOST & I/O POWER

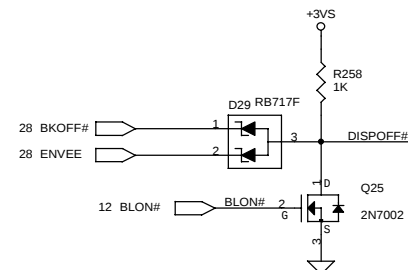
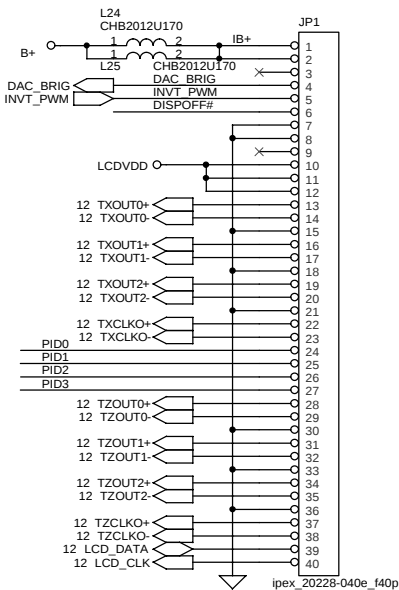
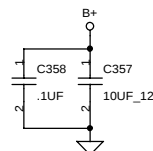
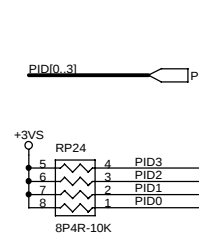
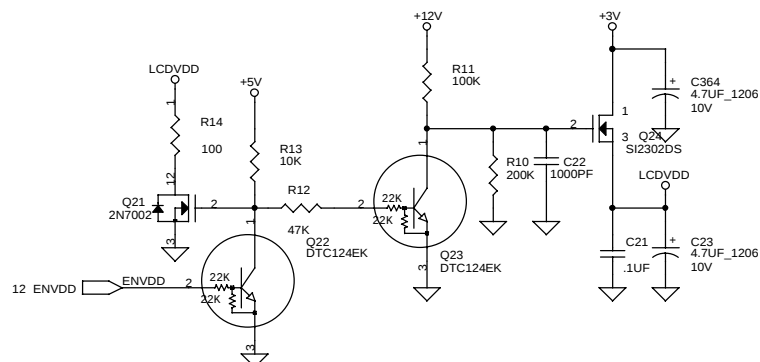
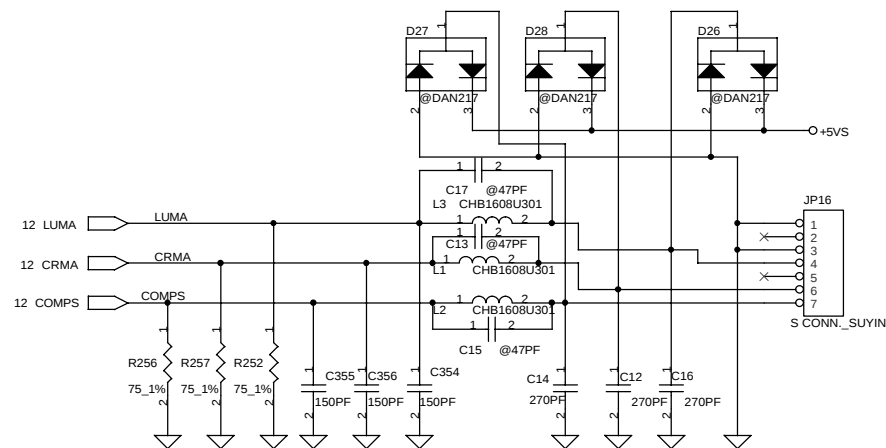
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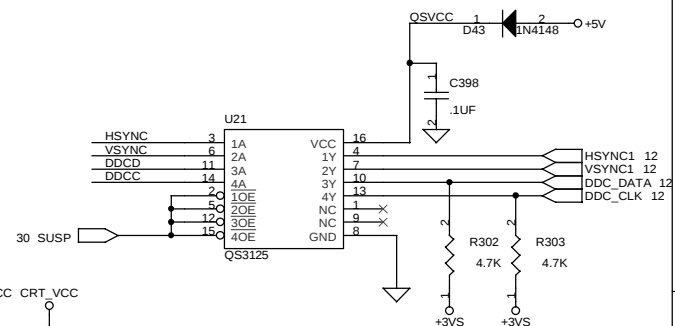
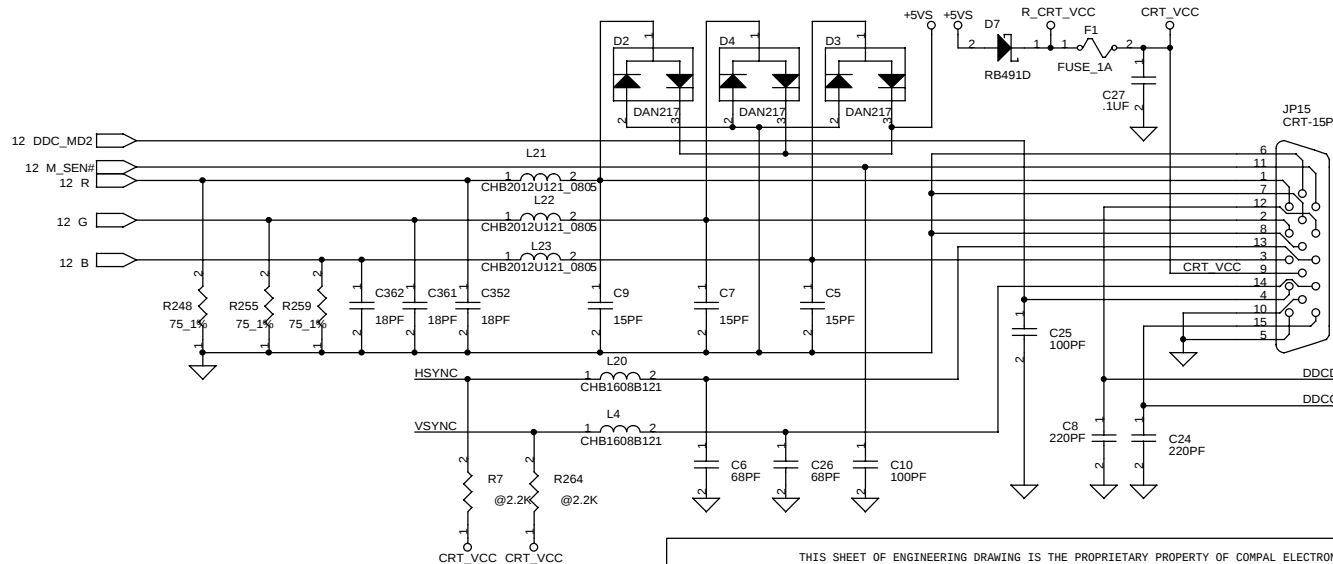
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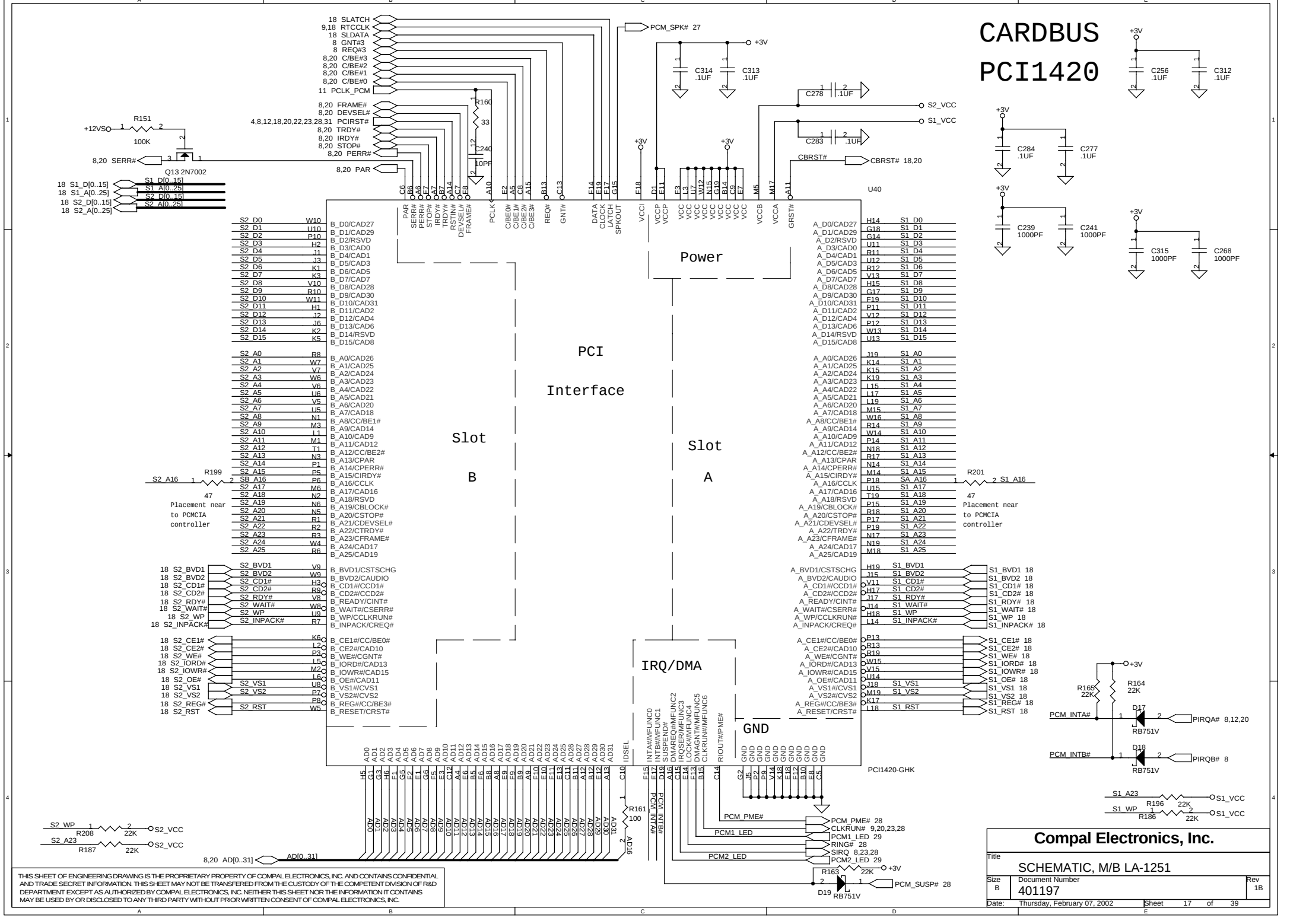
CRT Connector



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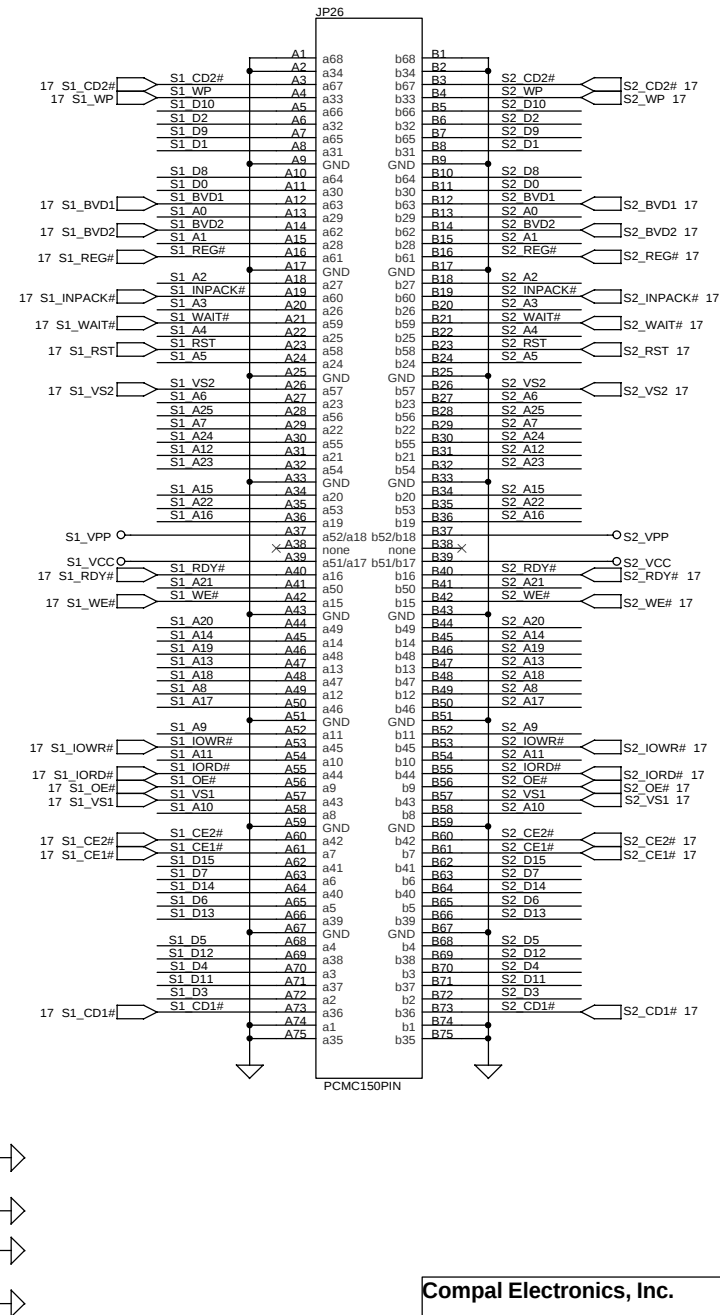
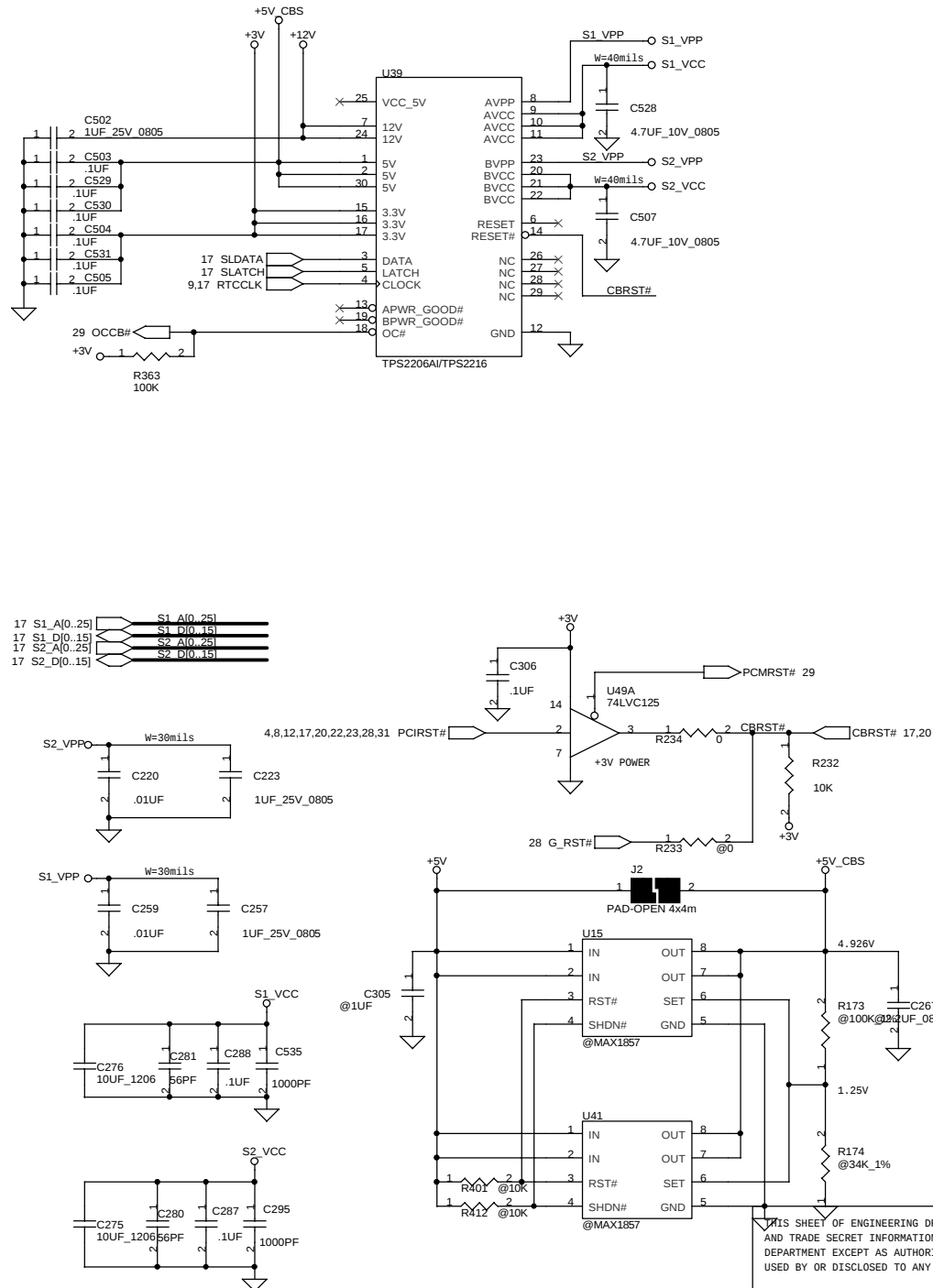


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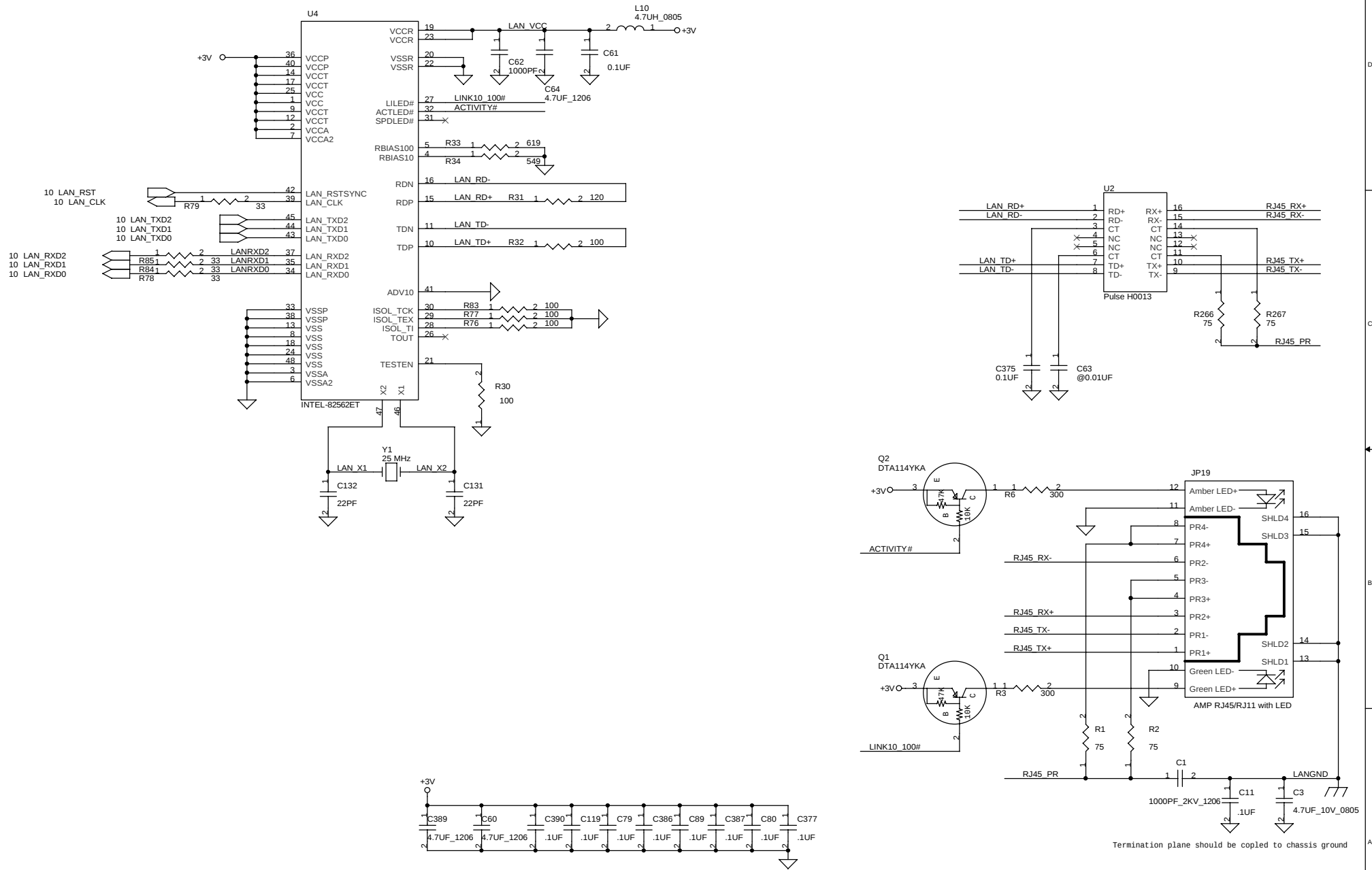
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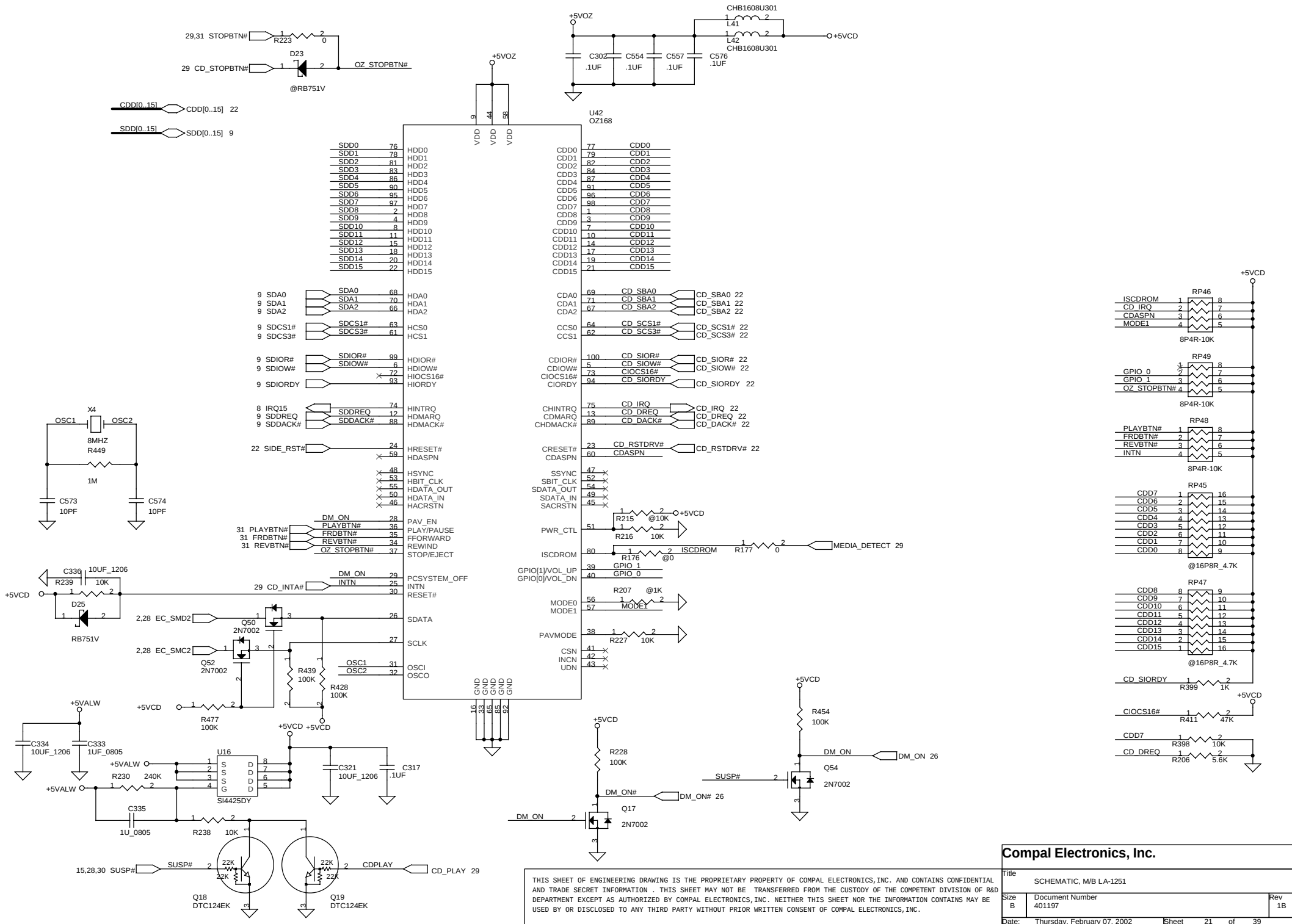
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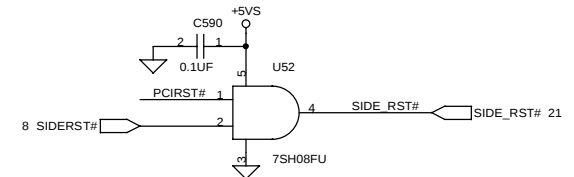
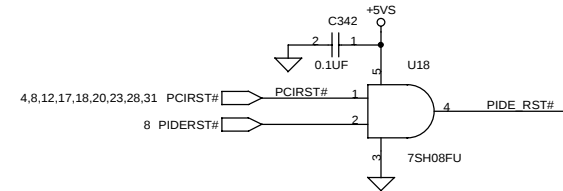
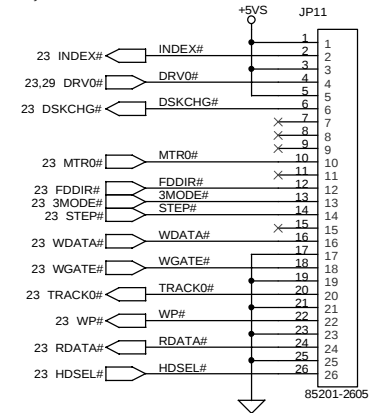
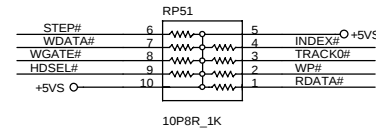
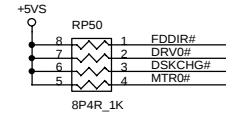
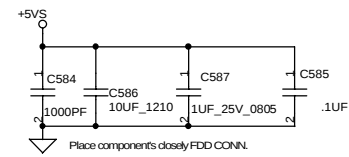
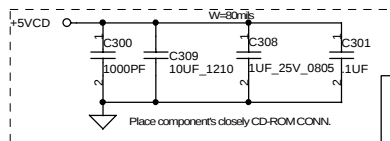
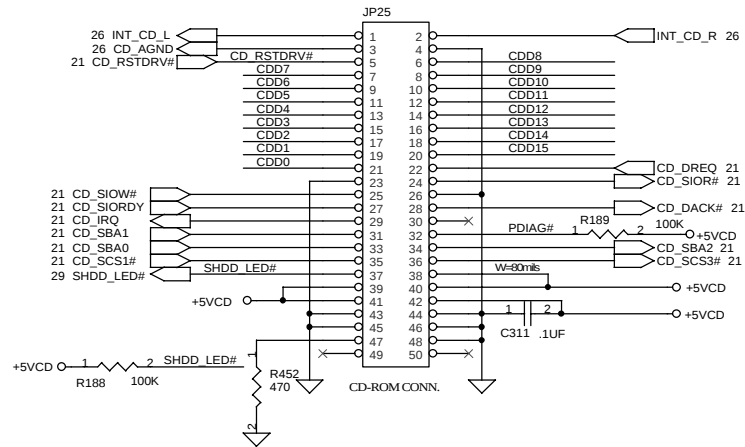
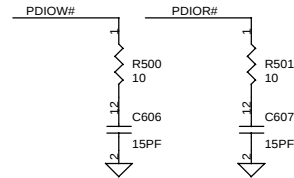
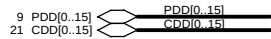
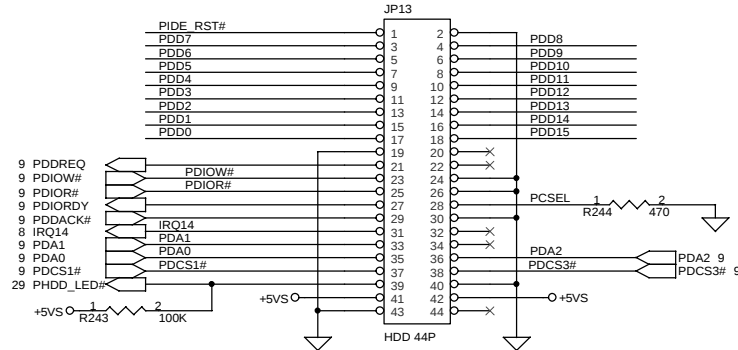
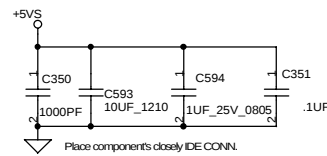
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IDE,CD-ROM Module CONN.

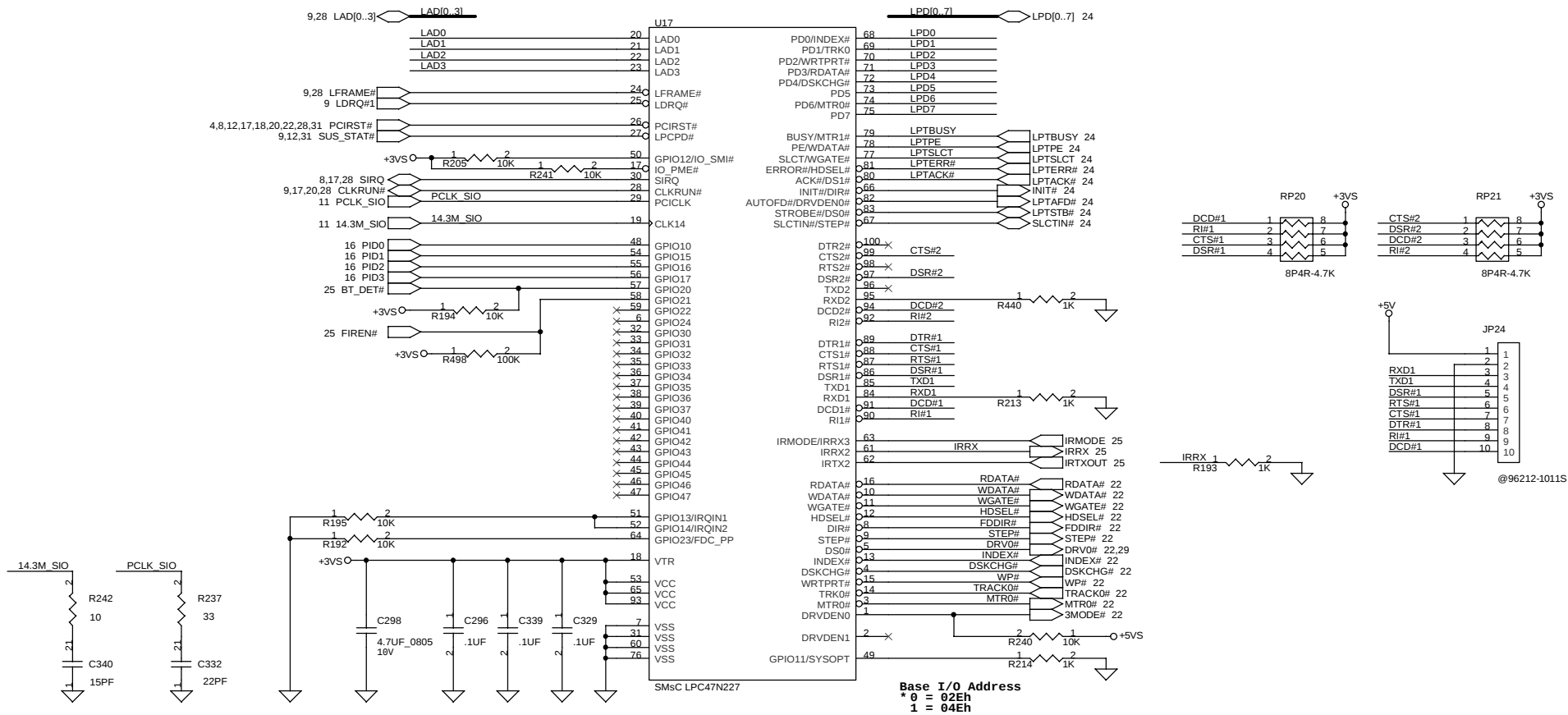


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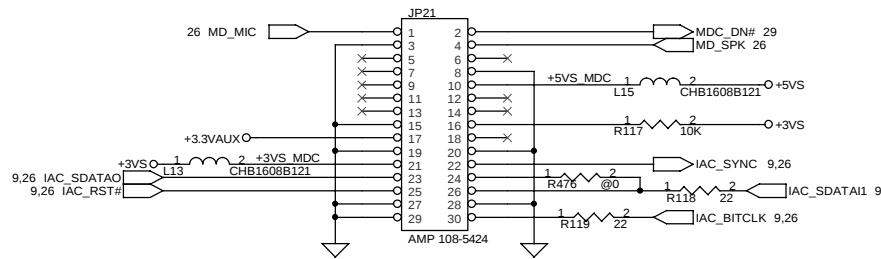
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SUPER I/O SMsC FDC47N227

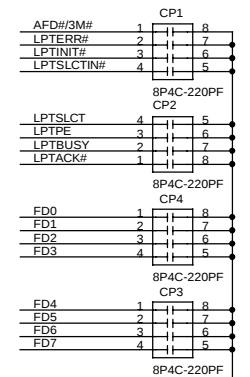
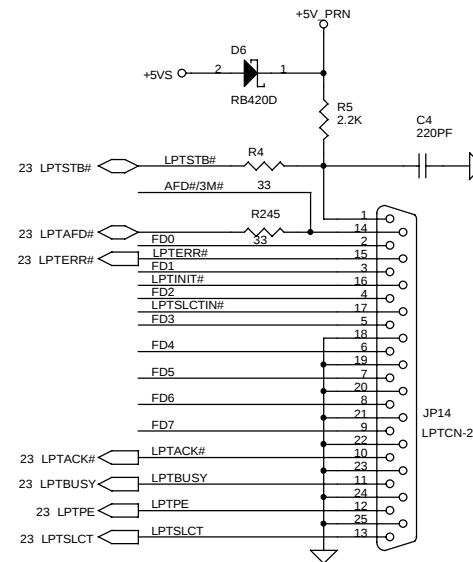
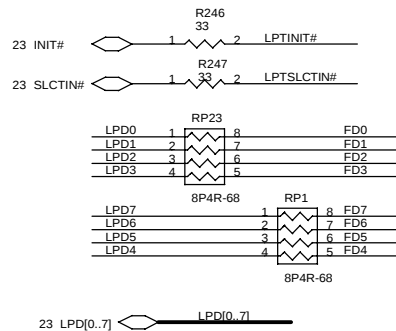
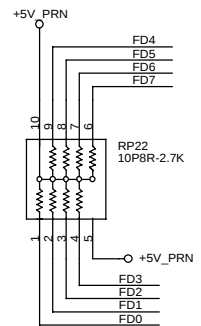
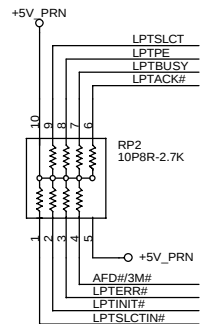


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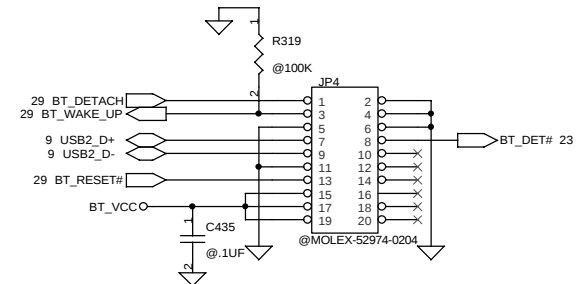
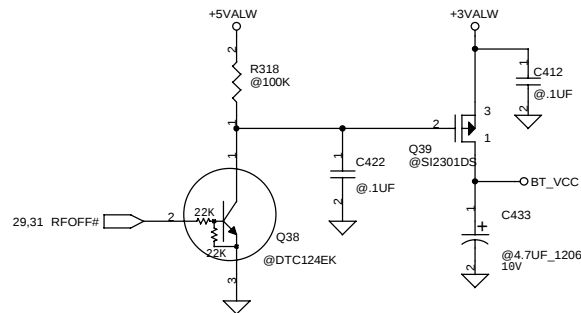
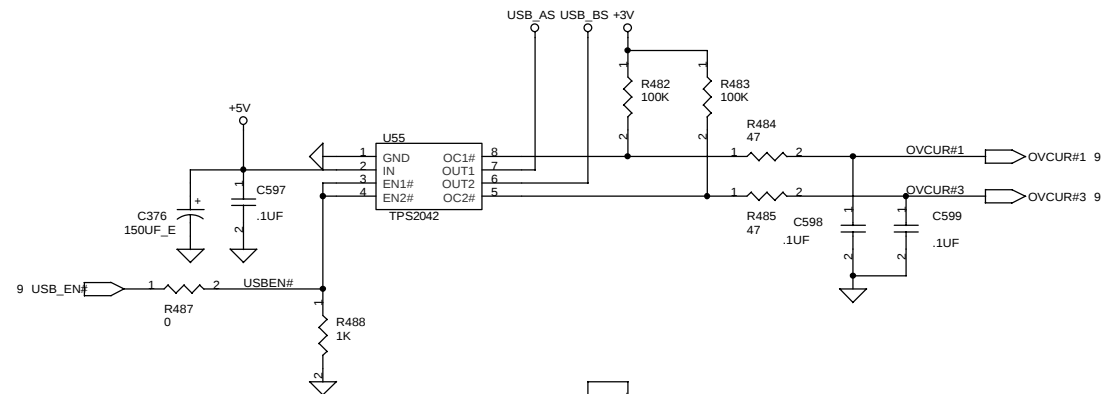
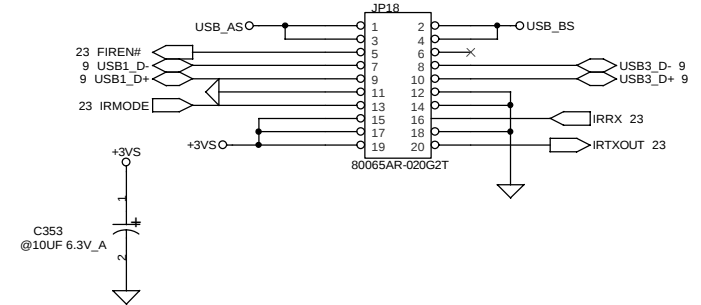
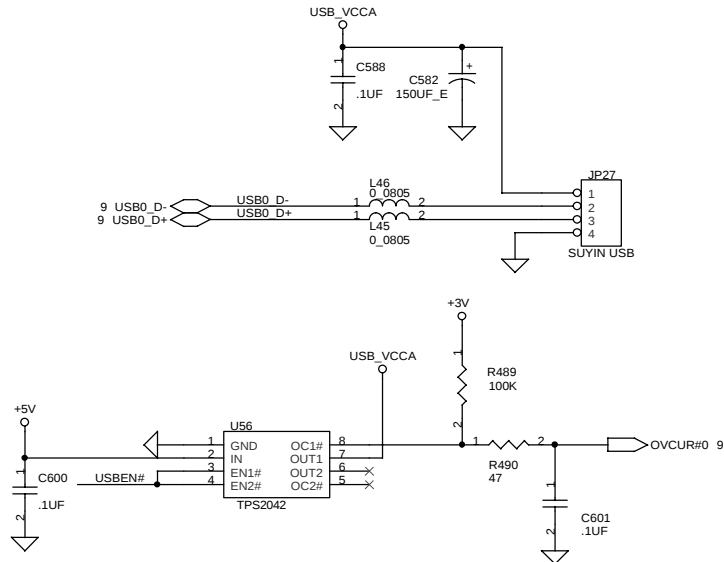
PARALLEL PORT



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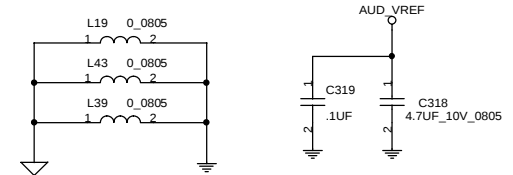
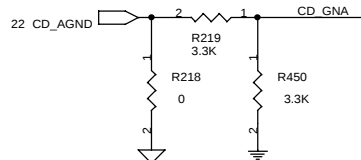
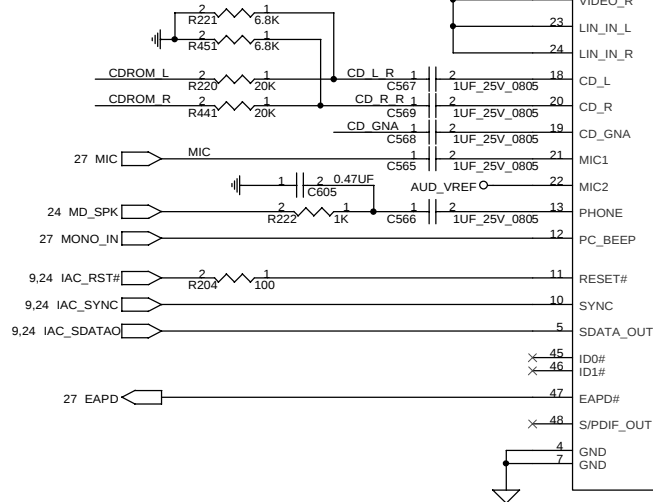
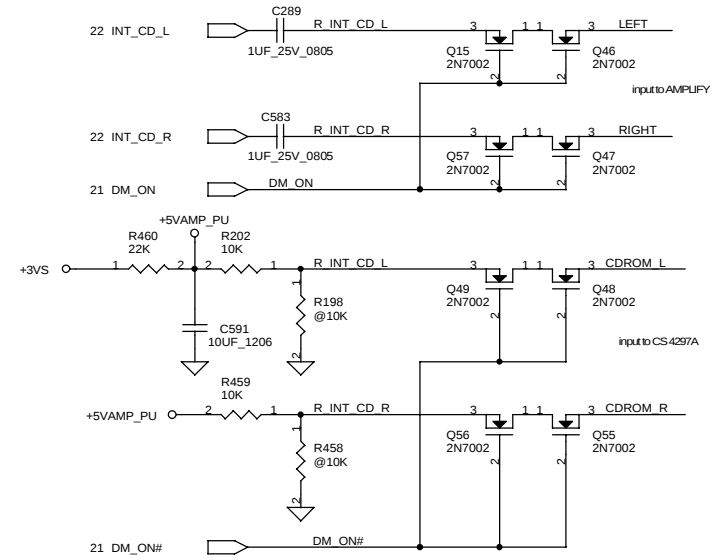
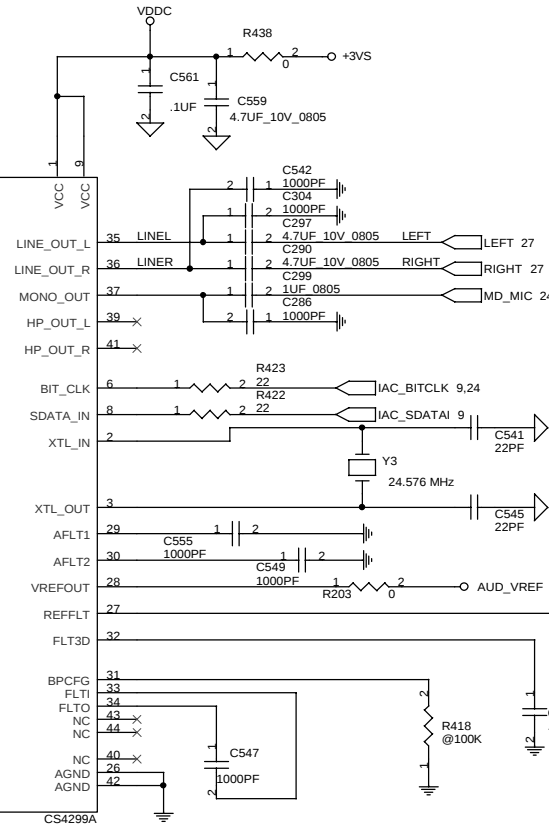
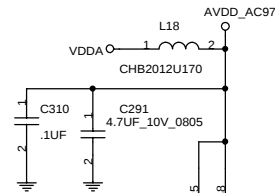
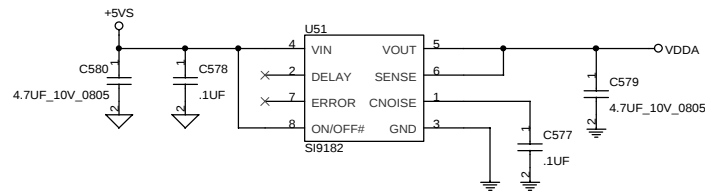


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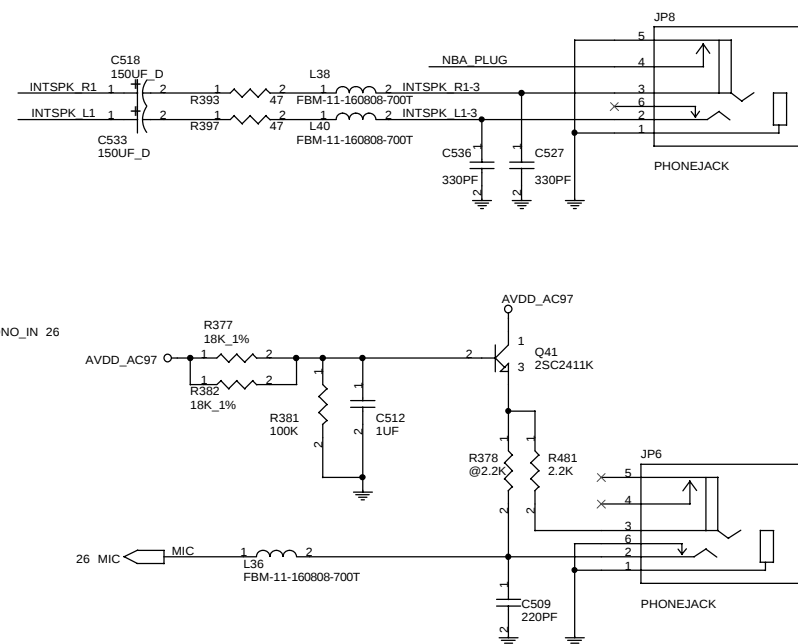
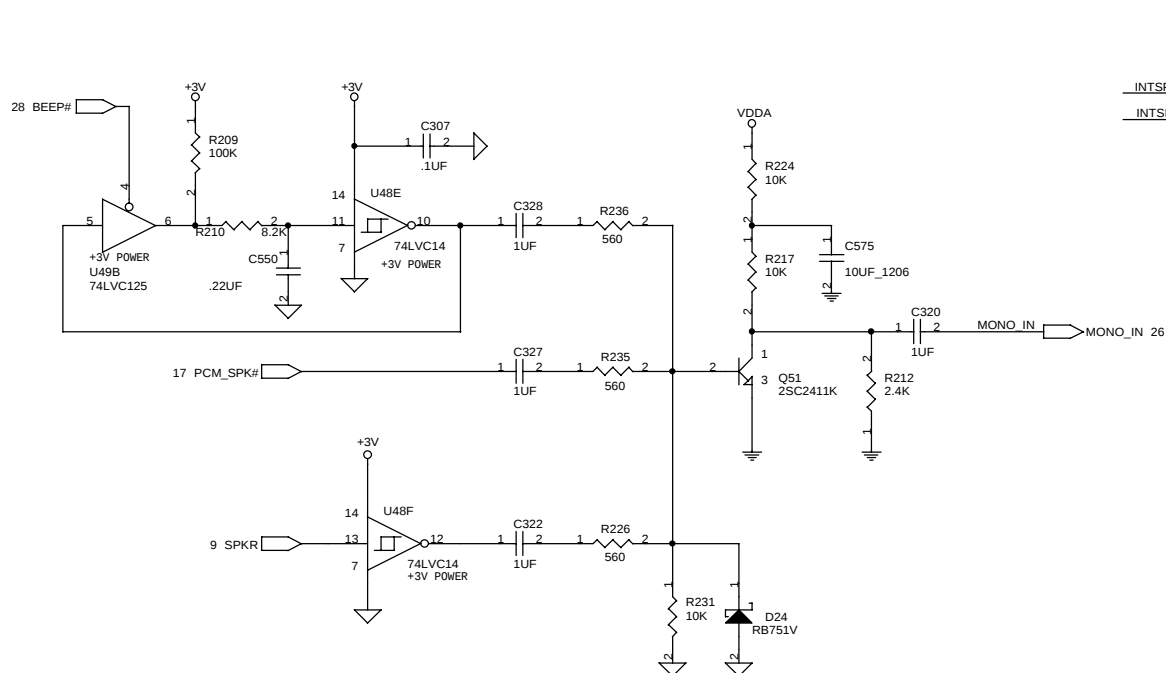
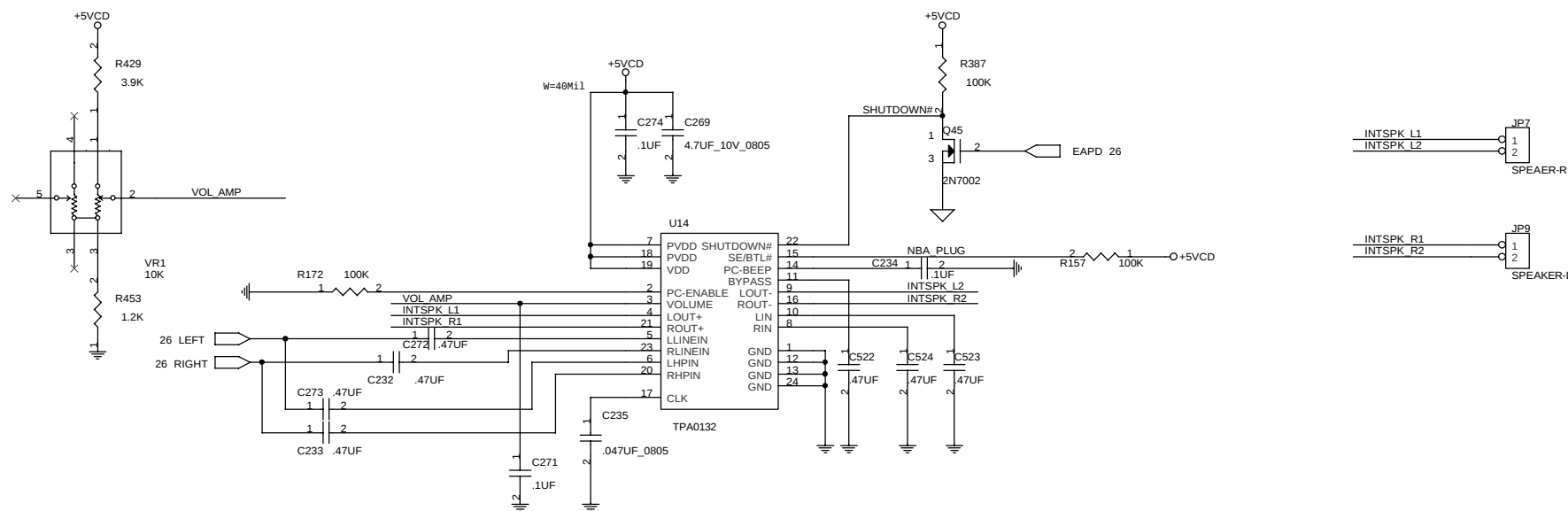
AC97 Codec



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AMP & Audio Jack



EXT.
MICPHONE
JACK

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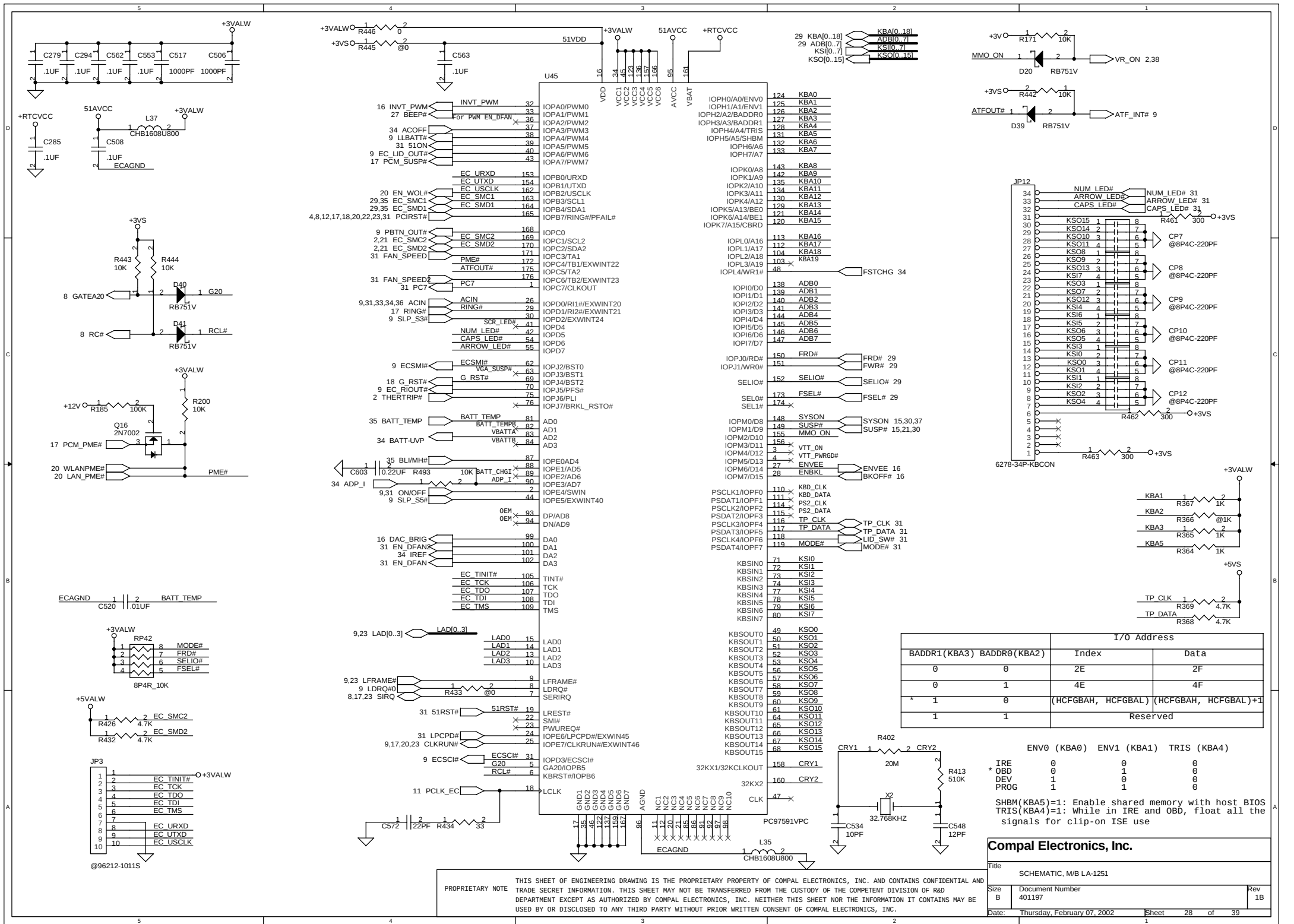
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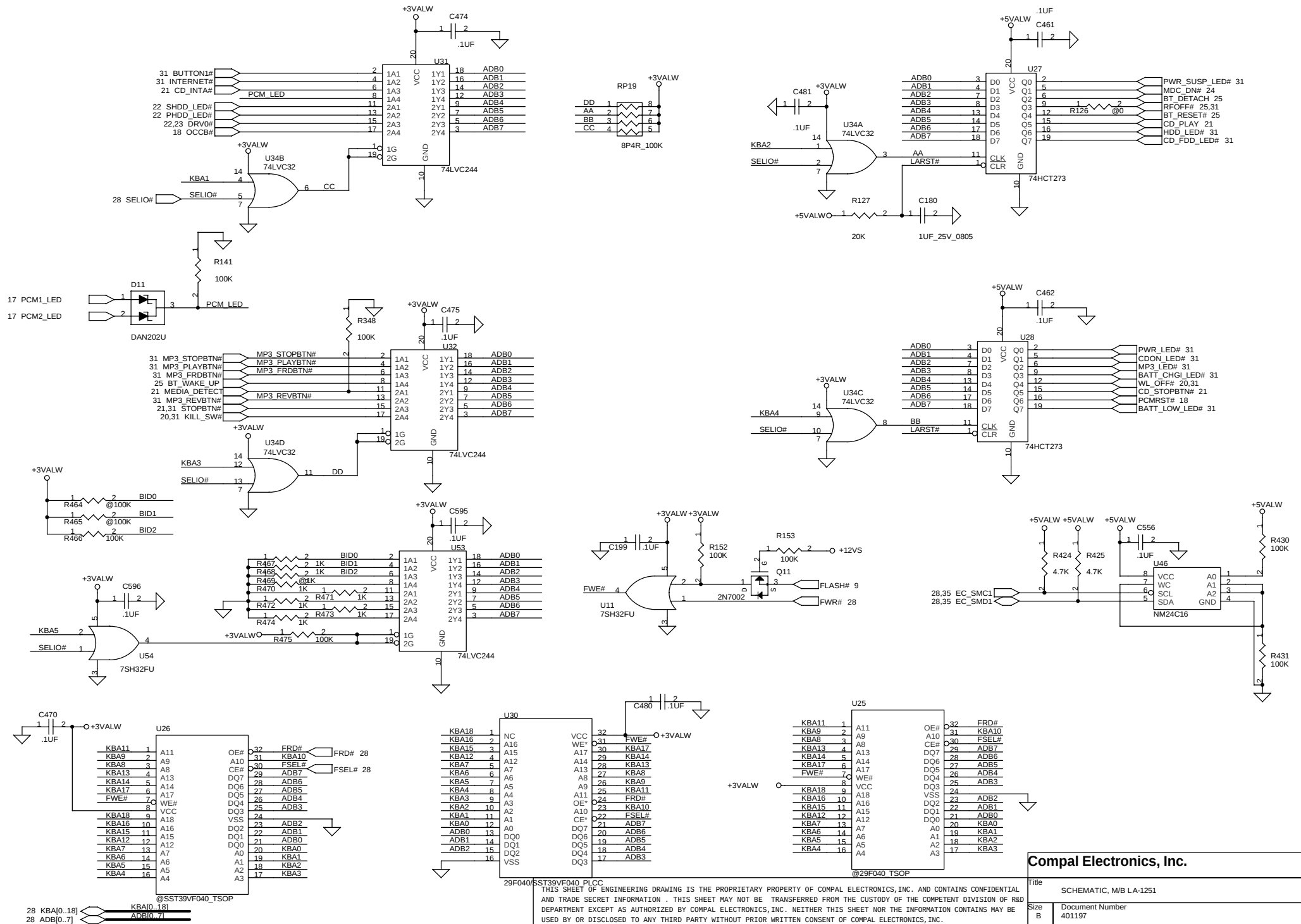
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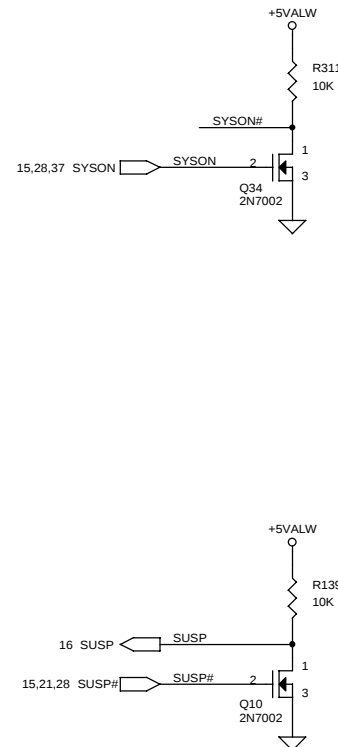
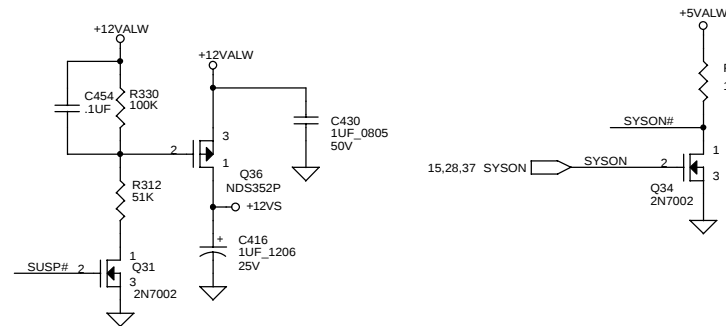
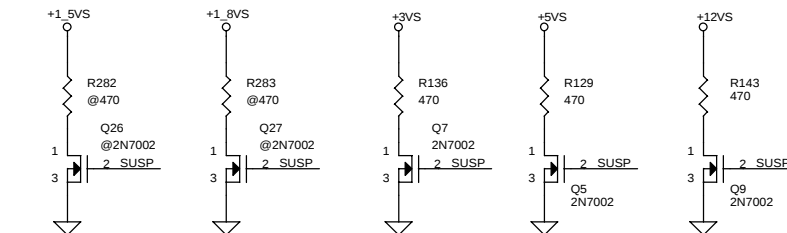
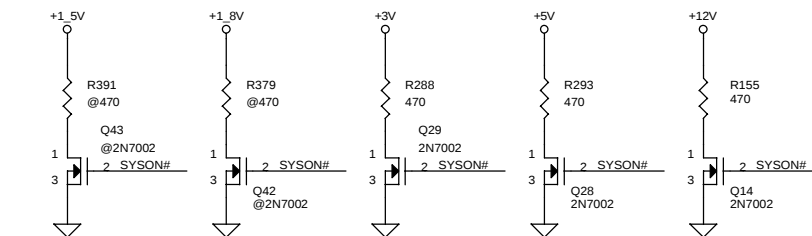
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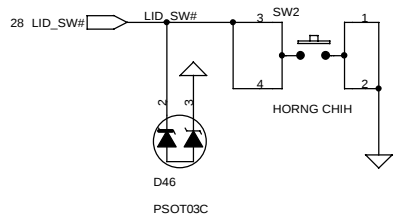
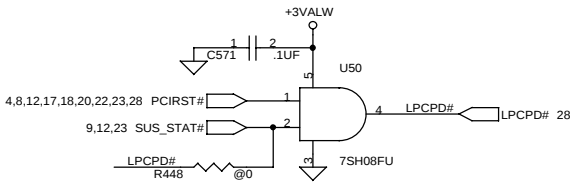
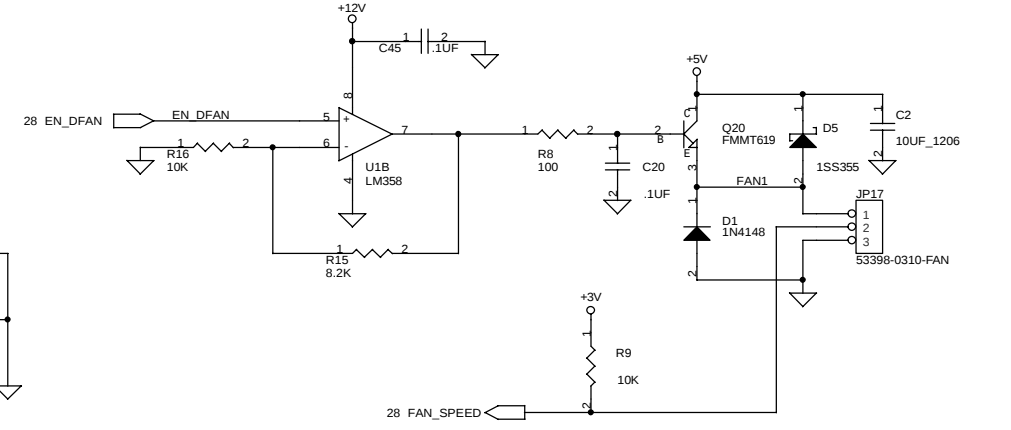
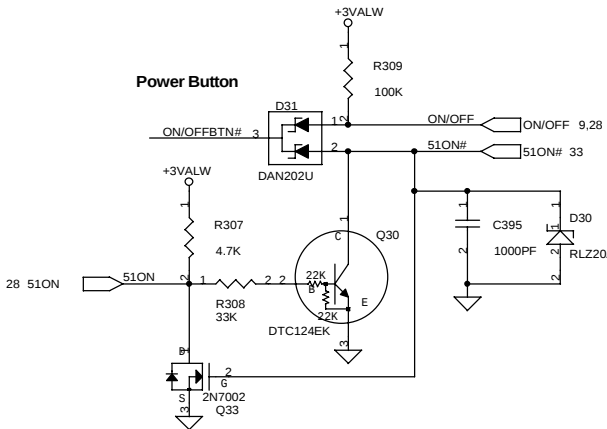
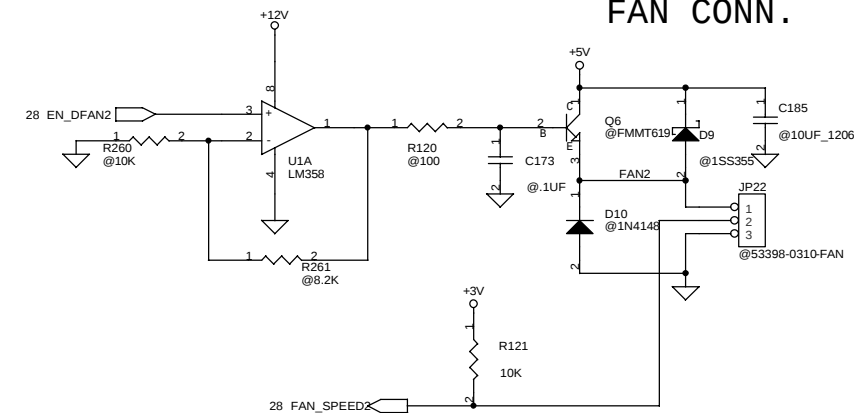
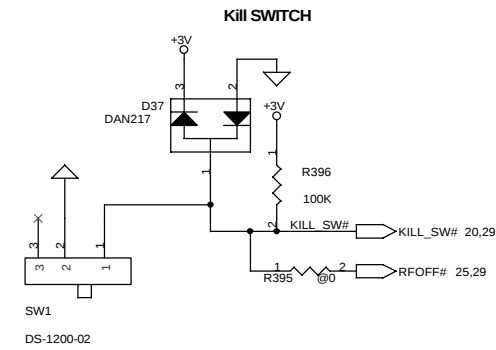
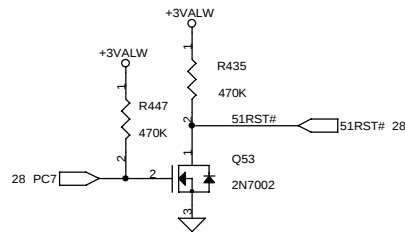
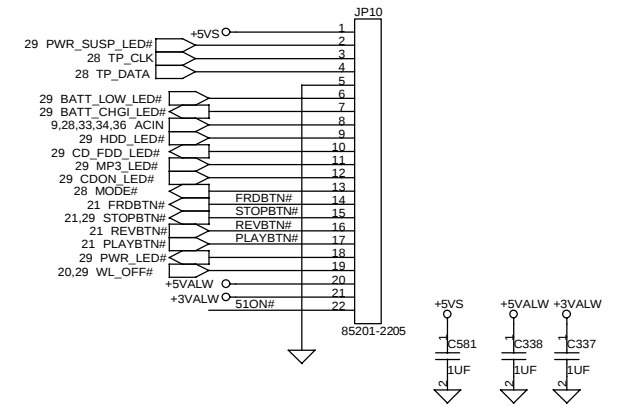
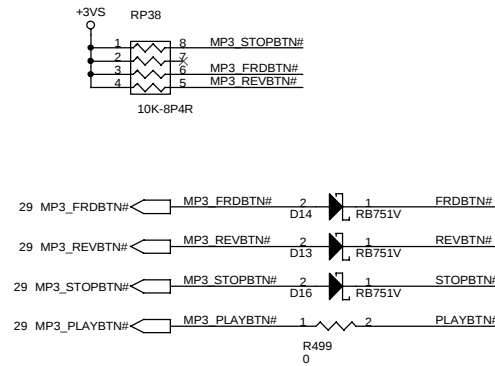
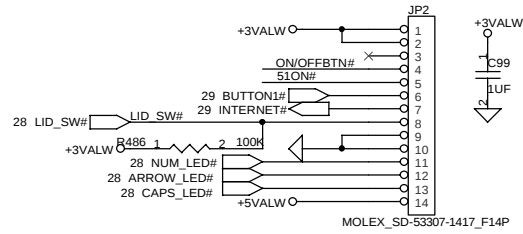


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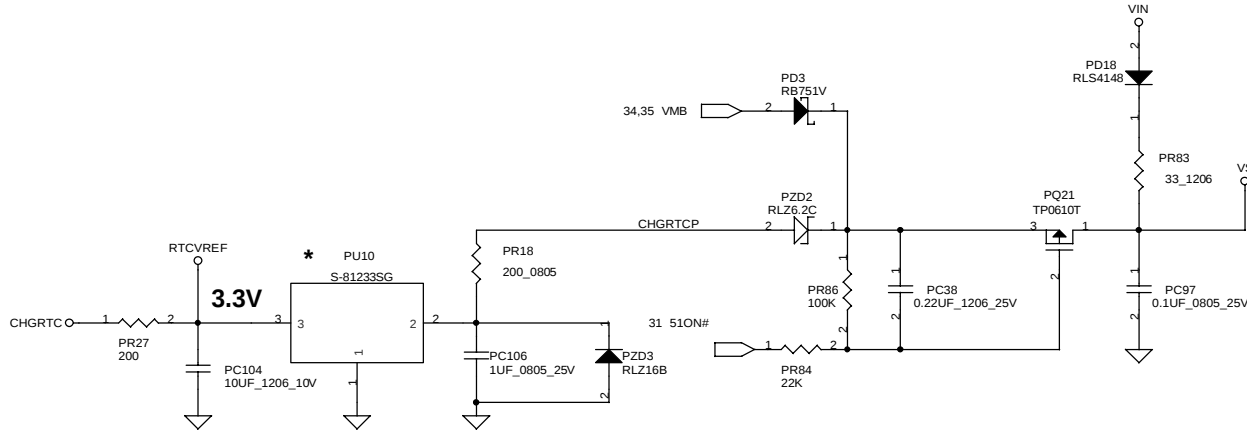
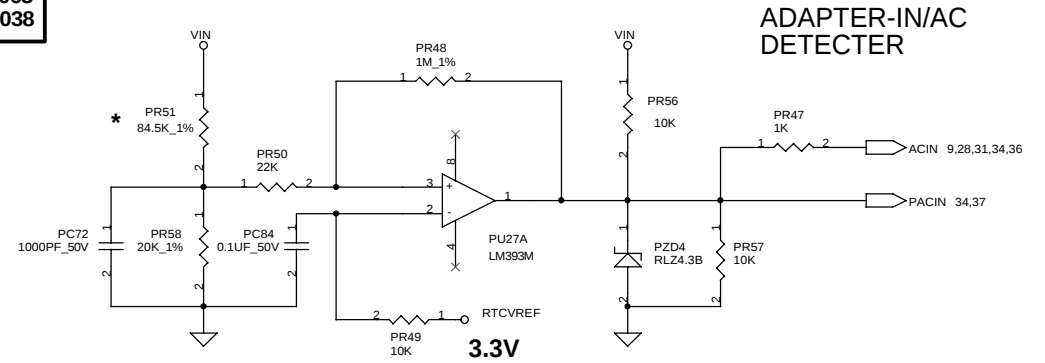
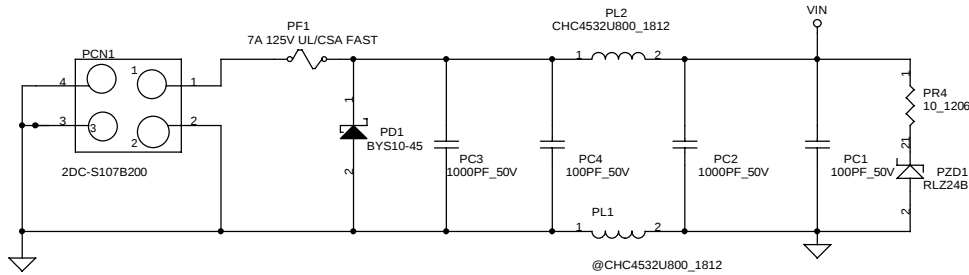
For PC87591 REV 0.A Only

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Vin Detector

High 18.764 17.901 17.063
Low 17.745 16.903 16.038



+1.8VP (3A,120mils ,Via N0.= 6)

+12VALWP (120mA,20mils ,Via N0.= 1)

+5VALWP (5A,200mils ,Via N0.= 10)

+3VALWP (5A,200mils ,Via N0.= 10)

+1.5VP (3A,120mils ,Via N0.= 6)

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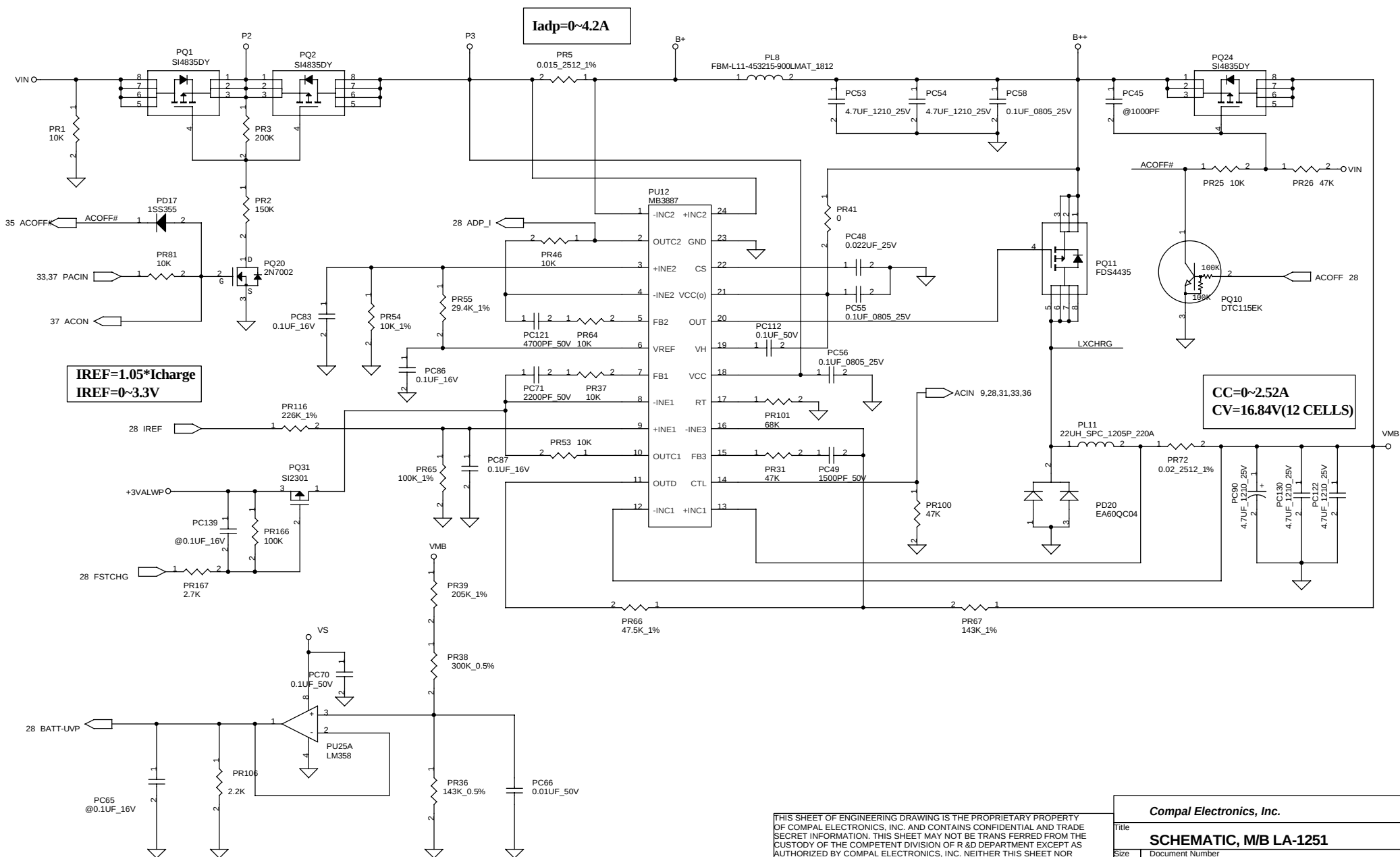
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CHARGER



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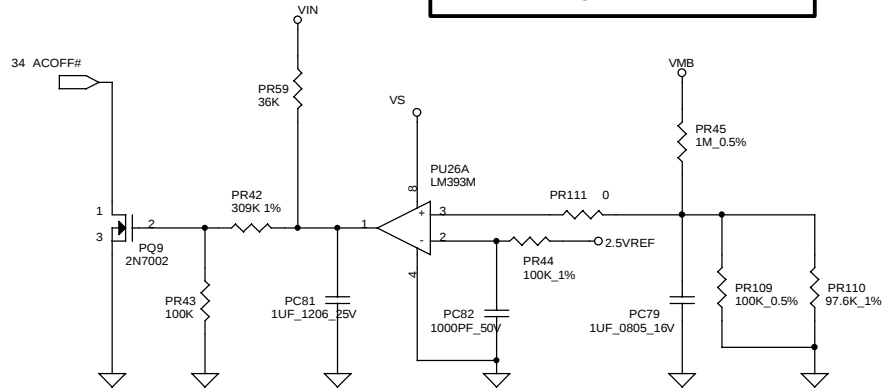
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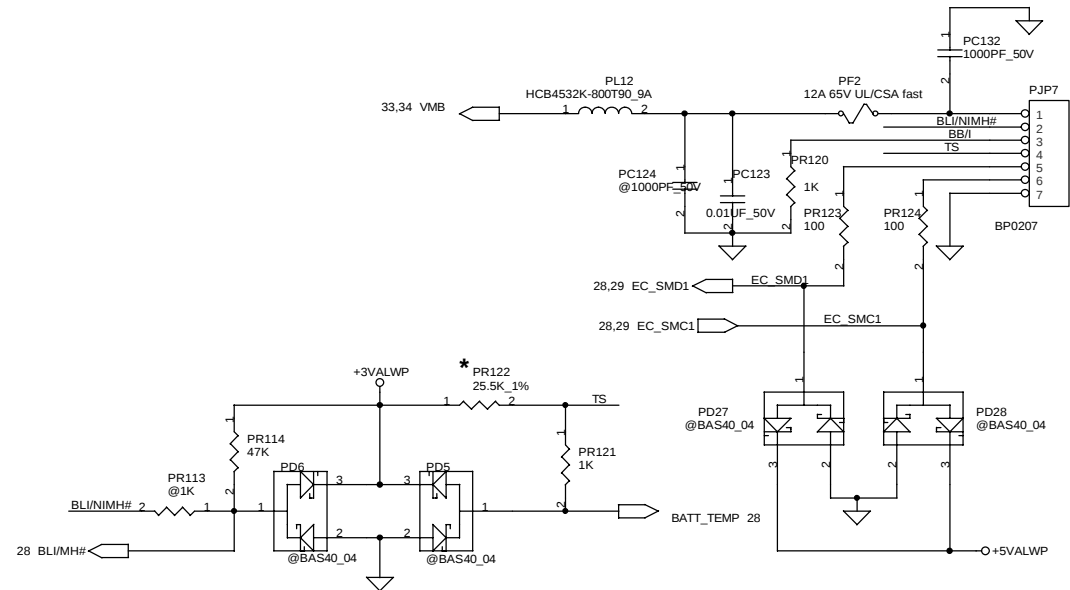
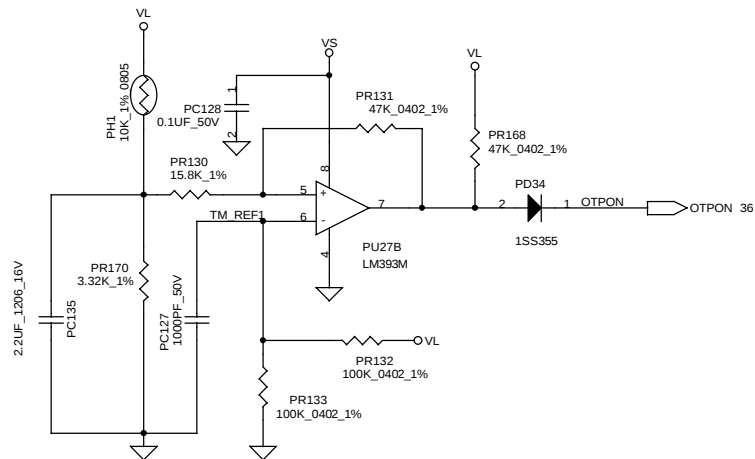
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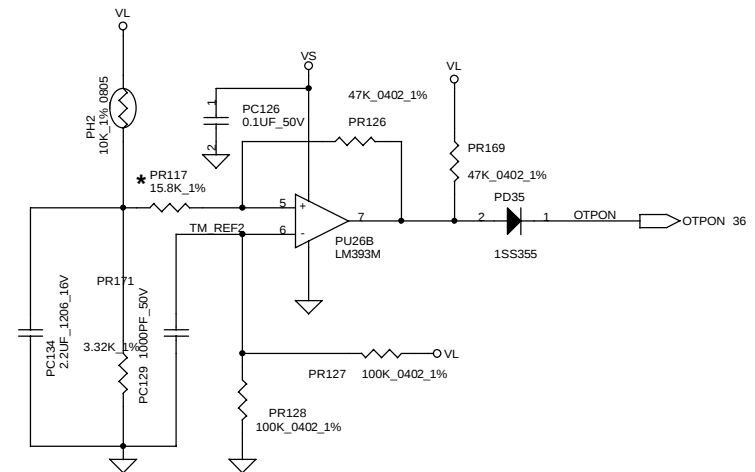
12 Cells Charger OVP : 18.059V



PH1 under CPU botten side :
CPU thermal protection at 82 degree C
Recovery at 50 degree C



PH2 near main Battery CONN :
BAT. thermal protection at 82 degree C
Recovery at 50 degree C

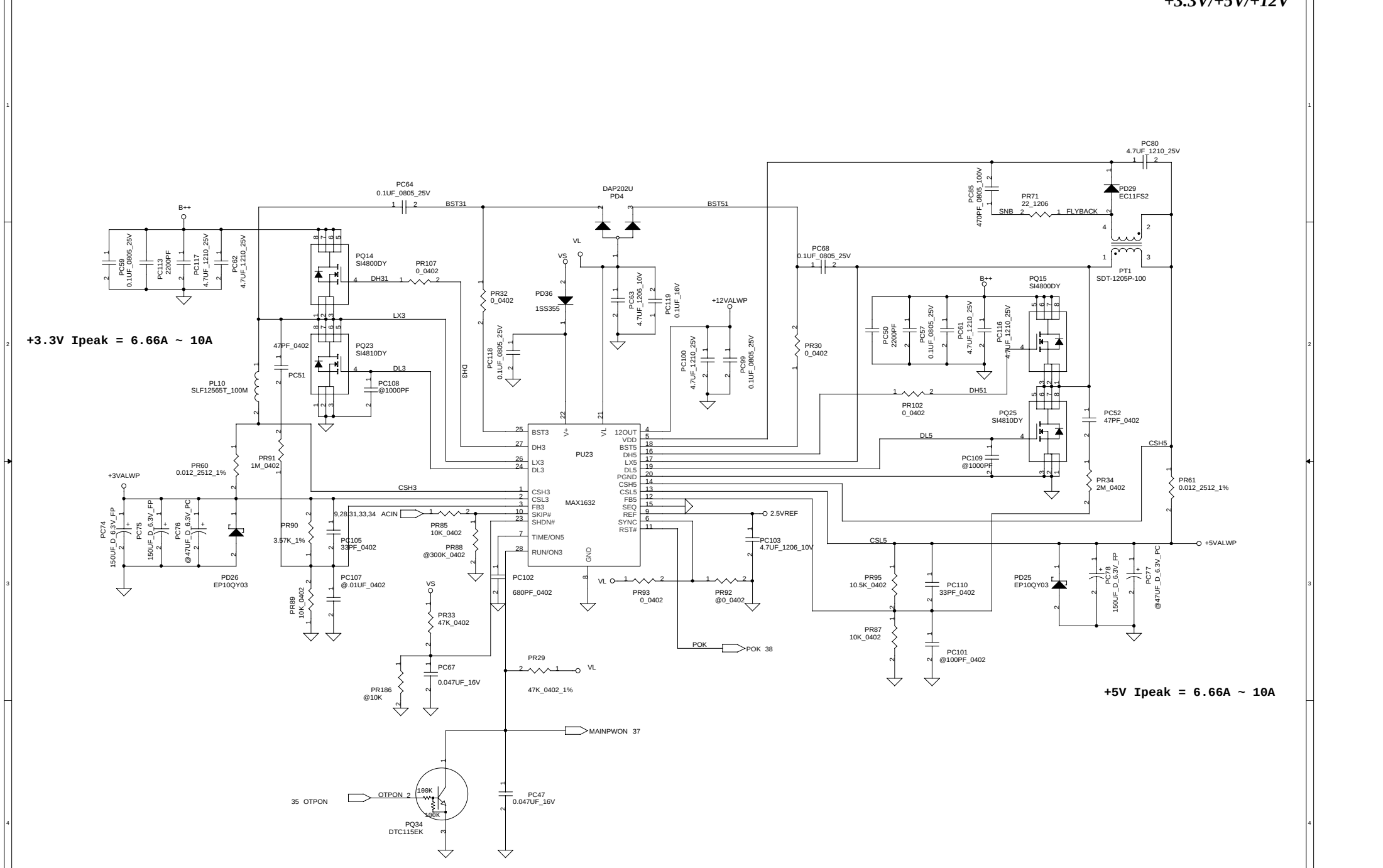


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	+2.3V/+5V/+12V
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PR186 @10K

0.047UF_16V

47K_0402_1%

+5V Ipeak = 6.66A ~ 10A

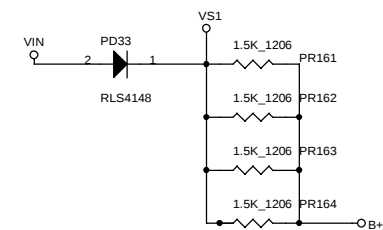
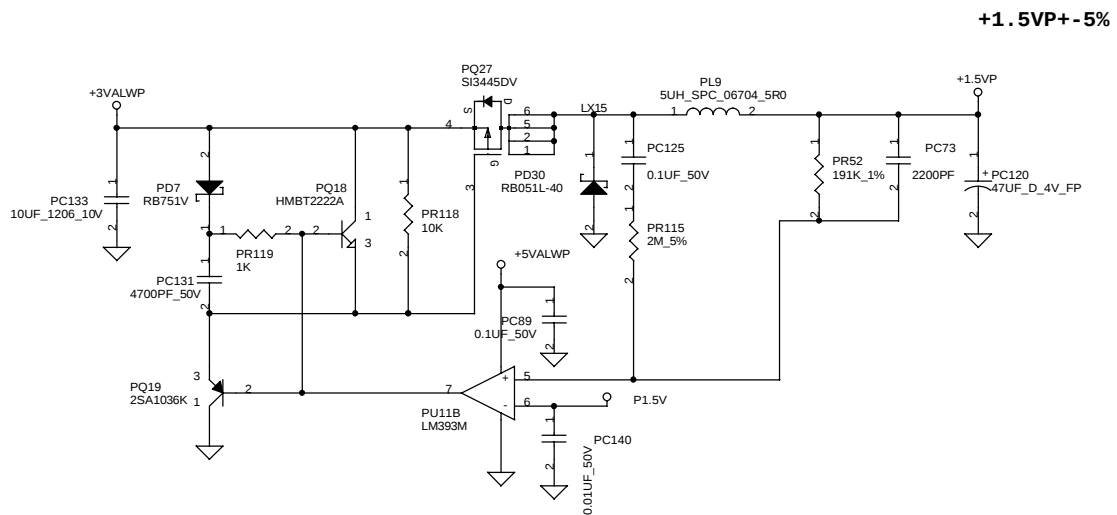
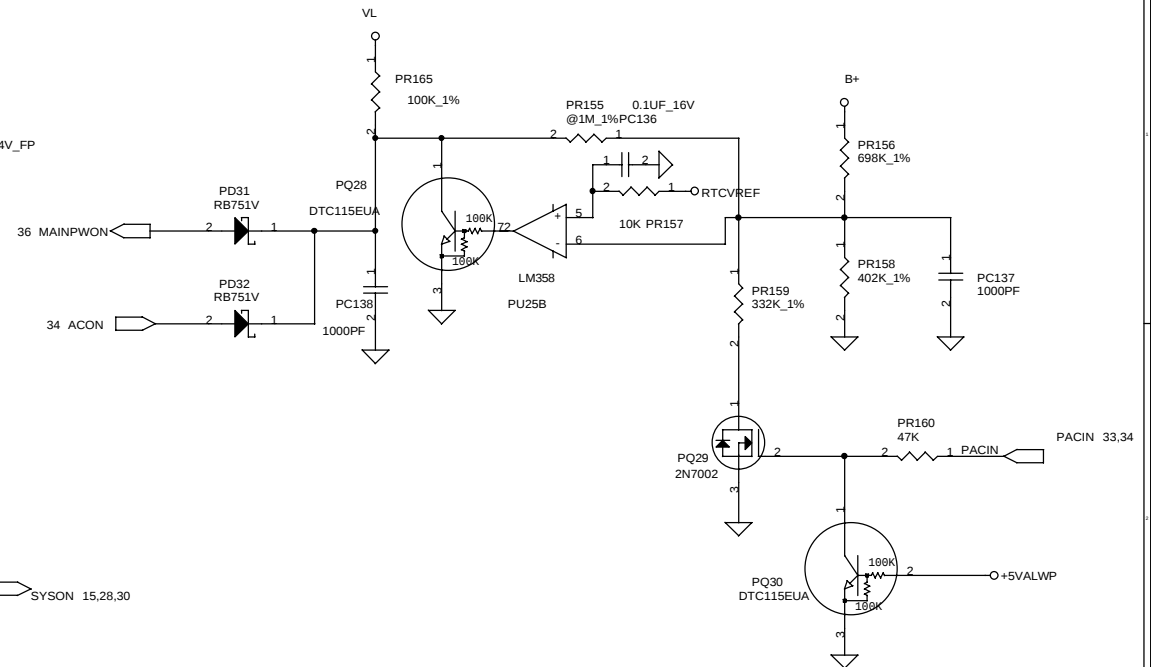
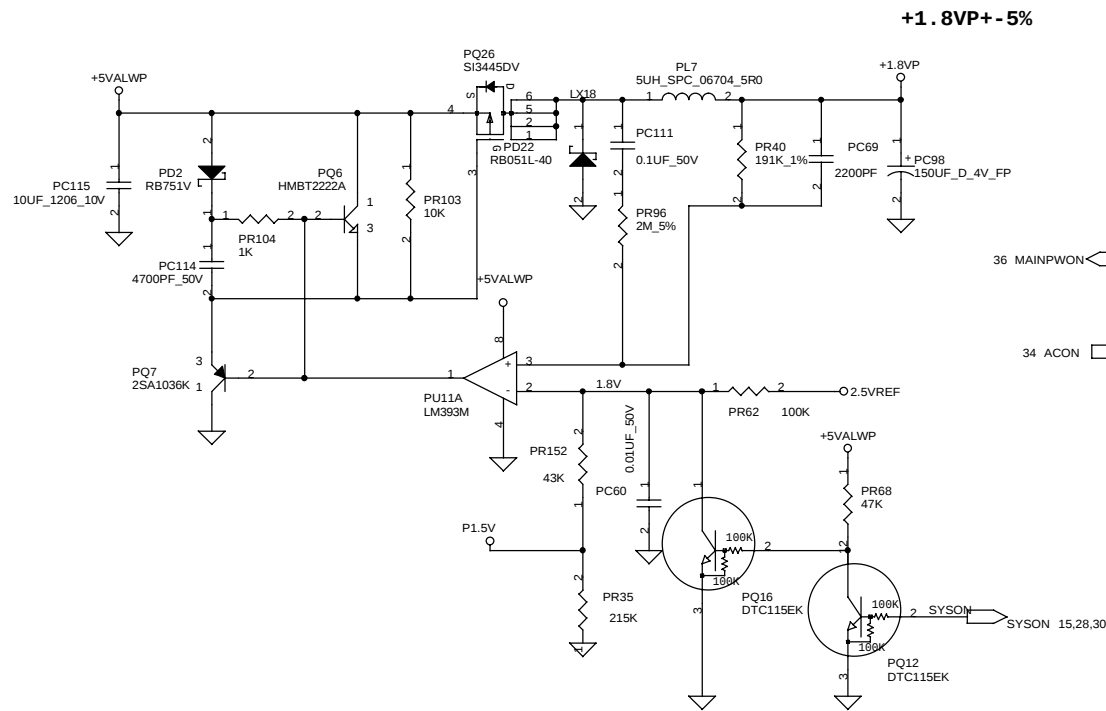
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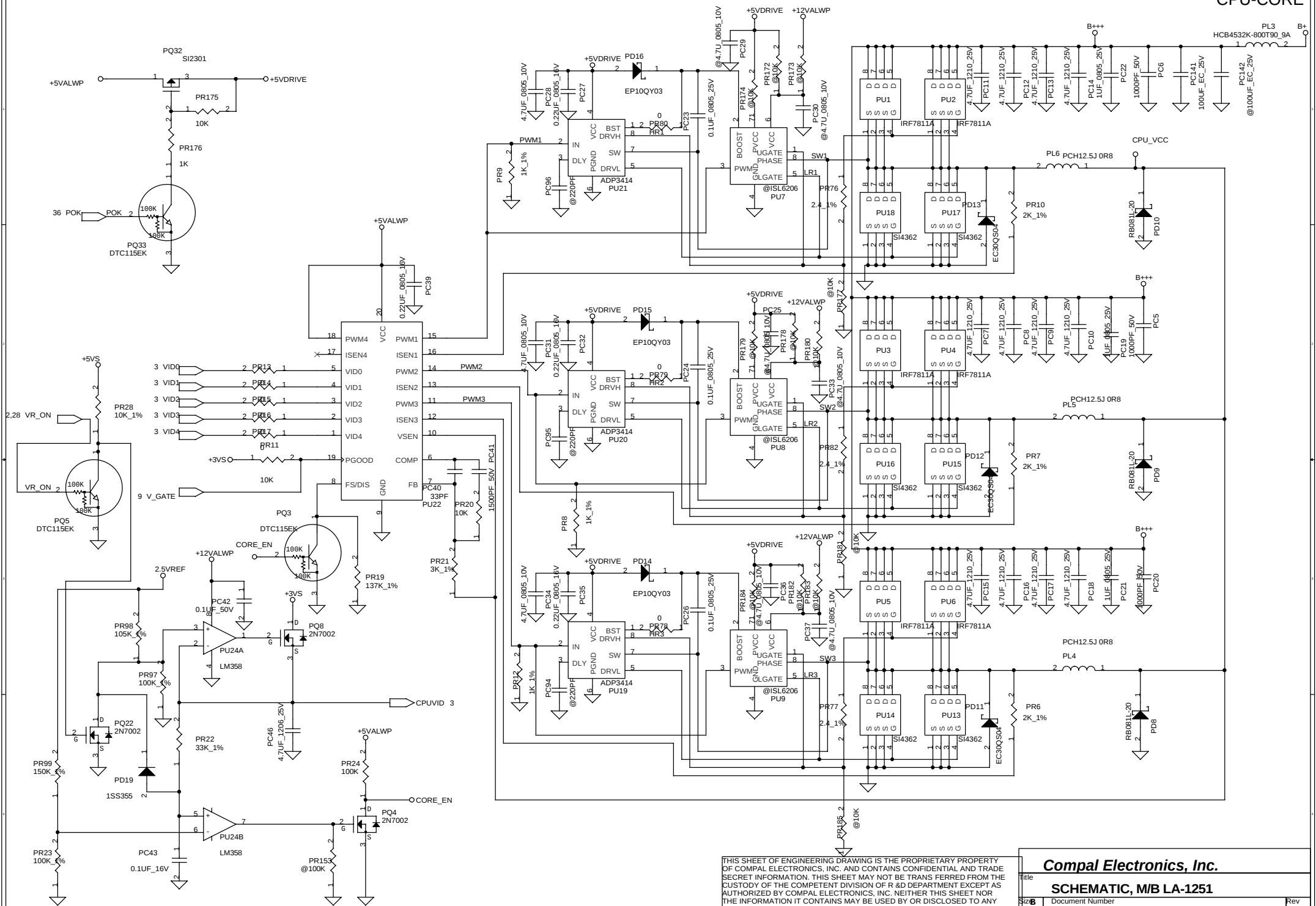
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ATR10 PIR LIST

HW PIR LIST

ATEST 9/6~

1	+2.5V LEVEL ERROR	Remove R111 100ohm	P15
2	SDRAM BANK select error	Change R108 from 100 to 0 ohm Change CLK_SDRAM3 from U22.F3 to U22.E3 Change CLK_SDRAM5 from U22.H5 to U22.G5 Change RRAS#3 from U22.H7 to U22.F7 Change RRAS#5 from U22.G6 to U22.H6	P5
3	Add CPU VCCVID pin	Add R492 connect to U23.AF3	P3
4	TV OUT output error	Change CRMA from U8.AF15 to U8.AF16 Change LUMA from U8.AF16 to U8.AF15	P12
5	Add MK1707 output terminate	Add R494 22 ohm	P12
6	Add low pass filter for MK1707 power pin	Add L47	P12
7	Change VCA option resistor to jumper	Change R284 to J3 Change R286 to J4 Change R295 to J5 Change R296 to J6	P15
8	Reserve +2.5V option control	Add R495 for SYSON control Add R496 for SUSP control	P15
9	Change LCD connector type	Change JP1	P16
10	Add TI TPS2042 for USB protect circuit	Add U56(TPS2042)	P25

PWR PIR LIST

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