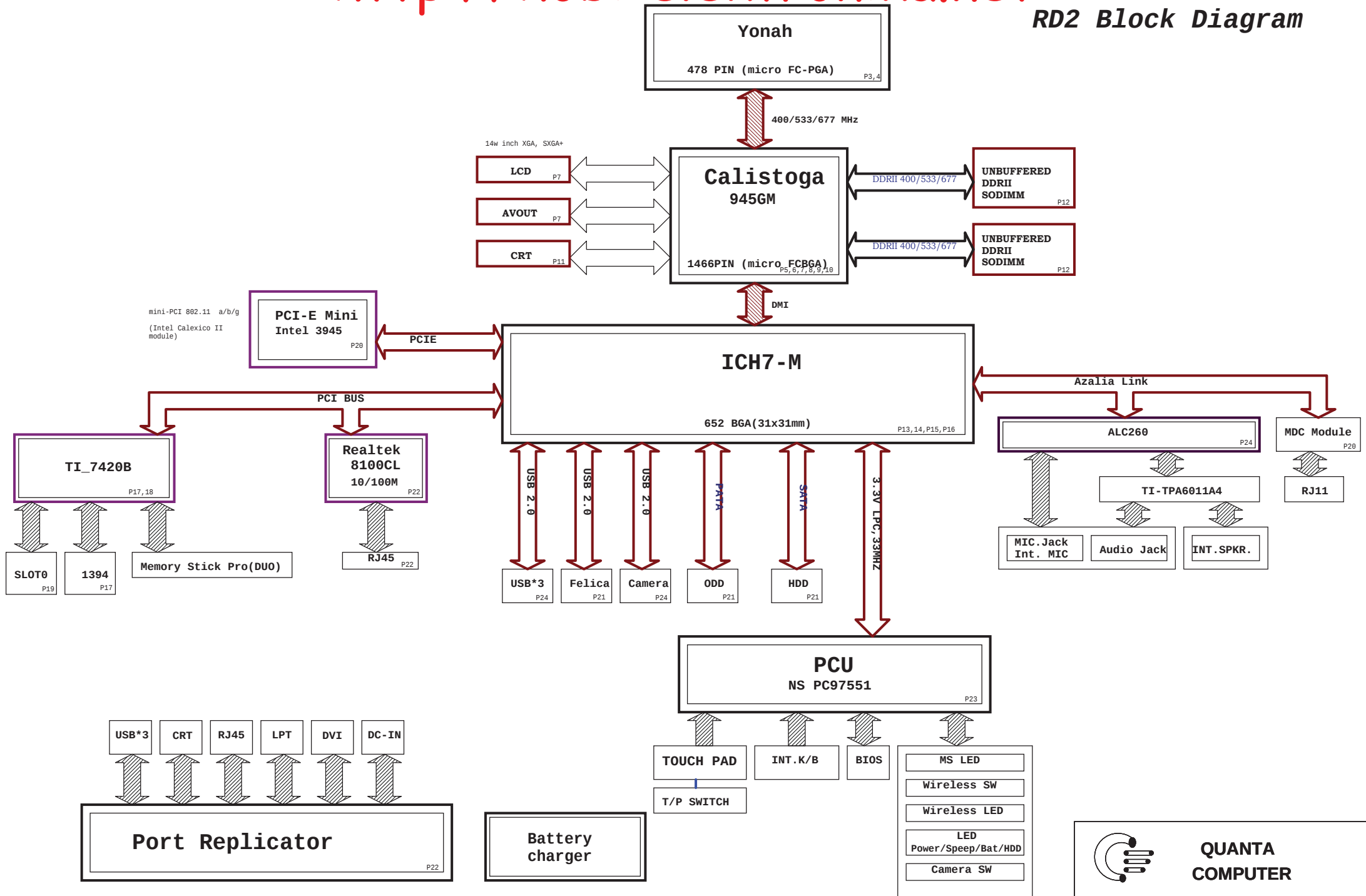
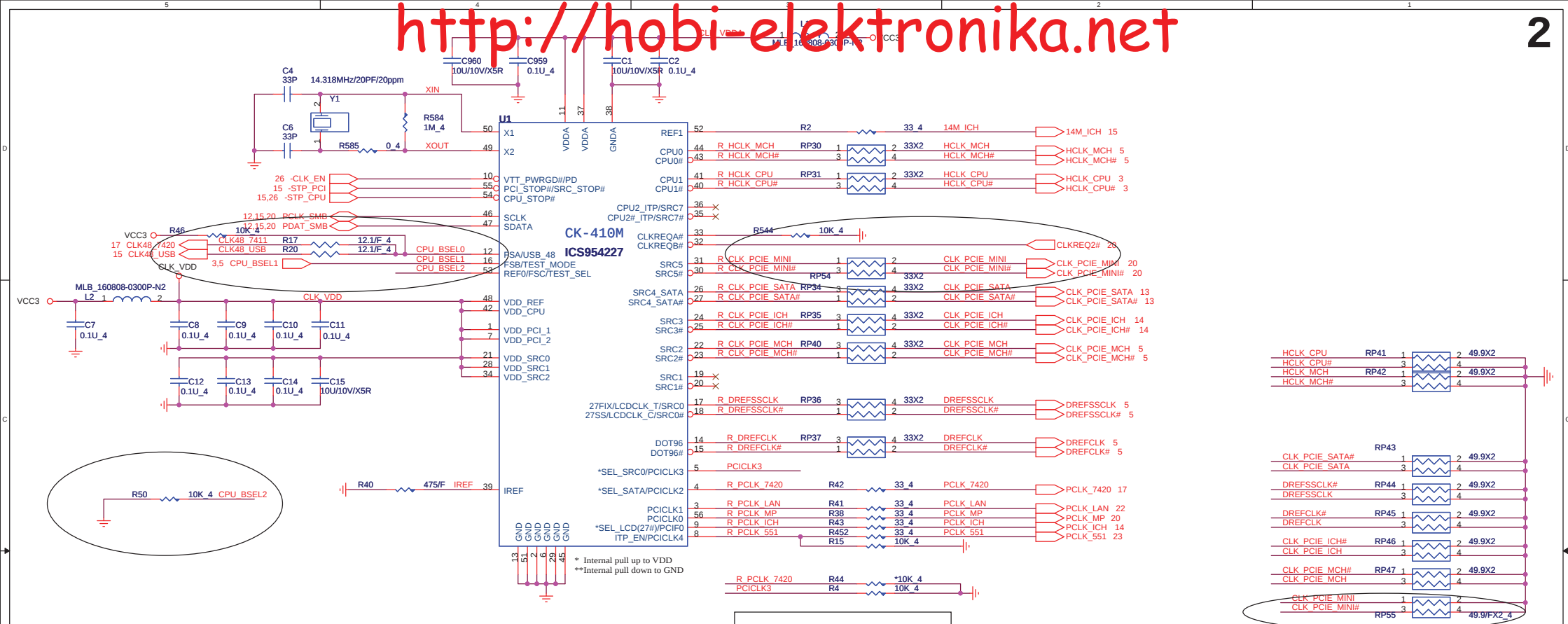


RD2 Block Diagram





R4:(Pin 17,18)
1: SRCCLK0 pair
* 0: 17,18 pin is set by pin9

R6:(Pin 17,18)
* 1: LCDCLK pair
0: 27MHZSS/27MHZSS# pair

	BSEL1	FSA BSEL0	CPU	SRC	PCI	REF	USB	DOT
	0		266.66	100	33.33	14.318	48	96
	1		133.33	100	33.33	14.318	48	96
	0		200.00	100	33.33	14.318	48	96
	1		166.66	100	33.33	14.318	48	96
	0		333.33	100	33.33	14.318	48	96
	1		100.00	100	33.33	14.318	48	96
	0		400.00	100	33.33	14.318	48	96
	1		RESERVE			14.318	48	96

ICS 954227 : Include SST

ICS 954227 Function:
Pin 53 : REF1
Pin 17,18: 96MHz_SST

ITP_EN Function(Pin 8)

* 0 : SRC_7 Pair (Pin 35,36)
1 : CPU_2 ITP Pair (Pin 35,36)

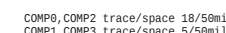
IREF (Pin 39) :
475_1 : Sets the IREF current to 2.32mA

96MHz SST Table :

S3	S2	S1	S0	Spread	Type
0	0	0	0	0.8	Down
0	0	0	1	1	
0	0	1	0	1.25	
0	0	1	1	1.5	
0	1	0	0	1.75	
0	1	0	1	2	
0	1	1	0	2.5	
0	1	1	1	3	
1	0	0	0	+/-0.3	
1	0	0	1	+/-0.4	
1	0	1	0	+/-0.5	Center
1	0	1	1	+/-0.6	
1	1	0	0	+/-0.8	
1	1	0	1	+/-1.0	
1	1	1	0	+/-1.25	
1	1	1	1	+/-1.5	



1.This part should not contain any substances which are specified in SS-00259-1.
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.



Thermal Sensor



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CPU1 of 2

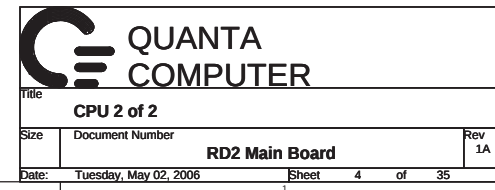
Size	Document Number	Rev
------	-----------------	-----

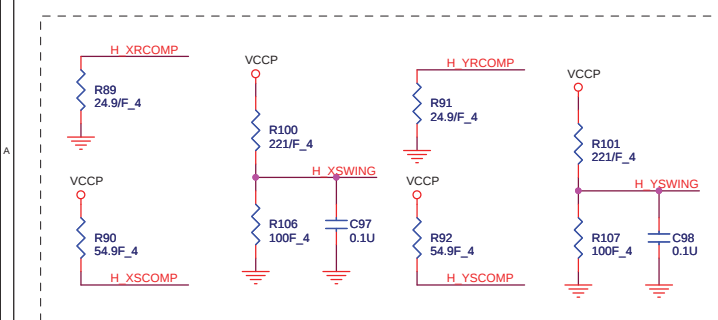
	RD2 Main Board	1A
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Date: Monday, April 10, 2006	Sheet 3 of 35
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1.This part should not contain any substances which are specified in SS-00259-1

2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners



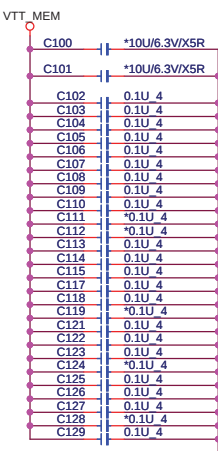
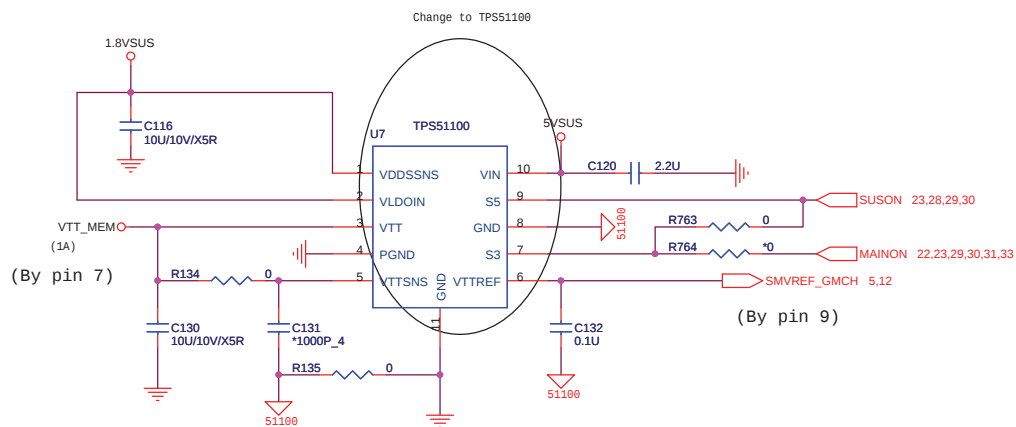
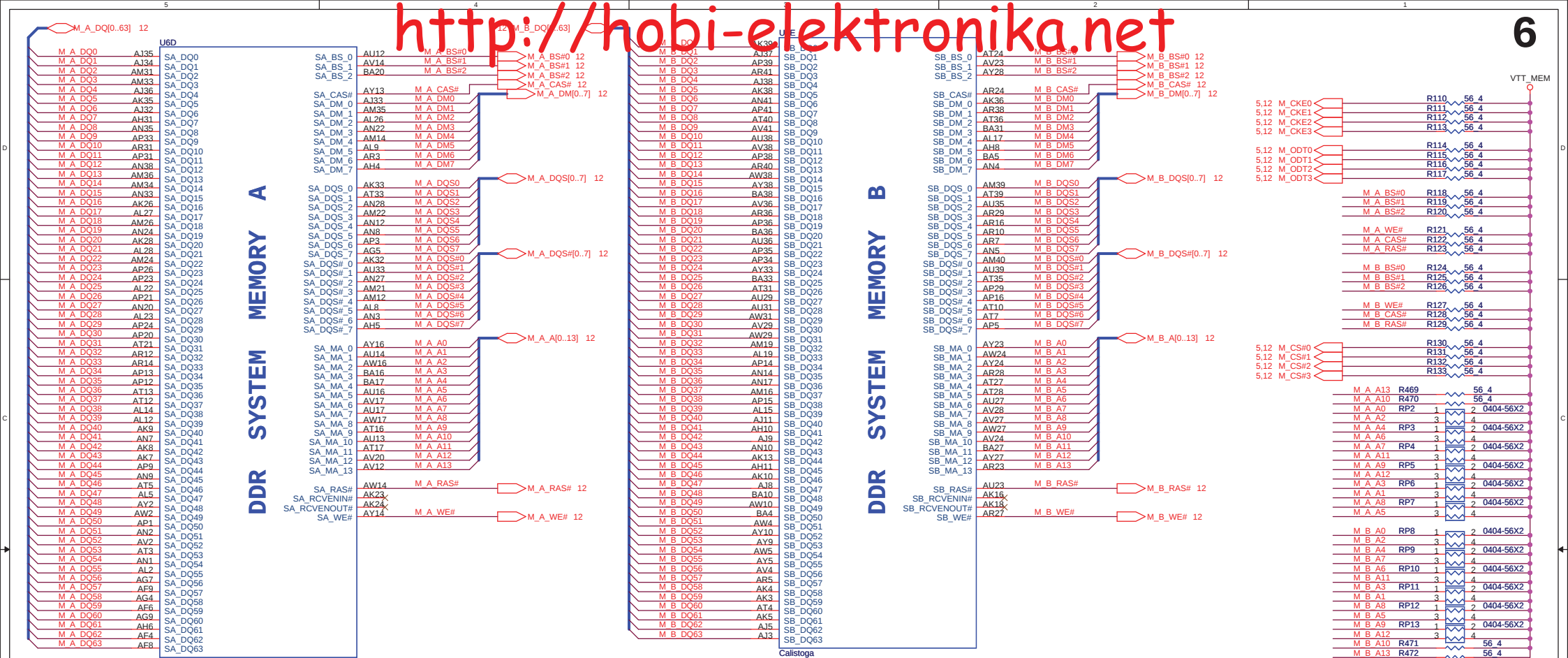


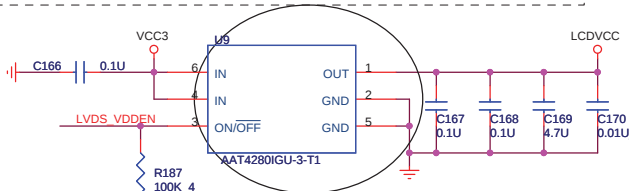
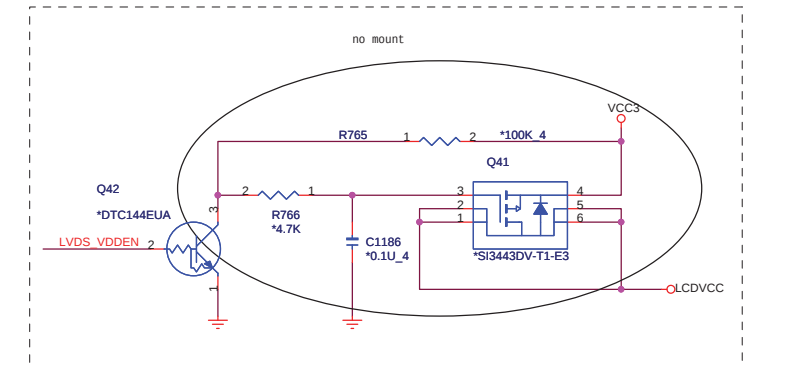
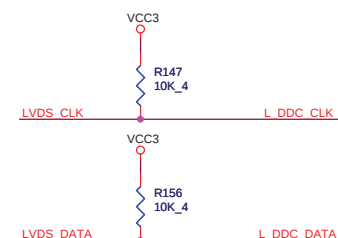
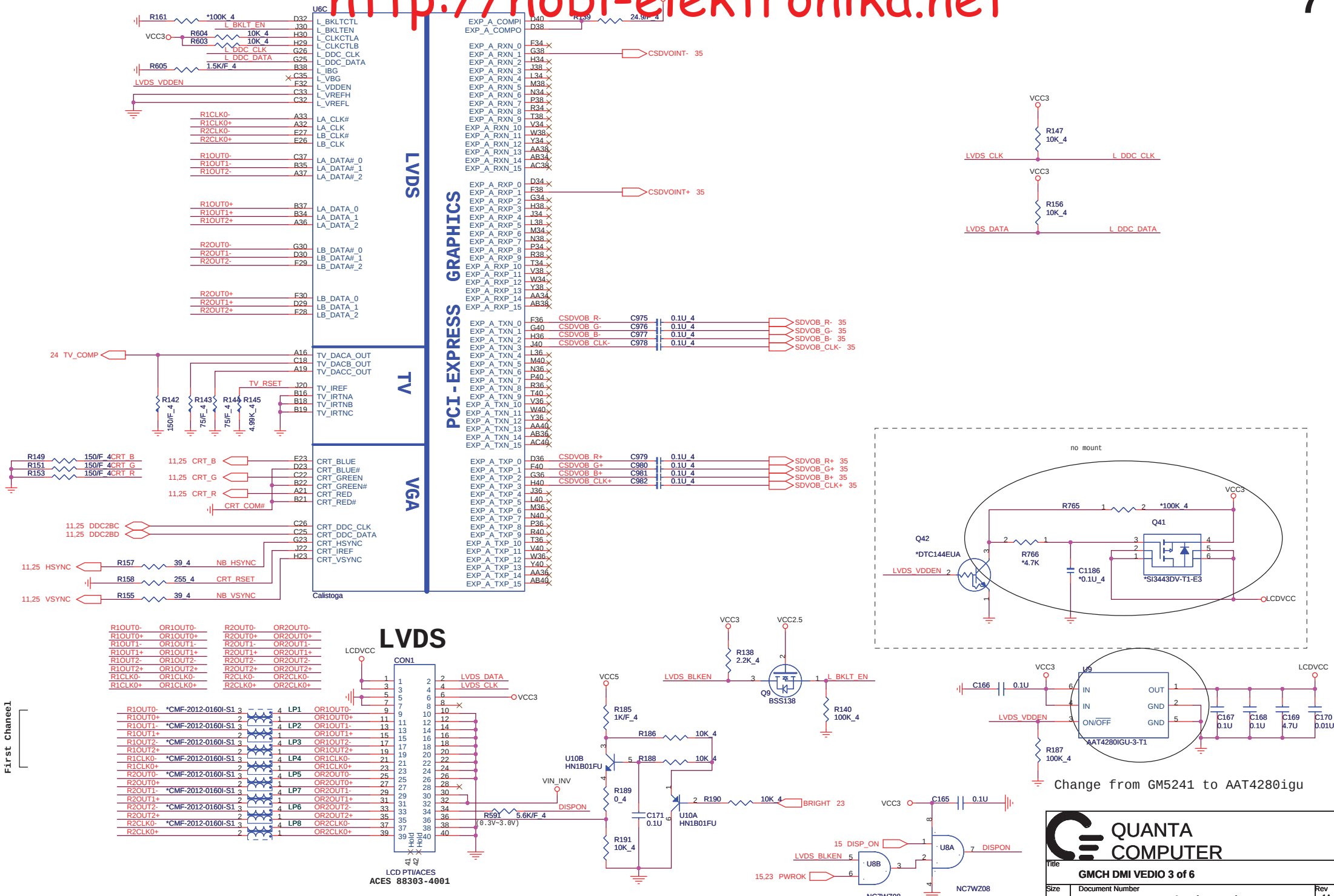
Low		High
CGF5	DMix2	* DMix4
CGF6	RSVD for 945GMS	
CGF7	RSVD	* CPU type: Mobile CPU
CGF9	PCIe Graphics lan : Reverse Lane	* PCIe Graphics lan : Normal operation
CGF10	reserved	
CGF11	reserved	
CGF16	FSB Dynamic ODT Disabled	* FSB Dynamic ODT Enabled
CGF18	* GMCH core: 1.05V	GMCH core: 1.5V
CGF19	* DMI LANE Normal	DMI LANE Reversed
CGF20	* only SDVO or PCIe x1 is operational	SDVO and PCIe x1 are operation simultaneously via the PEG port
CGF[13:12]	00 = Partial clock gating disable 01 = XOR mode enabled 10 = All-Z mode enabled * 11 = Normal Operation(Default)	

Int.Pull-down : CGF 18,19,20
Int.Pull-high : CGF 3-17

Component	P/N
945GM	AJ009450T04
945PM	AJ009450T12

1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners





Change from GM5241 to AAT4280igu

QUANTA COMPUTER

GMCH DMI VEDIO 3 of 6

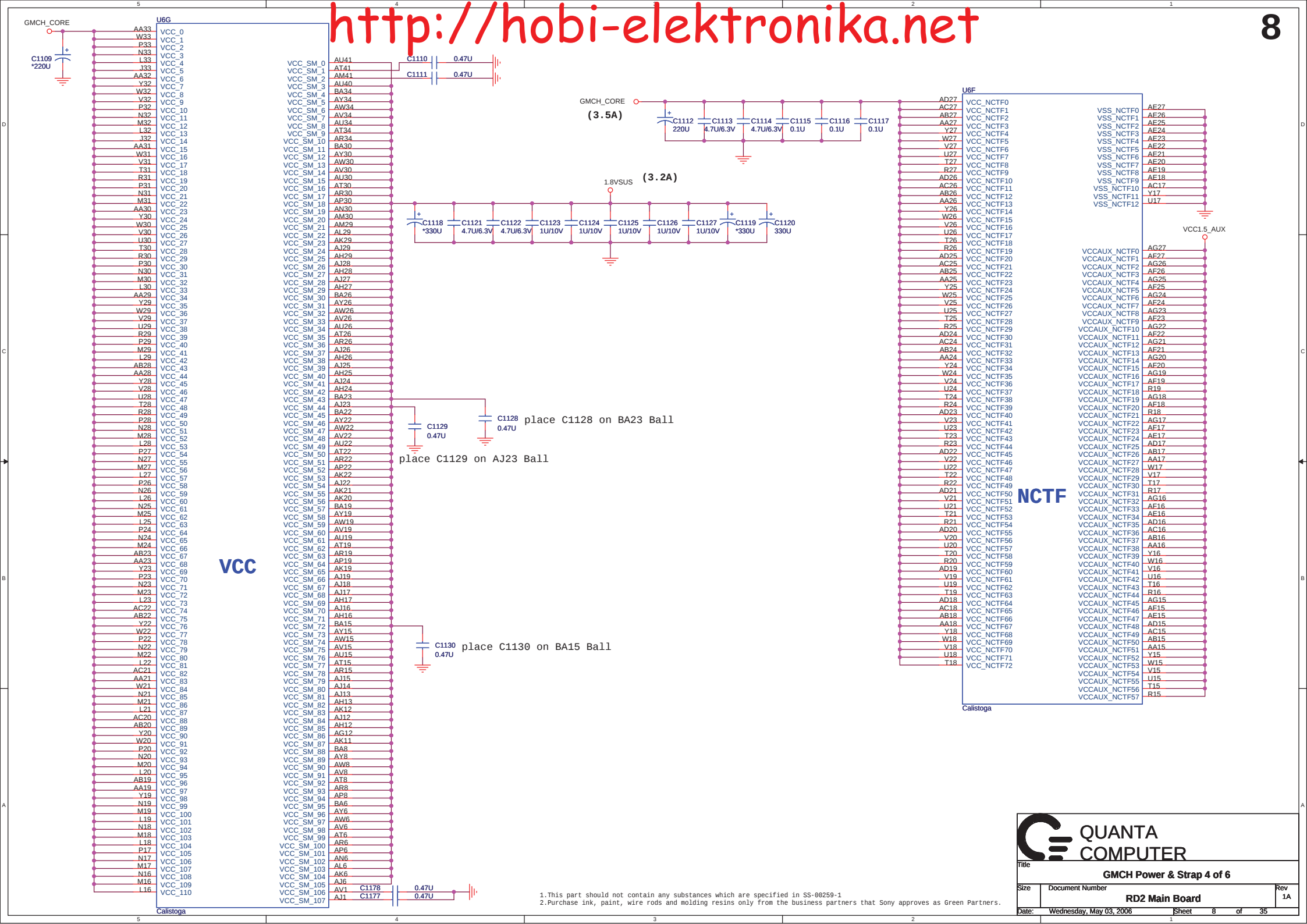
RD2 Main Board

Rev 1A

Monday, April 10, 2006

Sheet 7 of 35

1. This part should not contain any substances which are specified in S5-00259-1
2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

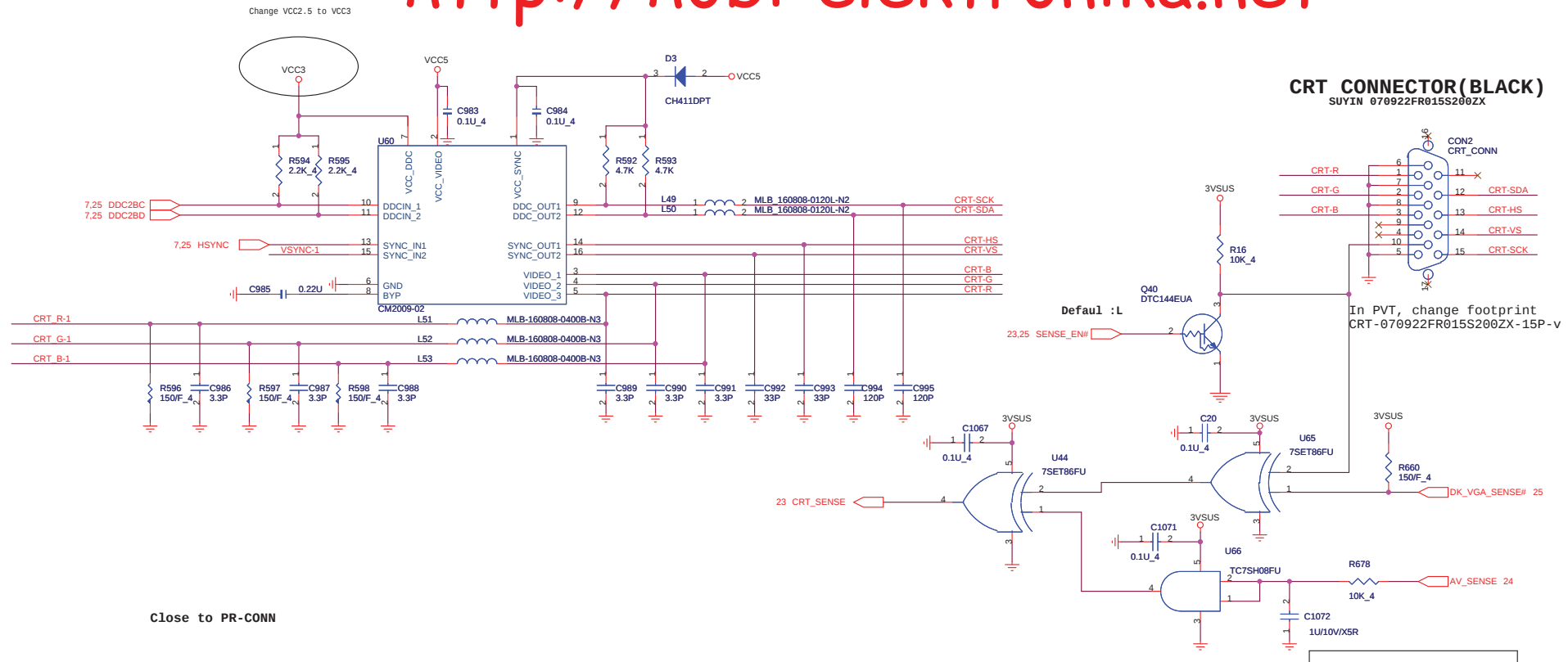




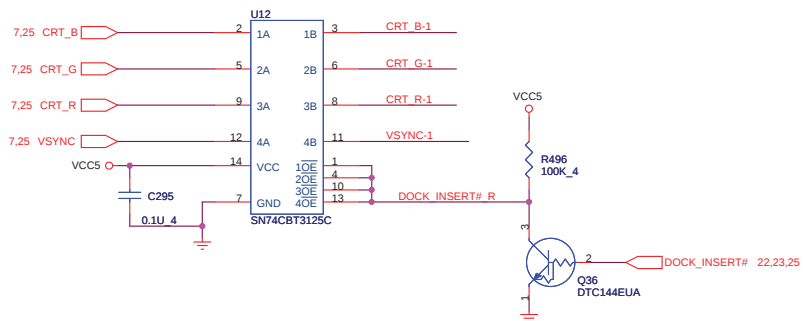
1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.



VSS



Close to PR-CONN

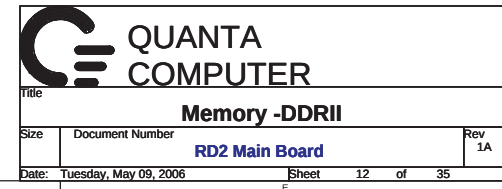


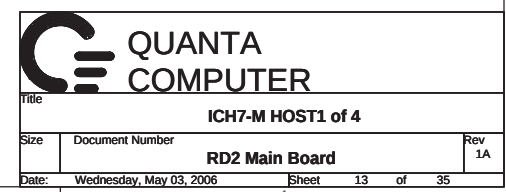
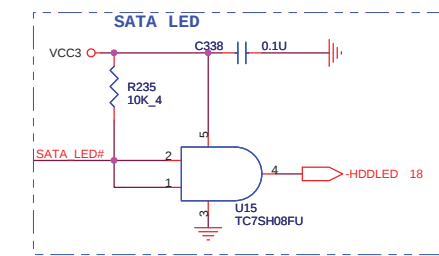
Plug Display true table

MB no CRT/AV
MB CRT Plug
MB AV-OUT Plug
MB CRT/AV-OUT Plug
DOCK No CRT/DVI /AV
DOCK CRT Plug
DOCK DVI Plug
DOCK AV Plug
DOCK CRT/DVI Plug
DOCK DVI/AV Plug
DOCK CRT/AV Plug
DOCK CRT/DVI/AV Plug

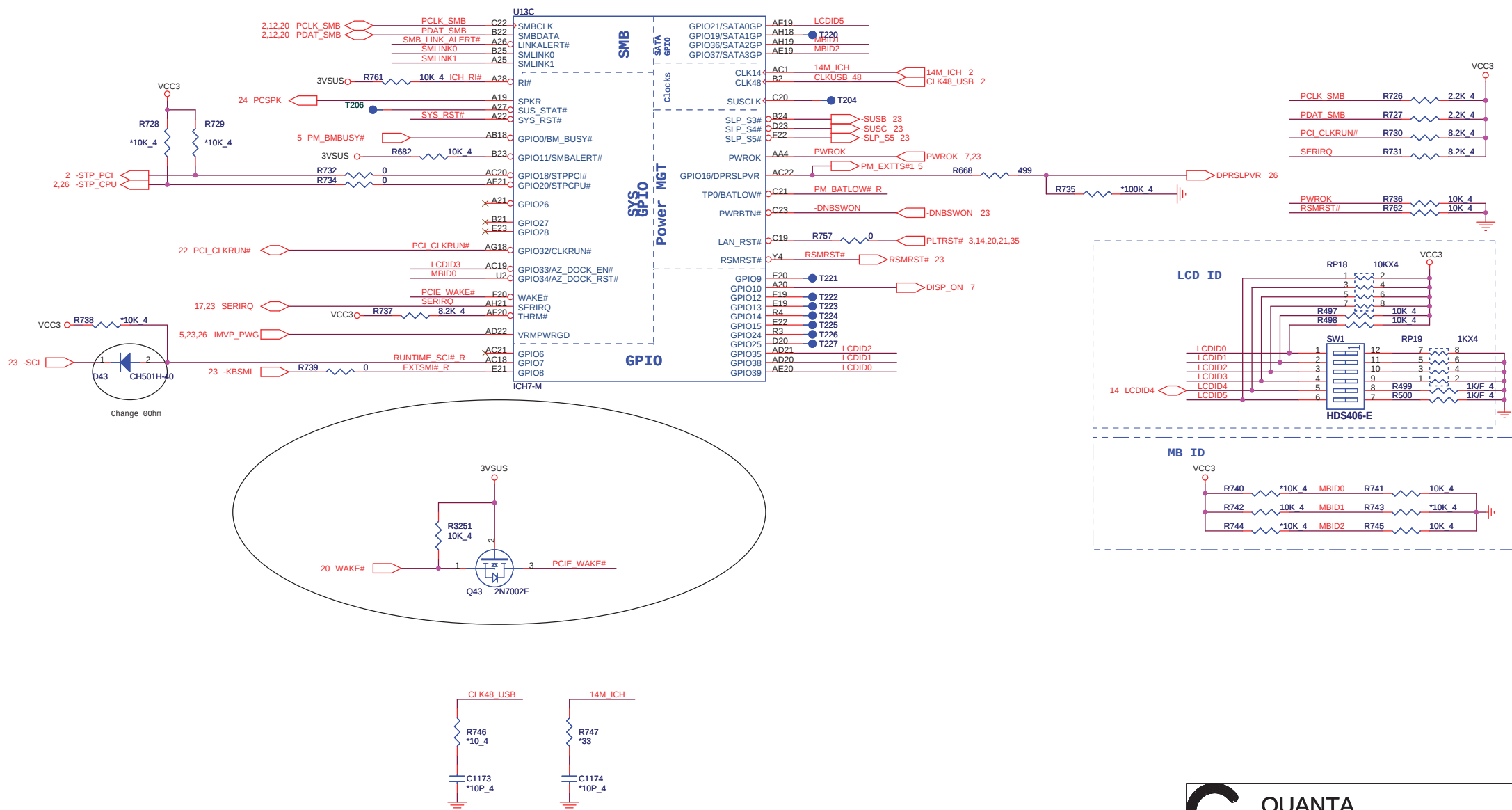
DOCK_INSERT#	SENSE_EN#	MB CRT	DK_VGA_SENSE#	DOCK DVI	DOCK CRT	AV_SENSE	CRT_SENSE
H	L	H	H	X	X	L	L
H	L	L	H	X	X	L	H
H	L	H	H	X	X	H	H
H	L	L	H	X	X	H	L
L	L	H	H	L	H	L	L
L	L	H	L	L	H	L	H
L	L	H	H	L	H	H	H
L	L	H	H	H	L	L	L
L	L	H	L	H	H	H	L
L	L	H	H	H	L	H	H
X	H	X	X	X	X	X	L



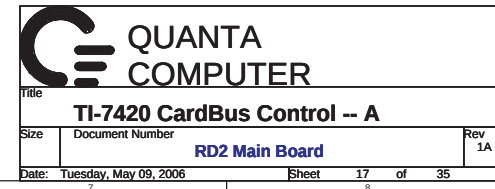




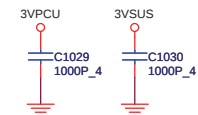
2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.



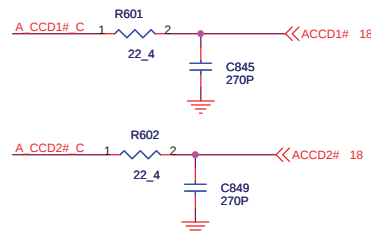
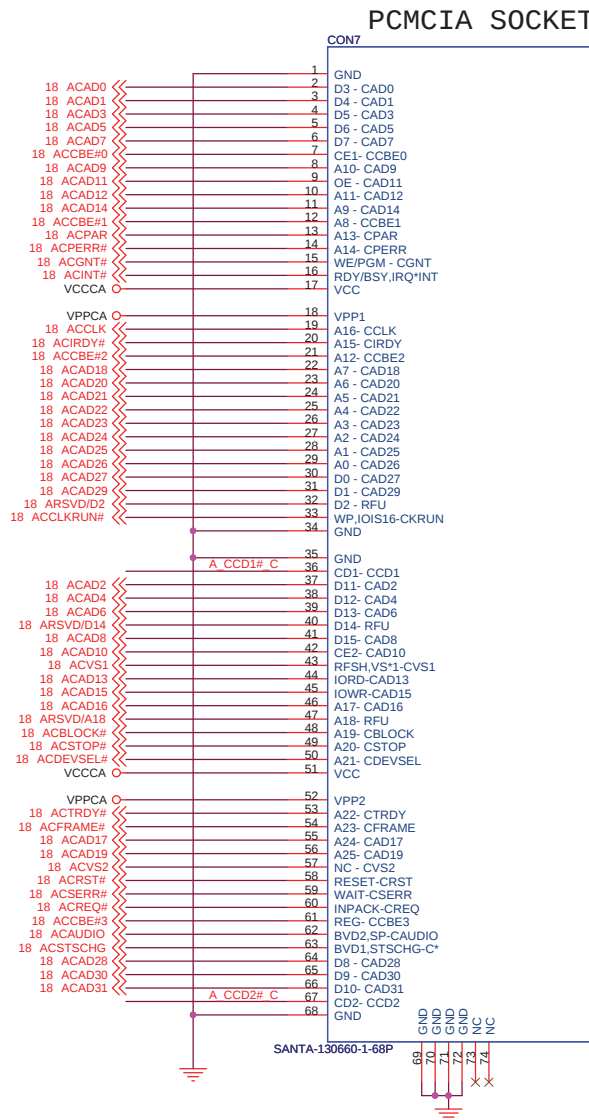




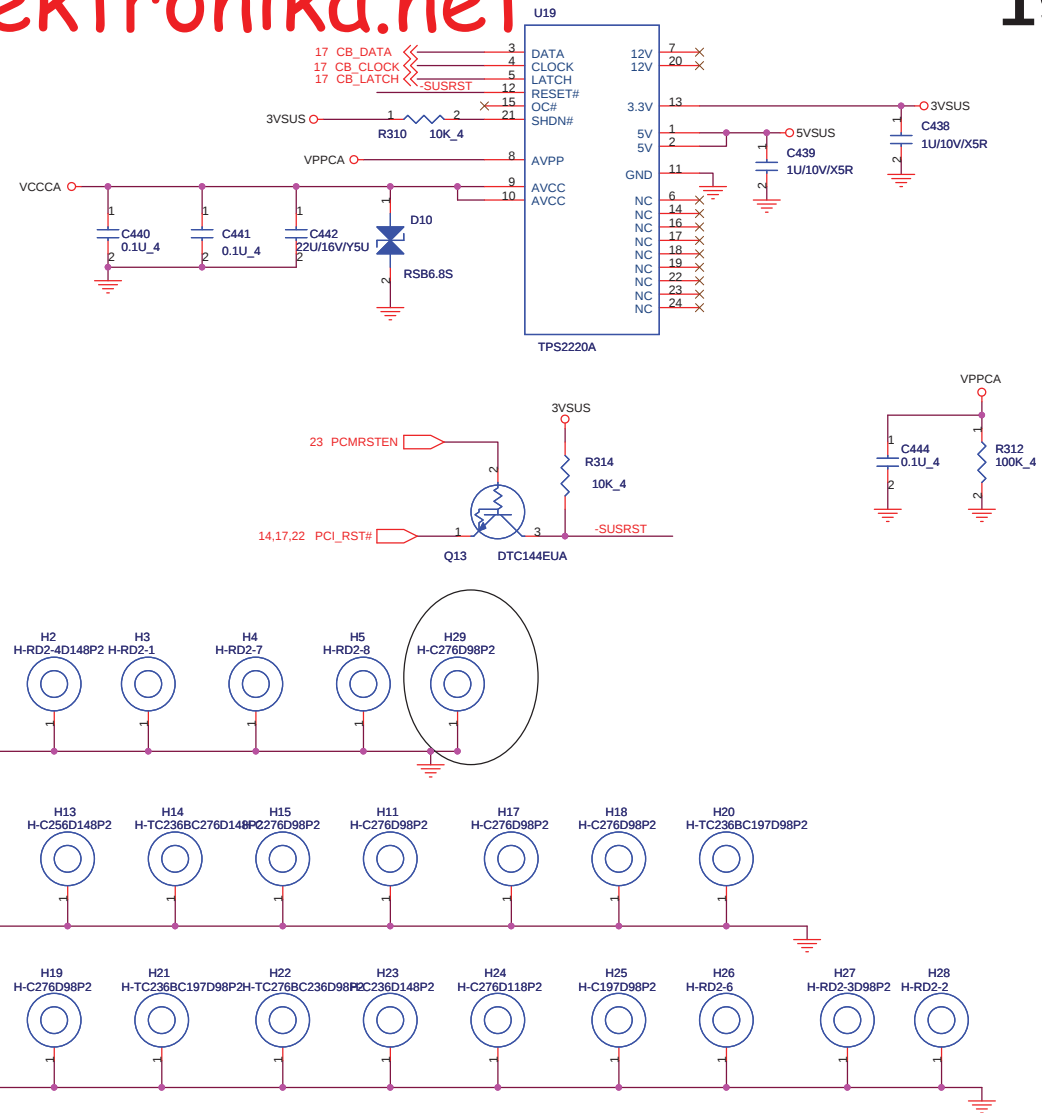
H=2.0mm



18 ACAD[0..31] <<==
18 ACCBE#[0..3] <<==



CardBus inject : 130660-1 , FB0A6063017
CardBus CONN : 130610-1



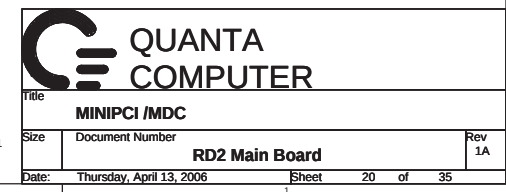
QUANTA COMPUTER

CARDBUS SLOT

RD2 Main Board

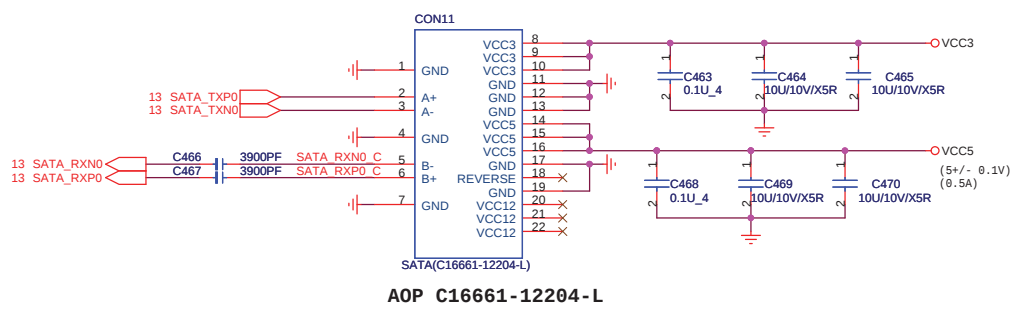
1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.

Date: Tuesday, May 09, 2006 Sheet 19 of 35

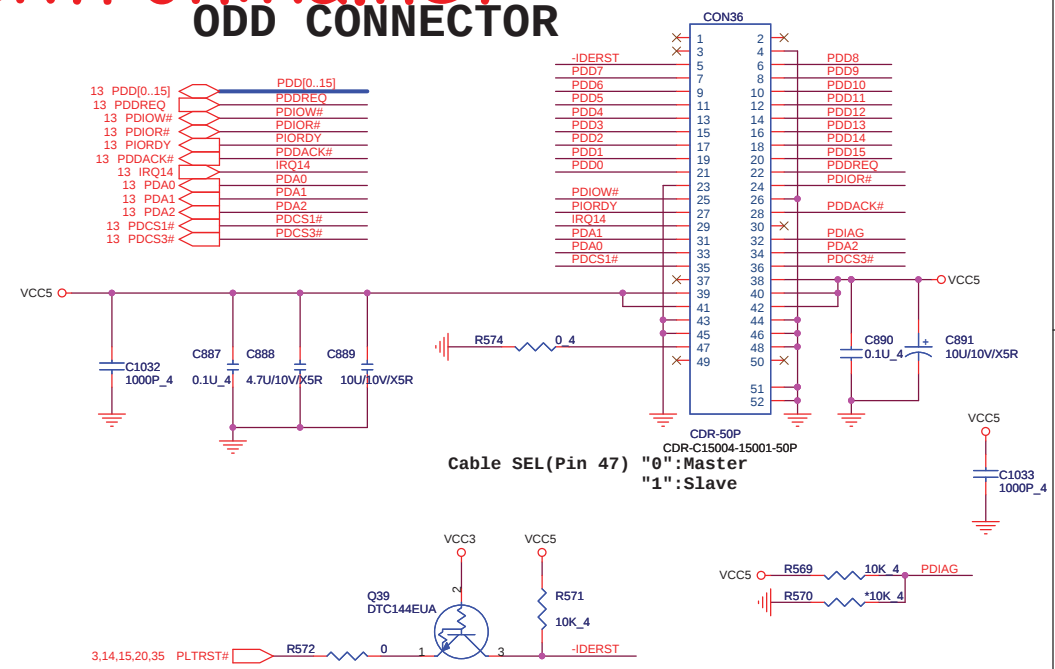


1.This part should not contain any substances which are specified in SS-00259-1
2.Purchase ink, paint, wire rods and molding resins only from the business
partners that Sony approves as Green Partners.

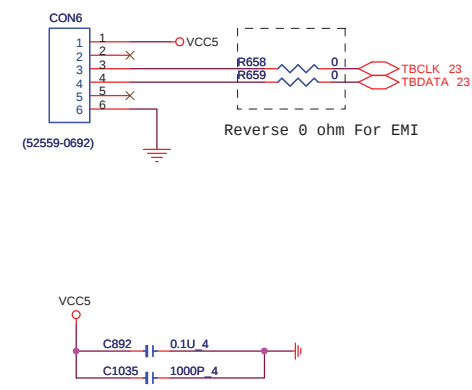
HDD CONNECTOR



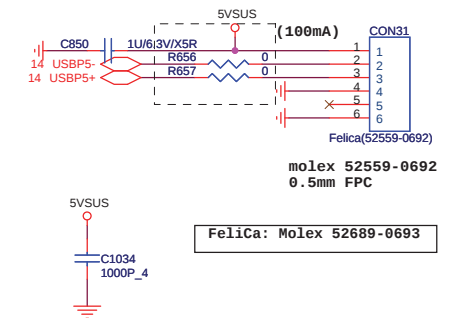
ODD CONNECTOR



TOUCH PAD CONNECTOR



FeliCa



File

Size

Date: Tuesday, May 09, 2006

HDD/ODD/TP/FeliCa

Document Number

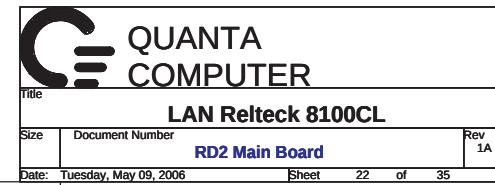
Tuesday, May 09, 2006

RD2 Main Board

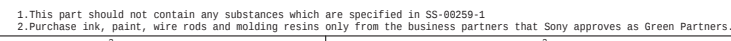
Sheet 21 of 35

Rev 1A

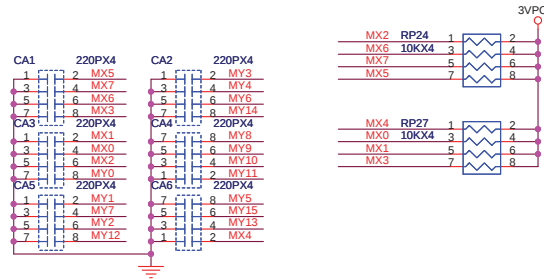
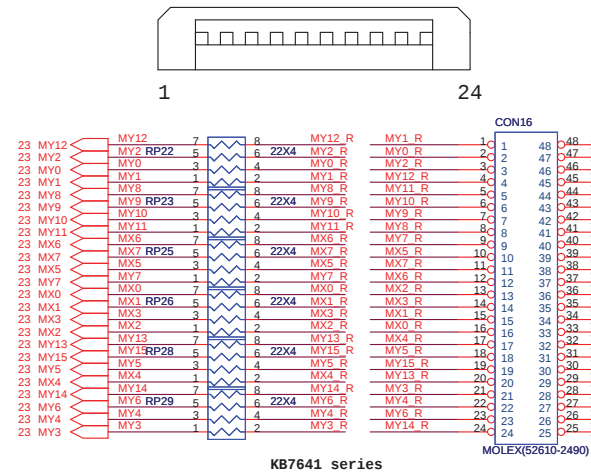
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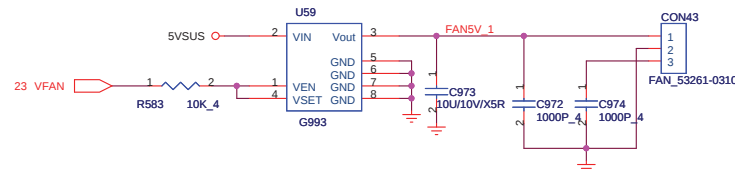
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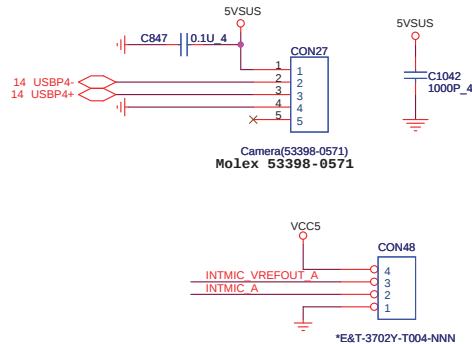
KEYBOARD



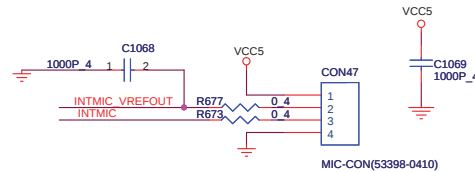
FAN



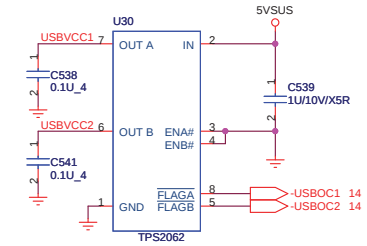
USB Camera



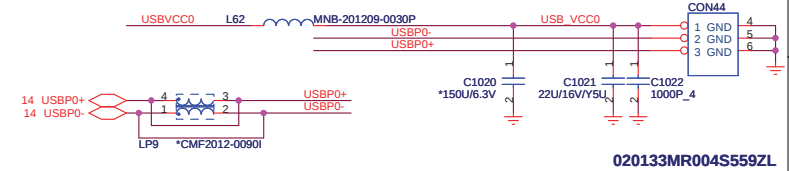
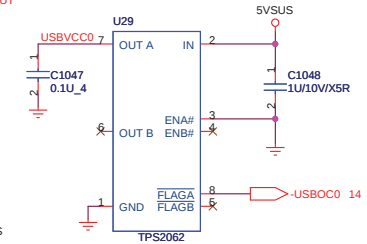
MIC CONN



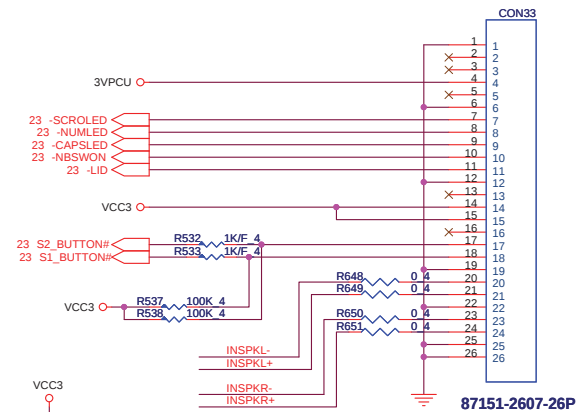
USB POWER SW



USB POWER SW



POWER SWITCH BOARD CONNECTOR

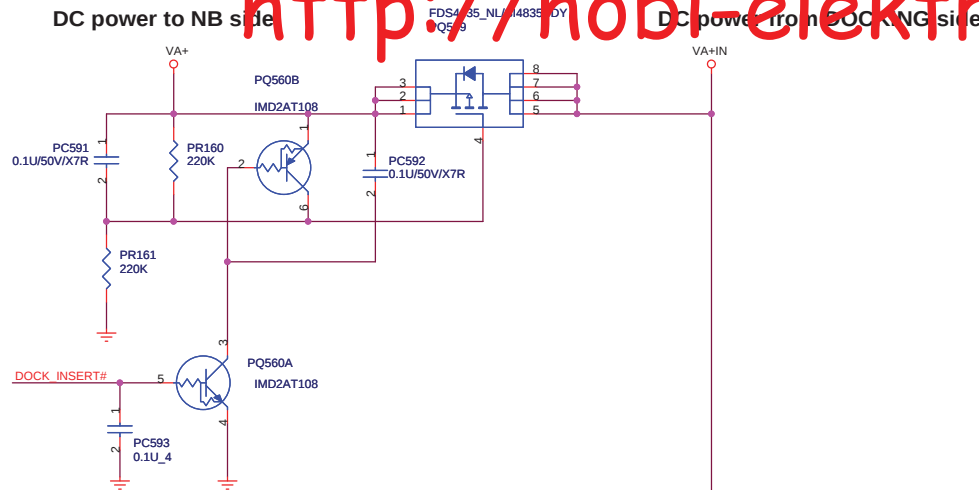


QUANTA COMPUTER

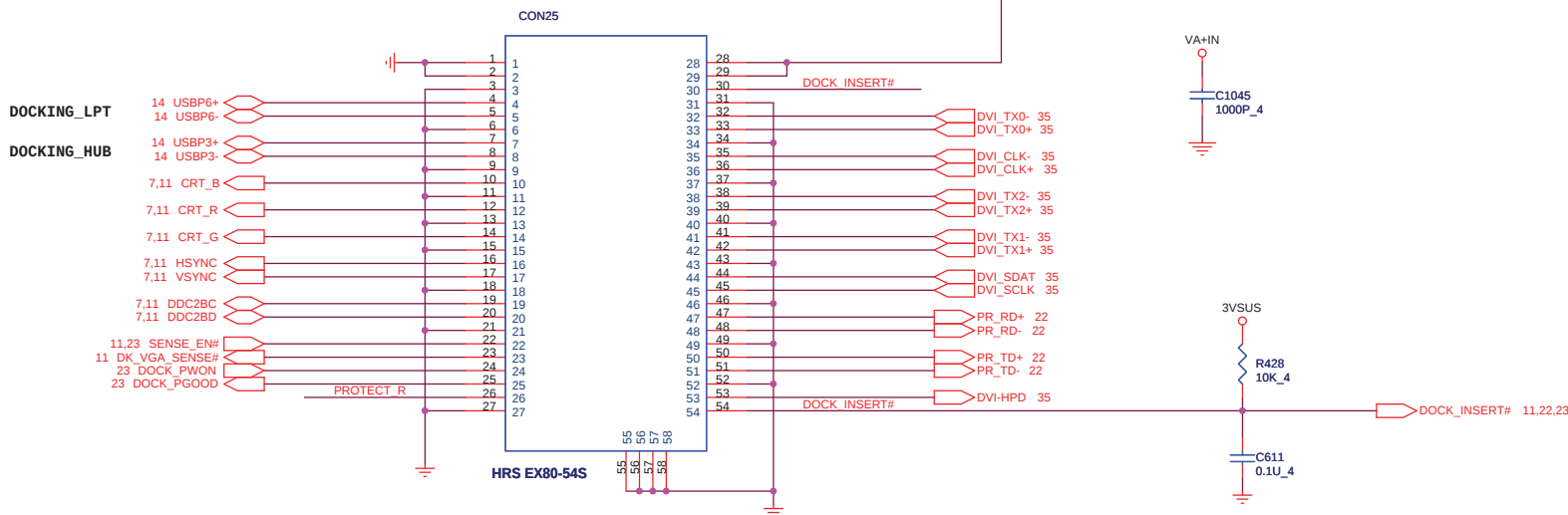
Title: K/B ,USB ,PS

Size: Document Number: RD2 Main Board

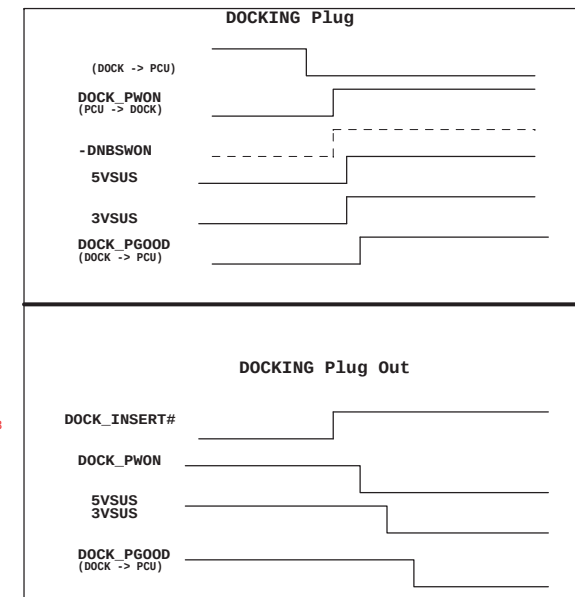
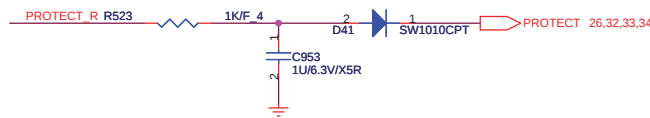
Date: Thursday, May 04, 2006 Sheet: 24 of 35



Port Replicator CONN

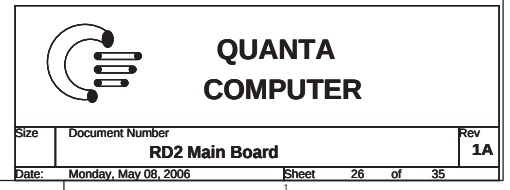


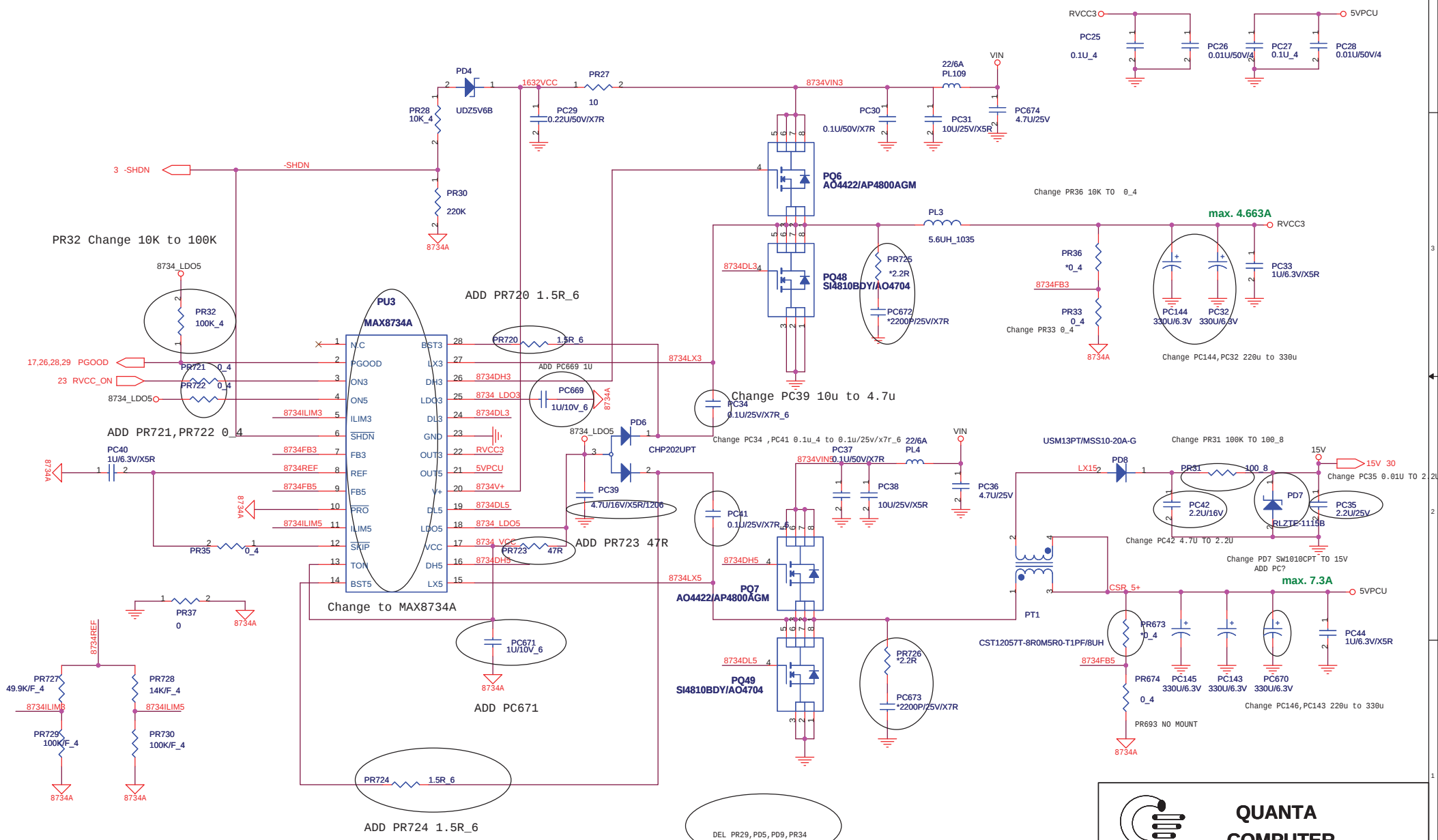
Docking detect Pin : PIN3,PIN30 and PIN 54



Title			Docking
Size	Document Number	RD2 Main Board	
Date:	Monday, April 10, 2006	Sheet	25 of 35

1.This part should not contain any substances which are specified in SS-00259-1
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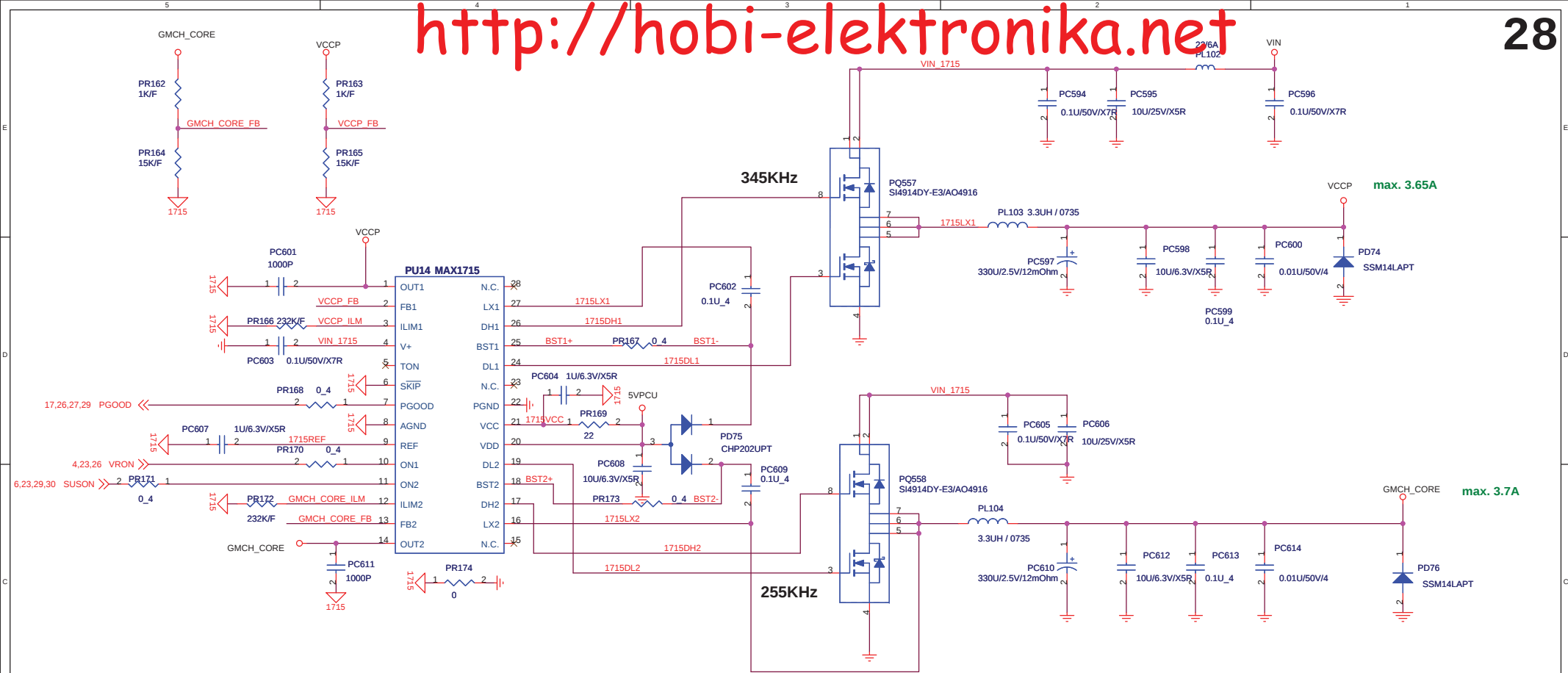


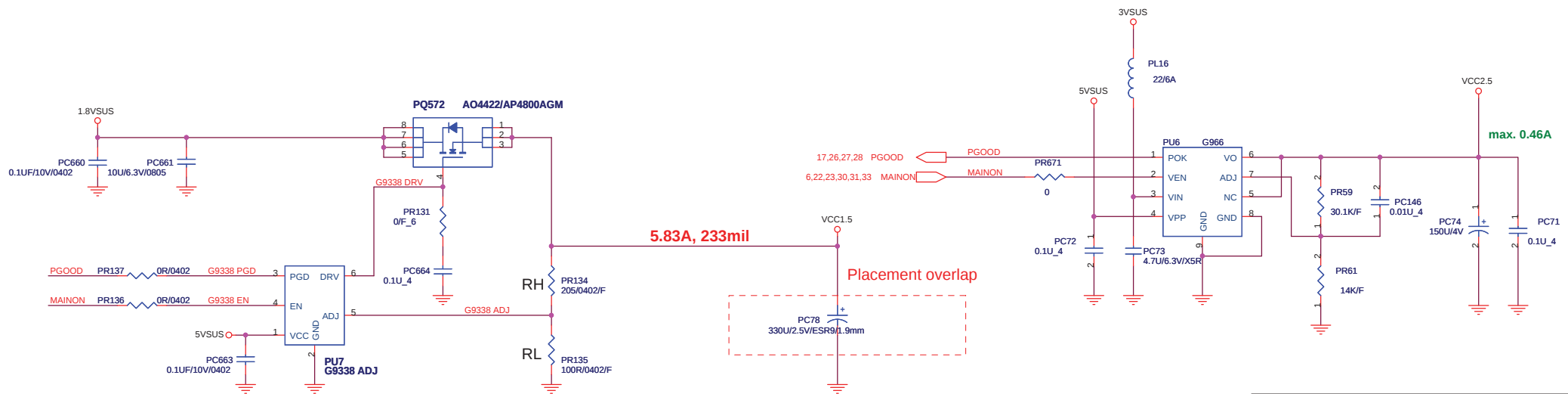
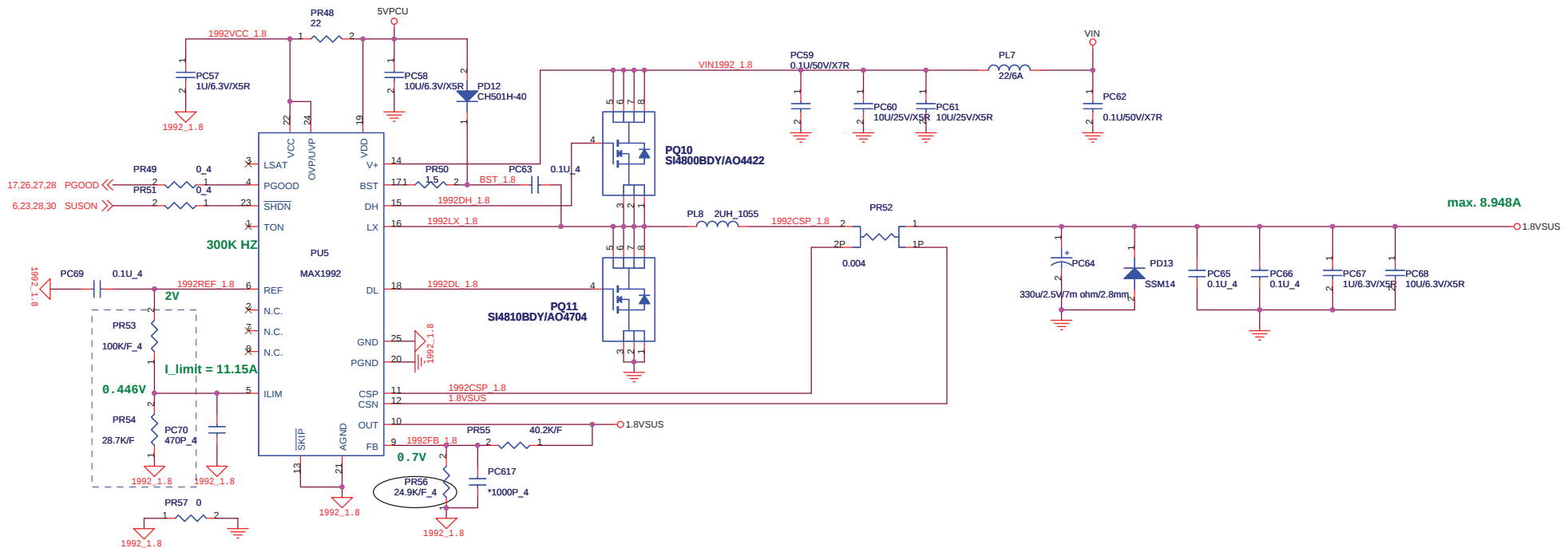


**QUANTA
COMPUTER**

Size	Document Number	Rev
	RD2 Main Board	1A
Date:	Monday, May 08, 2006	Sheet 27 of 35

2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners





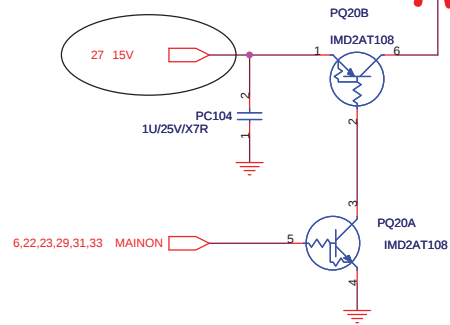
$$VCC1.5 = (1 + R_H / R_L) * 0.5$$



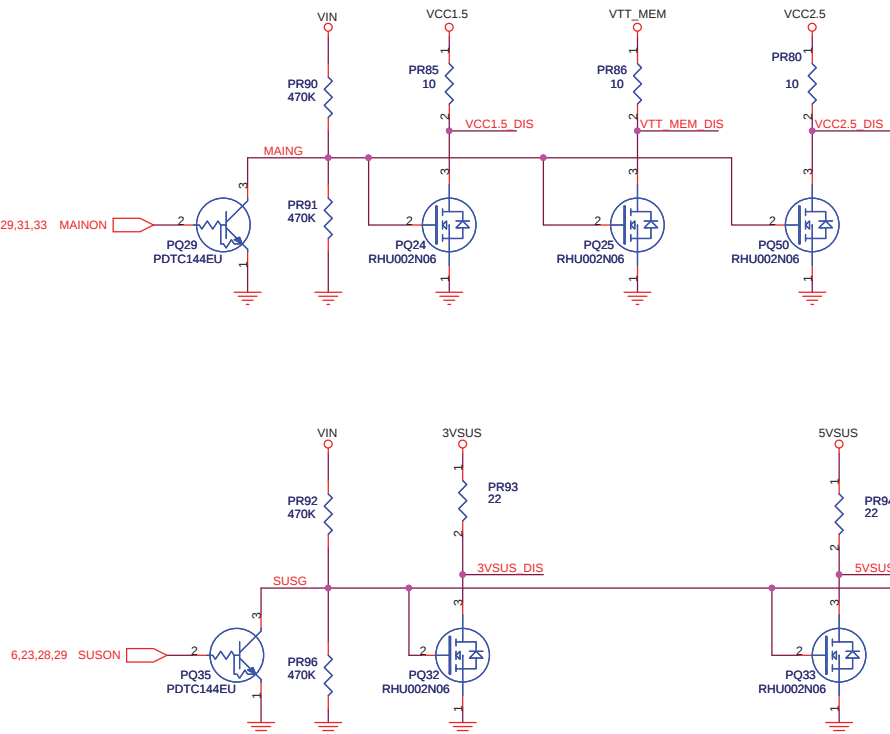
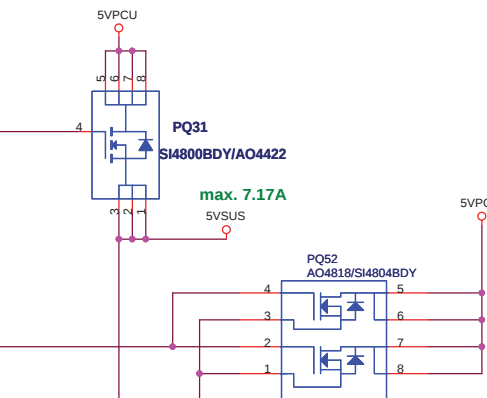
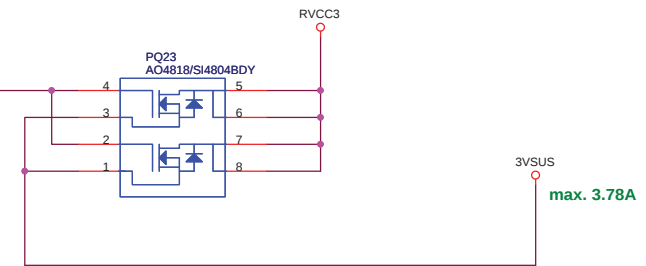
**QUANTA
COMPUTER**

Size	Document Number	Rev
	RD2 Main Board	1A
Date:	Friday, May 05, 2006	Sheet 29 of 35

1. This part should not contain any substances which are specified in SS-00259-1.
2. Purchase ink, paint, wire rods and molding resins only from the business partners that Sony approves as Green Partners.



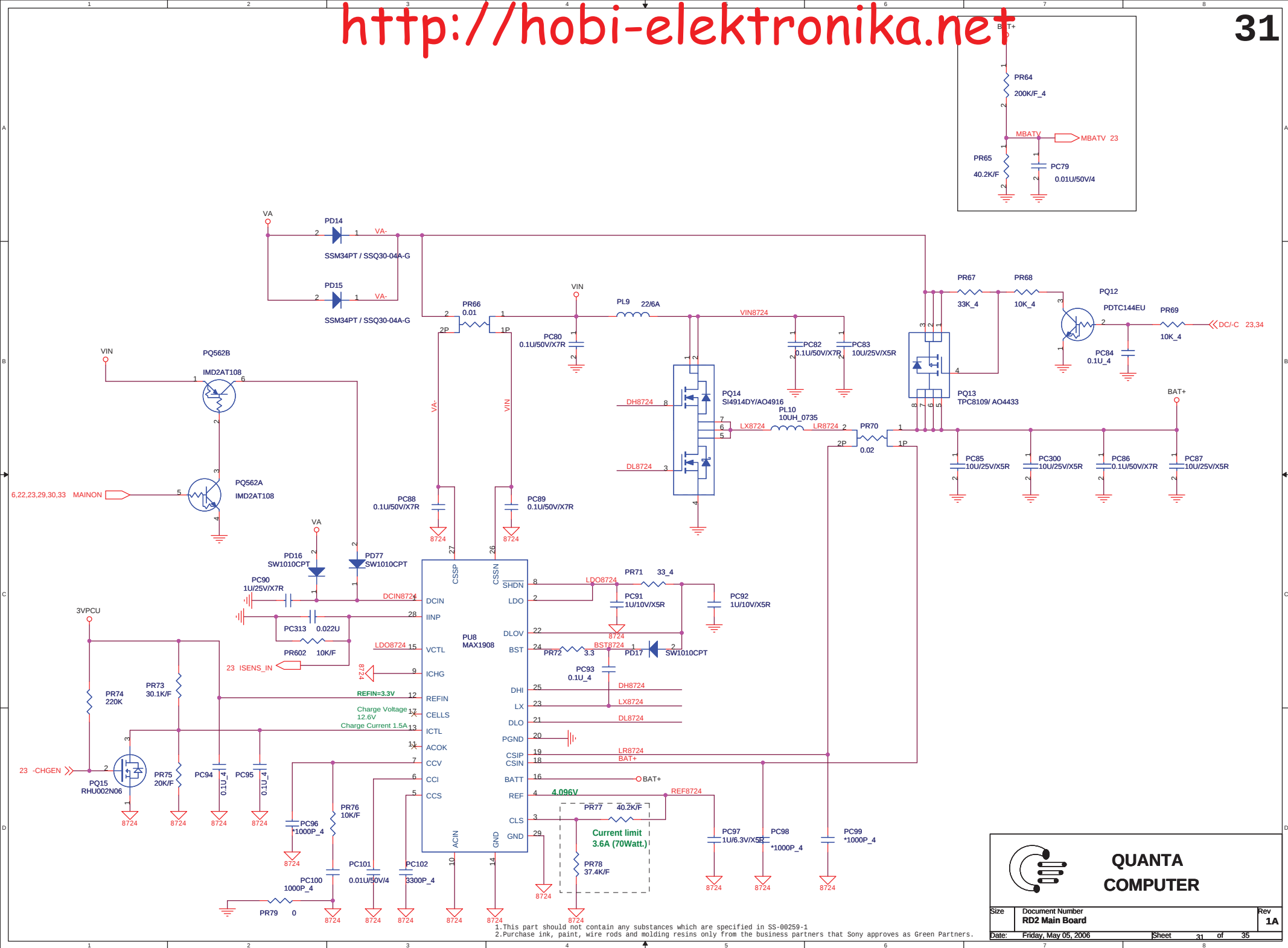
VCC1.8 1.8VSUS
VCC3 3VSUS
VCC5 5VSUS



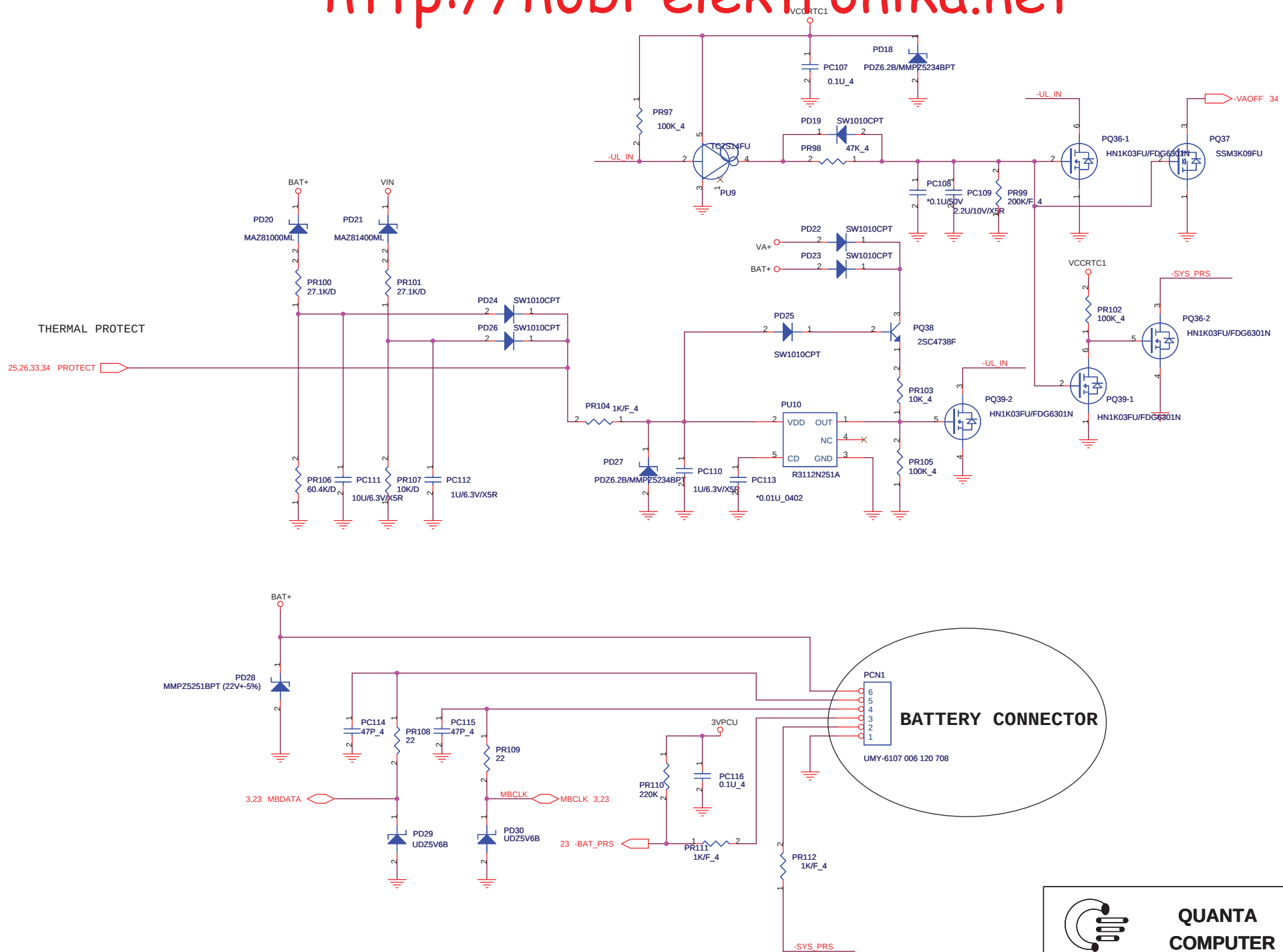
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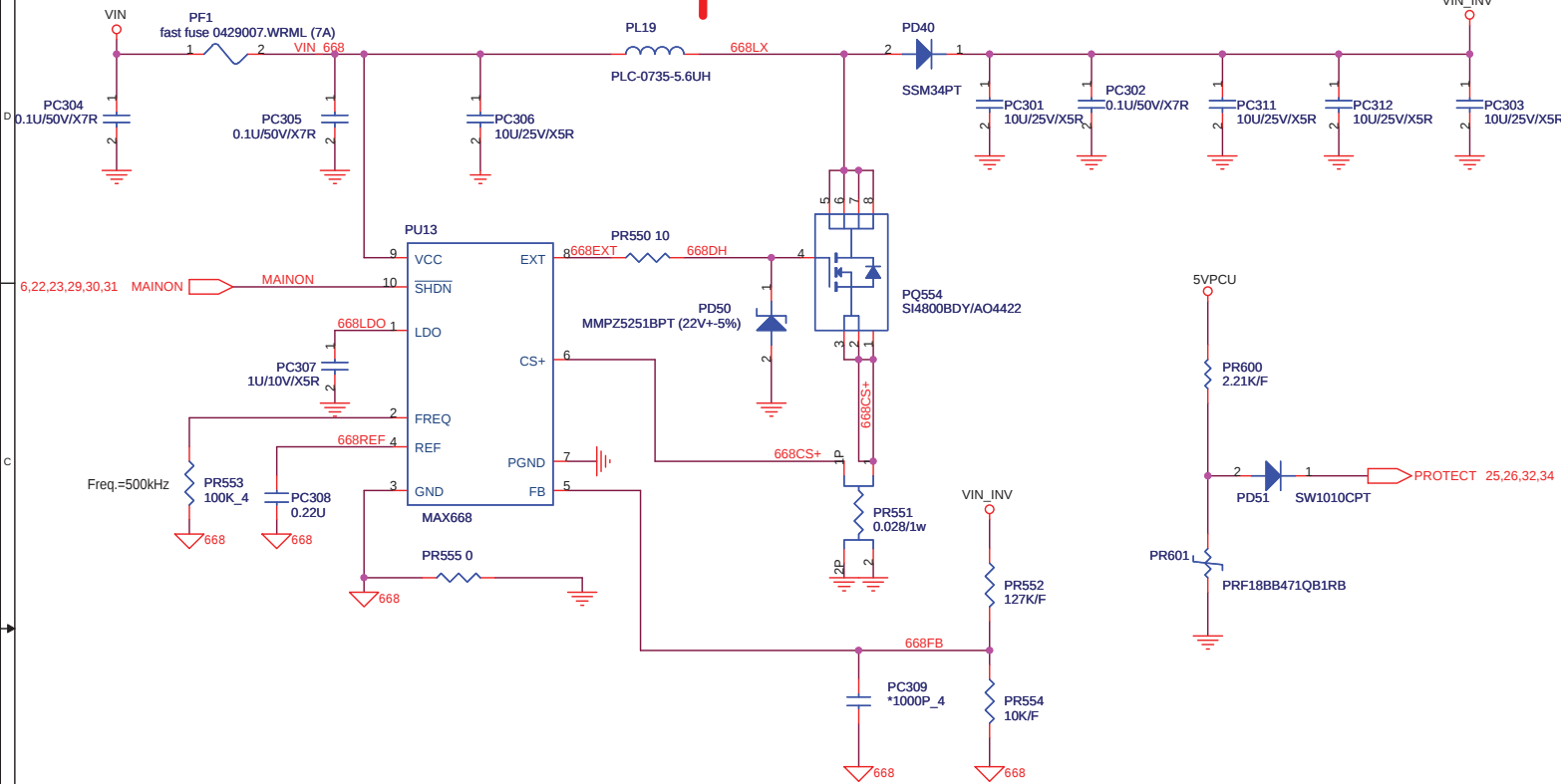
**QUANTA
COMPUTER**

Size	Document Number	Rev
	RD2 Main Board	1A
Date:	Friday, May 05, 2006	Sheet 30 of 35



 QUANTA COMPUTER		Rev
		1A
Size	Document Number	Date
	RD2 Main Board	Friday, May 05, 2006
Sheet		of
31		35





1.This part should not contain any substances which are specified in SS-00259-1
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**QUANTA
COMPUTER**

Size	Document Number RD2 Main Board	Rev 1A
Date:	Friday, May 05, 2006	Sheet 33 of 35

