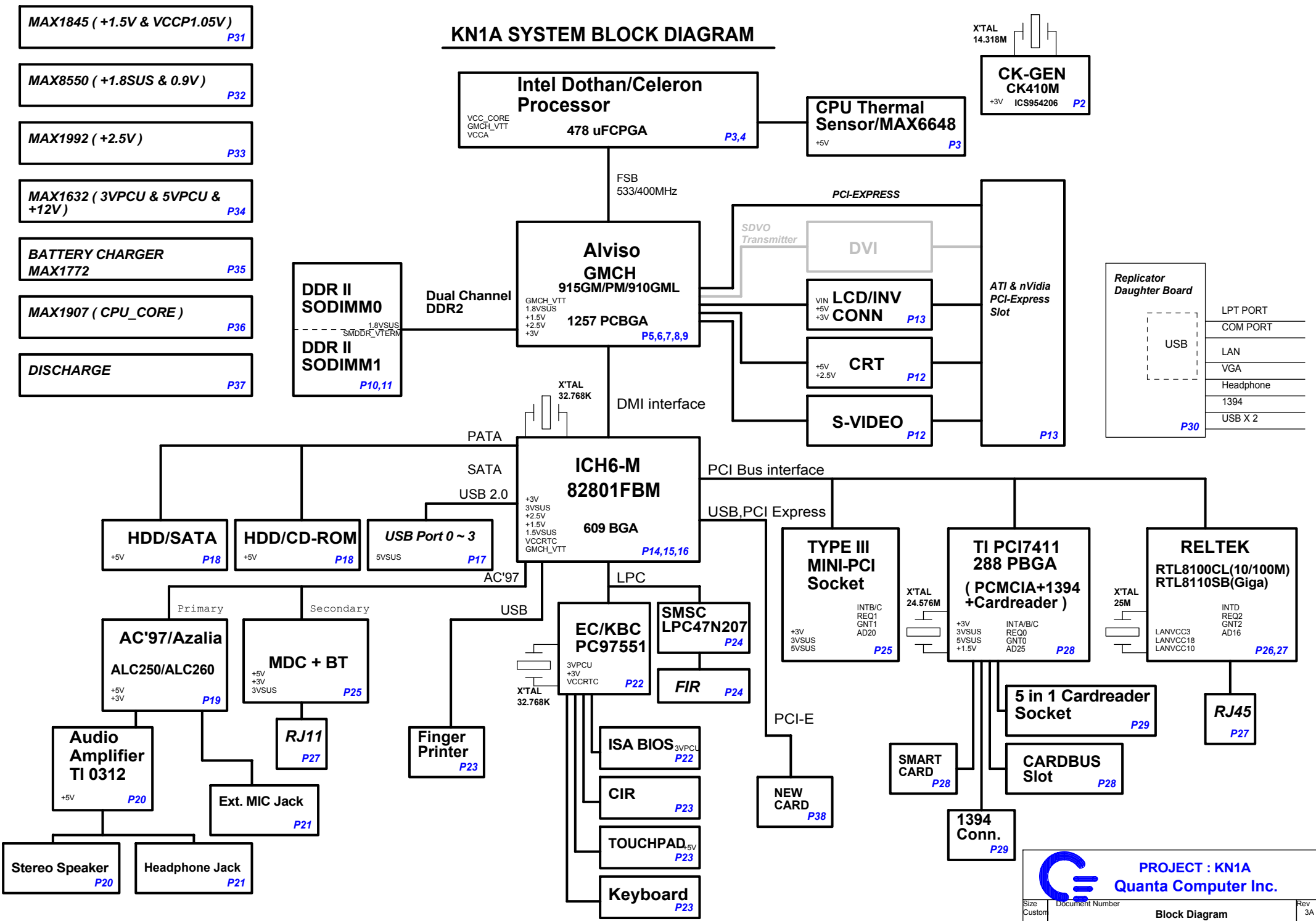
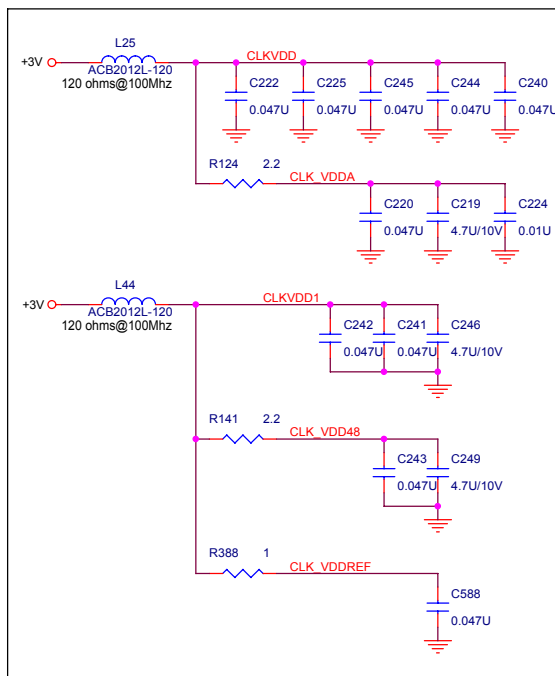


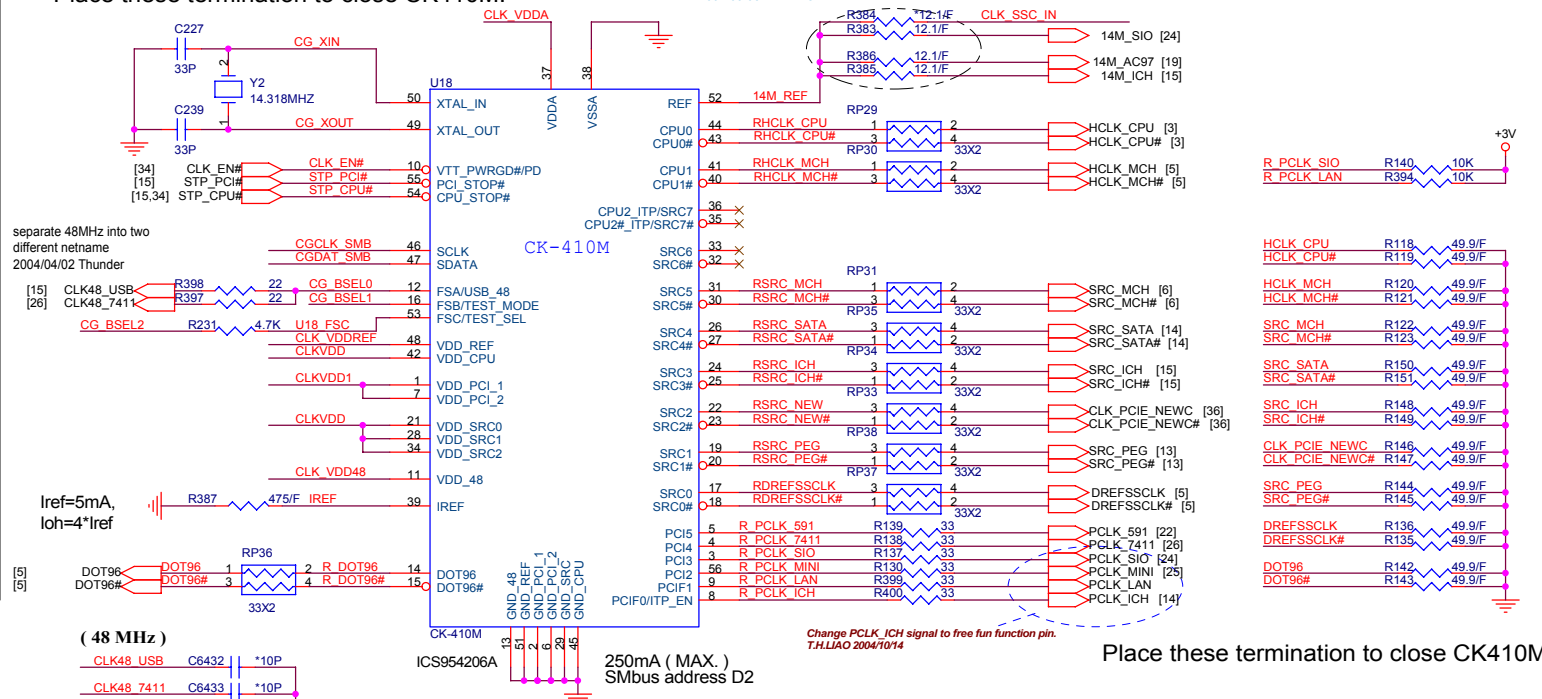
KN1A SYSTEM BLOCK DIAGRAM



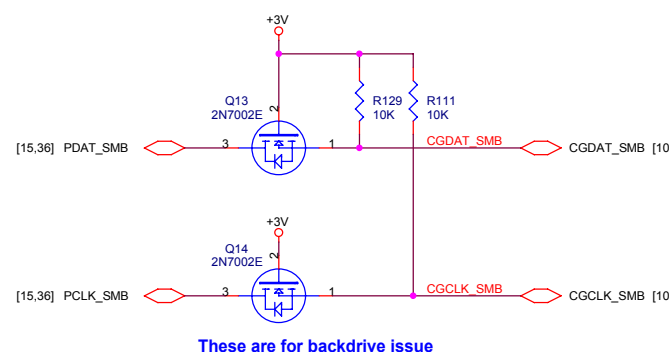
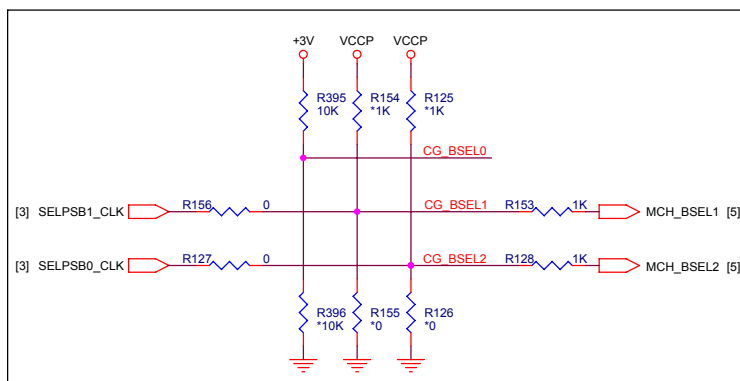
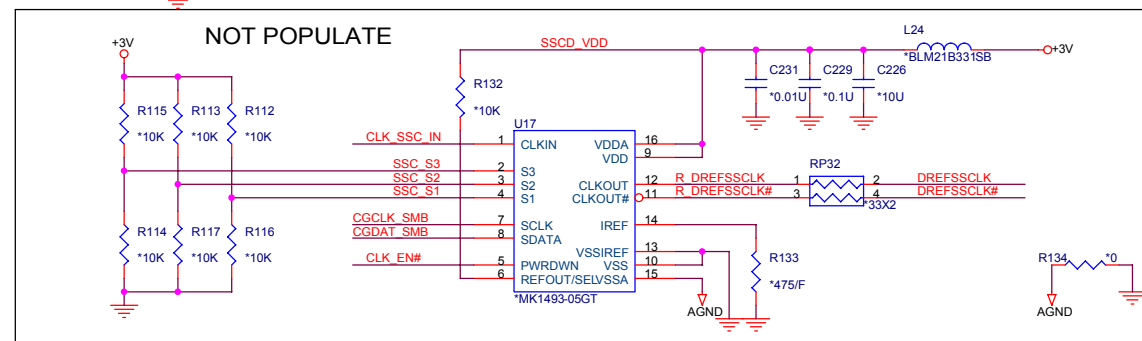


FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RESERVED		

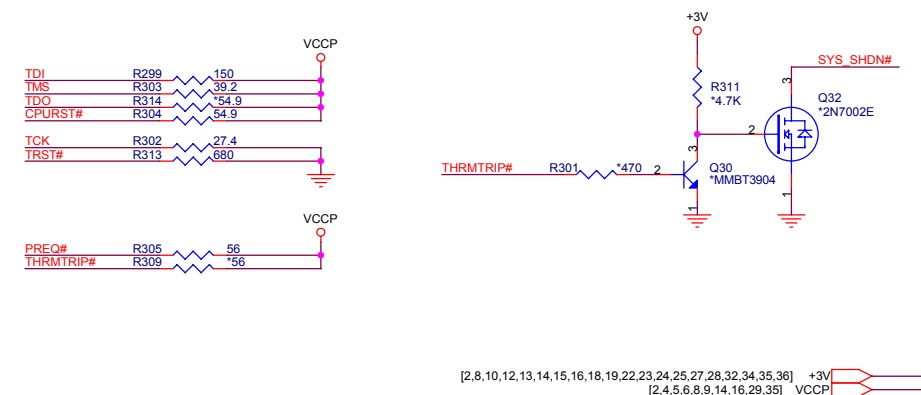
* *Frequency select by CPU auto sense.*

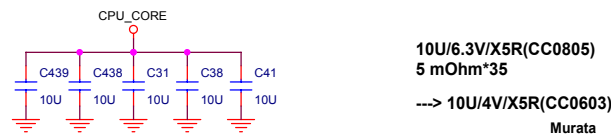
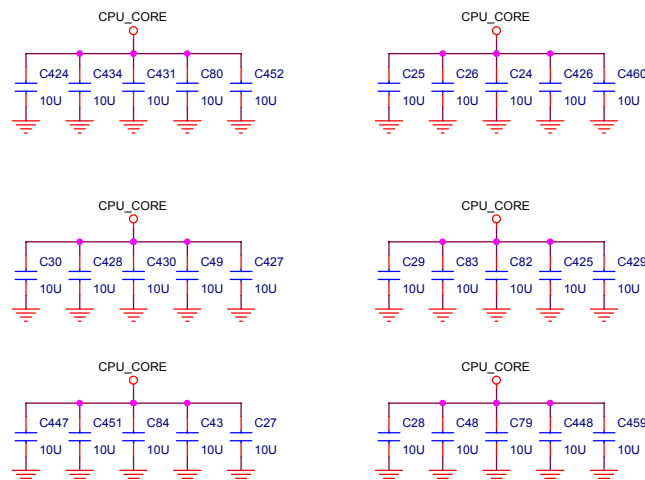
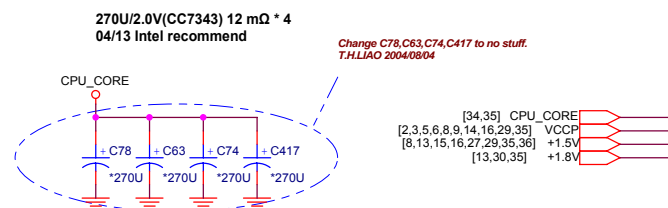
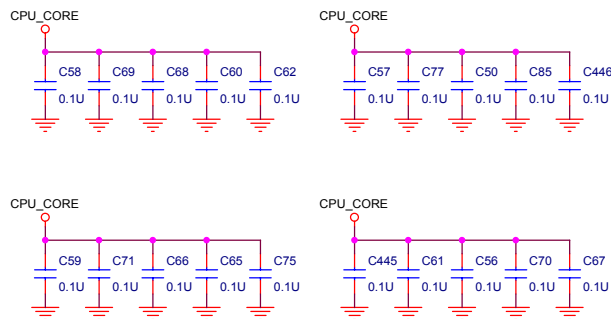


Place these termination to close CK410M.



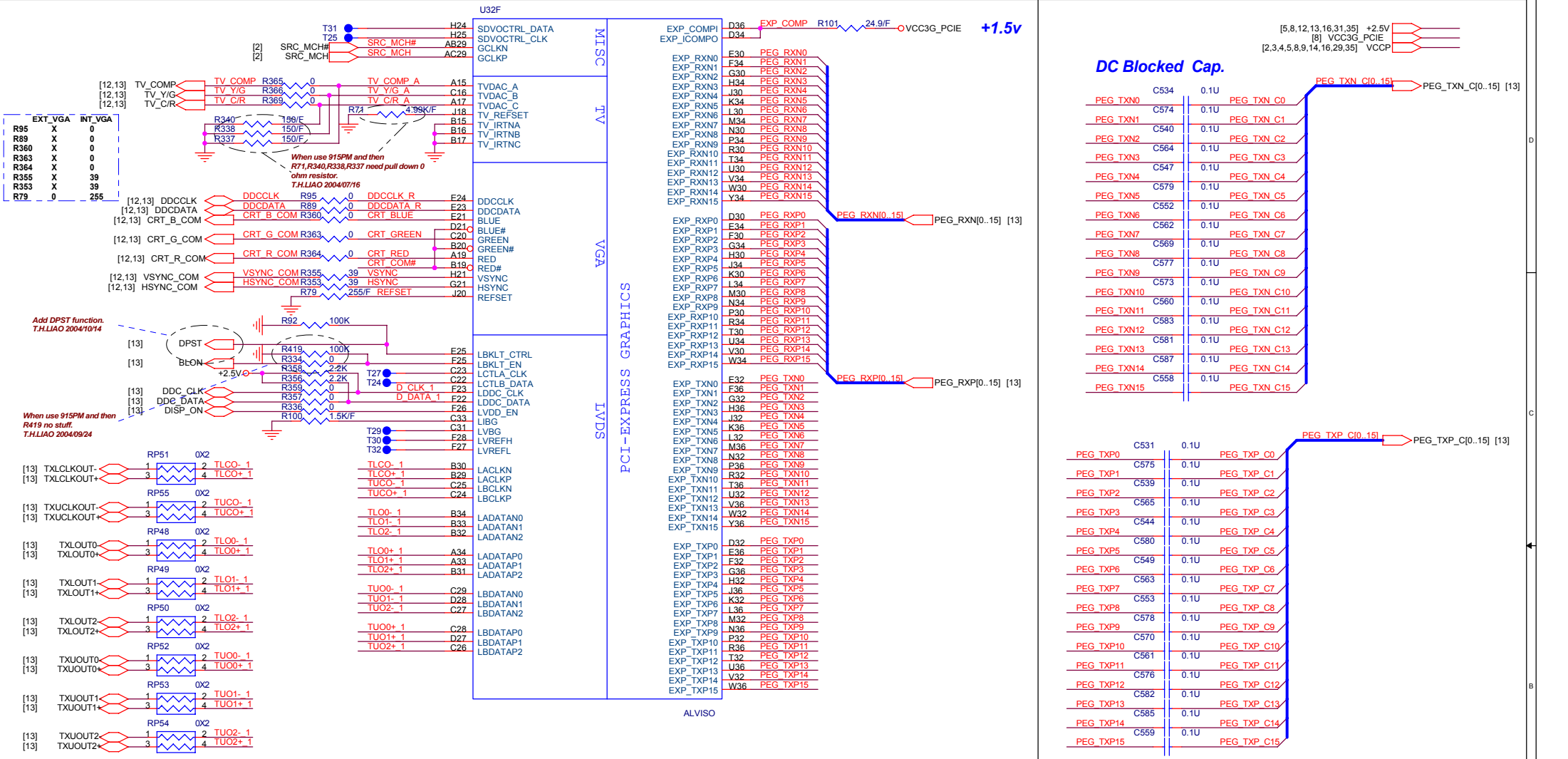
	S3	S2	S1	S0	Spread %	Spread Type
ICS954206A 96Mhz ss Spread Control	0	0	0	0	0.8	Down
	0	0	1	0	1.25	Down
	0	1	0	0	1.75	Down
	0	1	1	0	2.5	Down
	1	0	0	0	+/-0.3	Center
	1	0	1	0	+/-0.5	Center
	1	1	0	0	+/-0.8	Center
	1	1	1	0	+/-1.25	Center



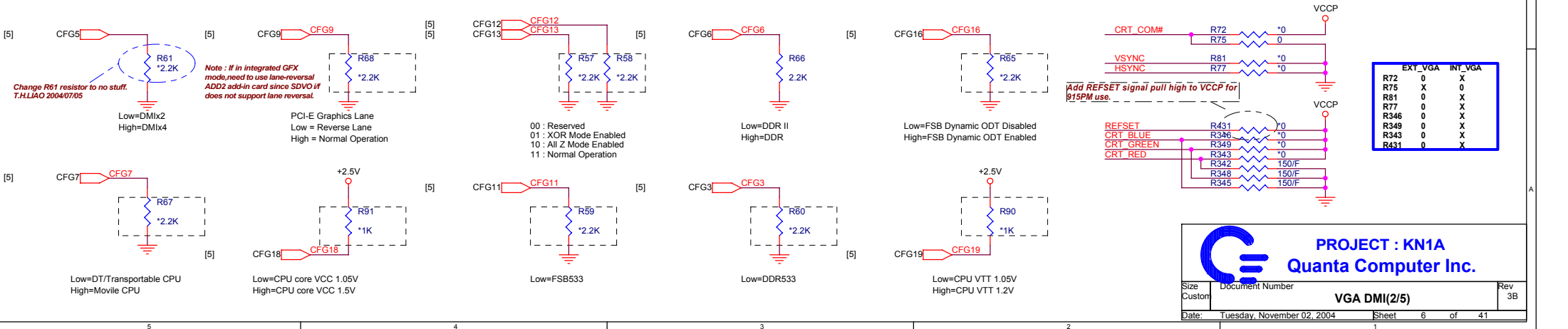
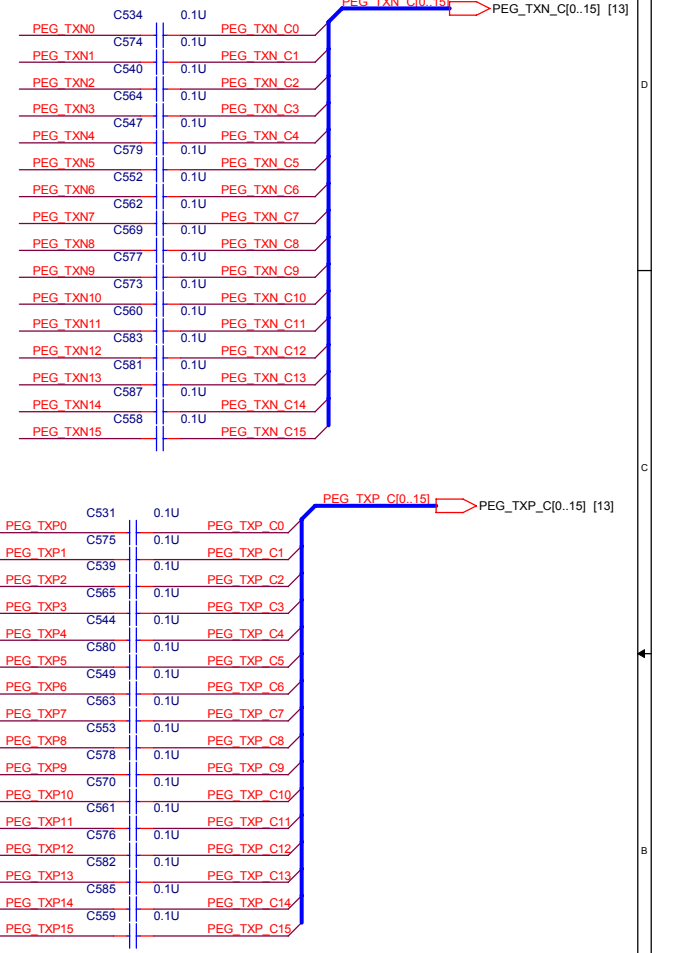


U29D		
A2	VSS0	VSS997
A5	VSS1	VSS998
A8	VSS2	VSS999
A11	VSS3	VSS100
A14	VSS4	VSS101
A17	VSS5	VSS102
A20	VSS6	VSS103
A23	VSS7	VSS104
A26	VSS8	VSS105
AA1	VSS9	VSS106
AA4	VSS10	VSS107
AA6	VSS11	VSS108
AA8	VSS12	VSS109
AA10	VSS13	VSS110
AA12	VSS14	VSS111
AA14	VSS15	VSS112
AA16	VSS16	VSS113
AA17	VSS17	VSS114
AA20	VSS18	VSS115
AA22	VSS19	VSS116
AA25	VSS20	VSS117
AB3	VSS21	VSS118
AB5	VSS22	VSS119
AB7	VSS23	VSS120
AB9	VSS24	VSS121
AB11	VSS25	VSS122
AB13	VSS26	VSS123
AB15	VSS27	VSS124
AB17	VSS28	VSS125
AB19	VSS29	VSS126
AB21	VSS30	VSS127
AB23	VSS31	VSS128
AB26	VSS32	VSS129
AC2	VSS33	VSS130
AC5	VSS34	VSS131
AC8	VSS35	VSS132
AC10	VSS36	VSS133
AC12	VSS37	VSS134
AC14	VSS38	VSS135
AC16	VSS39	VSS136
AC18	VSS40	VSS137
AC21	VSS41	VSS138
AC24	VSS42	VSS139
AD1	VSS43	VSS140
AD4	VSS44	VSS141
AD7	VSS45	VSS142
AD9	VSS46	VSS143
AD11	VSS47	VSS144
AD13	VSS48	VSS145
AD15	VSS49	VSS146
AD17	VSS50	VSS147
AD19	VSS51	VSS148
AD22	VSS52	VSS149
AD26	VSS53	VSS150
AE3	VSS54	VSS151
AE6	VSS55	VSS152
AE8	VSS56	VSS153
AE10	VSS57	VSS154
AE12	VSS58	VSS155
AE14	VSS59	VSS156
AE16	VSS60	VSS157
AE18	VSS61	VSS158
AE20	VSS62	VSS159
AE23	VSS63	VSS160
AE26	VSS64	VSS161
AF2	VSS65	VSS162
AF5	VSS66	VSS163
AF9	VSS67	VSS164
AF11	VSS68	VSS165
AF13	VSS69	VSS166
AF15	VSS70	VSS167
AF17	VSS71	VSS168
AF19	VSS72	VSS169
AF21	VSS73	VSS170
AF24	VSS74	VSS171
B3	VSS75	VSS172
B6	VSS76	VSS173
B9	VSS77	VSS174
B12	VSS78	VSS175
B16	VSS79	VSS176
B19	VSS80	VSS177
B22	VSS81	VSS178
B25	VSS82	VSS179
C1	VSS83	VSS180
C4	VSS84	VSS181
C7	VSS85	VSS182
C10	VSS86	VSS183
C13	VSS87	VSS184
C15	VSS88	VSS185
C18	VSS89	VSS186
C21	VSS90	VSS187
C24	VSS91	VSS188
D2	VSS92	VSS189
D5	VSS93	VSS190
D7	VSS94	VSS191
D9	VSS95	
D11	VSS96	

Dothan_478P



DC Blocked Cap.

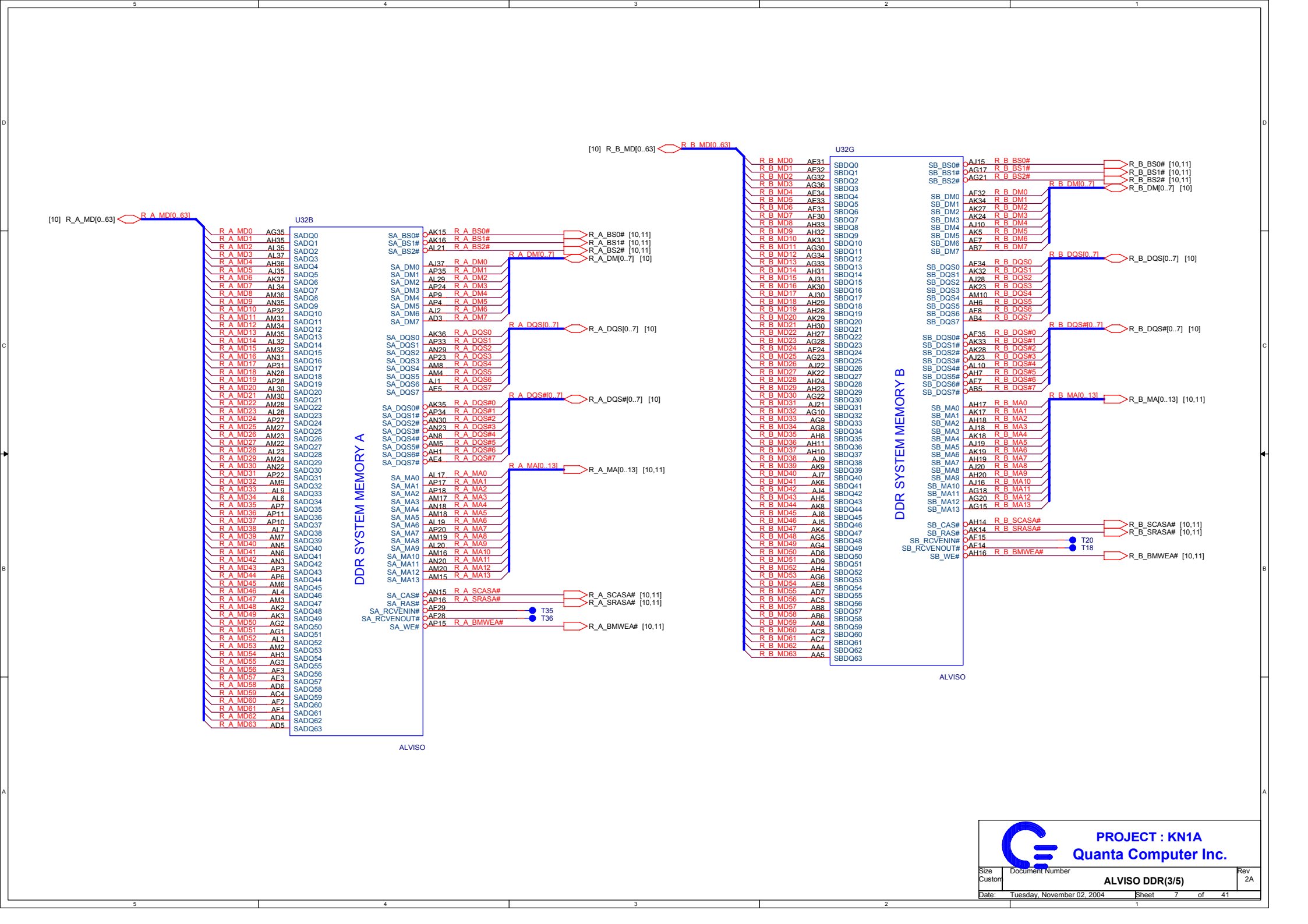


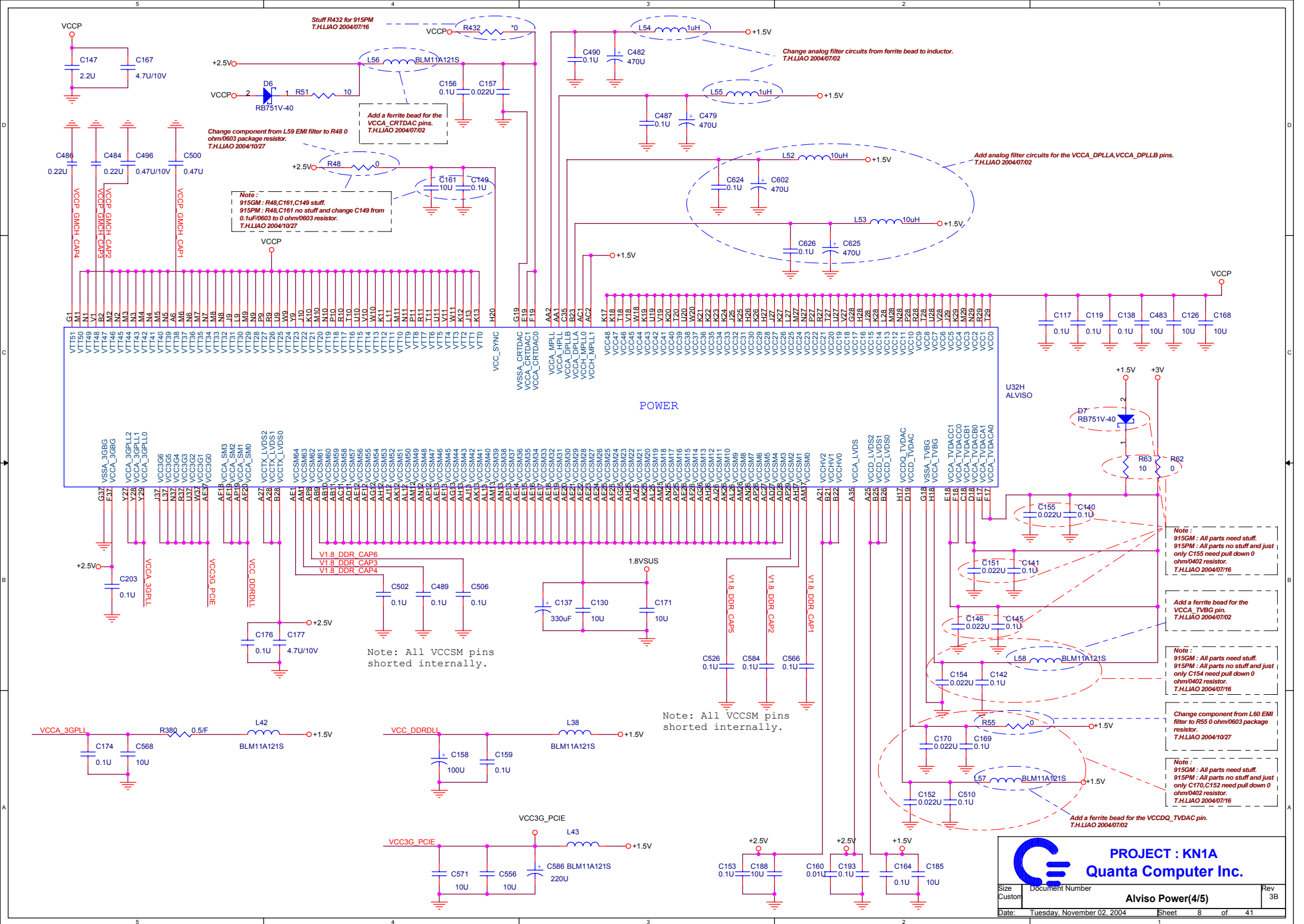


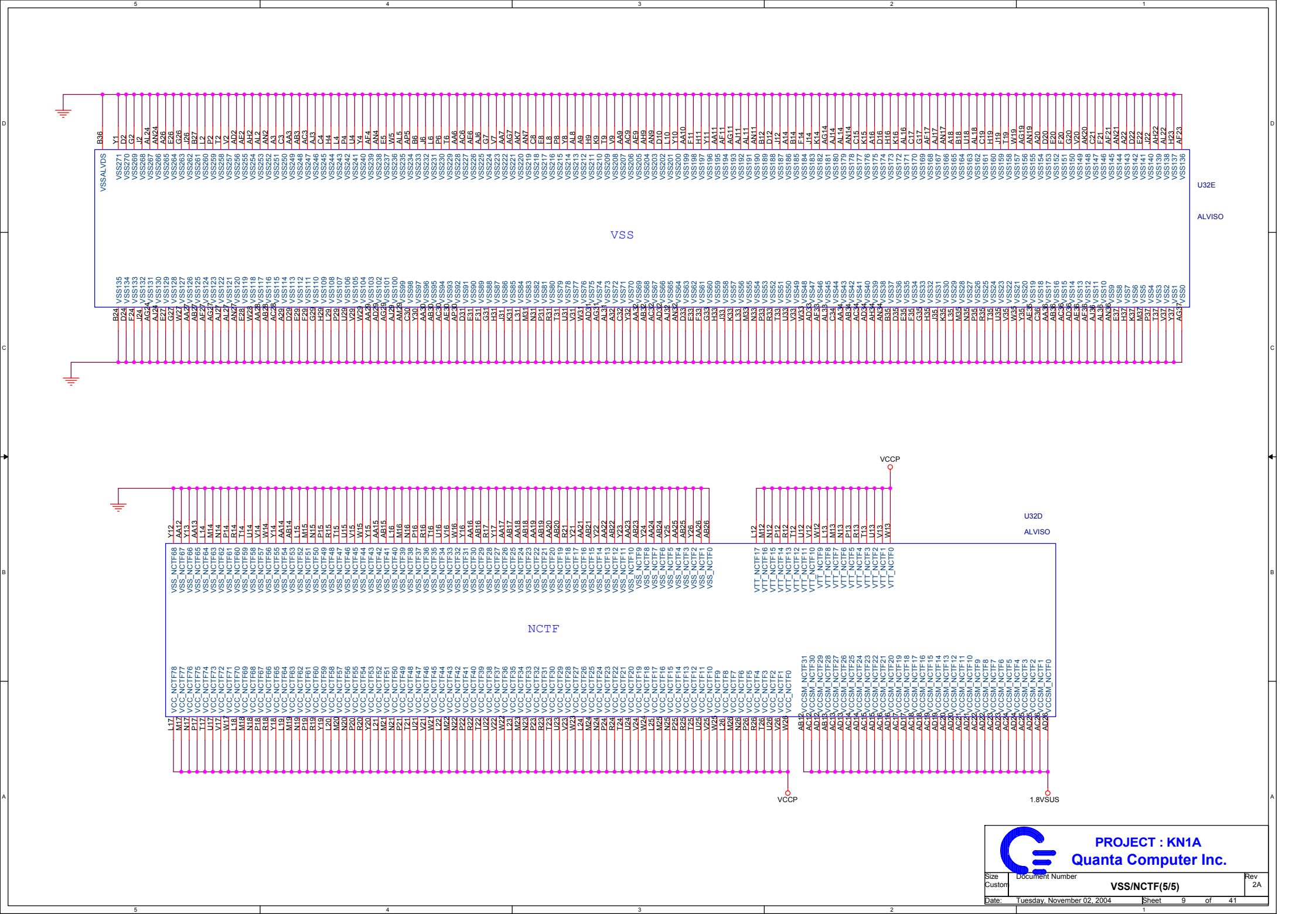
PROJECT : KN1A

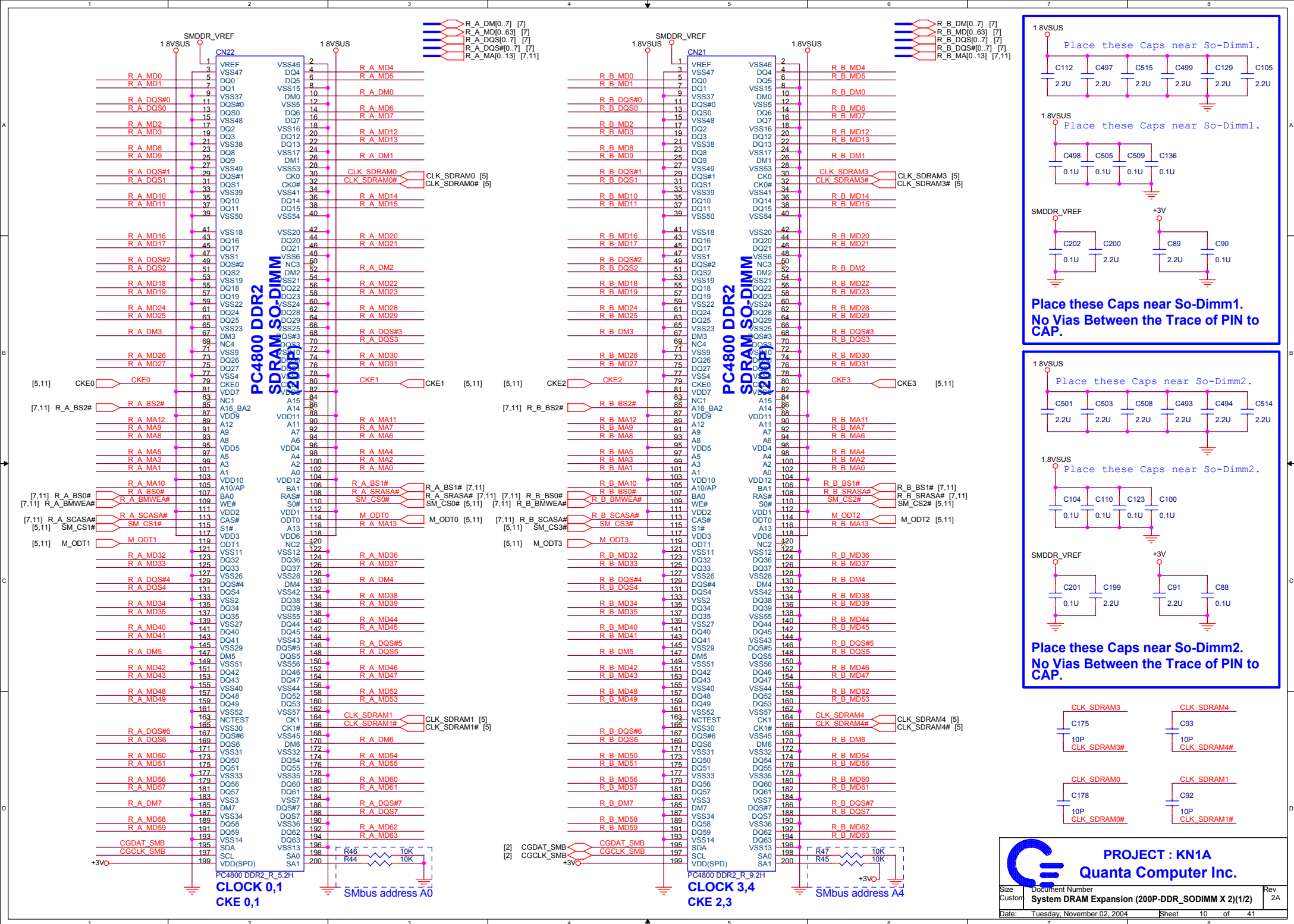
Quanta Computer Inc.

Size	Document Number	VGA DMI(2/5)	Rev
Custom			3B
Date:	Tuesday, November 02, 2004	Sheet 6 of 41	



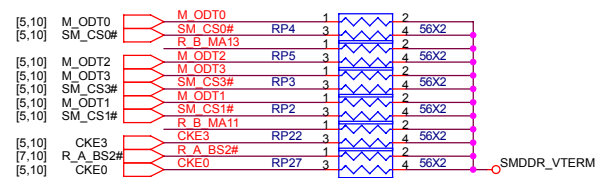
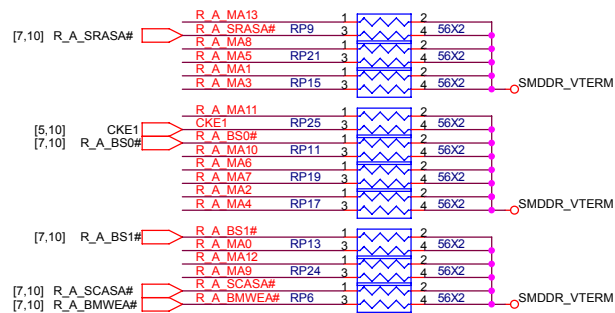
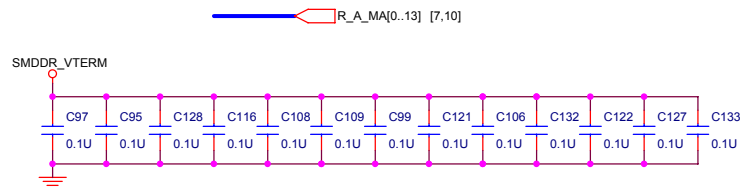




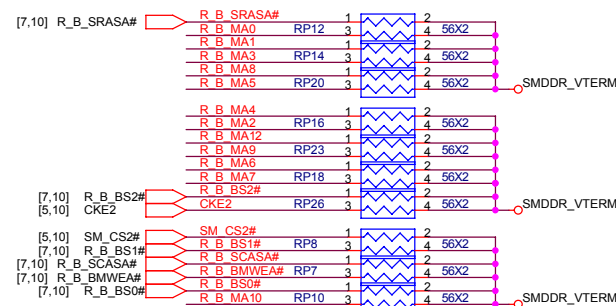
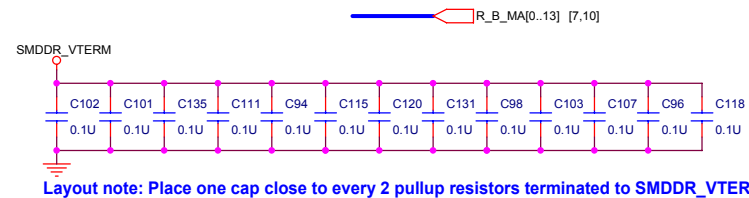


DDRII DUAL CHANNEL A,B.

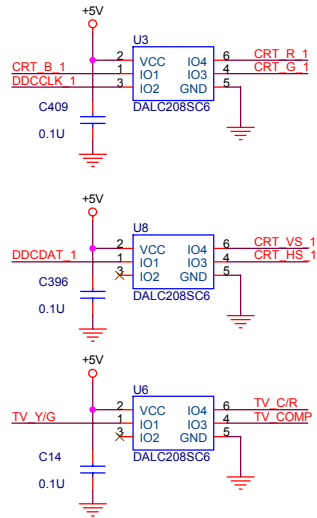
DDRII A CHANNEL



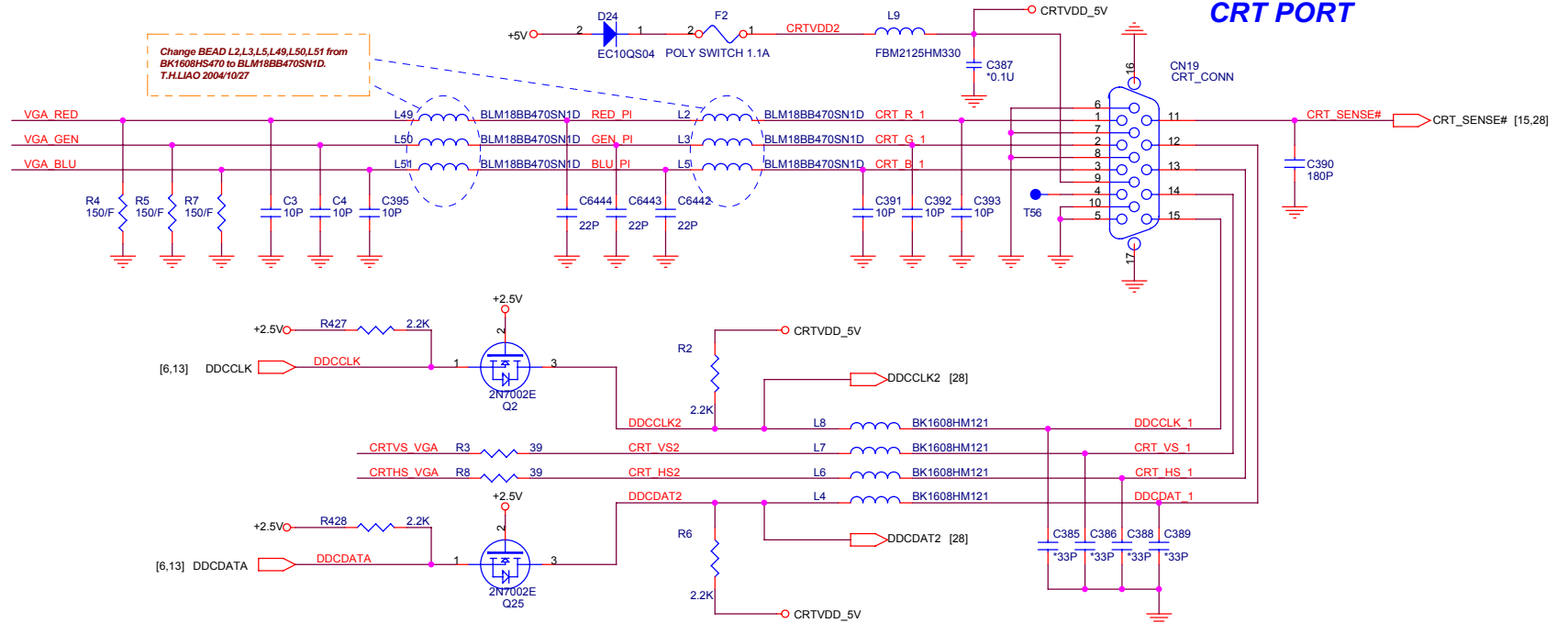
DDRII B CHANNEL



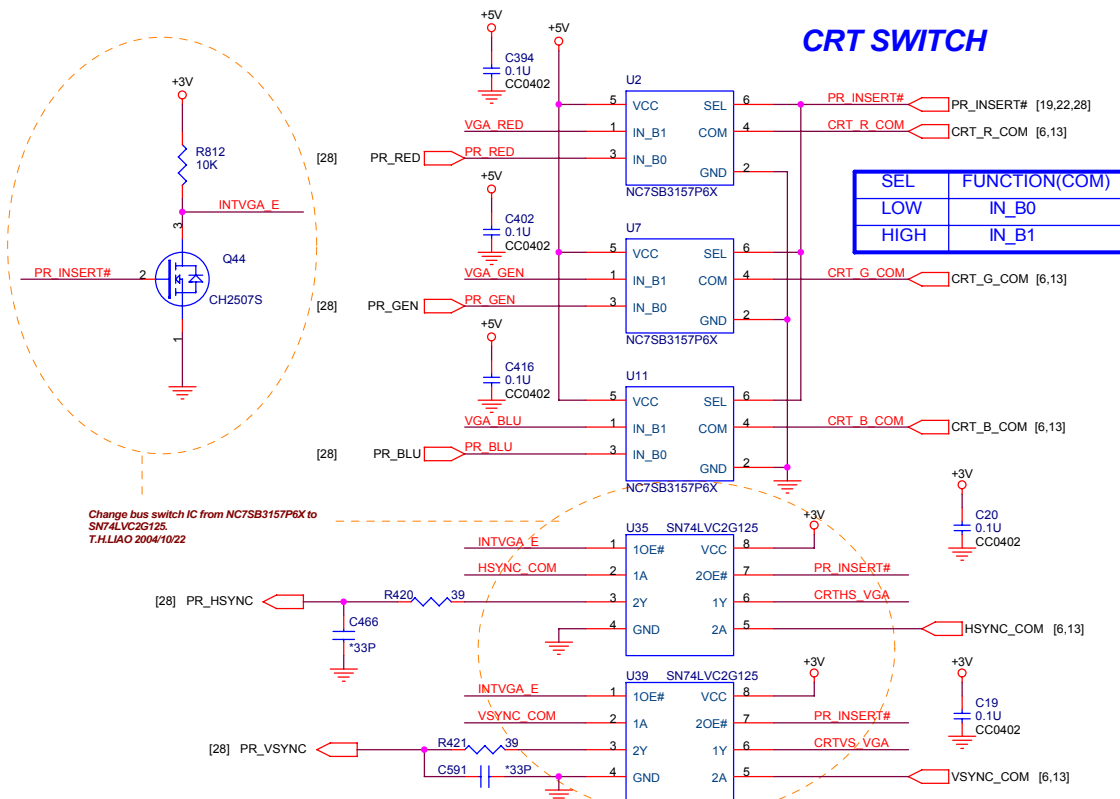
ESD PORTECTION



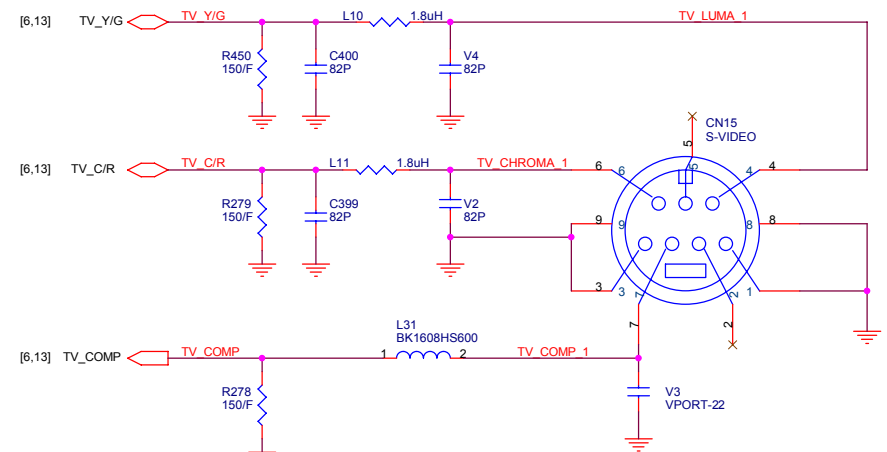
CRT PORT



CRT SWITCH



TV-OUT



PROJECT : KN1A
Quanta Computer Inc.

[illegible][illegible]

The diagram illustrates the connection of an LCD module to a microcontroller. The microcontroller's pins are connected to the LCD module's pins as follows:

- VIN_BLIGHT** (Microcontroller) to **VIN** (LCD module)
- LCDVCC** (Microcontroller) to **LCDVCC** (LCD module)
- VADJ_1** (Microcontroller) to **VADJ** (LCD module)
- EDID_CLK** (Microcontroller) to **EDID_CLK** (LCD module)
- TXLOUT0+** (Microcontroller) to **TXLOUT0+** (LCD module)
- TXLOUT1-** (Microcontroller) to **TXLOUT1-** (LCD module)
- TXLOUT2+** (Microcontroller) to **TXLOUT2+** (LCD module)
- TXLCLKOUT+** (Microcontroller) to **TXLCLKOUT+** (LCD module)
- TXUOUT0+** (Microcontroller) to **TXUOUT0+** (LCD module)
- TXUOUT1+** (Microcontroller) to **TXUOUT1+** (LCD module)
- TXUOUT2+** (Microcontroller) to **TXUOUT2+** (LCD module)
- LIGHT_SENSE** (Microcontroller) to **LIGHT_SENSE** (LCD module)
- TXUCLKOUT+** (Microcontroller) to **TXUCLKOUT+** (LCD module)

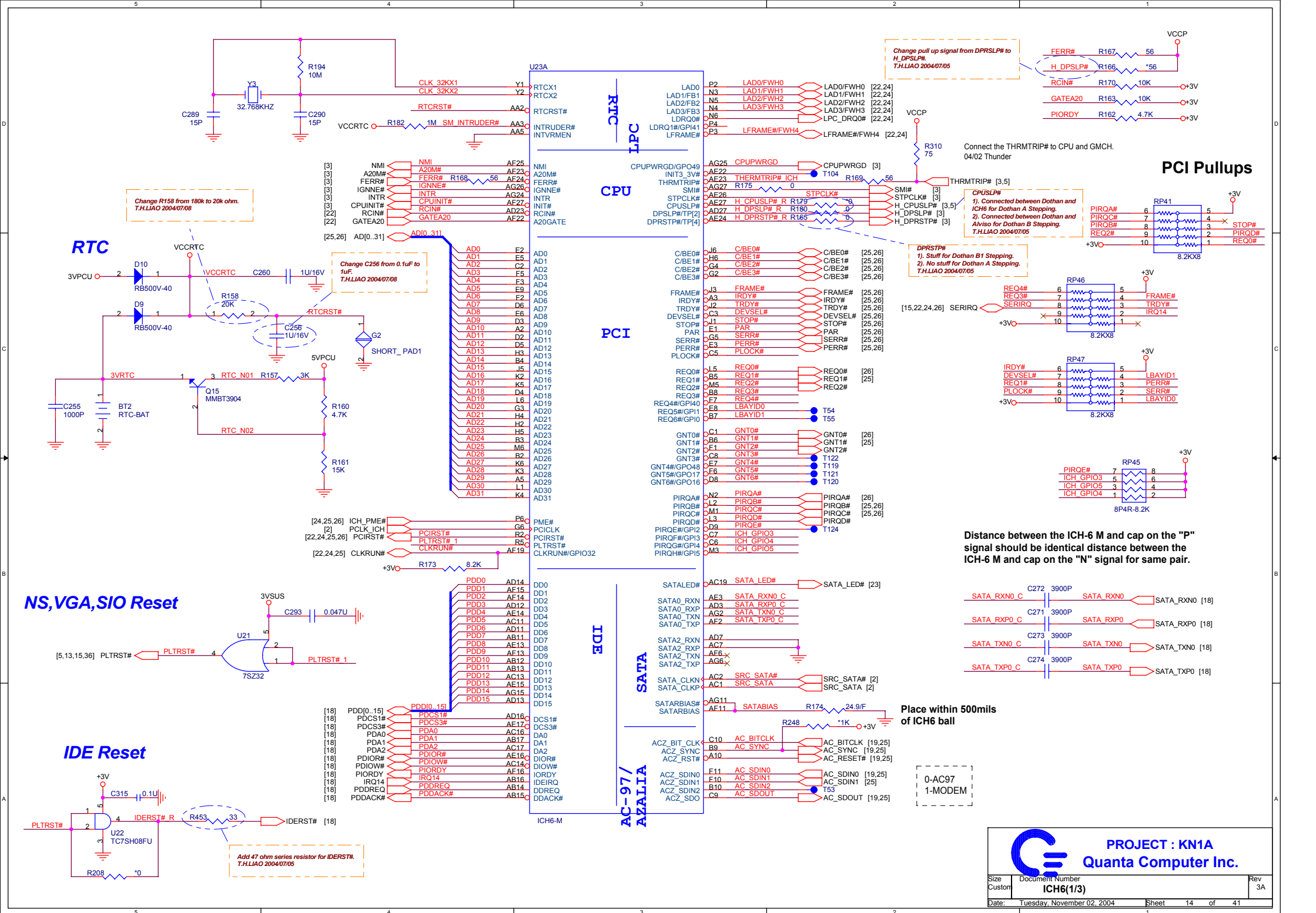
The LCD module is labeled **LCDCON40P 40 PIN**.

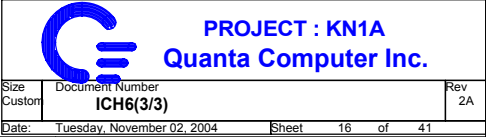
[illegible]

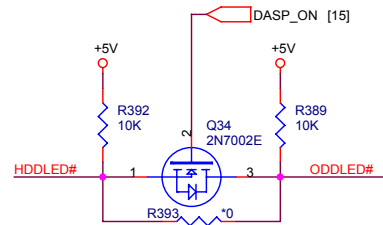
When use 915PM and then R462 no stuff.
T.H.LIAO 2004/09/27

R462

BLON

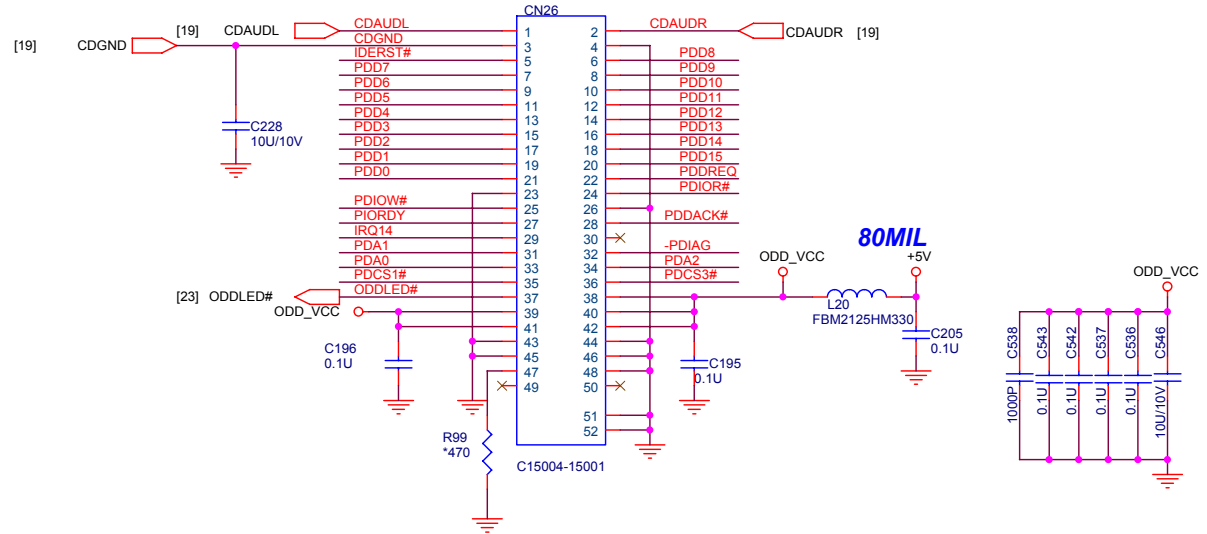




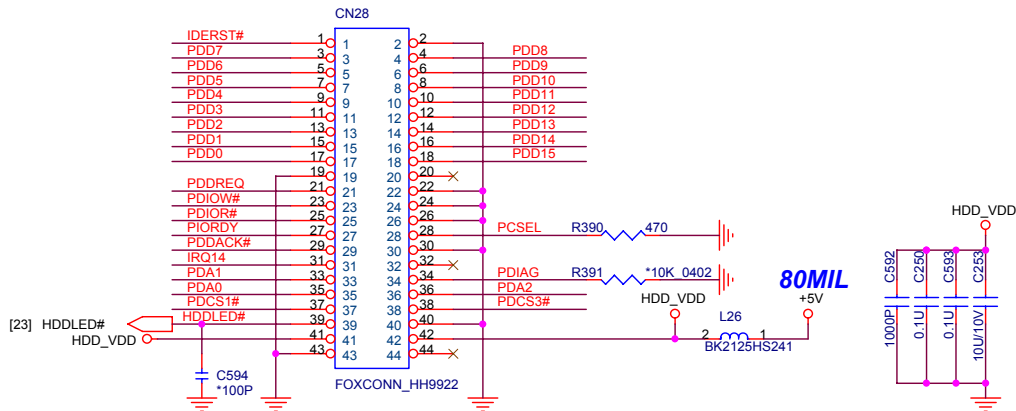


ADD DASP_ON FOR IDE CABLE SELECT
Thunder 04/08

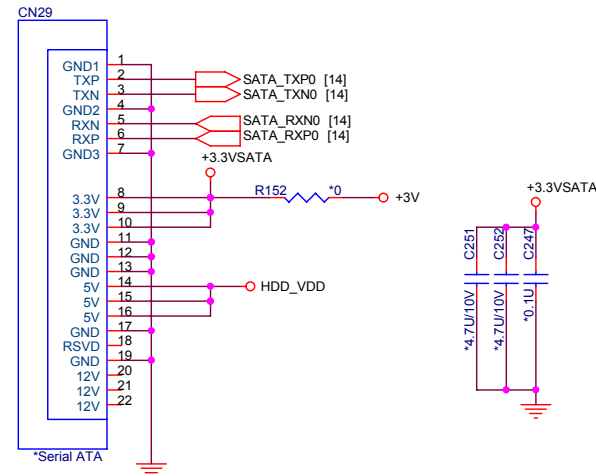
ODD CONNECTOR



HDD CONNECTOR



SATA CONNECTOR



PROJECT : KN1A
Quanta Computer Inc.

Add docking insert detect pin for ALC260 AP function use.
T.H.LIAO 2004/07/21

Add 33ohm series resistor for AC_RESET#.
T.H.LIAO 2004/07/05

Change AC_RESET# pull up resistor to no stuff.
T.H.LIAO 2004/07/22

ALC250 : No stuff
ALC260 : Stuff
T.H.LIAO 2004/07/22

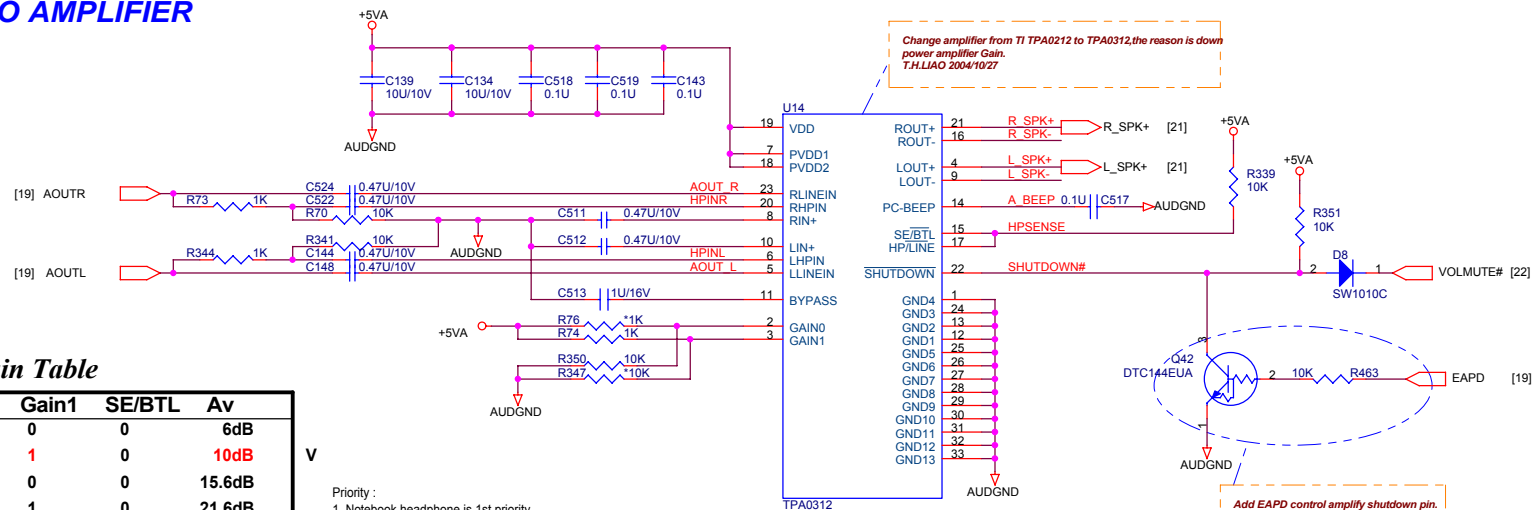
Codec Configuration:

Pin Assignment	ALC260	ALC250
LOUT (pin-35/36)	To 2W AMP	To 2W AMP
HP-OUT (pin-39/41)	HP-OUT Jack	HP-OUT Jack
LINE1 (pin-23/24)	LINE-IN Jack	LINE-IN Jack
MIC1 (pin-21/22)	MIC-IN Jack (Stereo)	MIC1-L: Mono MIC-IN Jack MIC1-R: Mono Internal MIC
MIC2 (pin-16/17)	Internal MIC-IN	X



PROJECT : KN1A
Quanta Computer Inc.

AUDIO AMPLIFIER

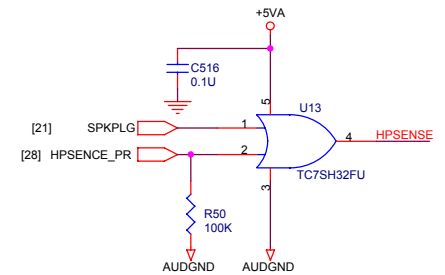


0312Gain Table

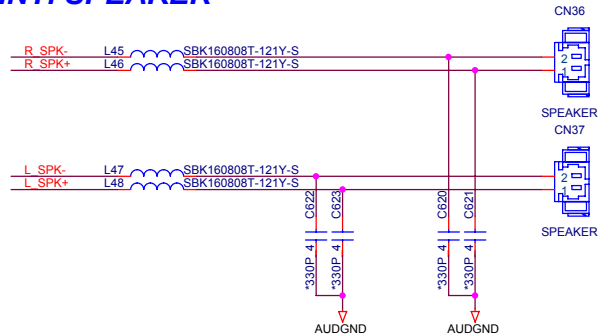
Gain0	Gain1	SE/BTL	Av
0	0	0	6dB
0	1	0	10dB
1	0	0	15.6dB
1	1	0	21.6dB
X	X	1	4.1dB

Priority :
 1. Notebook headphone is 1st priority
 2. Port-replicator headphone is 2nd priority
 3. Notebook internal speaker is 3rd priority

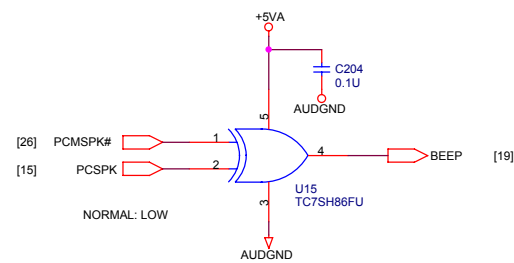
HEADPHONE SENSE



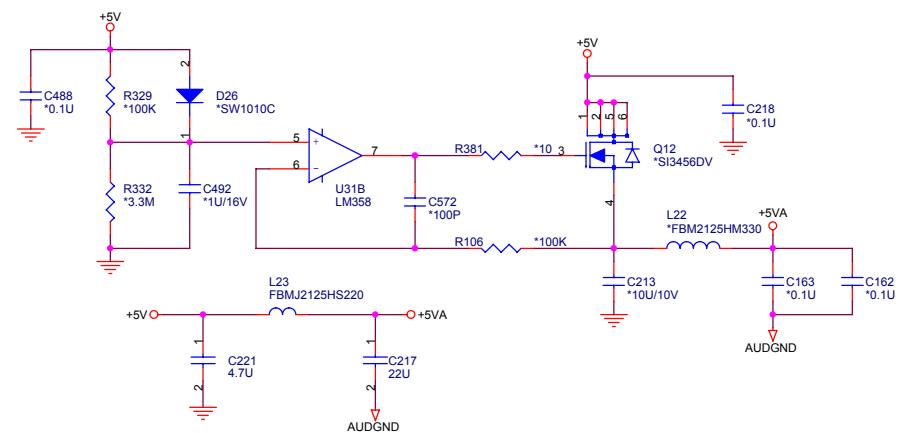
INT. SPEAKER



PC SPEAKER



AMP POWER



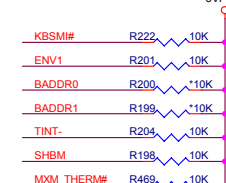
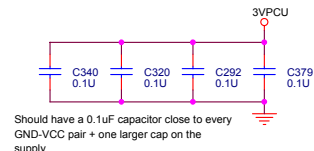
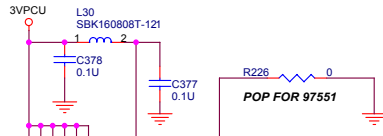
PROJECT : KN1A
 Quanta Computer Inc.

KBC-NS97551L

LDRQ#(pin 8) internal is no use

[14,24] LPC_DRQ0# $\rightarrow$ LPC_DRQ0# R258 10K DRQ0#

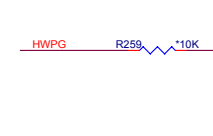
U25



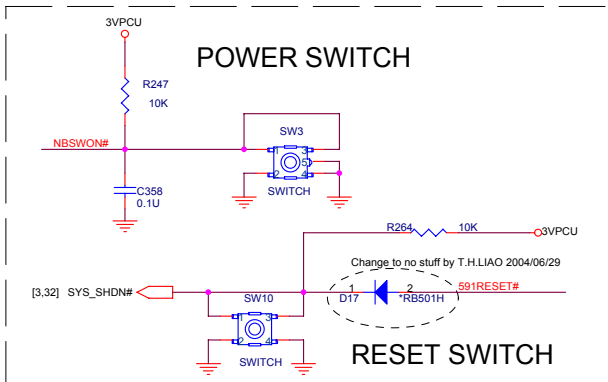
SHBM=1: Enable shared memory with host BIOS

BADDR1-0	Index	I/O Address	Data
0 0	2E	4E	2F
0 1	4E	4E	4E
1 0	HCFCGBAH, HCFCGBAL	(HCFCGBAH, HCFCGBAL)+1	Reserved
1 1			

Add MXM alert detect pin
T.H.LIAO 2004/10/14

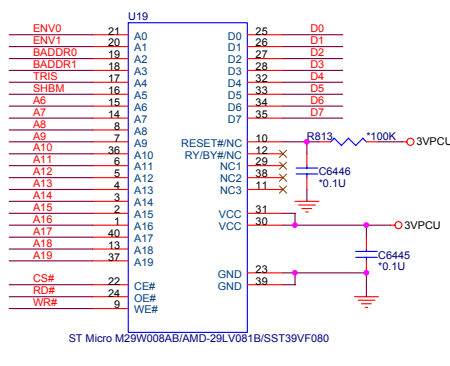


POWER SWITCH



RESET SWITCH

8Mbit (1M Byte), TSSOP40



PC97551VPC

Pin 103 internal is "A19". Can't use to GPIO

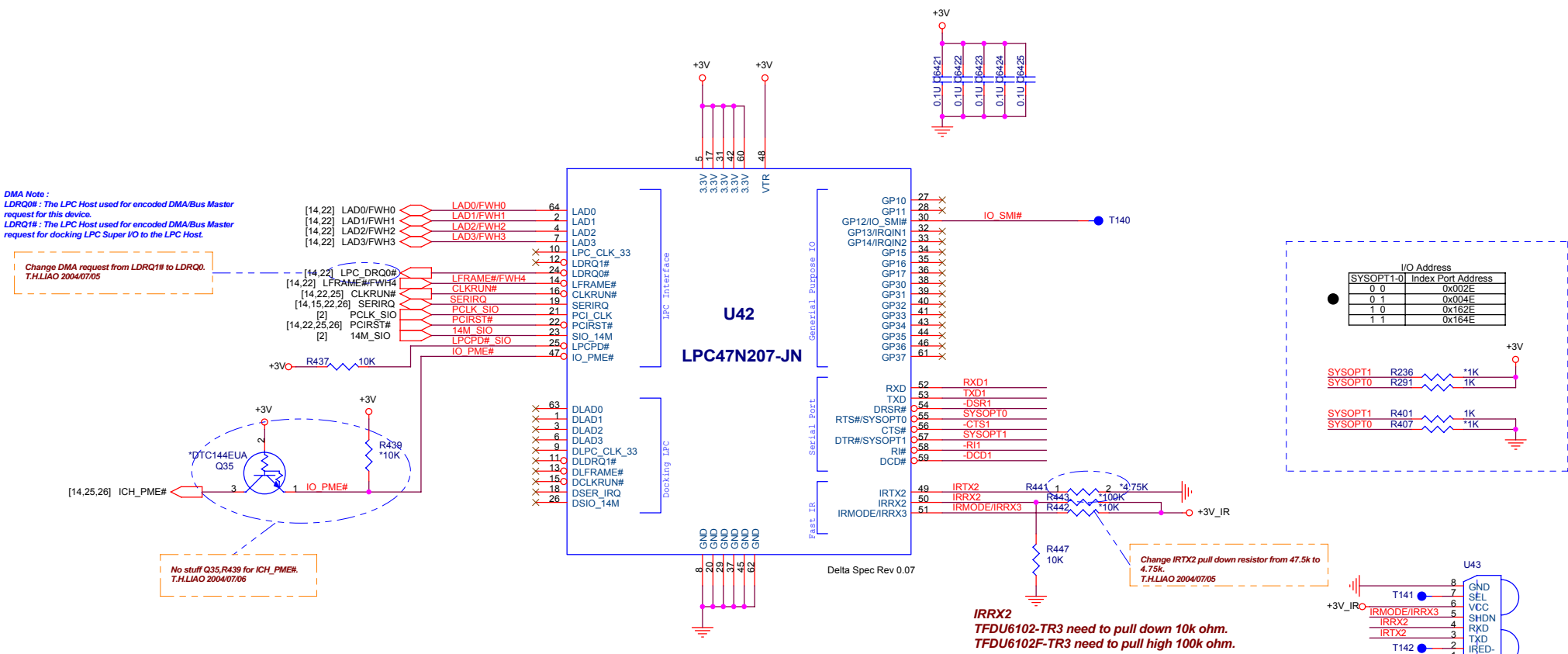
Change Flash rom size from 512K bytes to 1M bytes.
T.H.LIAO 2004/11/01



DMA Note :
LDRQ0# : The LPC Host used for encoded DMA/Bus Master request for this device.
LDRQ1# : The LPC Host used for encoded DMA/Bus Master request for docking LPC Super I/O to the LPC Host.

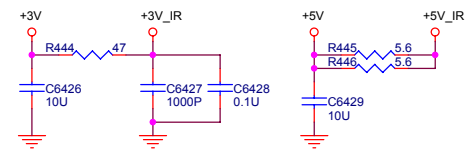
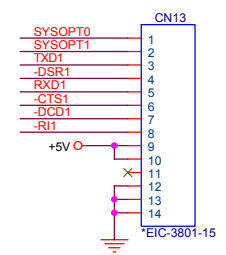
Change DMA request from LDRQ1# to LDRQ0.
 T.H.LIAO 2004/07/05

No stuff Q35,R439 for ICH_PME#.
 T.H.LIAO 2004/07/06

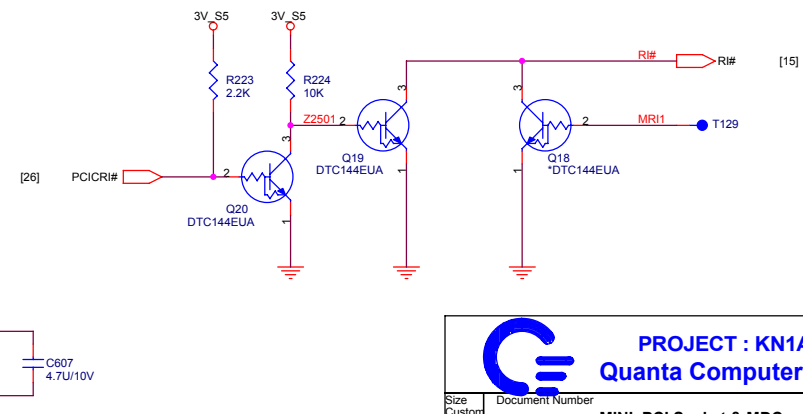
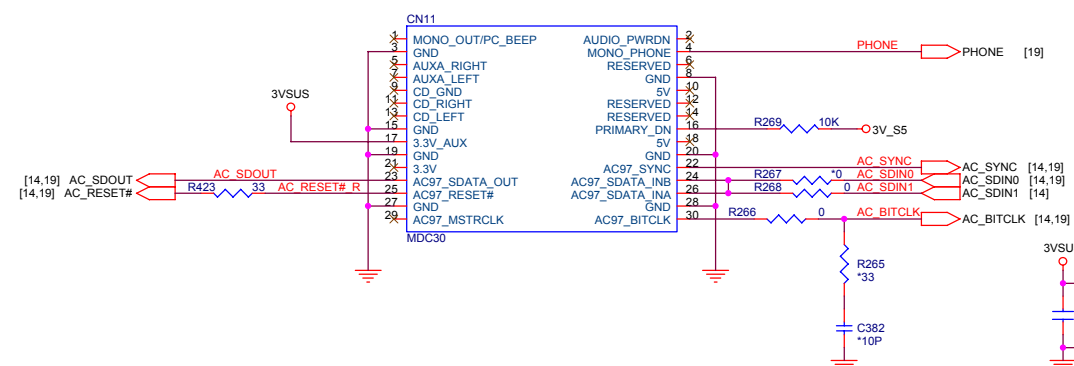
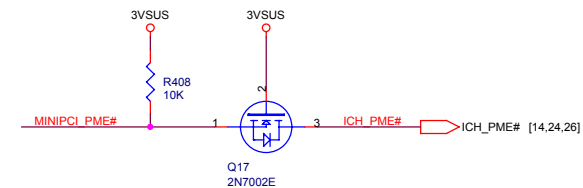
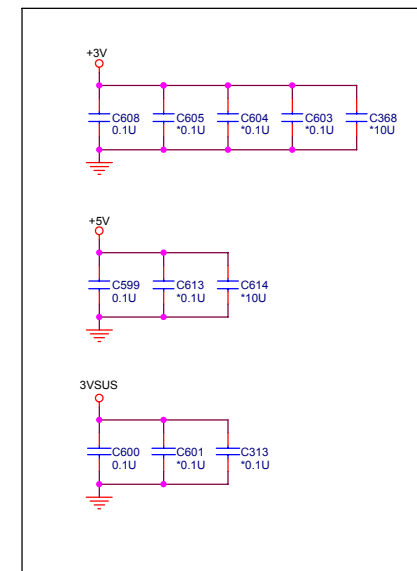
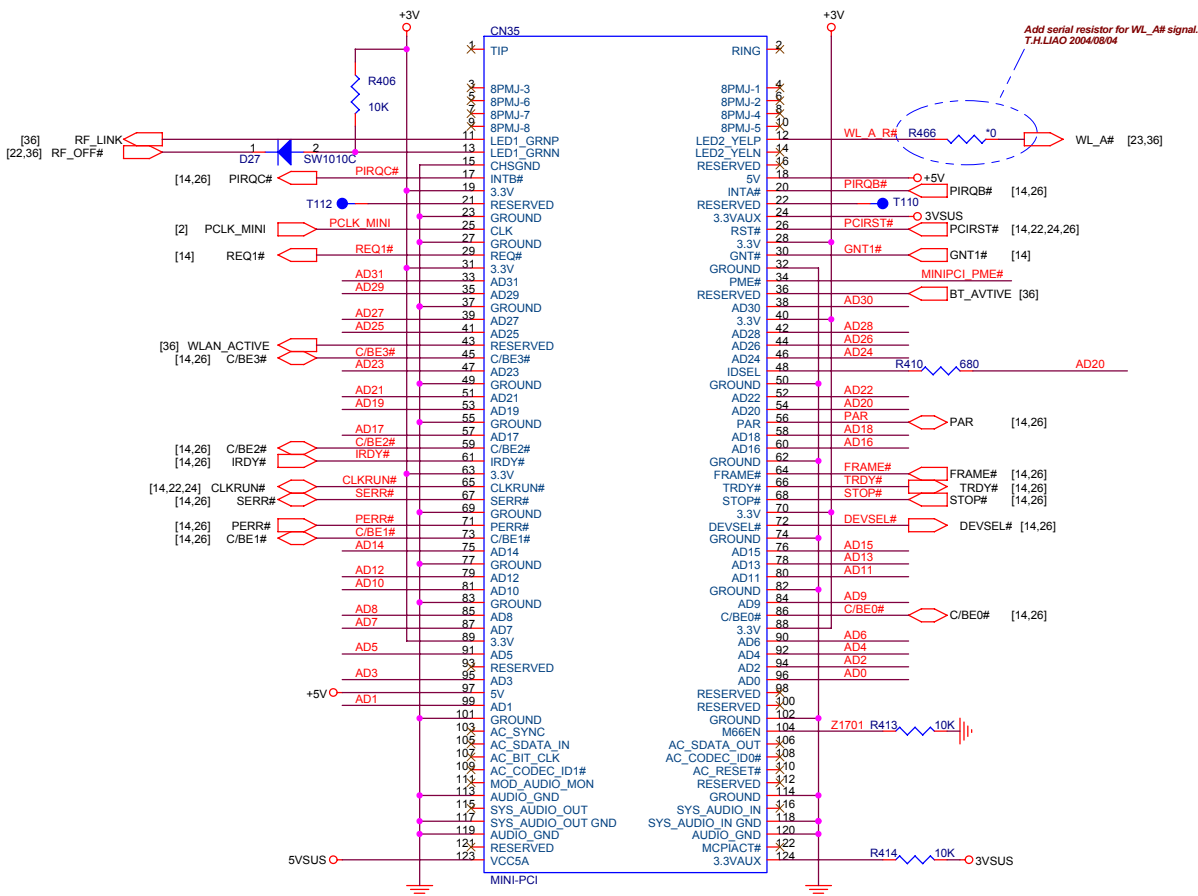


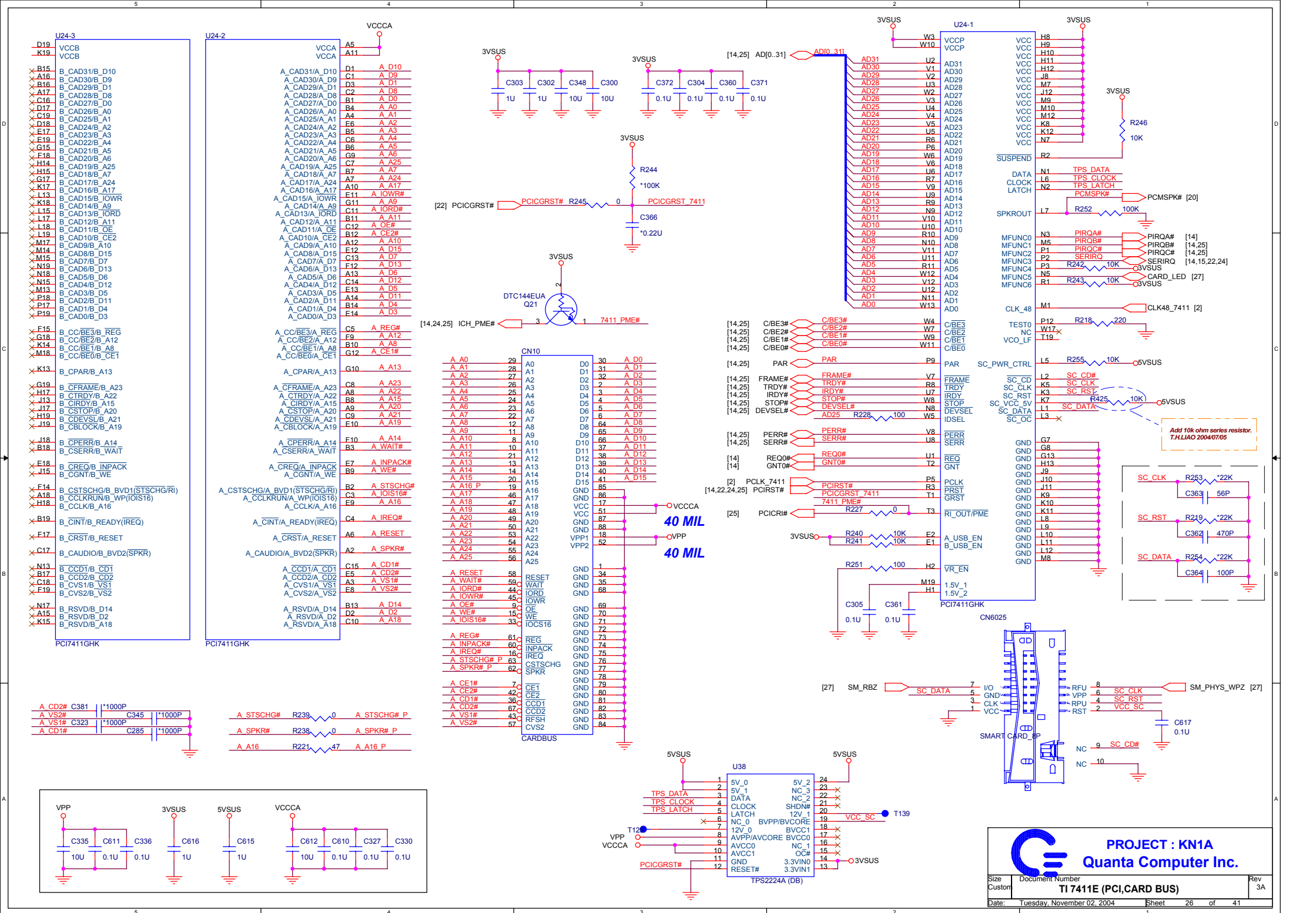
1. Disable serial port (UART1) in BIOS via setting CR02, bit3 to "0"
 (To disable the host address registers the logical device's base address must be set below 100h)
2. IO_SMI# function is unused.

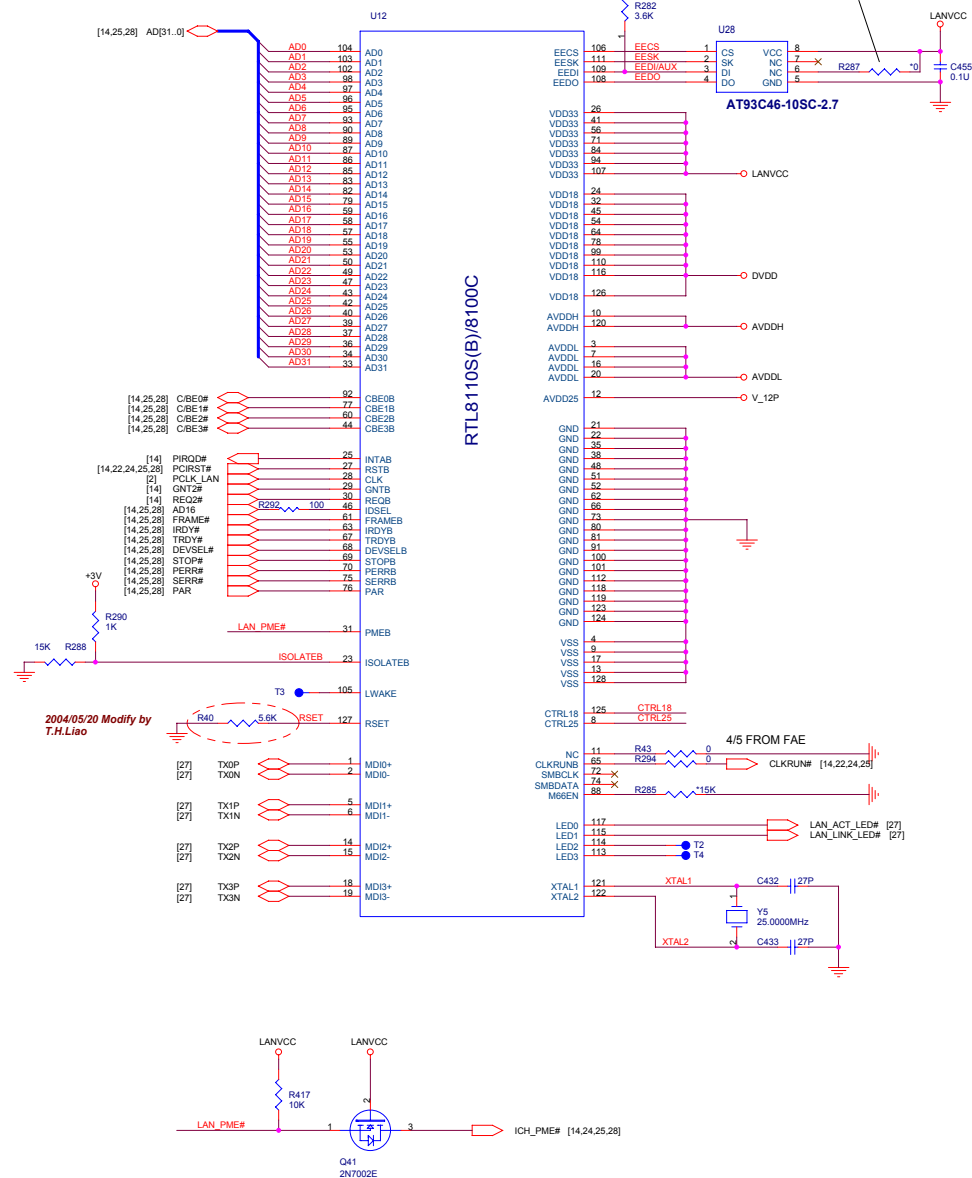
COM PORT CONN FOR BIOS DEBUG

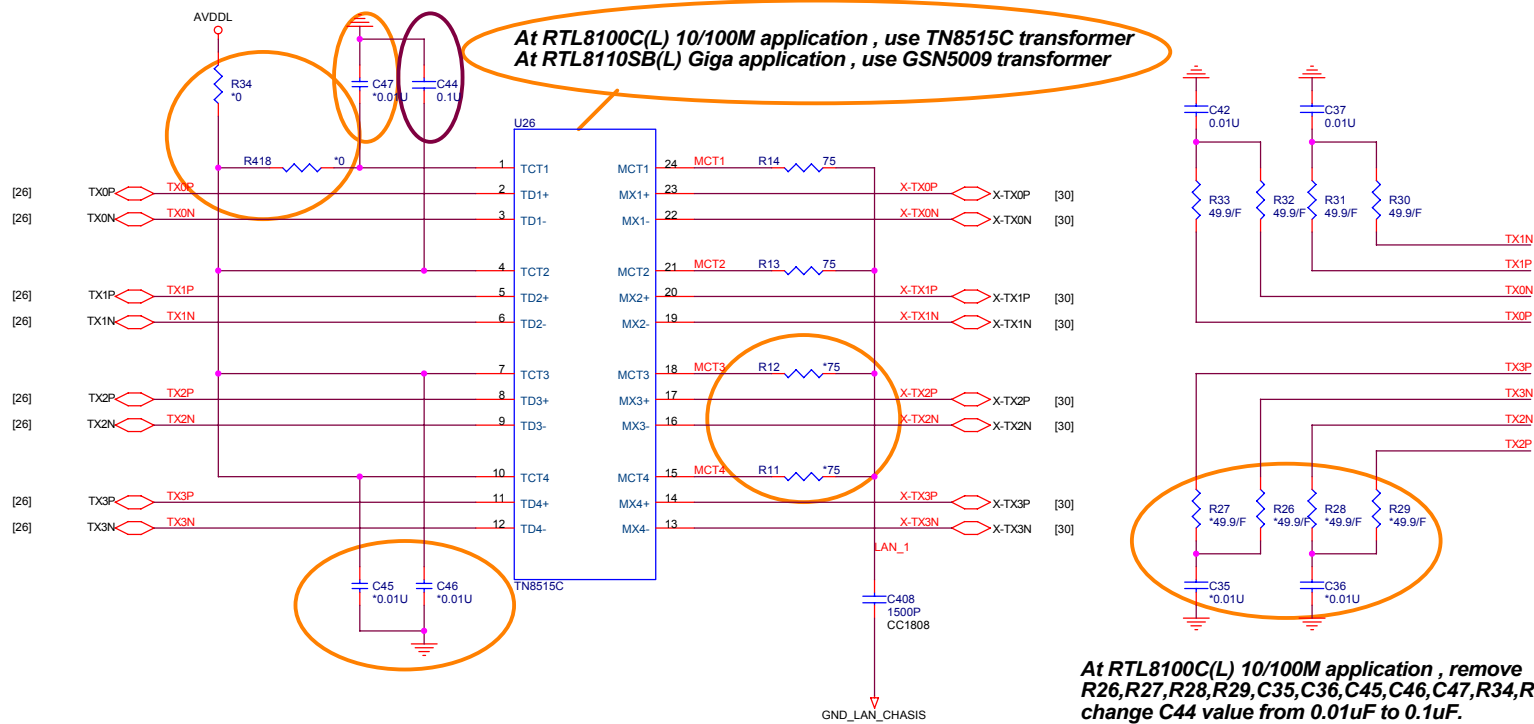


TYPE III MINI PCI SOCKET

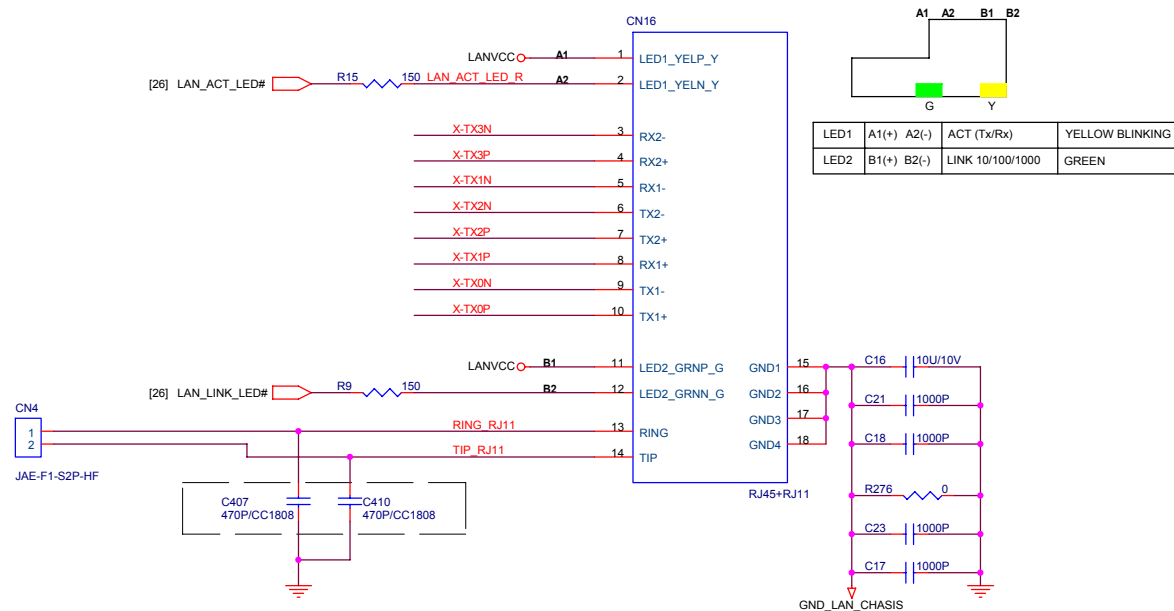






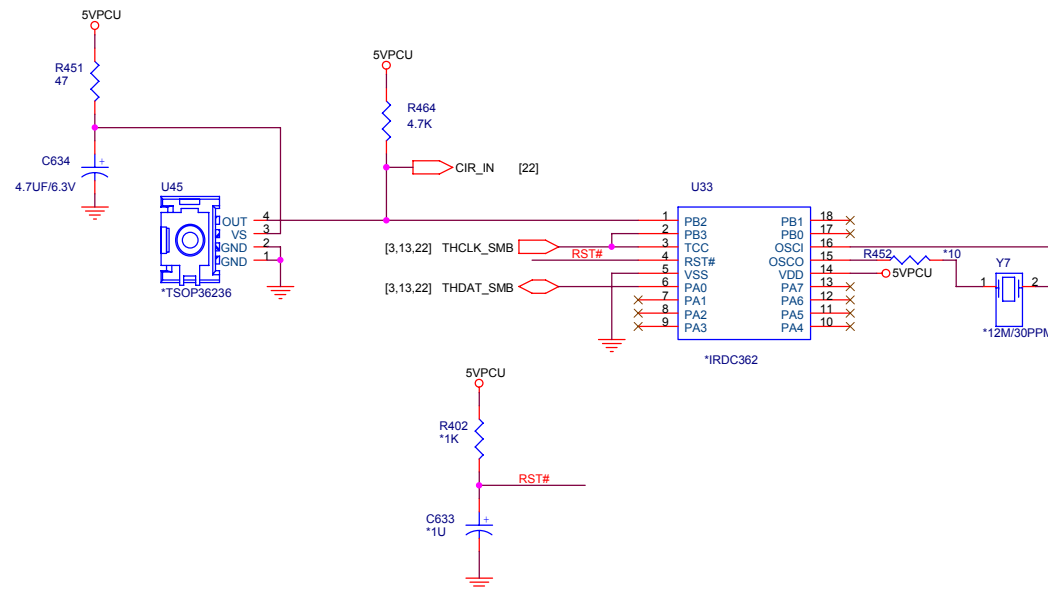
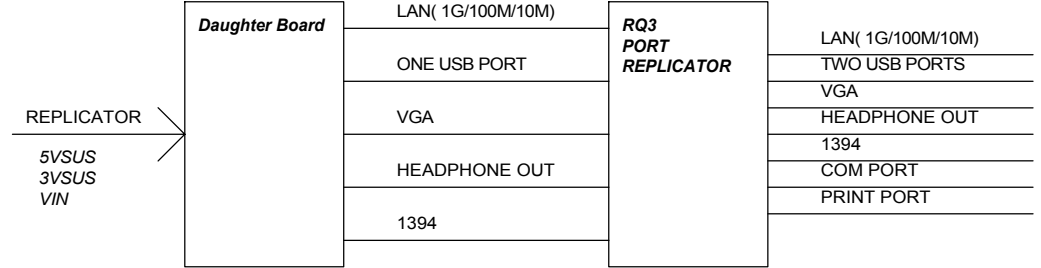
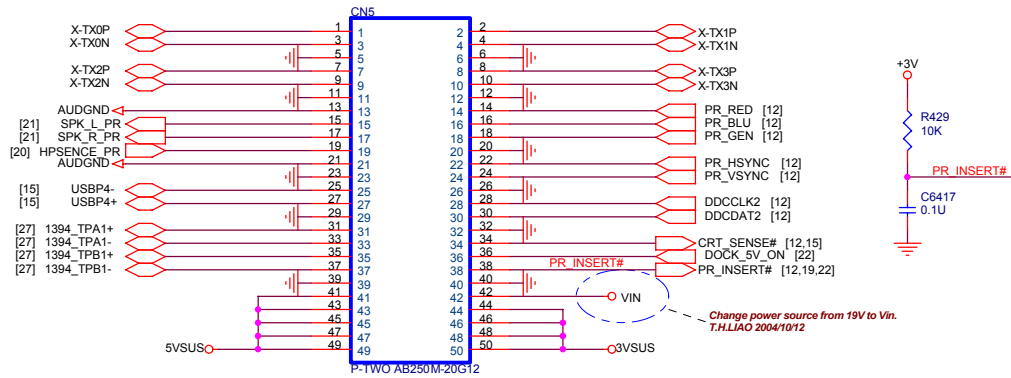


RJ45 and RJ11 CONNECTOR



PROJECT : KN1A
Quanta Computer Inc.

DOCKING BOARD CONNECT



VCCP
1.05V
7A

S0

1.5V_S5
176mA

S0-S5

$$V_{out} = (1 + R_c/R_d) * 1$$

$$V_{out} = (1 + R_e/R_f) * 1$$

Or

V_{out}=1845FB1 connect to 1845VCC
Fix V_{out}=1.5V

+1.5V
6A

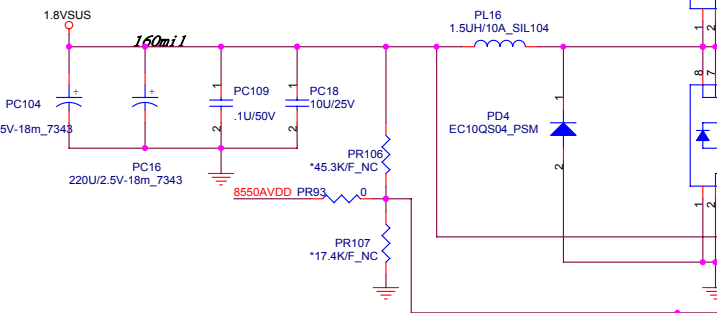
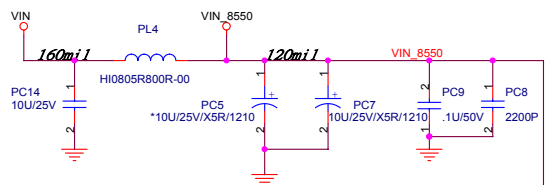
S0-S1



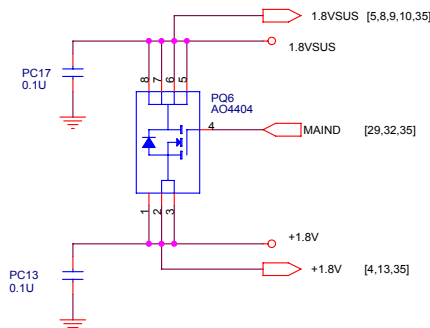
PROJECT : KN1A
Quanta Computer Inc.

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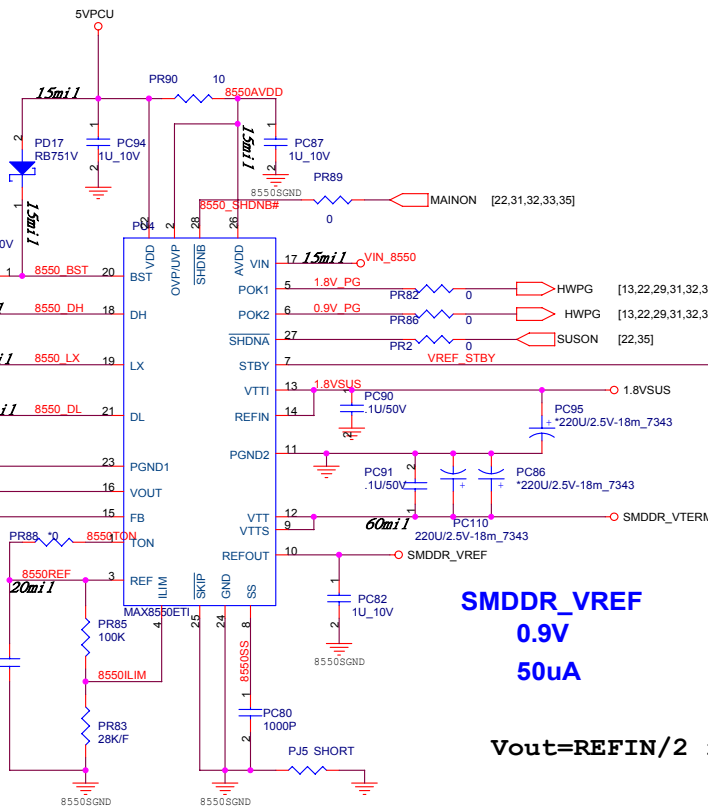
1.8VSUS
8A
S0-S3



Vout=8550FB connect to 8550AVDD
Fix Vout=1.8V



+1.8V
5.1A
S0-S1



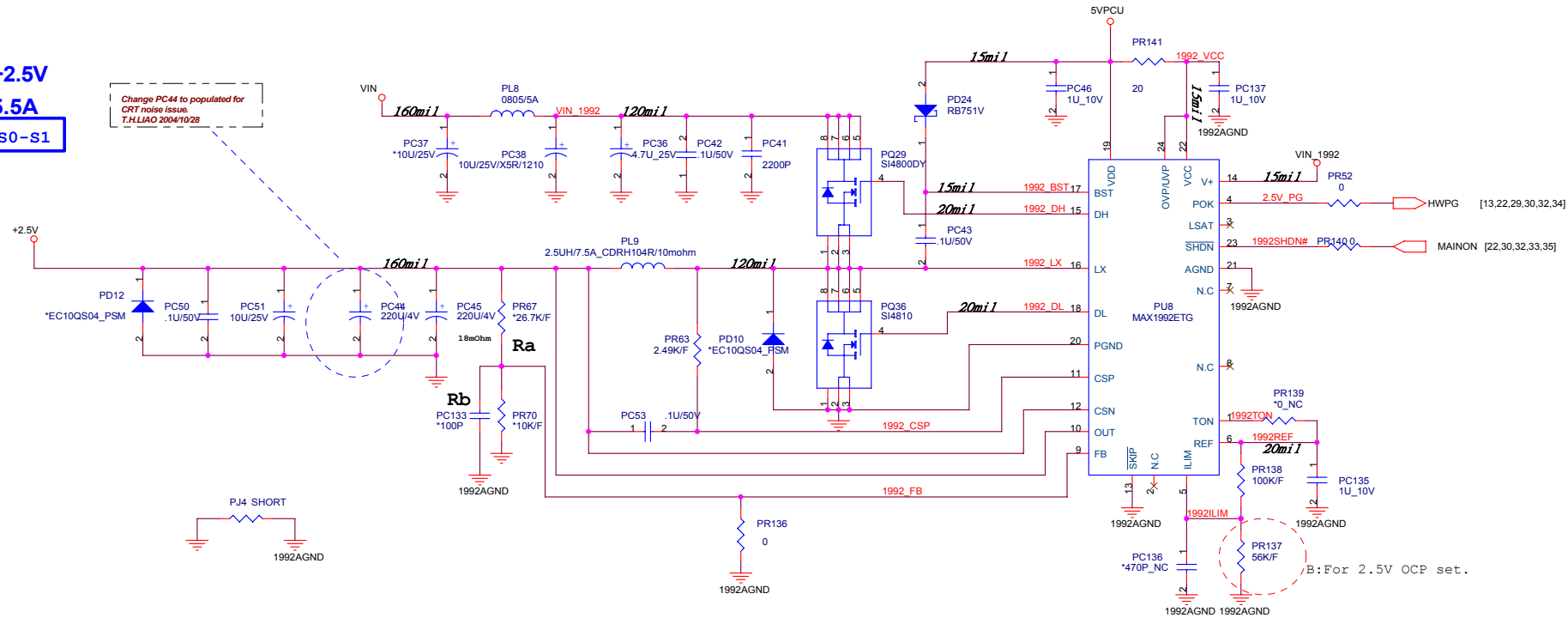
SMDDR_VREF
0.9V
50uA

Vout=REFIN/2 for VTT & VTTR(REFOUT)

SMDDR_VTERM
0.9V
4A
S0-S1

+2.5V
5.5A
S0-S1

Change PC44 to populated for CRT noise issue.
T.H.LIAO 2004/10/28



$$V_{out} = V_{FB} (1 + R_a / R_b)$$

$$\#V_{FB} = 0.7V$$

Or

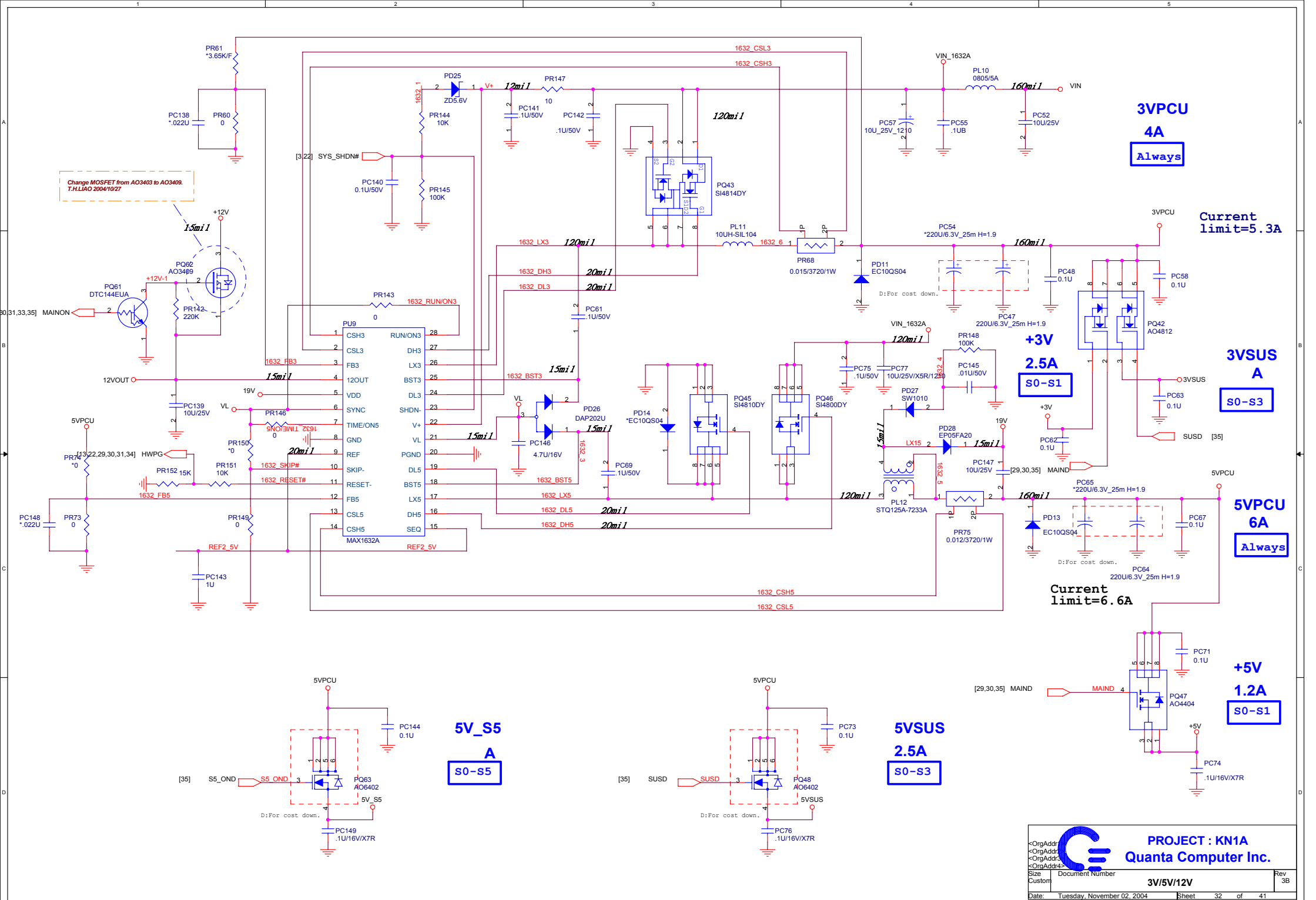
$V_{out} = 1992_FB$ connect to AGND

Fix $V_{out} = 2.5V$

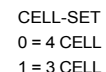


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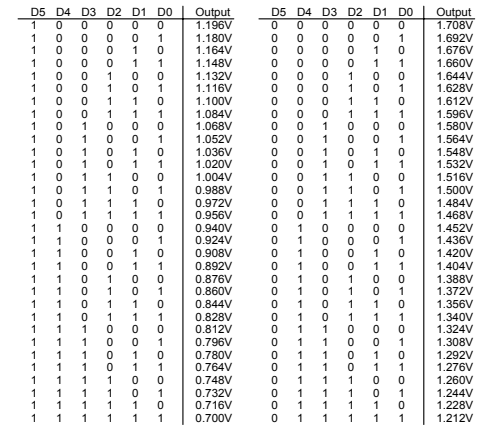


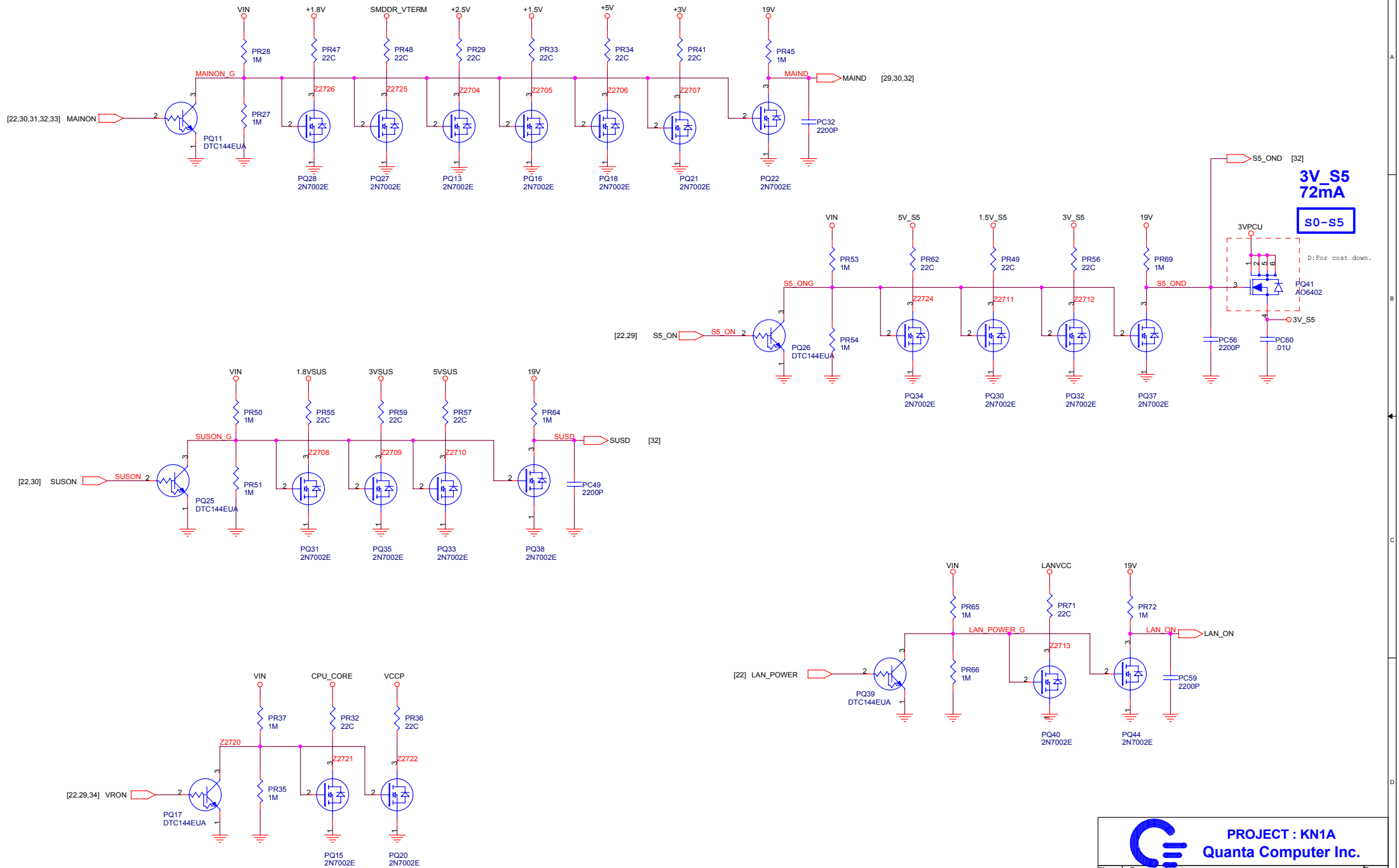
BATTERY 4400mAh 8cell/6cell
4800mAh 8cell/6cell



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A diagram of a 3-to-1 multiplexer. It has three input lines on the left, each labeled with a number (1, 2, 3) and a letter (A, B, C). These lines converge into a single output line on the right. The output line is labeled with a letter (D).





3V_S5
72mA
S0-S5

D: For cost down.

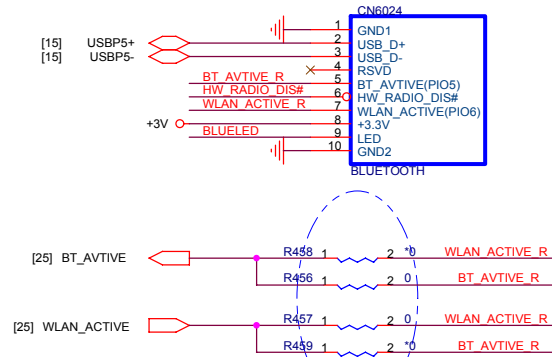


PROJECT : KN1A

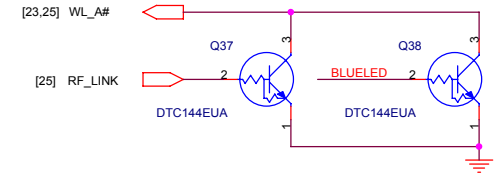
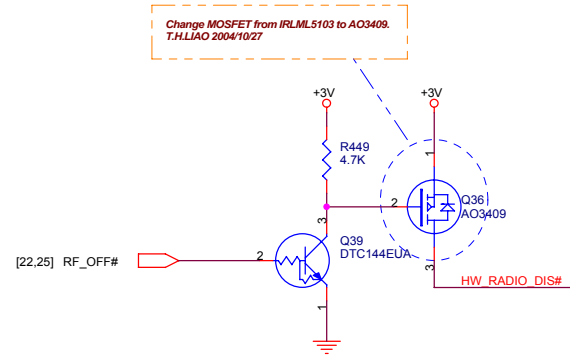
Quanta Computer Inc.

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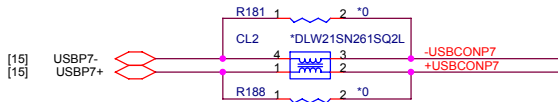
BLUETOOTH CONNECTOR



For bluetooth V1.2 spec option and combine intel wireless lan module use, BC2-ext also provide the co-existence solution.
T.H.LIAO 2004/07/06

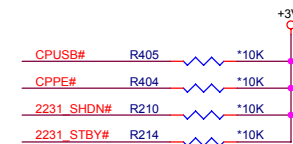
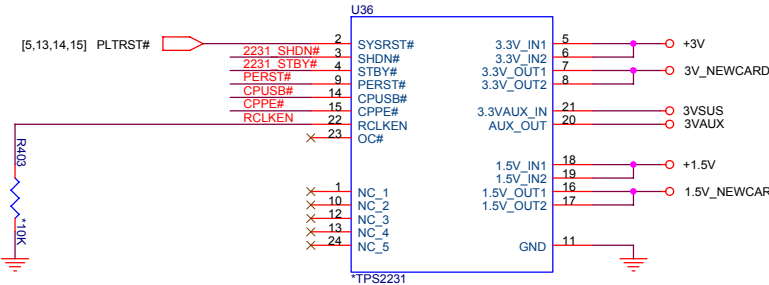
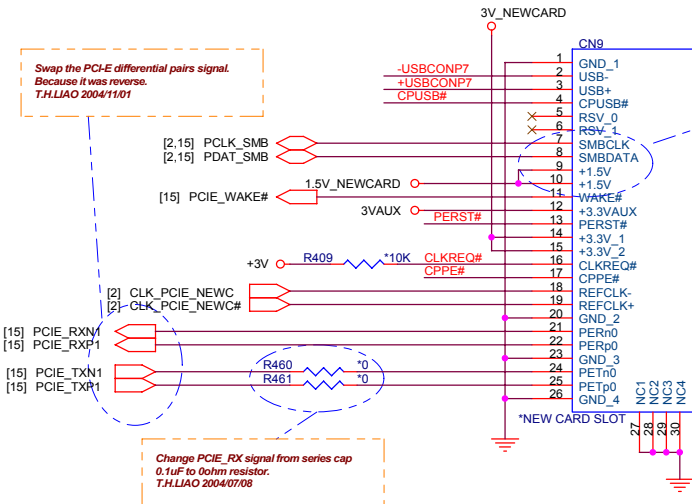


NEWCARD (PCIEXPRESS*1 + USB*1)



Swap the PCIe differential pairs signal. Because it was reverse.
T.H.LIAO 2004/11/01

Modify NEW CARD pin7,8,9 define.
T.H.LIAO 2004/07/09



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Quanta Computer Inc.

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Custom				
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MODEL	REV	CHANGE LIST	Model	KN1A M/B BOARD	
			Page	From	To
KN1A M/B	2A	FIRST RELEASE (2004-04-29 Release B1 version gerber)	1	2A	2B
	2B	PAGE02 : a). Add 14M SIO to 47N207 SIO chip. b). Change R386,R385 from 22.6 ohm 1% to 12.1 ohm 1%. c). Change R397,R398 from 33 ohm to 22 ohm. PAGE03 : Change R315 to no-stuff. PAGE05 : Delete RP28 and connect direct from clock generator to Alviso chip. PAGE06 : a). Modify VGA function text note. b). Change R340,R338,R337 to 150ohm +/- 1% and stuff it another change R342,R348,R345 to stuff. c). Add VSYNC,HSYNC NO-STUFF 33 pF cap to GND for slew rate control. PAGE08 : Change R62 to stuff for LCD can not display issue. PAGE12 : a). Add 2.2k ohm pull high to +2.5v for DDCCLK,DDCDATA signal. b). Change R4,R5,R7,R227,R279,R278 from 75ohm to 150ohm +/- 1%. PAGE13 : a). Add a pull high resistor on DISP_ON signal for nVidia NV4XM card use. b). Add 0ohm resistor between +1.5V and CN25,add 0ohm resistor between +2.5V and CN25 connect. c). Change R367,R368 to no stuff. PAGE14 : a). Add LPC_DRQ1# to 47N207 DMA Request pin. b). Modify DPRSLP signal text note. PAGE15 : a). Add RSMRST# delay circuit and connect to EC control. b). Delete R231,R236 and move C347,C357 to page2 as close as clock gen. c). Add USB5 differential pair to bluetooth. d). Swap SCI#,SWI# signal for ICH6. e). Change PCIE_WAKE# pull up power from 3VSUS to 3V_S5. PAGE17 : Modify USB connect stack for increase FIR mechanical issue. PAGE21 : Add microphone switch circuit for docking. PAGE22 : a). Add RSMRST# signal of EC pin 27 to ICH6. b). Add pull up 1k ohm for PWROK signal. c). Modify RESET switch circuit. d). Change U20 flash rom pin define. e). Add DOCK_5V_ON signal of EC pin39. f). Change CC-SET from PWM pin to DA pin output. g). Add KBSMI# pull up 10k ohm to 3VPCU. PAGE23 : a). Reverse the CN8 keyboard connector pin. b). Change QUICK switch key matrix from MY9 to MY7. PAGE24 : Add SMSC LPC47N207 chip for FIR function. PAGE25 : a). Add Bluetooth and Wireless Co-existence of mini pci pin36,43. b). Add RF_LINK signal of pin11. c). Delete R407 and move C602 to page2 as close as clock gen. PAGE26 : Delete R291 and move C466 to page2 as close as clock gen. PAGE27 : Modify CN16 connect library. PAGE28 : a). Delete R222 and move C331 to page2 as close as clock gen. b). Change CN33 smart card pin define. PAGE29 : Change CN34 4 in 1 card reader vendor type. PAGE30 : a). Add DOCK_5V_ON signal for cable docking use. b). Add MISCENCE and MIC_PR signal for docking. c). Add PR_INSERT# pull up 10k ohm to +3v. PAGE31 : Change PQ14 part number. PAGE32 : Change PC97,PU4 parts number. PAGE33 : a). Change PC136 part number. b). PR137 change to 56K/F 0402 (CS35602FB02).	2	2A	2B
			3	2A	2B
			4	2A	
			5	2A	2B
			6	2A	2B
			7	2A	
			8	2A	2B
			9	2A	
			10	2A	
			11	2A	
			12	2A	2B
			13	2A	2B
			14	2A	2B
			15	2A	2B
			16	2A	
			17	2A	2B
			18	2A	
			19	2A	
			20	2A	
			21	2A	2B
			22	2A	2B
			23	2A	2B
			24	2A	2B
			25	2A	2B
			26	2A	2B
			27	2A	2B
			28	2A	2B
			29	2A	2B
			30	2A	2B
			31	2A	2B
			32	2A	2B
			33	2A	2B
			34	2A	2B
			35	2A	2B
			36	2A	2B
			37	2A	2B
			38	2A	2B
			39	2A	2B
			40	2A	2B
			41	2A	2B

PROJECT : KN1A

Quanta Computer Inc.

Size Custom Document Number

CHANGE LIST - 2B

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Rev 3A

Quanta Computer Inc. KN1A	PROJECT: KN1A APPROVED BY : James Chiang	PCBA NO. CHECK BY : T.H.LIAO	REV: 2B DRAWING BY : Thunder Chiang	DOC. NO : DATE :05/05/2004	SHEET 1
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MODEL	REV	CHANGE LIST	Model	KN1A M/B BOARD	
			Page	From	To
KN1A M/B	3A	PAGE02 : a). Change R155,R126 to no stuff. b). Add a serial resister between clock gen pin53 and SELPSB0_CLK signal. c). Modify spread spectrum text note. PAGE03 : Change R308 from 330 to 200 ohm. PAGE04 : Change C78,C63,C74,C417 to no stuff. PAGE05 : a). Change pull up from +2.5V to VCCP power plane. b). Delete RSVD23 C150 pull down 0.1uF cap. PAGE06 : a). LBKLT_EN add a pull down 100k ohm resistor. b). Add REFSET signal pull high to VCCP for 915PM use. c). When use 915PM and then R71,R340,R338,R337 need pull down 0 ohm resistor. PAGE08 : a). Change analog filter circuits from ferrite bead to inductor. b). Add analog filter circuits for the VCCA_DPLLA,VCCA_DPLL_B pins. c). Add a ferrite bead for the VCCA_CRTDAC pins. d). Add a ferrite bead for the VCCDQ_TVDAC pin. e). Add a ferrite bead for the VCCA_TVBG pin. f). Add VCC_SYNC,VCCA_CRTDAC minimize power/leakage for 915PM solution . PAGE12 : a). Add a 39 ohm series resistor for external VGA card VSYNC,HSYNC use. b). Add one RGB pole PI filter circuit for VGA. PAGE13 : Change GPU SMBUS power source from +2.5V to +3V. PAGE14 : a). Change R179 to no stuff for Dothan B1 stepping. b). Change pull up signal from DPRSLP# to H_DPSLP#. c). Change R165 to no stuff for Dothan B stepping. d). Add 47 ohm series resistor for IDERST#. e). Change R158 from 180k to 20k ohm and change C256 from 0.1uF to 1uF. PAGE15 : a). Change R211 to stuff 10k ohm resistor. b). Change R187 pull down 10k ohm resistor. c). Swap THRM# and SUSB# pull high resistor and change R172 to no stuff. d). Add MXM card detect pin and change CRT_SENSE# pull high from +3V to 3VSUS. PAGE19 : a). ACZ_RST# needs 33ohm series resistor. b). Modify codec circuit for ALC260 Azalia codec solution. PAGE20 : Add EAPD control amplify shutdown pin. PAGE21 : a). No stuff component for MIC switch circuit. b). Change AC97 connect ro Azalia+SPDIF connect. PAGE22 : a). Change D17 to no stuff. b). Add R465 serial resistor for PWROK signal. PAGE24 : a). Change I/O address from 162E to 004E. b). Change R441,R442,,R443 to no stuff. c). Change DMA request from LDRQ1# to LDRQ0. d). Change IRTX2 pull down resistor from 47.5k to 4.75k. e). No stuff Q35,R439 for ICH_PME# of SIO47N207. PAGE25 : a). Add 33ohm series resistor for AC_RESET#. b). Add serial resistor for WL_A# signal. PAGE26 : Change Q28 pin define. PAGE28 : a). Delete R222 and move C331 to page2 as close as clock gen. b). Change CN33 smart card pin define. PAGE29 : a). Add 10k ohm series resistor. b). Change cardread vendor. c). Add 1394 signal for port replicator used. PAGE30 : a). Modify board to board pin define. b). Add CIR function.	1	2B	3A
			2	2B	3A
			3	2B	3A
			4	2A	2B
			5	2B	3A
			6	2B	3A
			7	2A	
			8	2B	3A
			9	2A	
			10	2A	
			11	2A	
			12	2B	3A
			13	2B	3A
			14	2B	3A
			15	2B	3A
			16	2A	
			17	2A	2B
			18	2A	
			19	2A	2B
			20	2A	2B
			21	2B	3A
			22	2B	3A
			23	2A	2B
			24	2B	3A
			25	2B	3A
			26	2B	3A
			27	2A	2B
			28	2B	3A
			29	2B	3A
			30	2B	3A
			31	2B	3A
			32	2B	3A
			33	2B	3A
			34	2B	3A
			35	2B	3A
			36	2B	3A
			37	2B	3A
			38	2B	3A
			39	3A	
			40	3A	
			41	3A	

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Quanta Computer Inc.

Size Custom Document Number Rev 3A

CHANGE LIST - 3A

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Quanta Computer Inc. KN1A	PROJECT: KN1A APPROVED BY : James Chiang	PCBA NO. CHECK BY : T.H.LIAO	REV: 3A DRAWING BY : Thunder Chiang	DOC. NO : DATE :05/05/2004	SHEET 1
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MODEL	REV	CHANGE LIST	Model	KN1A M/B BOARD	
			Page	From	To
KN1A M/B	3A	PAGE31 : a). Delete JP2,JP3,PR40,JP8,JP9 for layout & cost down. b). Delete +1.05V,+1.5V_OUT,1845VDD for layout. c). NC PC24,PC39 for cost down. d). Change PQ60 => AO4404 (BAM44040012) PAGE32 : a). Delete JP5,JP6,PR92,JP7 for layout & cost down. b). Delete 8550OUT,8550VDD,8550_VTERM for layout. c). NC PC7,PC86,PC95,PC104 for cost down. d). Change PQ6 => AO4404 (BAM44040012) PAGE33 : a).Delete JP4,PR58 for layout & cost down. b).Delete 1992_OUT,1992_VDD for layout. c).NC PC45 for cost down. PAGE34 : a). NC PC54,PC65 for cost down. b). PQ62 => AO3403 (BAM34030Z02) c). PQ42 => AO4812 (BAM48120002) d). PQ47 => AO4404 (BAM44040012) e). PQ48 => CEH2325 (BAM23250000) f). PQ63 => CEH2325 (BAM23250000) PAGE35 : NC PC134 for cost down. PAGE36 : a). Delete PR99 for cost down. b). NC PC85,PC12,PC92,PC101 for cost down. c). Add PR153,PR154,PR155 for Intel change C4 Deeper sleep voltage. d). Change PR8 to 2K/F (CS22002FB19),for load line set. e). NC PC11,PC92,PC93. PAGE37 : Change PQ41 => CEH2325 (BAM23250000) PAGE38 : a). Add BC2-ext co-existence solution . b). Change PCIE_RX signal from series cap 0.1uF to 0ohm resistor. c). Modify NEW CARD pin7,8,9 define. THIRD RELEASE (2004-08-10 Release Pre C version gerber)	1	2B	3A
			2	2B	3A
			3	2B	3A
			4	2A	2B
			5	2B	3A
			6	2B	3A
			7	2A	
			8	2B	3A
			9	2A	
			10	2A	
			11	2A	
			12	2B	3A
			13	2B	3A
			14	2B	3A
			15	2B	3A
			16	2A	
			17	2A	2B
			18	2A	
			19	2A	2B
			20	2A	2B
			21	2B	3A
			22	2B	3A
			23	2A	2B
			24	2B	3A
			25	2B	3A
			26	2B	3A
			27	2A	2B
			28	2B	3A
			29	2B	3A
			30	2B	3A
			31	2B	3A
			32	2B	3A
			33	2B	3A
			34	2B	3A
			35	2B	3A
			36	2B	3A
			37	2B	3A
			38	2B	3A
			39	3A	
			40	3A	
			41	3A	

MODEL	REV	CHANGE LIST	Model	KN1A M/B BOARD	
			Page	From	To
KN1A M/B	3B	PAGE02 : Change PCLK_ICH signal to free fun function pin. PAGE06 : Add DPST function. PAGE08 : a). Change component from L59 EMI filter to R48 0 ohm/0603 package resistor. b). Change component from L60 EMI filter to R55 0 ohm/0603 package resistor. PAGE12 : a). Change bus switch IC from NC7SB3157P6X to SN74LVC2G125. b). Change BEAD L2,L3,L5,L49,L50,L51 from BK1608HS470 to BLM18BB470SN1D. PAGE13 : a). Add MXM battery low detect signal. b). Delete GND trace prevent when MXM card insert and cause the pin short. c). Delete C527,C528 cap for MXM 5VPCU power plane use. d). Delete MXM card FAN control from MBCLK/MBDATA change to THCLK_SMB/THDAT/SMB by EC. e). Change MXMII card sequence from PWROK to HWPG, since PWROK signal slowly than CPU power good. PAGE15 : a). Change CRT_SENSE# pull high from 3VSUS to +3V, the purpose is prevent +3V power leakage. b). Change THRM#,SCI# signal pull high from 3VSUS to +3V for power leakage. c). Swap the PCI-E control host. PAGE19 : Add docking insert detect (PR_INSERT#) pin for ALC260 AP function use. PAGE20 : Change amplifier from TI TPA0212 to TPA0312,the reason is down power amplifier Gain. PAGE21 : a). Modify CN30 connect pin define. b). Separate the internal MIC layout trace. PAGE22 : a). Add MXM allert detect pin. b). Change Flash rom size from 512K bytes to 1M bytes. PAGE23 : Change MOSFET from SI3456DV to AO6402. PAGE30 : Change power source from 19V to Vin. PAGE33 : Change PC44 to populated for CRT noise issue. PAGE34 : a). Change part number PC54,PC47,PC65,PC64 (220uF/6.3V_25m H=1.9/CH7221M8854) for cost down. b). Change PQ63,PQ48 from CEH2325 to AO6402. c). Change MOSFET from AO3403 to AO3409. PAGE36 : a). Change PR9,PU2,PC10,PQ5 to no stuff. b). Change PR98 from 100K ohm to 10K for Alviso timing. PAGE37 : Change PQ41 from CEH2325 to AO6402. PAGE38 : a). Change MOSFET from IRLML5103 to AO3409. b). Swap the PCI-E differential pairs signal. Because it was reverse. FOURTH RELEASE (2004-11-03 Release C version gerber) Notices : ICT test coverage target 98.7%	1	2B	3A
			2	3A	3B
			3	2B	3A
			4	2A	2B
			5	2B	3A
			6	3A	3B
			7	2A	
			8	3A	3B
			9	2A	
			10	2A	
			11	2A	
			12	3A	3B
			13	3A	3B
			14	2B	3A
			15	3A	3B
			16	2A	
			17	2A	2B
			18	2A	
			19	2B	3A
			20	2B	3A
			21	3A	3B
			22	3A	3B
			23	2B	3A
			24	2B	3A
			25	2B	3A
			26	2B	3A
			27	2A	2B
			28	2B	3A
			29	2B	3A
			30	3A	3B
			31	2B	3A
			32	2B	3A
			33	3A	3B
			34	3A	3B
			35	2B	3A
			36	3A	3B
			37	3A	3B
			38	3A	3B
			39	3A	
			40	3A	
			41	3A	
			42	3B	
			43	3B	
			 PROJECT : KN1A Quanta Computer Inc. Size Custom Document Number Rev 3B CHANGE LIST - 3B Date: Tuesday, November 02, 2004 Sheet 41 of 41		

 Quanta Computer Inc. KN1A	PROJECT: KN1A	PCBA NO.	REV: 3B	DOC. NO :	
APPROVED BY : James Chiang	CHECK BY : T.H.LIAO	DRAWING BY : Thunder Chiang	DATE :05/05/2004	SHEET 1	