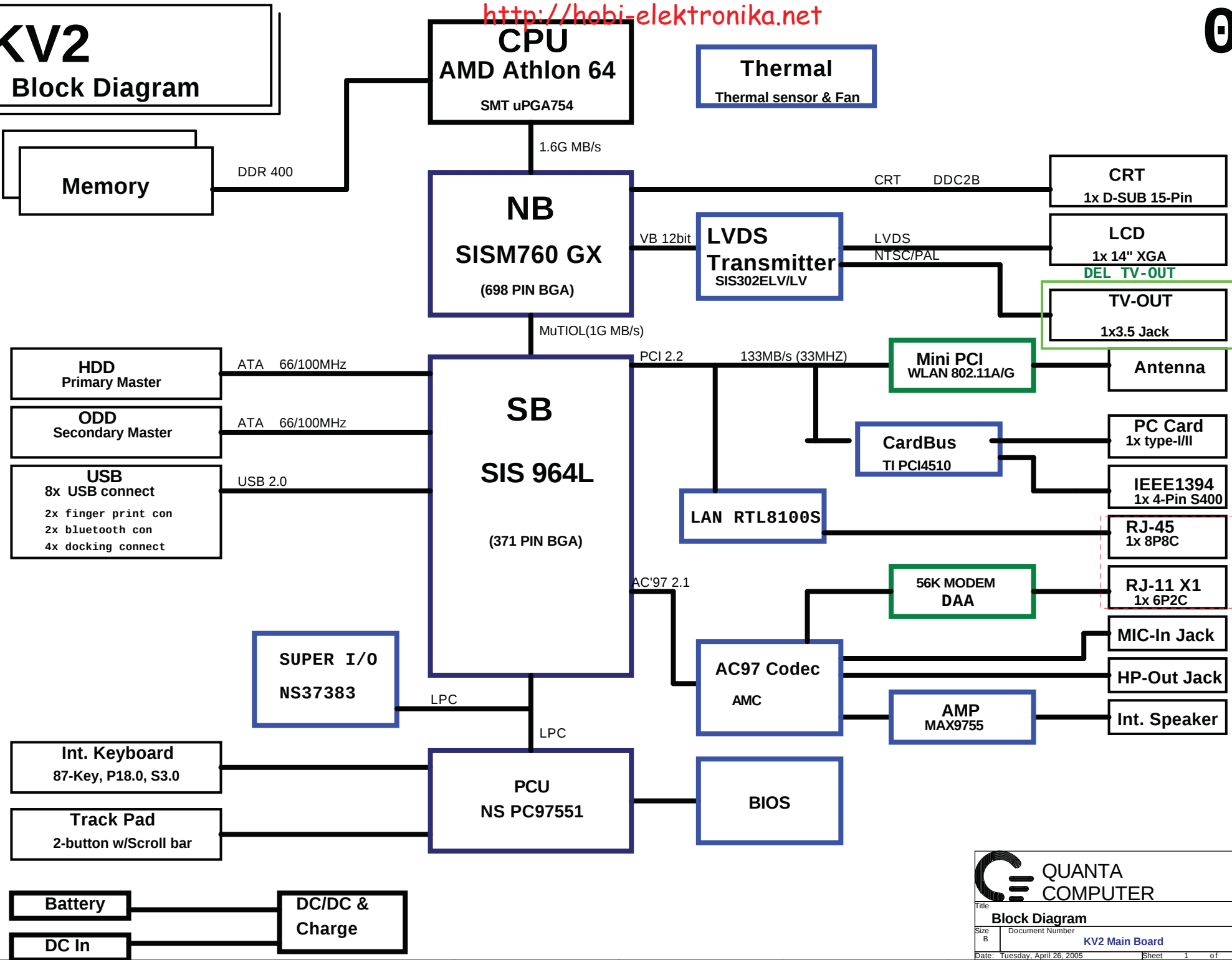


KV2

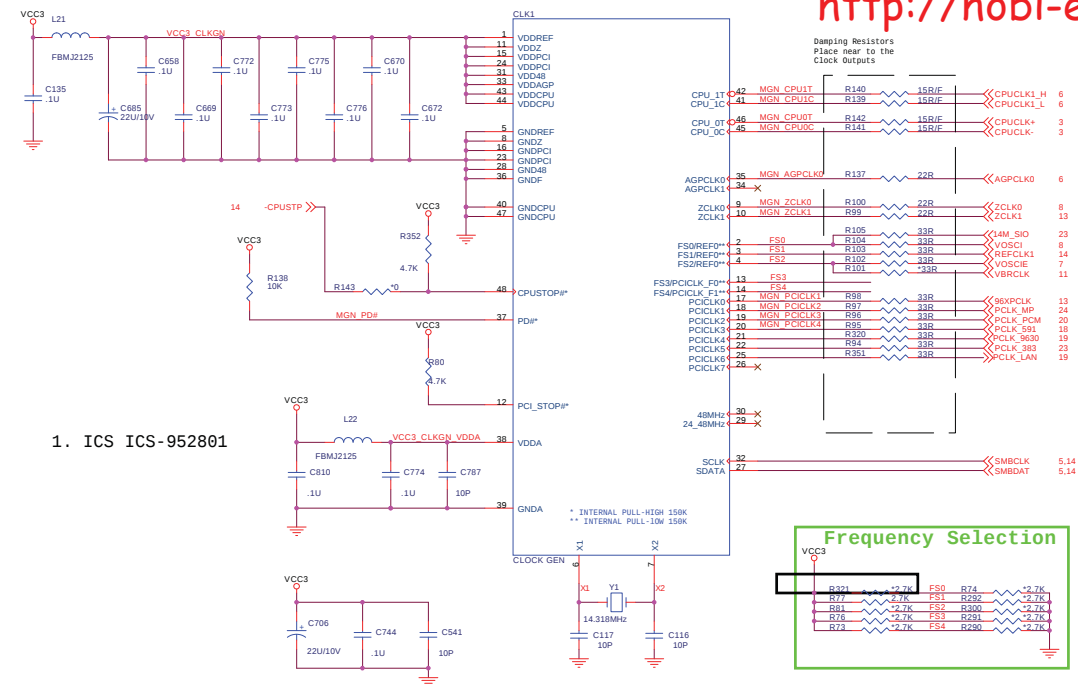
Block Diagram

<http://hobi-elektronika.net>

01



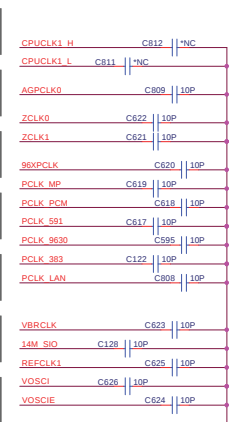
1. ICS ICS-952801



CLK Table for SiS M760 (Not For ICS ICS-952801)

SiS 755/M760 CLOCK										SiS 755/M760 CLOCK									
(FS4)	(FS3)	(FS2)	(FS1)	(FS0)	CPU (MHz)	ZCLK (MHz)	AGPCLK (MHz)	PC1 (MHz)	VCO (MHz)	(FS4)	(FS3)	(FS2)	(FS1)	(FS0)	CPU (MHz)	ZCLK (MHz)	AGPCLK (MHz)	PC1 (MHz)	VCO (MHz)
0	0	0	0	0	200	66.66	66.66	33.33	400	1	0	0	0	0	180	132.14	66.67	33.04	35.75
0	0	0	0	1	200	100	66.66	33.33	400	1	0	0	0	1	180	132.14	66.67	33.04	35.75
0	0	0	1	0	200	133.33	66.66	33.33	400	1	0	0	1	0	190	135.71	67.08	33.93	36.5
0	0	0	1	1	200	166.66	66.66	33.33	1000	1	0	0	1	1	190	135.71	67.08	33.93	36.5
0	0	1	0	0	233	66.66	66.66	33.33	466	1	0	1	0	0	205	136.66	68.33	34.17	37.25
0	0	1	0	1	233	93.2	66.66	33.33	466	1	0	1	0	1	210	140	70	35	38
0	0	1	1	0	233	133.28	66.64	33.33	933	1	0	1	1	0	215	129	64.5	32.25	38
0	0	1	1	1	233	199.8	69.9	33.33	699	1	0	1	1	1	220	132	66	33	38
0	1	0	0	0	266	66.66	66.66	33.33	266	1	1	0	0	0	66.66	66.66	66.66	33.33	33.33
0	1	0	0	1	266	100.4	66.5	33.33	532	1	1	0	0	1	66.66	100	66.66	33.33	33.33
0	1	0	1	0	266	133	66.5	33.33	532	1	1	0	1	0	100	100	66.66	33.33	33.33
0	1	0	1	1	266	159.6	66.5	33.33	798	1	1	0	1	1	100	133.33	66.66	33.33	33.33
0	1	1	0	0	200	133	50	33.33	400	1	1	1	0	0	133	100	66.66	33.33	33.33
0	1	1	0	1	200	114	66.66	33.33	800	1	1	1	0	1	133	133.33	66.66	33.33	33.33
0	1	1	1	0	200	142	66.66	33.33	1000	1	1	1	1	0	166	100	66.66	33.33	33.33
0	1	1	1	1	200	160	66.66	33.33	800	1	1	1	1	1	166	133.33	66.66	33.33	33.33

By-Pass Capacitors
Place near to the Clock Outputs



03

Thermal 14
HERMDA 17
HERMDC 17

● T42
 ● T41

Near Socket754

R317 169 1%
 CPU CLK C629 3900P
 CPU CLK- C607 3900P
 FBCLKOUT- R326 80.6 1% FBCLKOUT

VCC_CORE
 R313 51.1 1% COREFB+
 R310 51.1 1% COREFB-
 +1.2HT
 R299 44.2 1% REF1
 R306 44.2 1% REF0

● T58
 ● T51

● T60
 ● T62

● T77

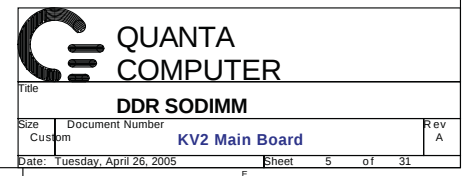
CPU_VCCA

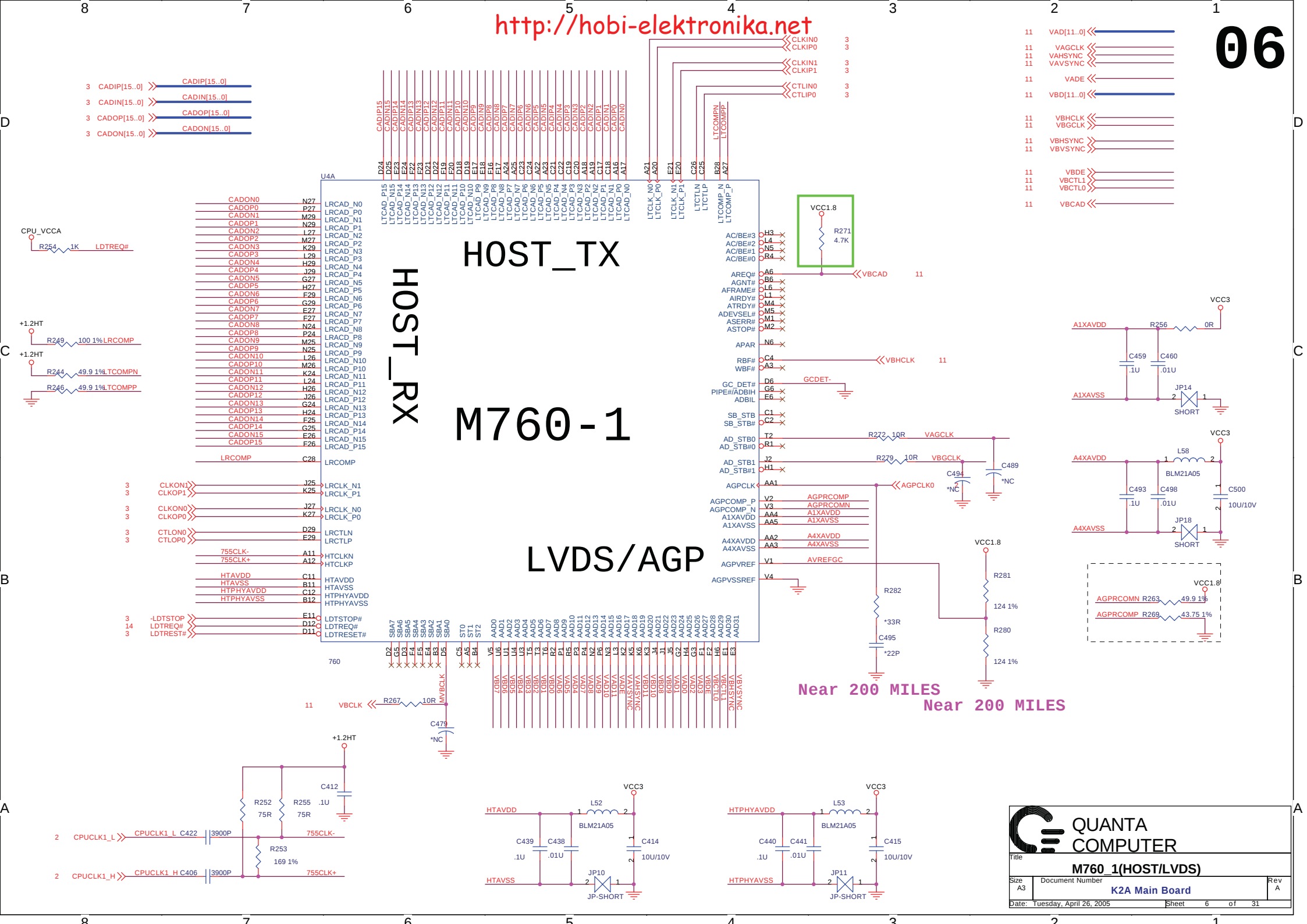
JP19 SHORT PAD

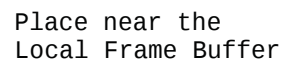
R324 680-0402 LDTREST# 1
 R294 680-0402 LDTSTOP
 R318 680-0402 THERMTRIP
 R323 680-0402 BRN-
 R319 680-0402 SINCHN
 R329 680-0402 DBRDY
 R315 680-0402 DBREQ-
 1 2 TMS
 3 4 TRST-
 5 6 TDI
 7 8
 RP5 680-8P4R
 R316 680-0402 TDO
 2.5VSUS
 R342 51.1 1% DDIO_SENSE

Must shut down when asserted

[illegible]





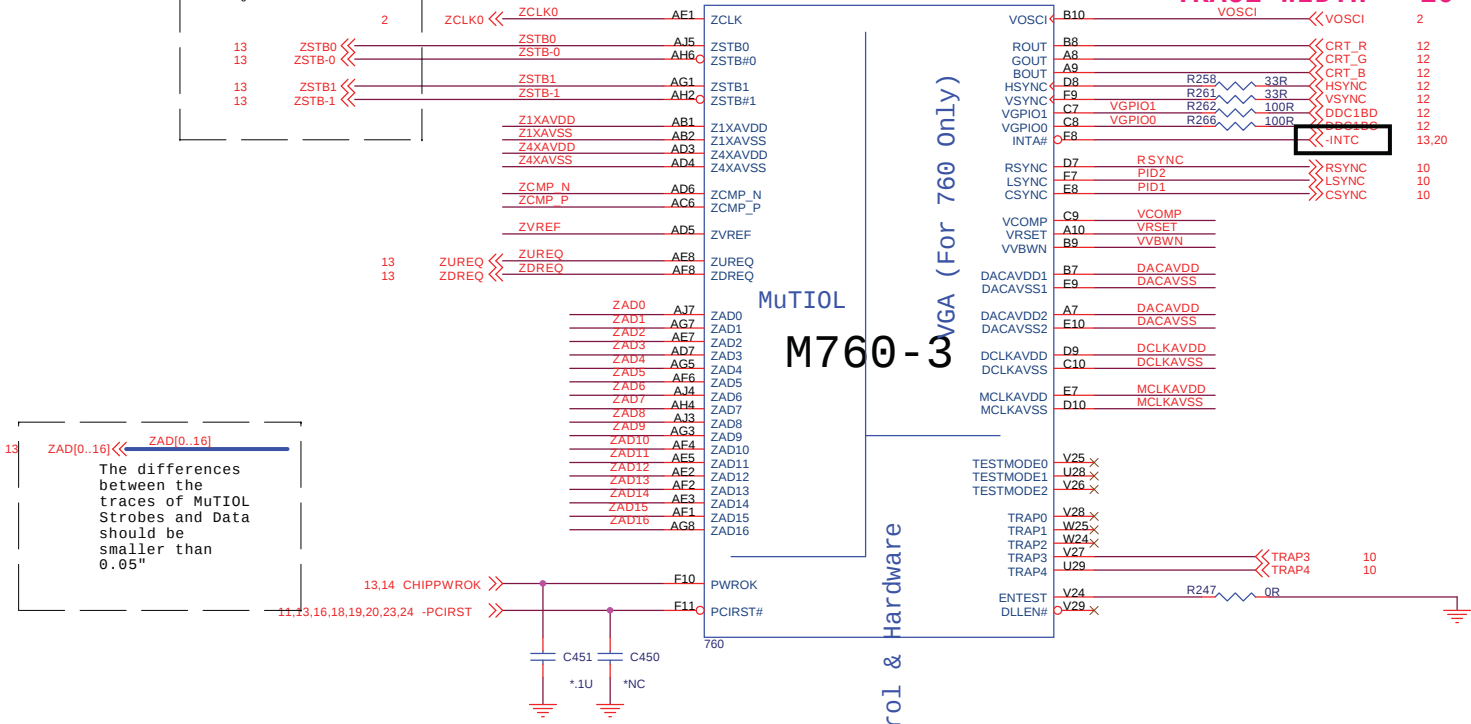


 QUANTA COMPUTER			
Title			
M760_2(Memory For Frame Buffer)			
Size	Document Number		Rev
A3	K2A Main Board		A
Date:	Tuesday, April 26, 2005	Sheet	7 of 31

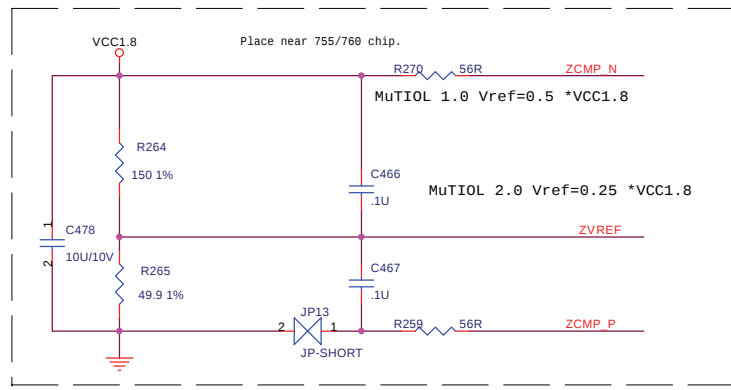
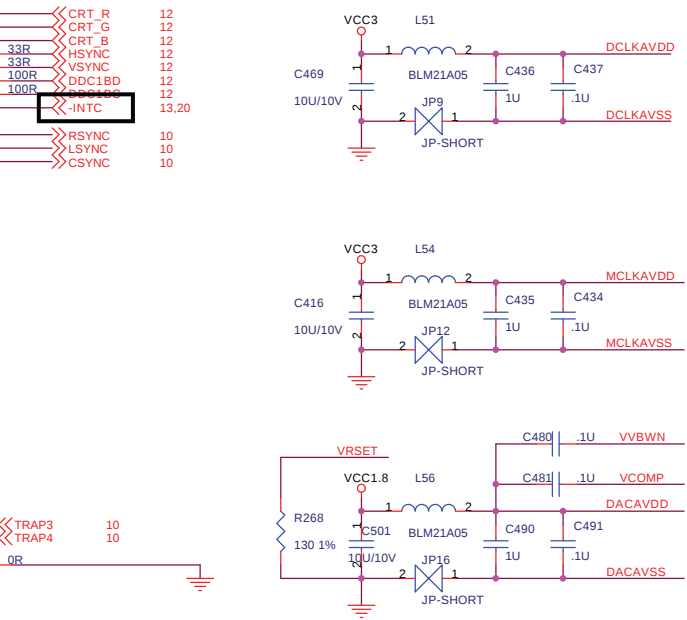
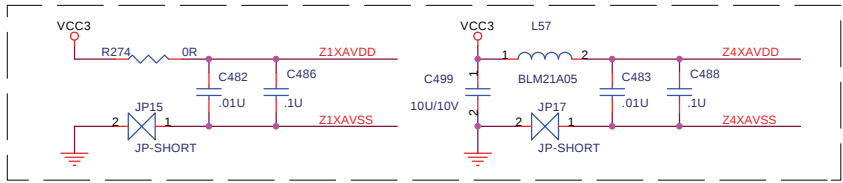
The differences between the traces of MuTIOL Strobes and Data in each group should be smaller than 0.05", and strobes need guide GND trace

Near 200 MILES

TRACE WIDTH = 10~14 MILES



The differences between the traces of MuTIOL Strobes and Data should be smaller than 0.05"



If Support SiS M760LV IVDD=1.5V

M760-4

Power

M760-5

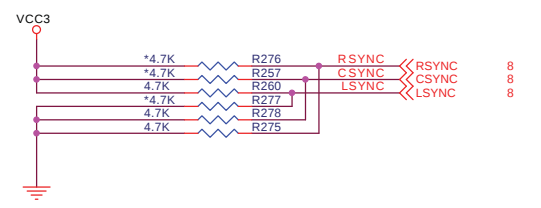
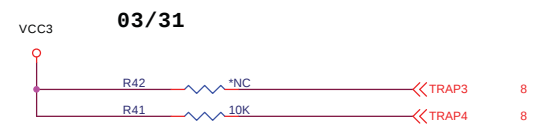
Power

LAYOUT: Place HT bypass caps on
topside near connected Lokar HT link.

Place these capacitors under 760 solder side.

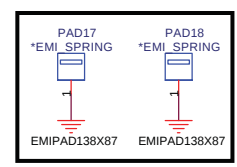
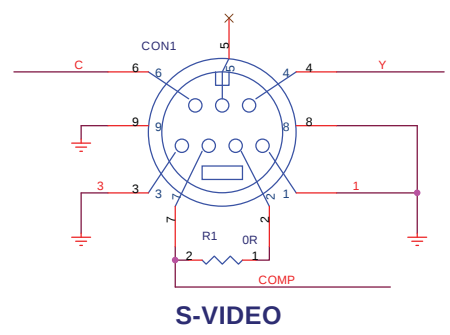
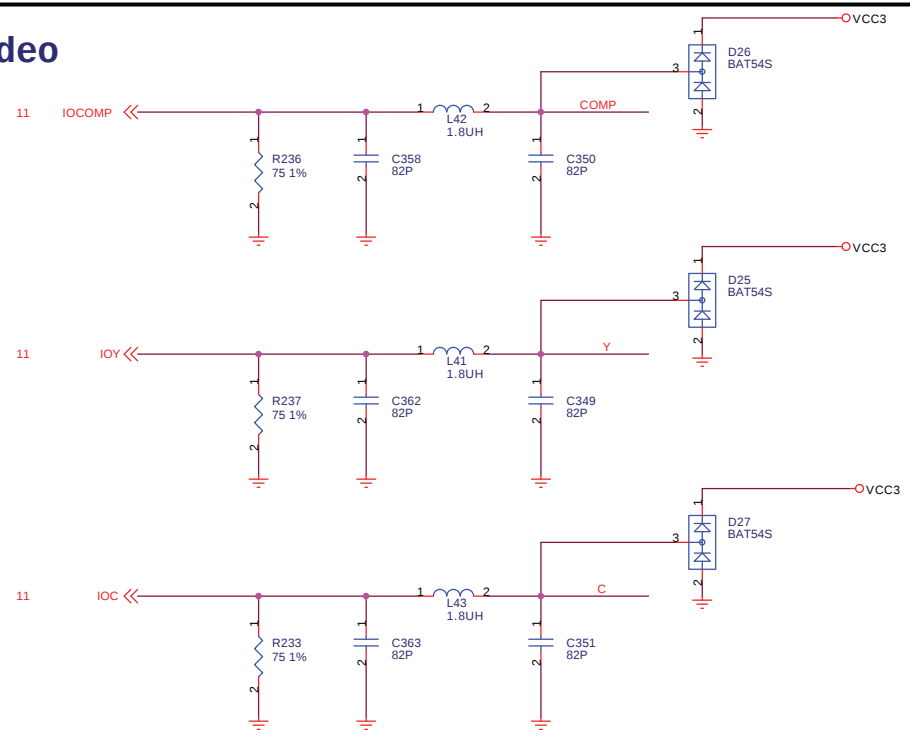


Title		
M760 4(Powers)		
Size	Document Number	Rev
A3	Rev A	
Date: Tuesday, April 26, 2005		
Sheet 9 of 31		



SIS755/760 NB Hardware Trap Table							
	755/760	ON/1	OFF/0	Internal	Default		
DLEN#	CPUCLK/ZCLK/AGPCLK PLL/DLL Circuit Enable	DISABLE	ENABLE	Pull Low	Low/0		
TESTMODE0				Pull Low			
TESTMODE1	755 Test Mode Selection	1: Mode1	0: Mode0	Pull Low			
TESTMODE2	755 Test Mode Enable	ENABLE	DISABLE	Pull Low	Low/0		
TRAP0	MuTIOL Version 1 , 2	Ver. 1	Ver. 2	Pull Low	Low/0		
TRAP1	MuTIOL DBI Mode	DISABLE	ENABLE	Pull Low	Low/0		
TRAP2	MuTIOLInitial Packet Series Mode	DISABLE	ENABLE	Pull Low	Low/0		
TRAP3	Reserve			Pull Low	High/1		
TRAP4	Reserve			Pull Low	High/1		

S-video





QUANTA
COMPUTER

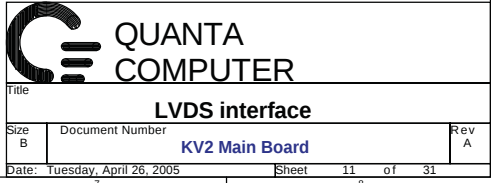
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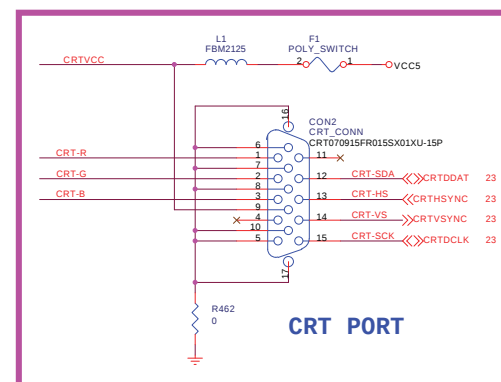
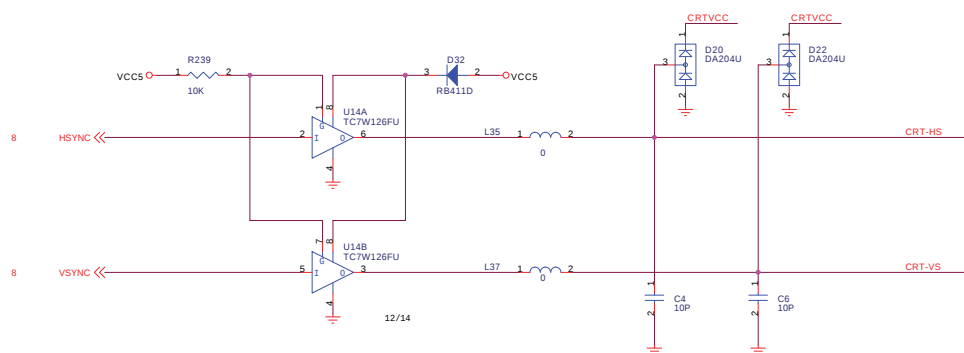
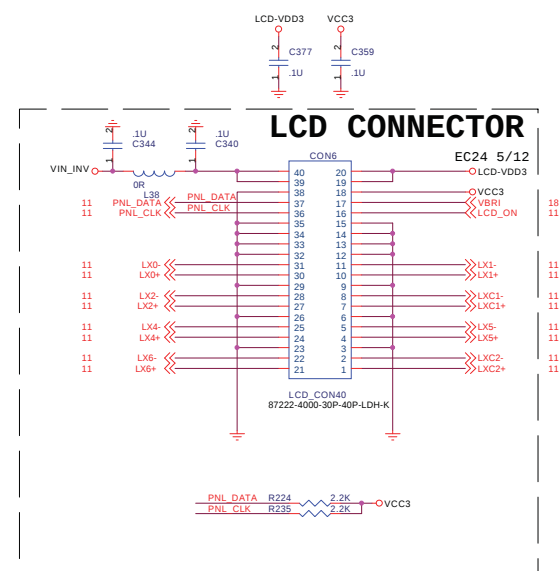
Hardware Trap & TV-OUT

Size B	Document Number	Rev A
	KV2 Main Board	

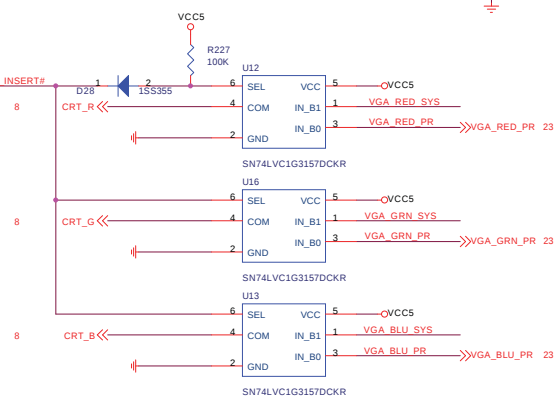
Date: Tuesday, April 26, 2005

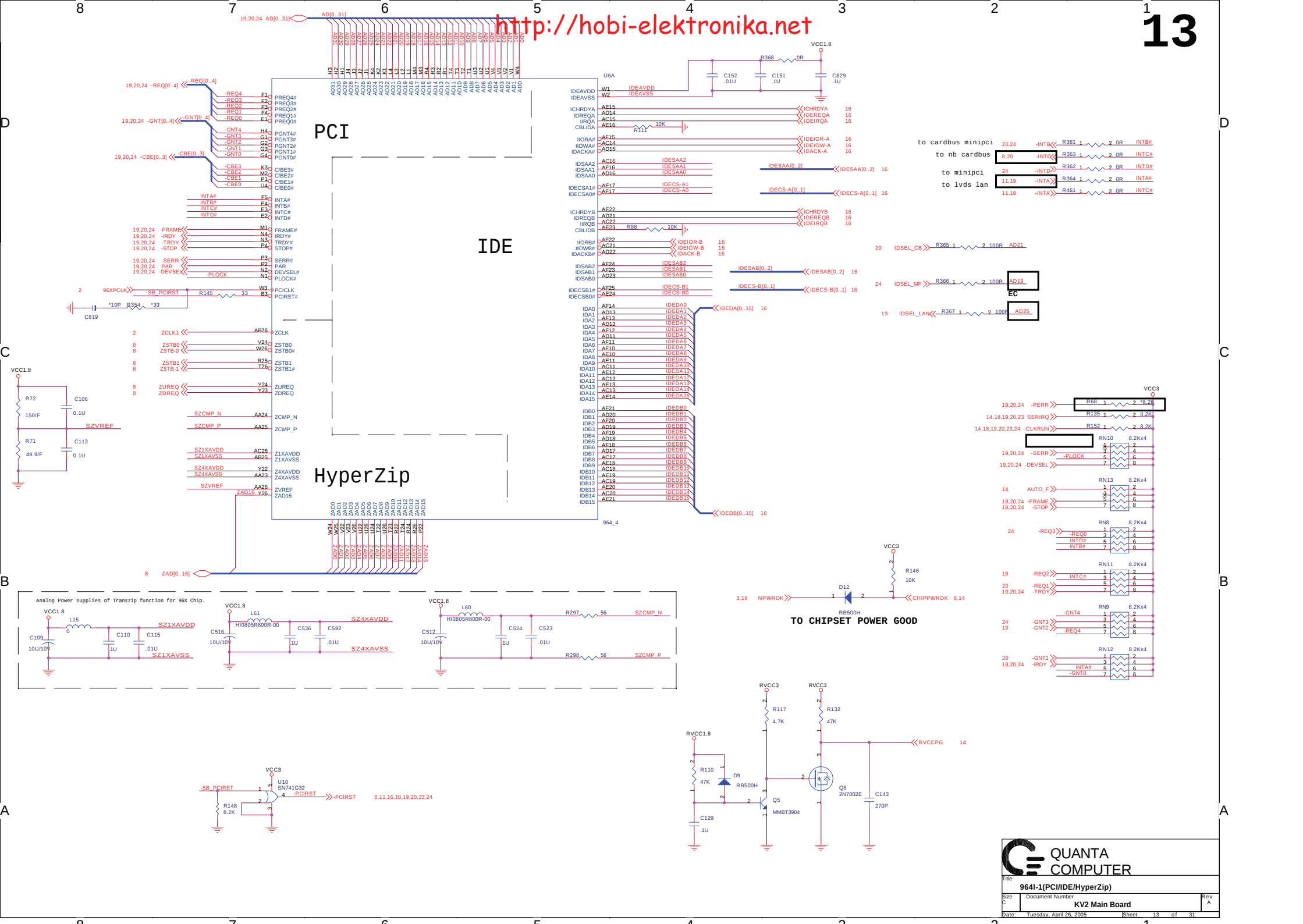
Sheet 10 of 31





SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1





Programable on-die pull-high strength for CPU_S:
(Infinite, 150, 110, 50 Ohm)

Near 200 MILES

<http://hobi-elektronika.net>

14

D

D

C

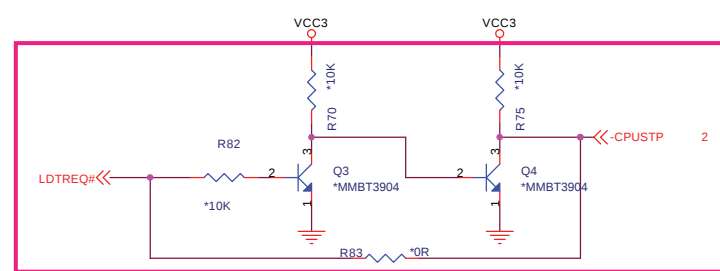
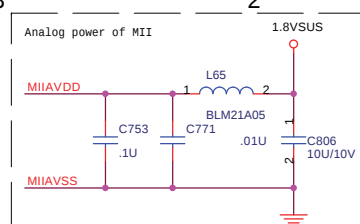
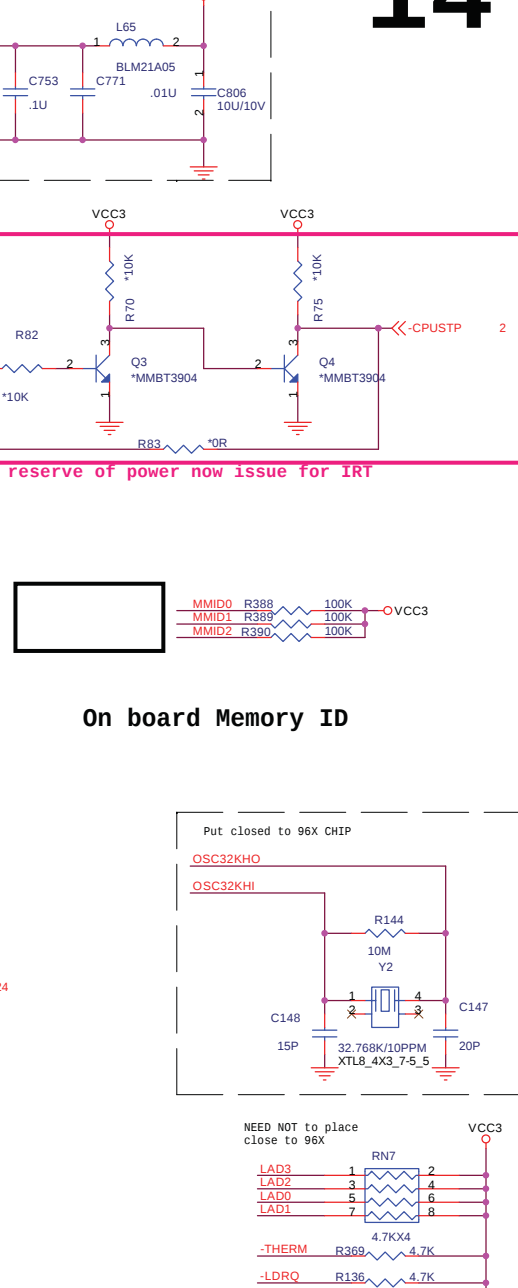
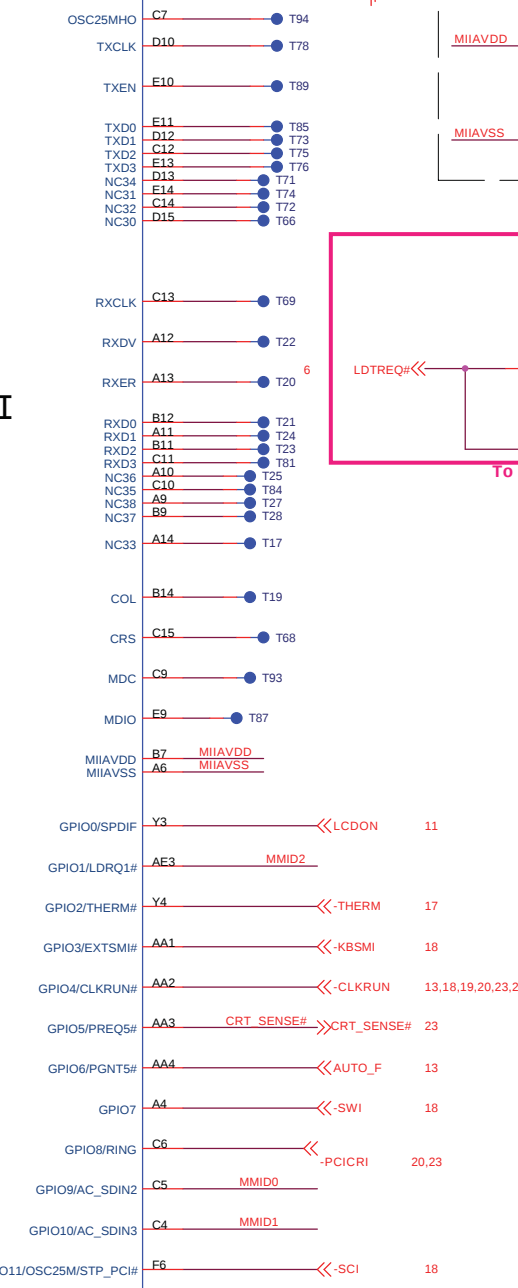
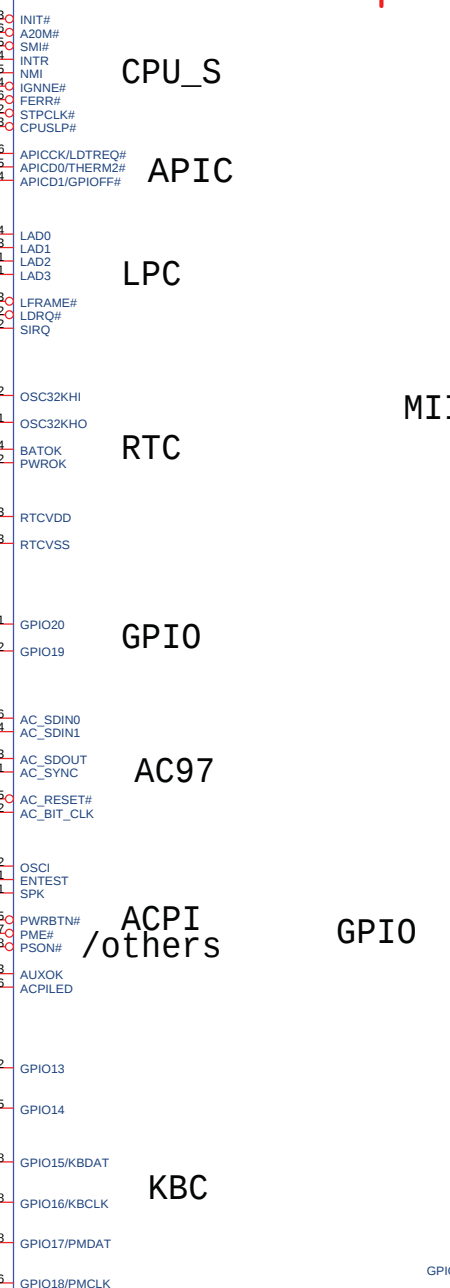
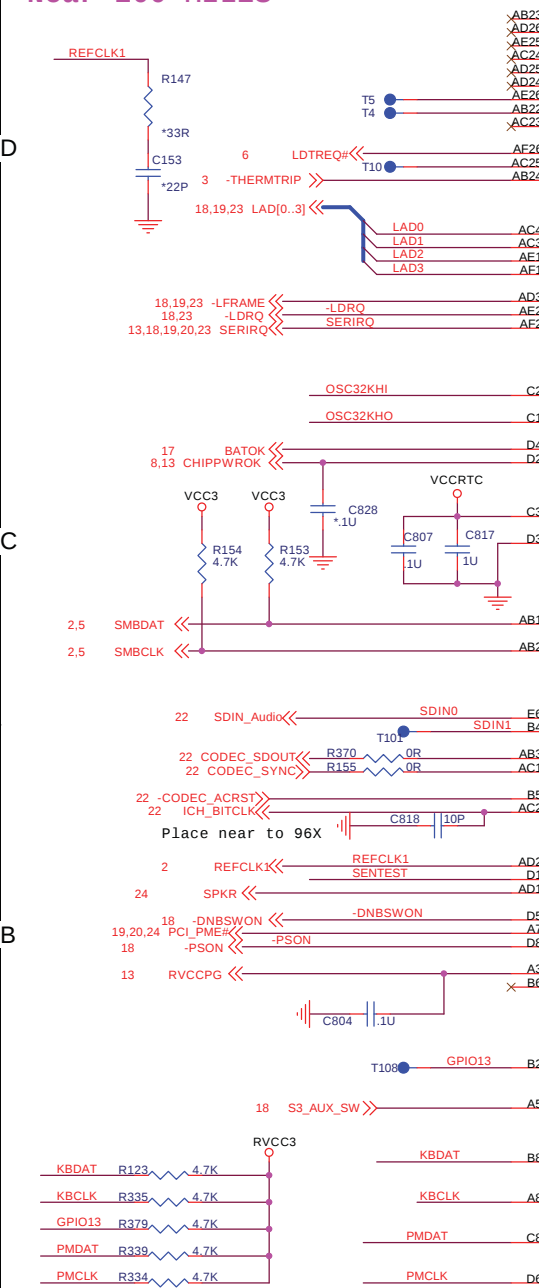
C

B

B

A

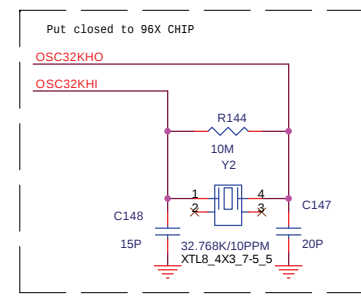
A



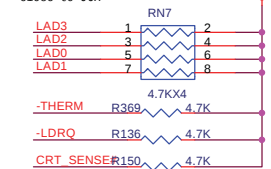
To reserve of power now issue for IRT



On board Memory ID



NEED NOT to place close to 96X



File

9641-2(LPC/MII/GPIO)

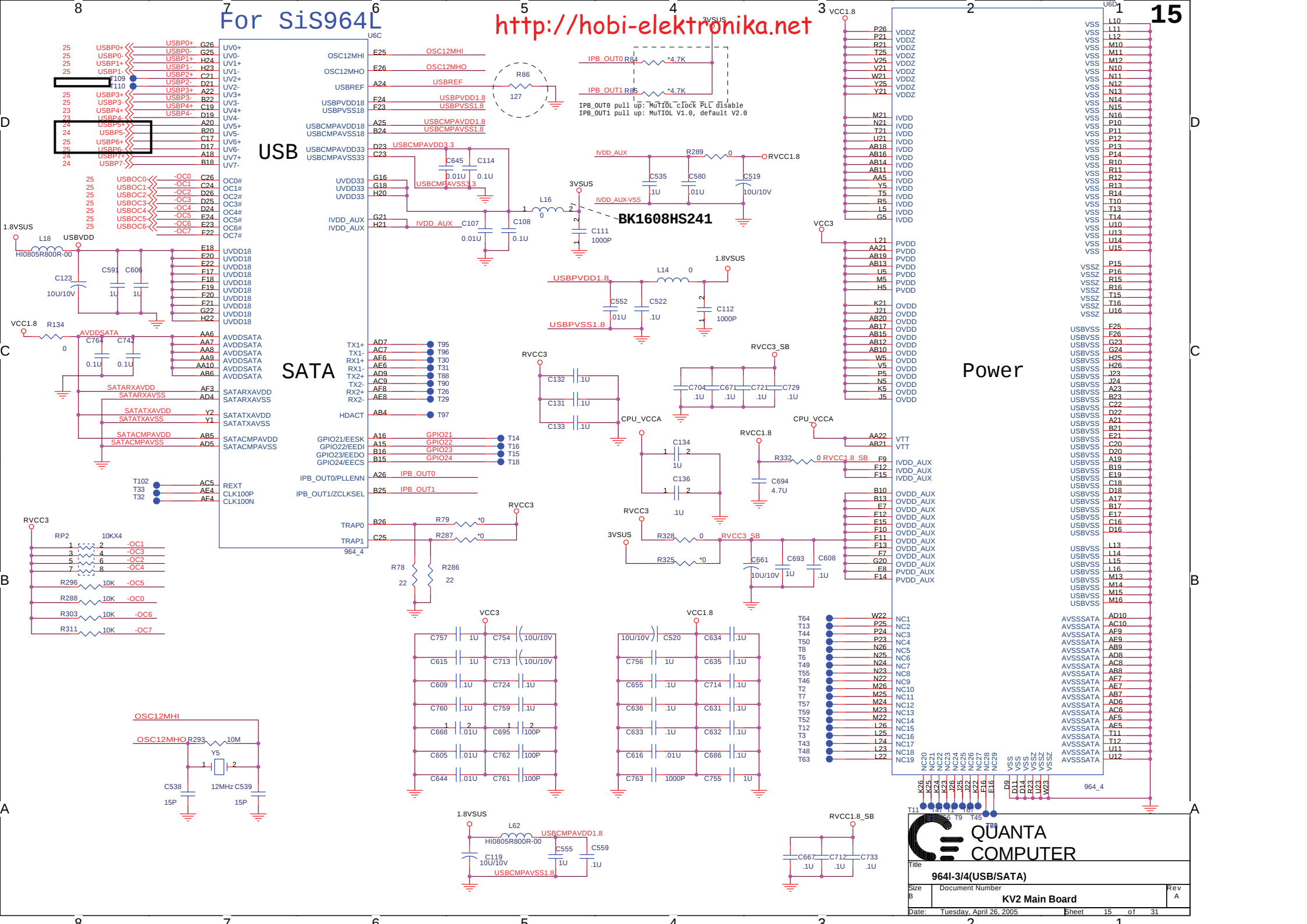
Size B

Document Number

Rev A

Date: Tuesday, April 26, 2005

Sheet 14 of 31



QUANTA COMPUTER

Title: 9641-3/4(USB/SATA)

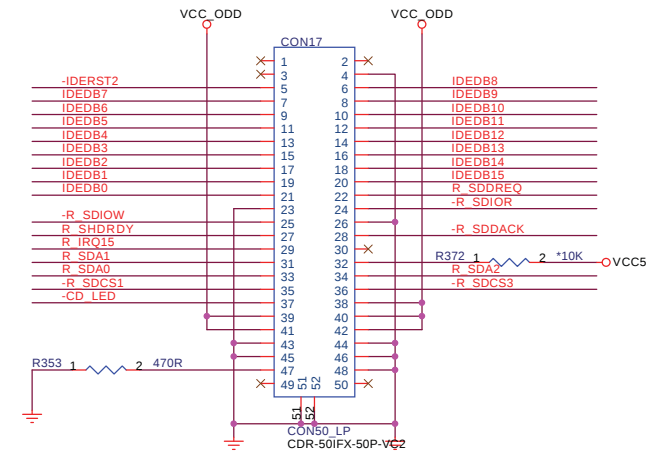
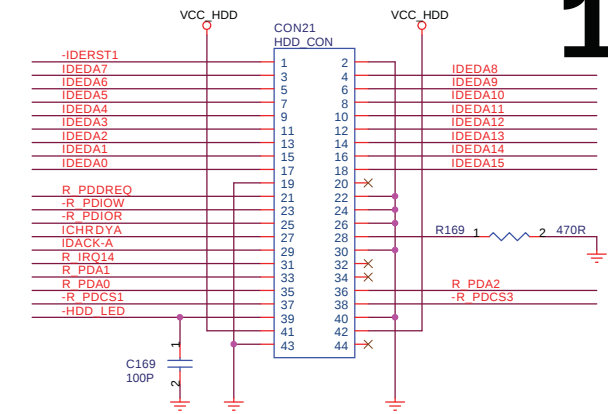
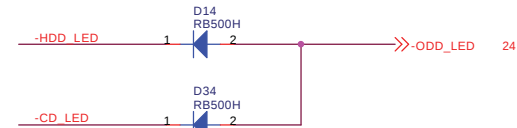
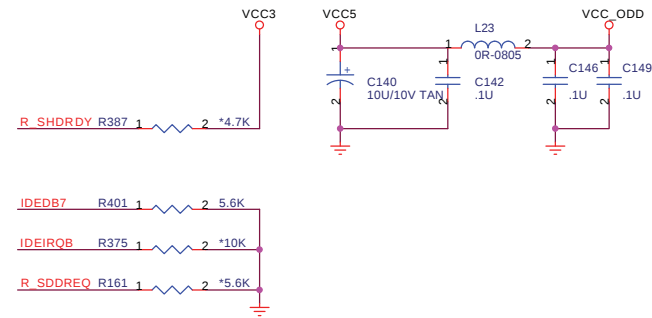
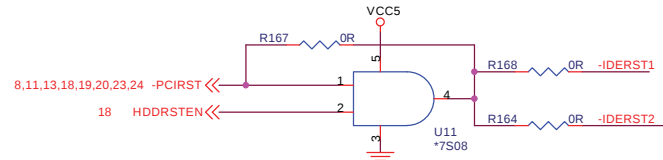
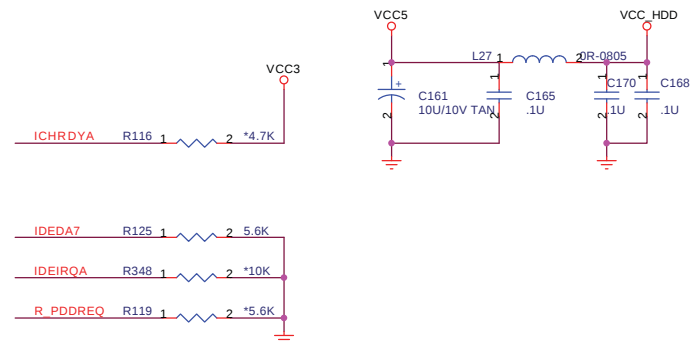
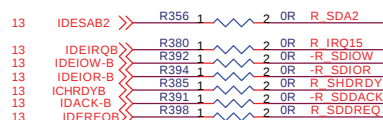
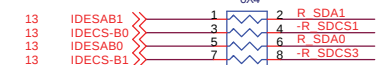
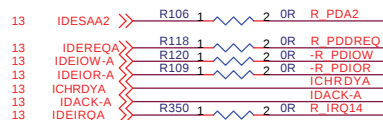
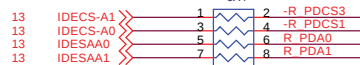
Size: B

Document Number: KV2 Main Board

Date: Tuesday, April 26, 2005

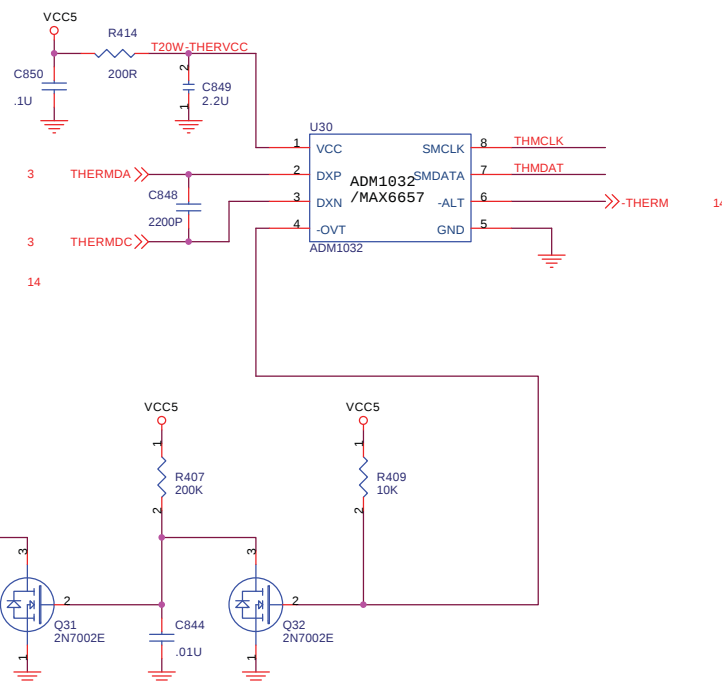
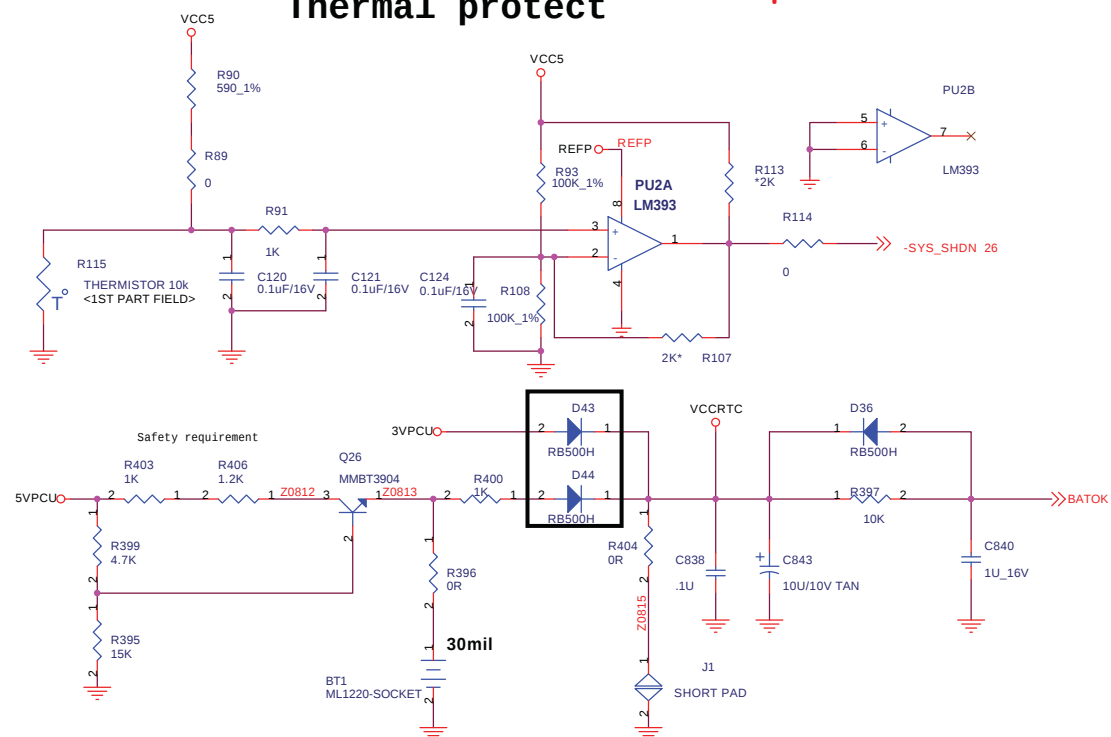
Sheet: 15 of 31

Rev: A

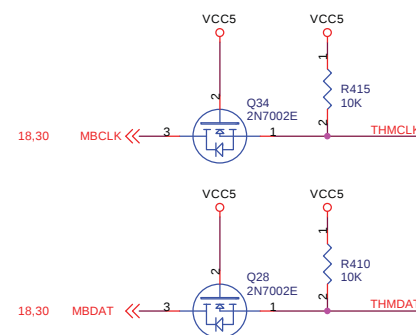
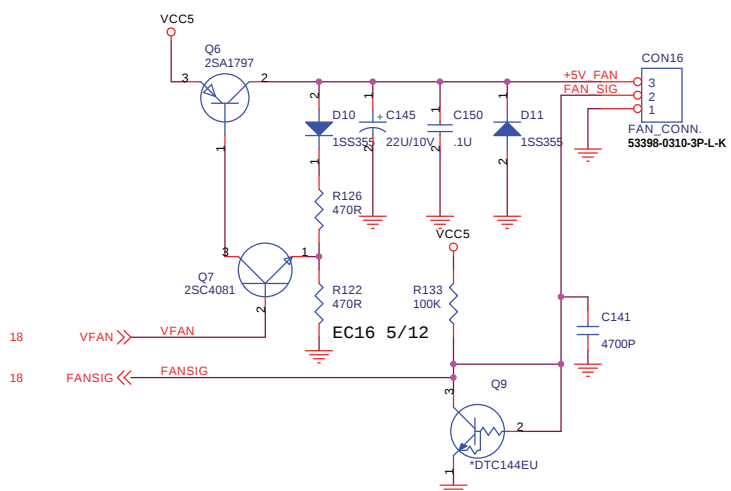


Thermal protect

Thermal Sensor



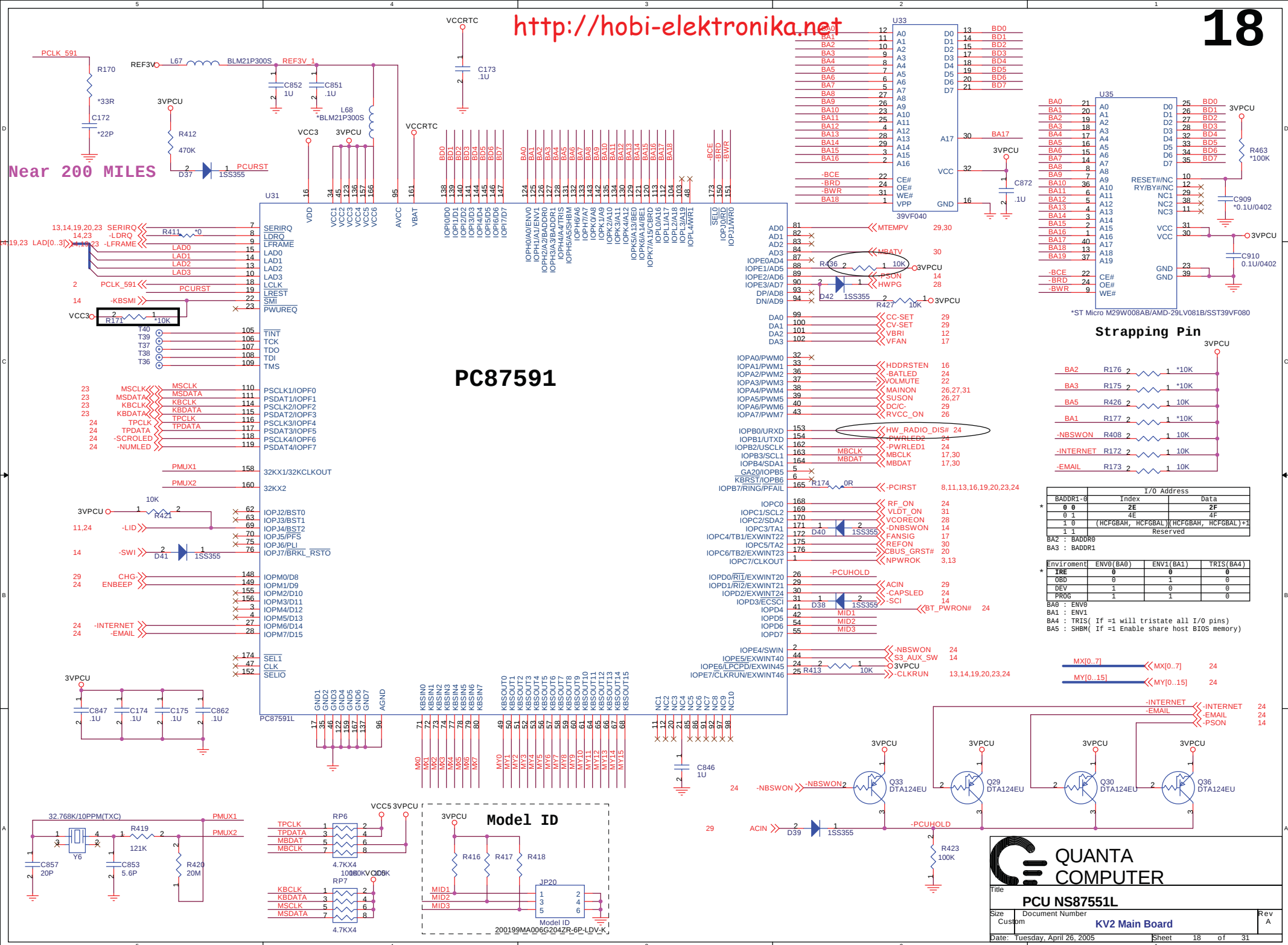
Fan



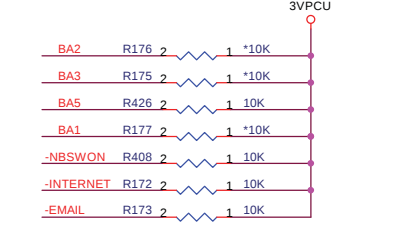
QUANTA
COMPUTER

Near 200 MILES

PC87591



Strapping Pin



I/O Address		Data	
BADDR1-0	Index	2E	2F
0 0	4E	4F	
1 0	(HCFGBAH, HCFGBAL)	(HCFGBAH, HCFGBAL)+1	
1 1	Reserved		

Environment	ENV0(BA0)	ENV1(BA1)	TRIS(BA4)
IRE	0	0	0
OB0	0	1	0
DEV	1	0	0
PROG	1	1	0

BA0 : ENV0
BA1 : ENV1
BA4 : TRIS(If =1 will tristate all I/O pins)
BA5 : SHBM(If =1 Enable share host BIOS memory)

QUANTA COMPUTER

File: **PCU NS87551L**

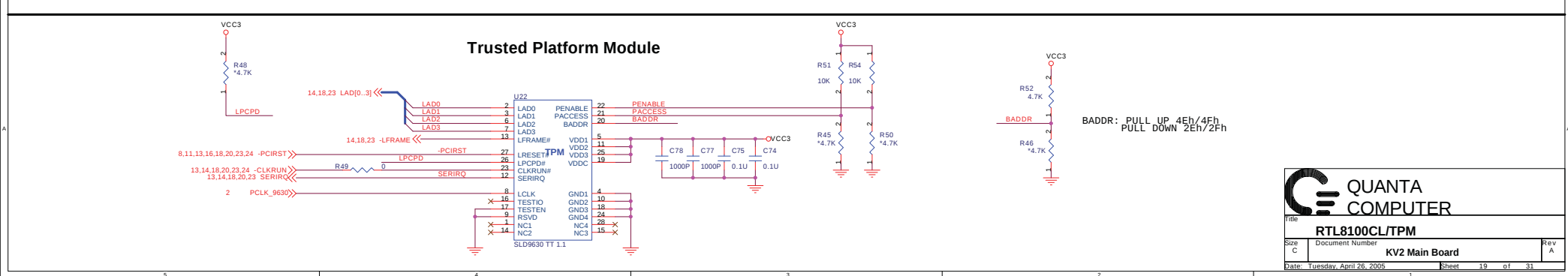
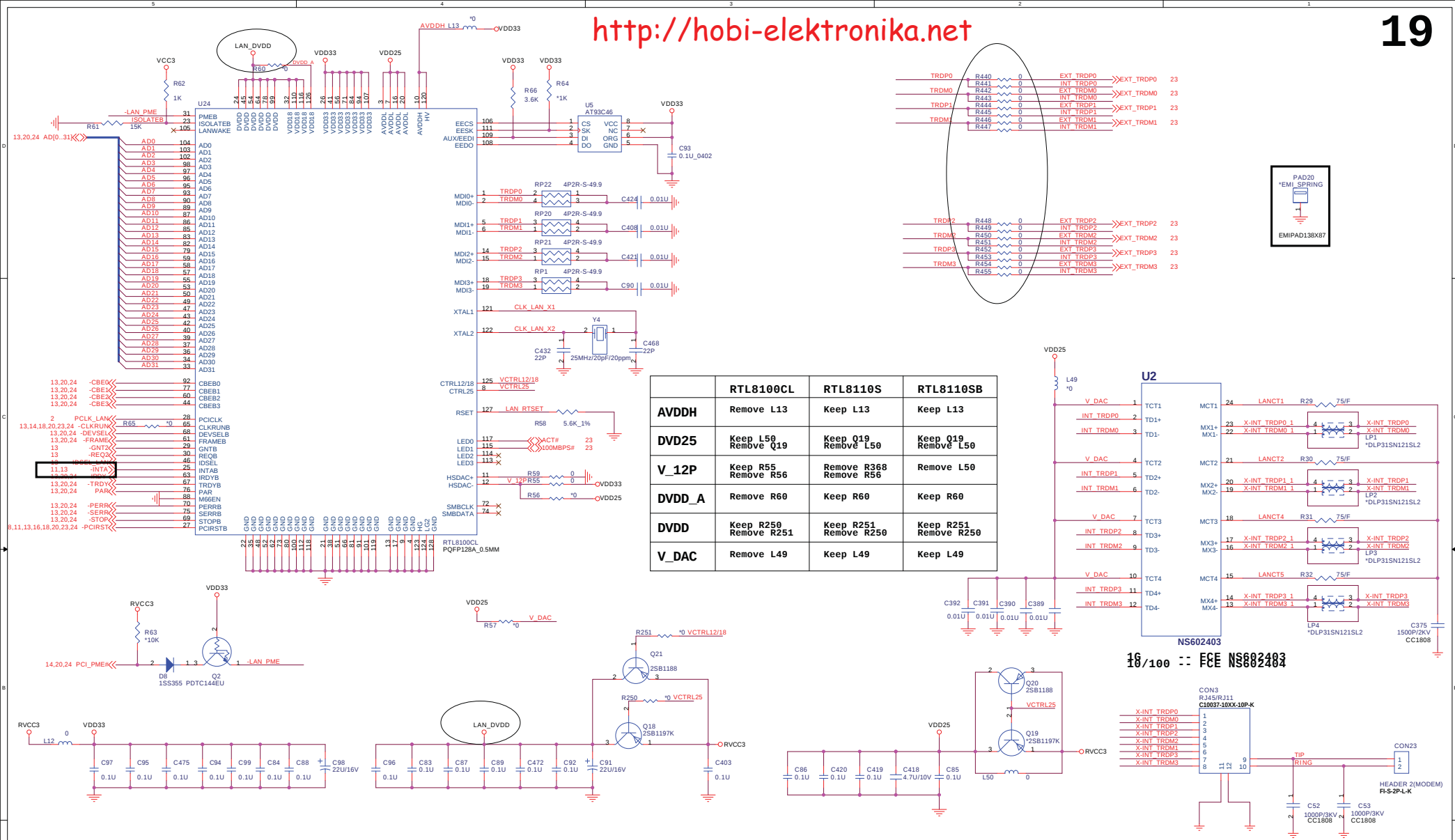
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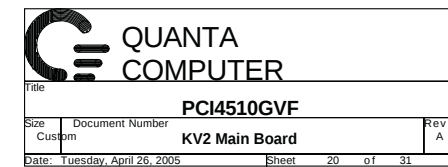
Customer: **KV2 Main Board**

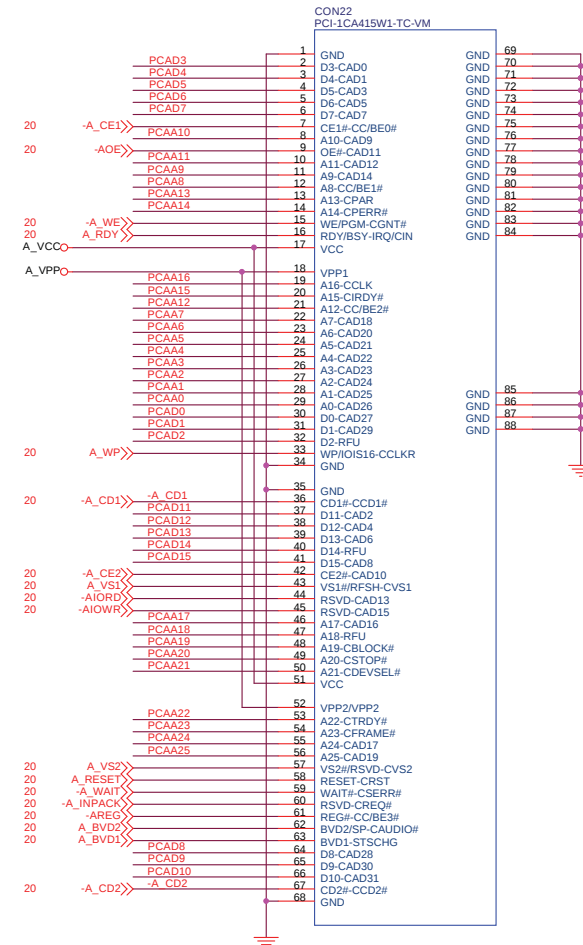
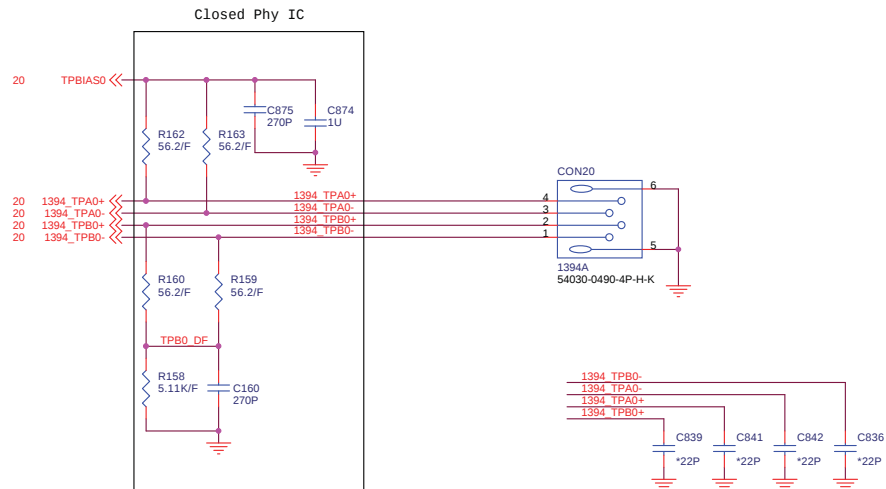
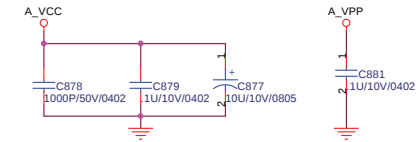
Date: Tuesday, April 26, 2005

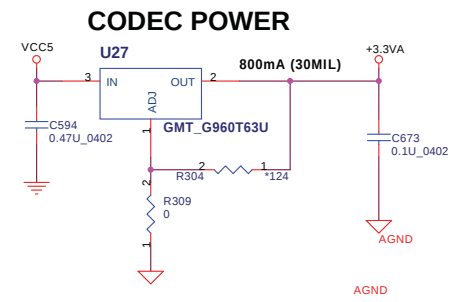
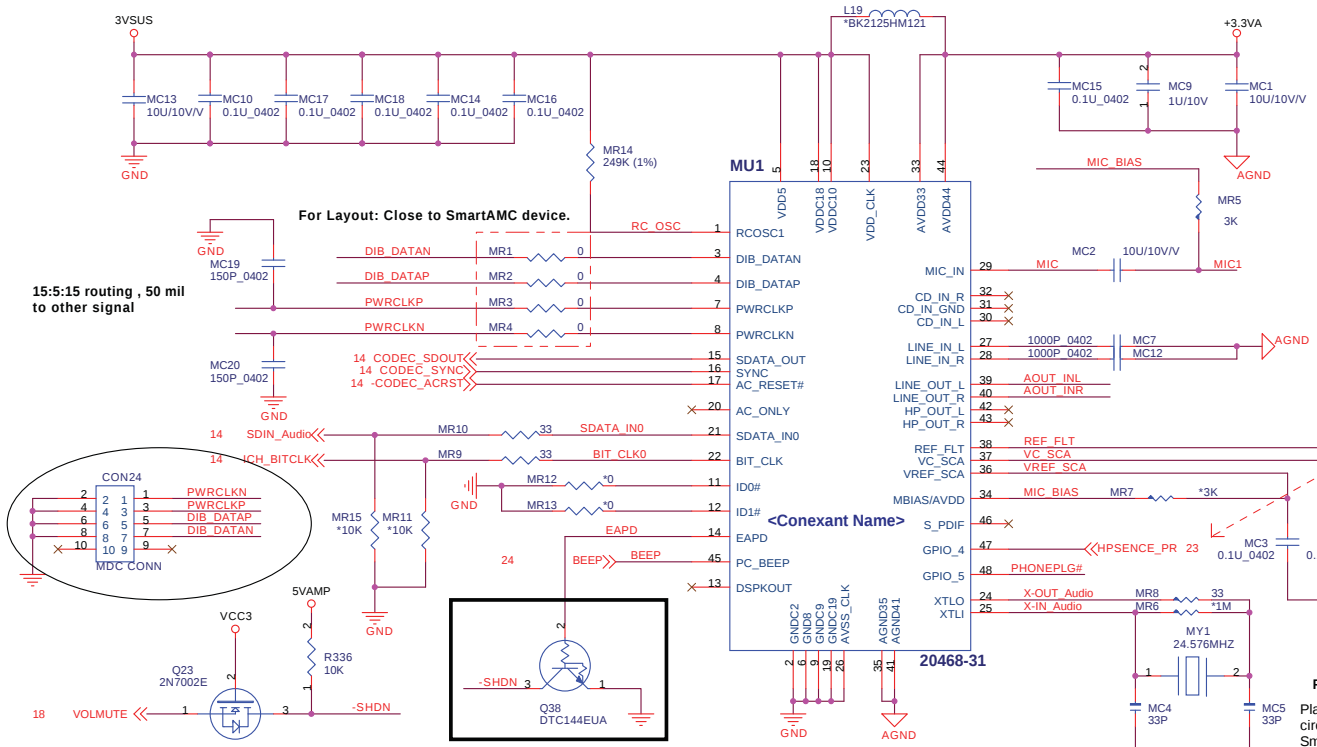
Sheet 18 of 31

Rev A









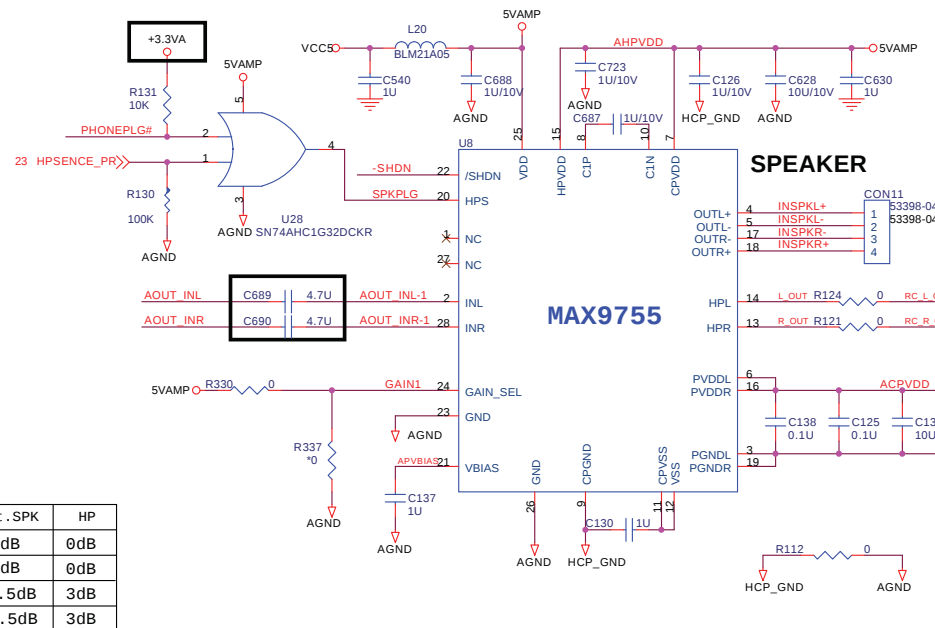
SOFTWARE EQ CONTROL TABLE

GPIO4	GOIO5	S/W E
LOW	LOW	ON
LOW	HIGH	OFF
HIGH	LOW	OFF
HIGH	HIGH	OFF

SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

For Layout:
Place crystal and associated
circuitry very near
SmartAMC Device.

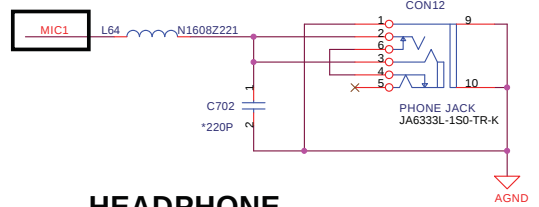
AUDIO AMPLIFIER



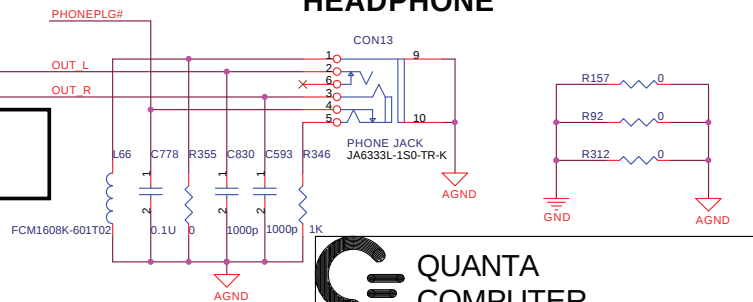
SPEAKER



MIC



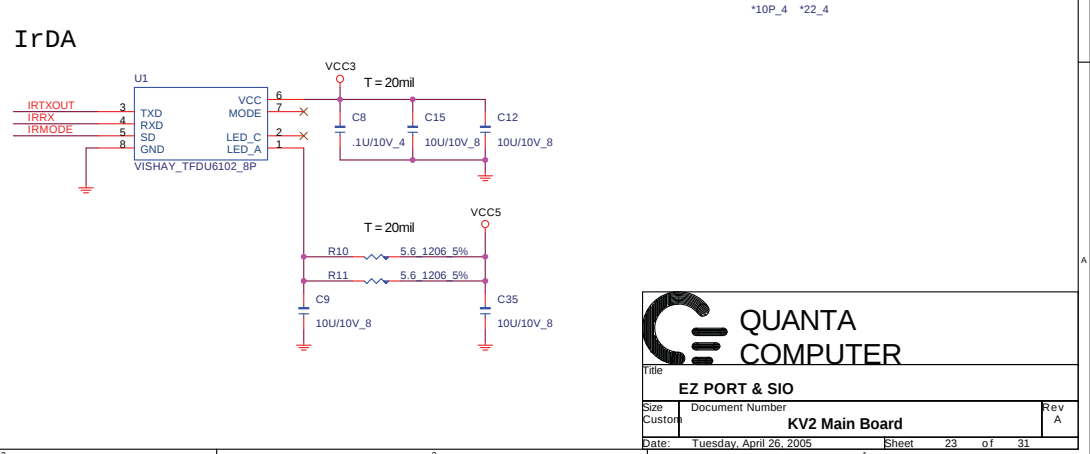
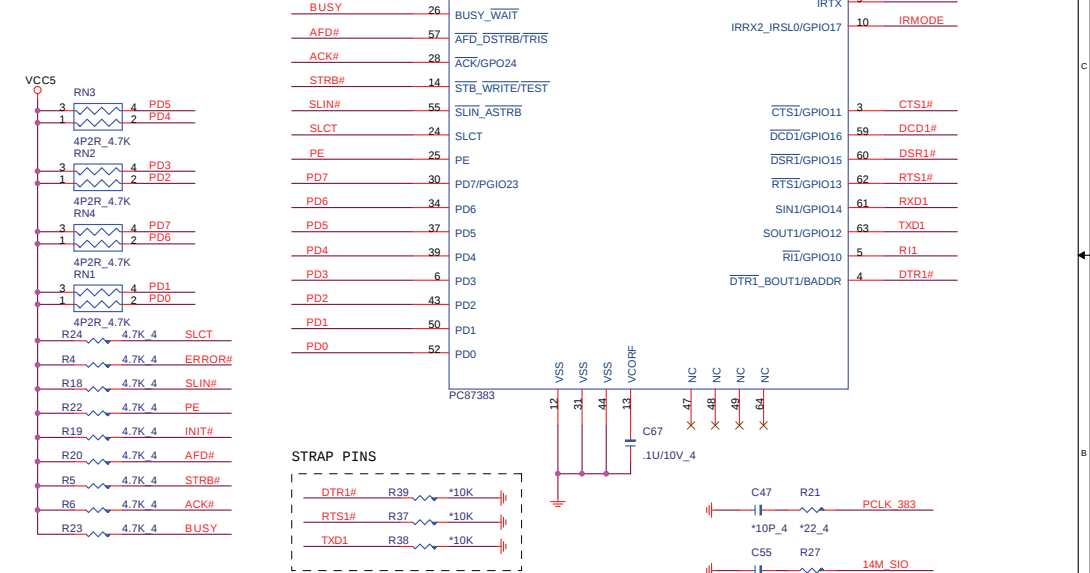
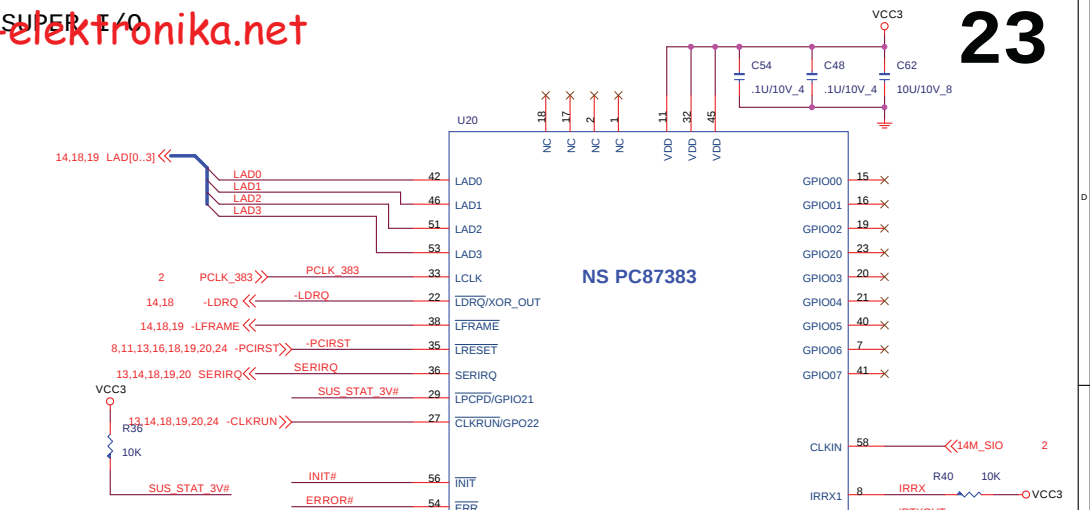
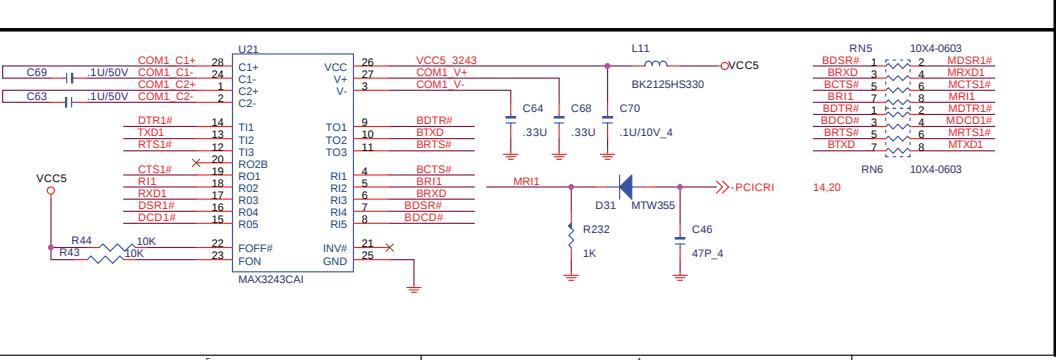
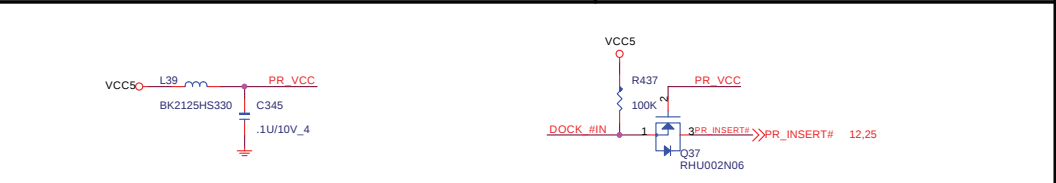
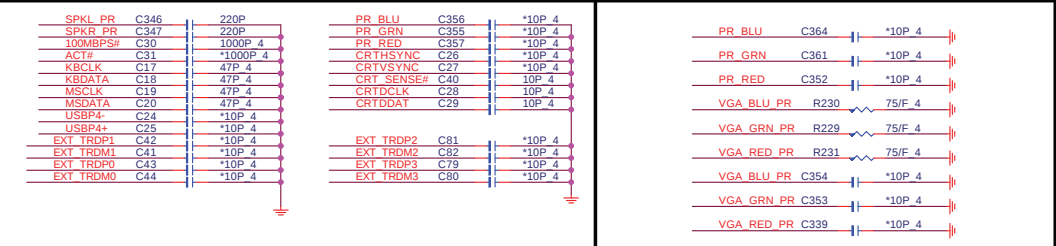
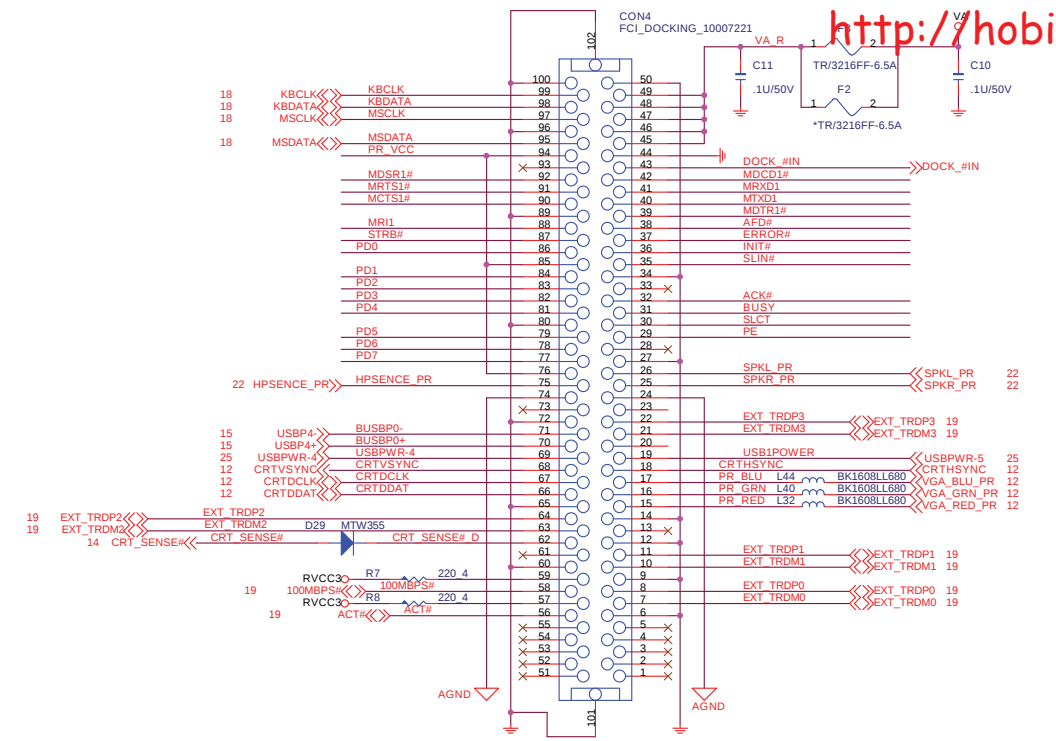
HEADPHONE

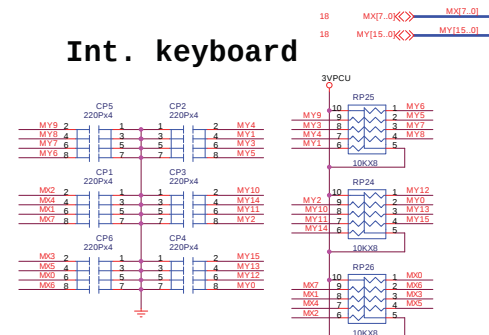
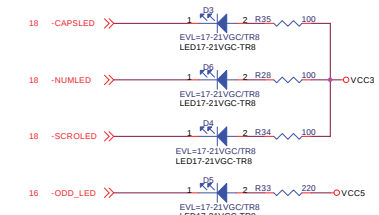


QUANTA
COMPUTER

Title				SmartAMC&			
Size		Document Number					Rev
Custom		KV2 Main Board					A
Date: Tuesday, April 26, 2005				Sheet 22 of 31			

GAIN1	GAIN2	Int.SPK	HP
0	0	6dB	0dB
0	1	9dB	0dB
1	0	7.5dB	3dB
1	1	10.5dB	3dB





NORMAL : LOW

20 -PCMSPK >>> 1

14 SPKR >>> 2

U29 75H86

VCC3

C185 0.1U_0402

R402 2.2K

Q07 DTCL44EUA

Q10 MMB7904

BEEP A

BEEP B

C845 .47U

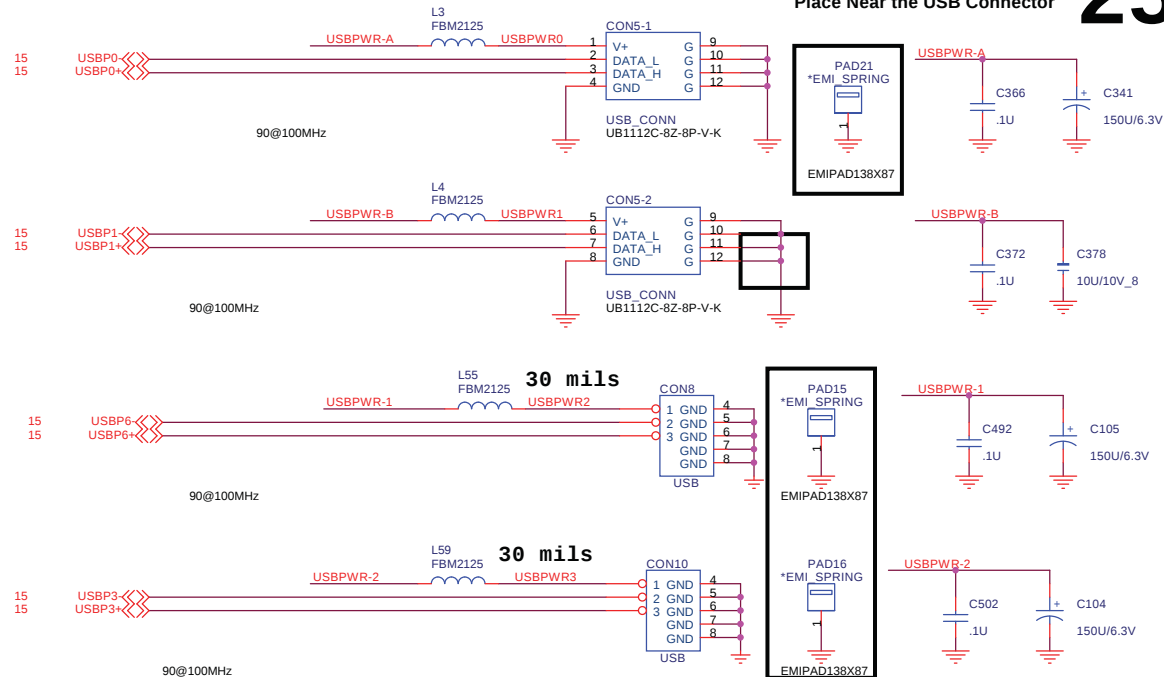
C187 1000P_0402

R405 1K

Ground Tie

18 ENBEEP >>>

25



TH2
H-C157D126P2

TH8
H-C315D177P2-KH1

TH11
H-C315D177P2-KH1

TH12
H-C315D177P2-KH1

TH9
H-C315D177P2-KH1

TH18
H-C157D142P2

TH16
H-C157D110P2-KH1

TH5
H-C157D110P2-KH1

TH14
H-C157D110P2-KH1

TH19
H-C157D110P2-KH1

TH6
H-C315D118P2

TH1
H-C157D126P2

TH4
H-C157D110P2

TH3
H-C157D110P2-KH1

TH10
H-C197D138P2

TH7
H-C315D118P2

TH13
H-C157D110P2-KH1

TH17
H-C157D110P2-KH1

TH15
H-C157D110P2

TH20
H-TC7BC5D3_7D4_7P2

TH21
H-TC276BC197D1H-TC7BC5D3_7D4_7P24AP2

PAD11
*EMI_SPRING

PAD5
*EMI_SPRING

PAD7
*EMI_SPRING

PAD6
*EMI_SPRING

PAD4
*EMI_SPRING

PAD8
*EMI_SPRING

PAD1
*EMI_SPRING

PAD3
*EMI_SPRING

PAD2
*EMI_SPRING

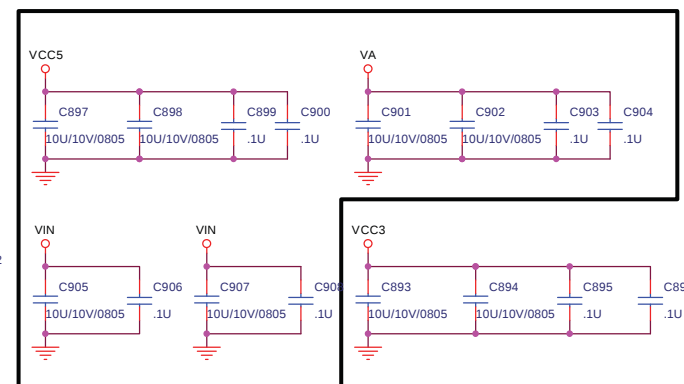
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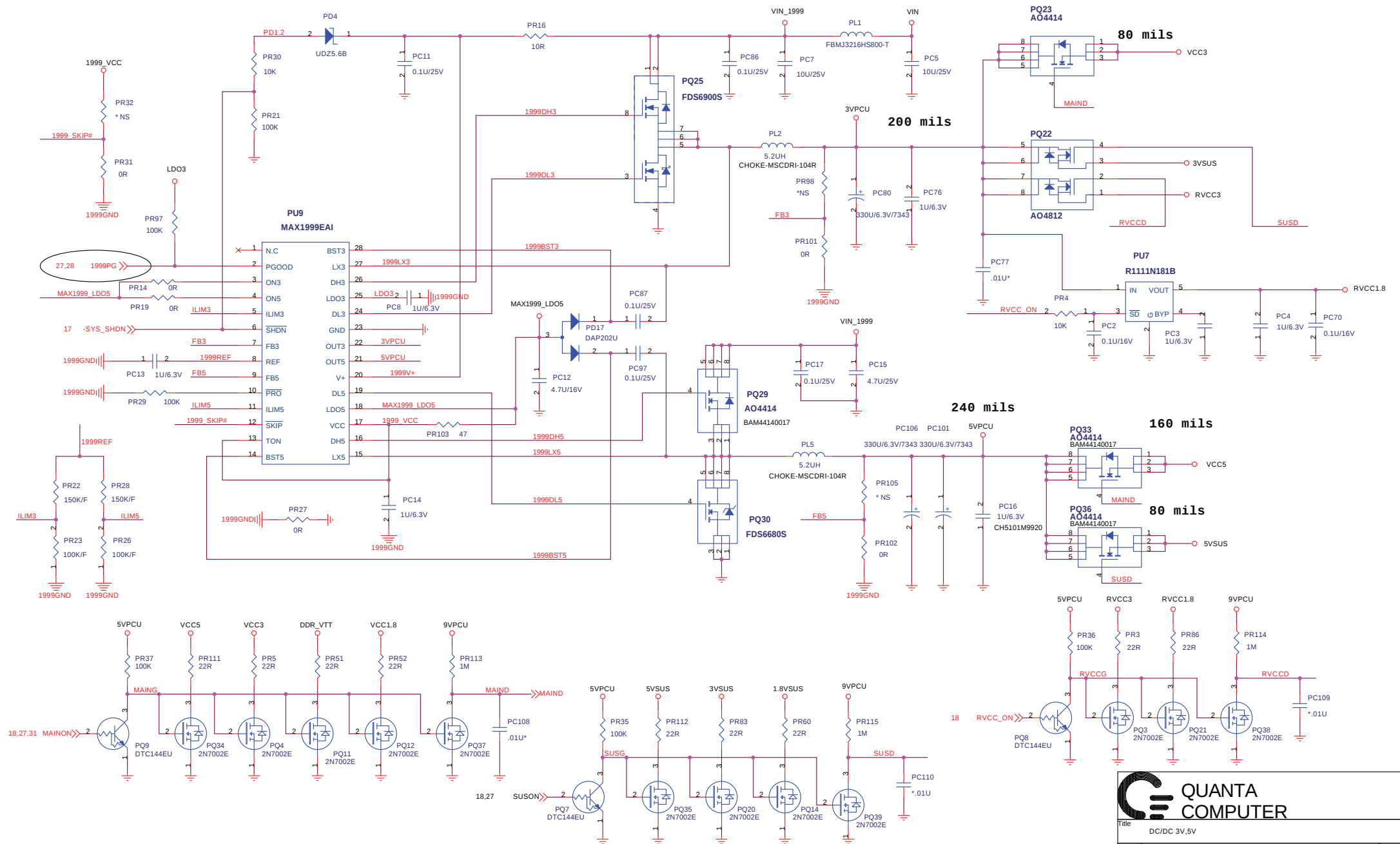
PAD9
*EMI_SPRING

PAD12
*EMI_SPRING

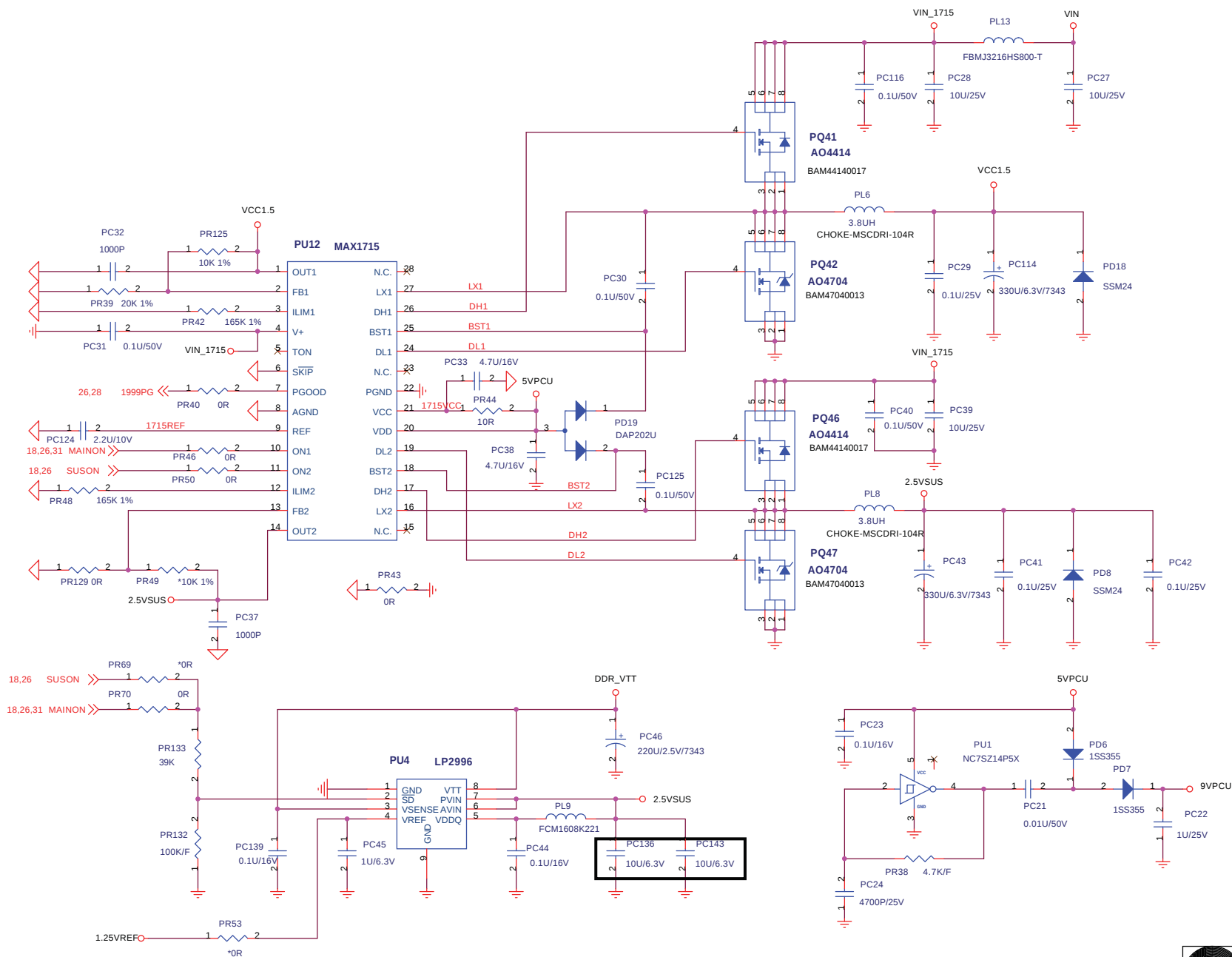
PAD13
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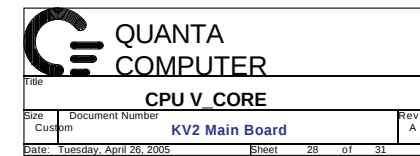
PAD14
*EMI_SPRING



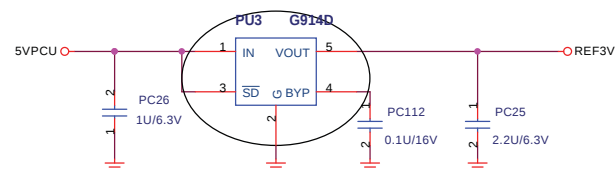
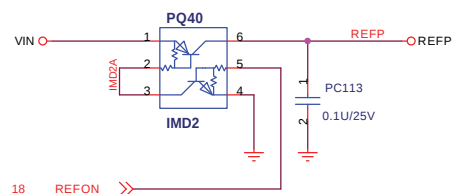
**QUANTA
COMPUTER**

Title			DC/DC 3V,5V
Size	Document Number	Rev 1A	
KV2 Main Board			
Date:	Tuesday, April 26, 2005	Sheet	26 of 31









+1.2HT & VCC1.8V & 1.8VSUS

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31

