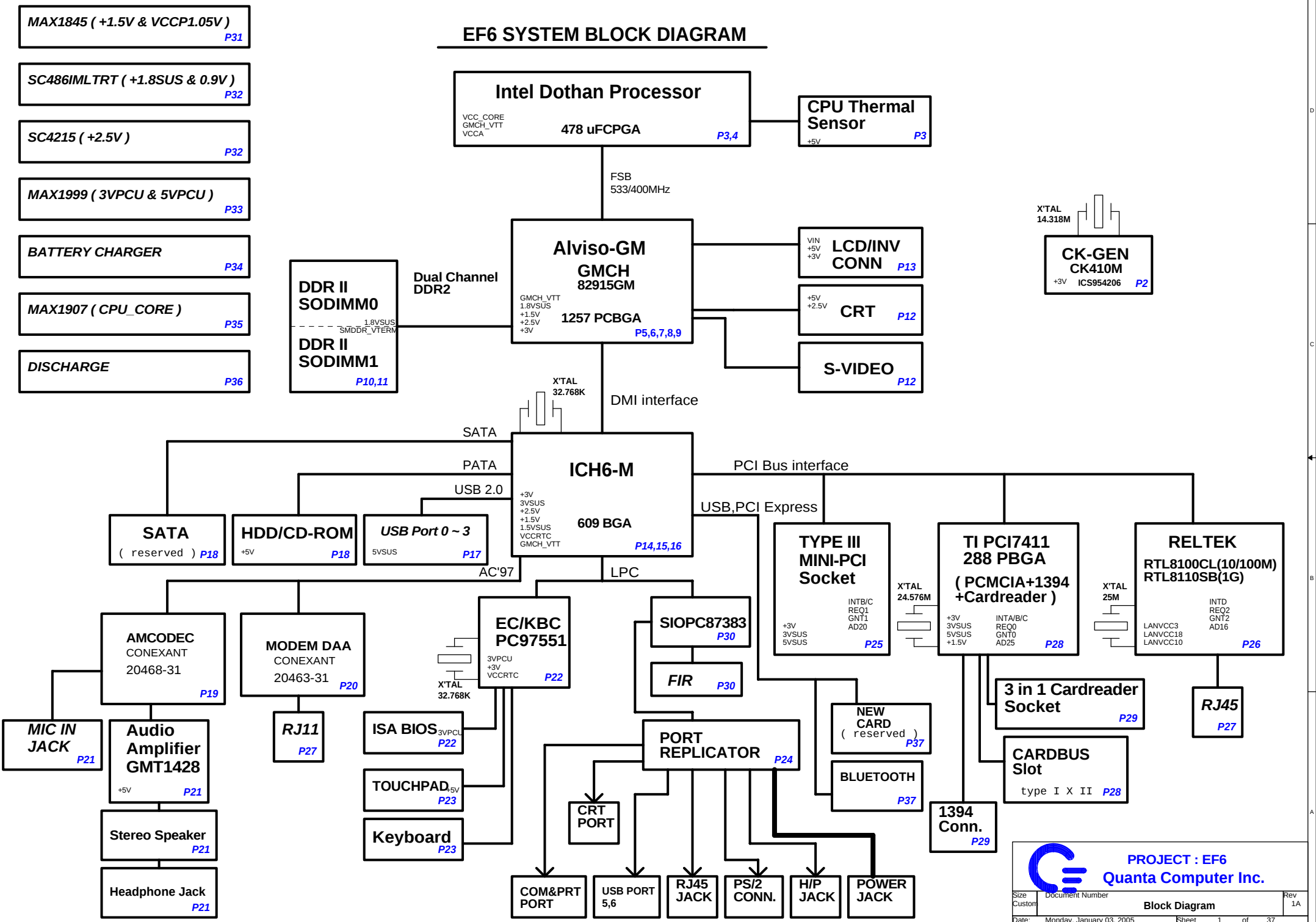
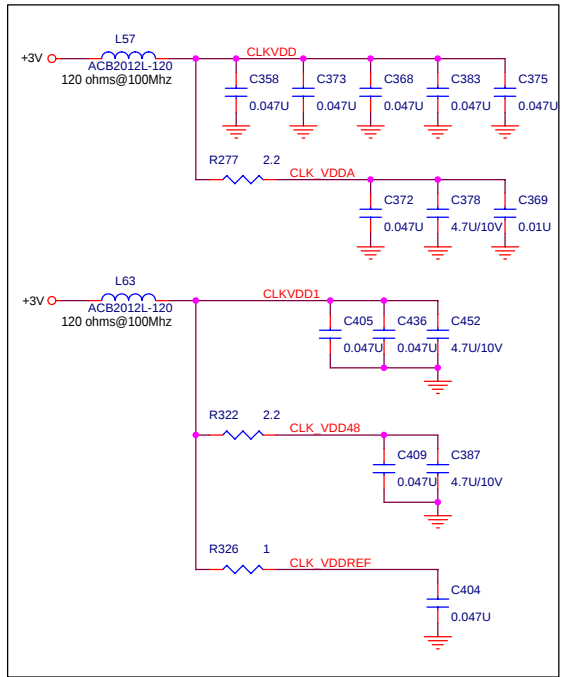


EF6 SYSTEM BLOCK DIAGRAM

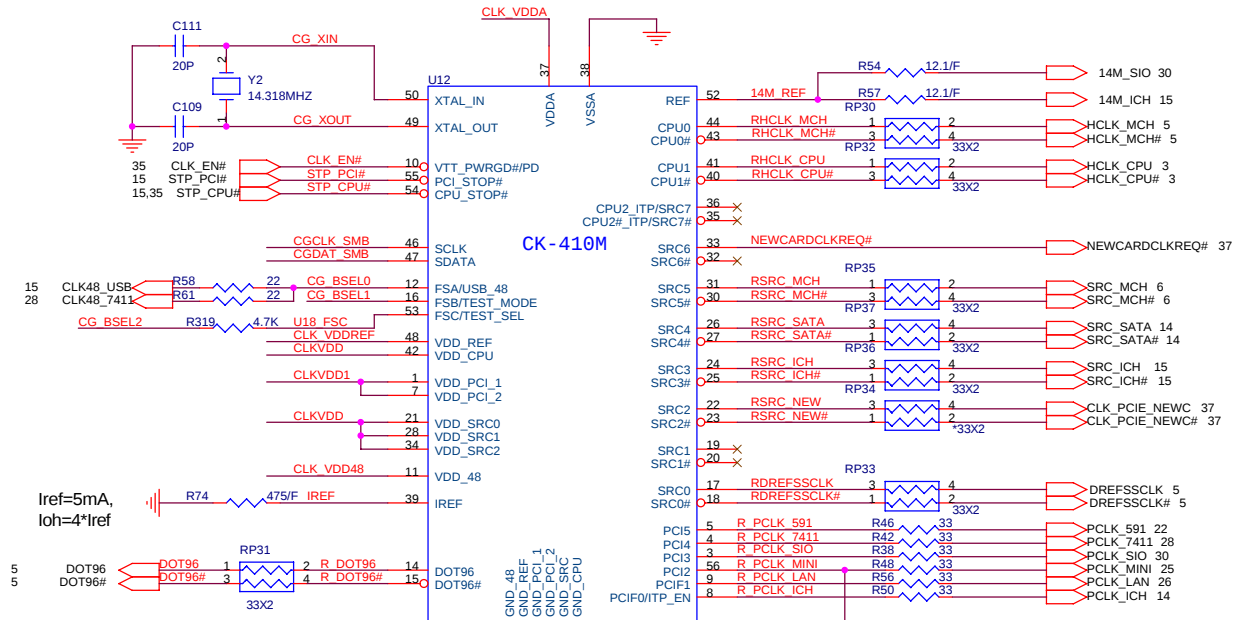




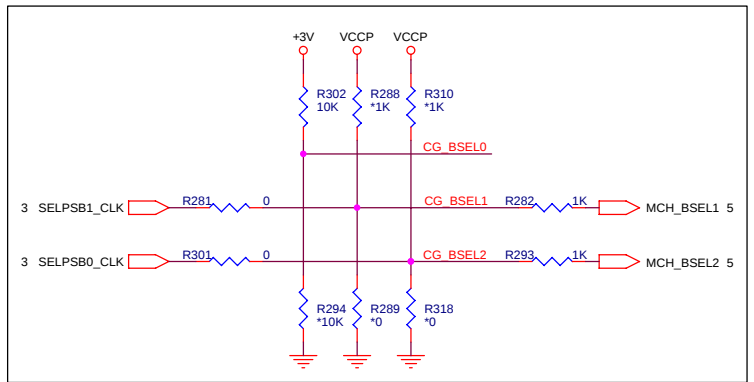
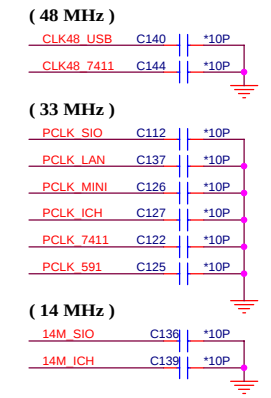
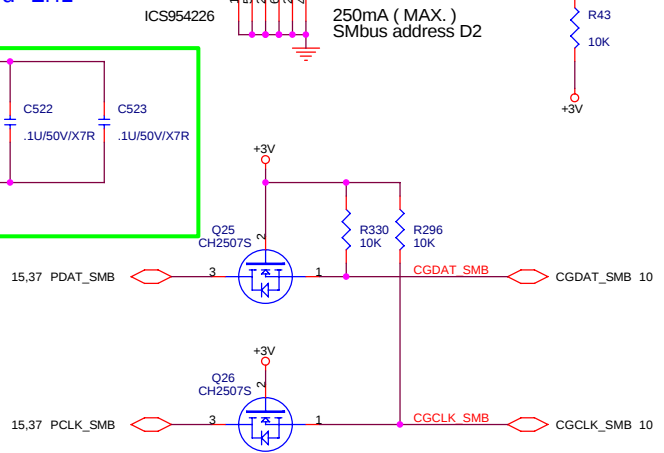
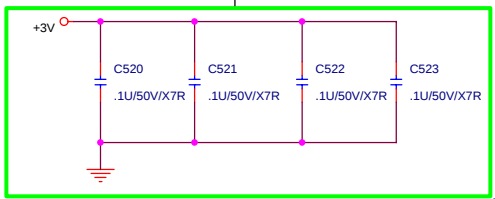
FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RESERVED		

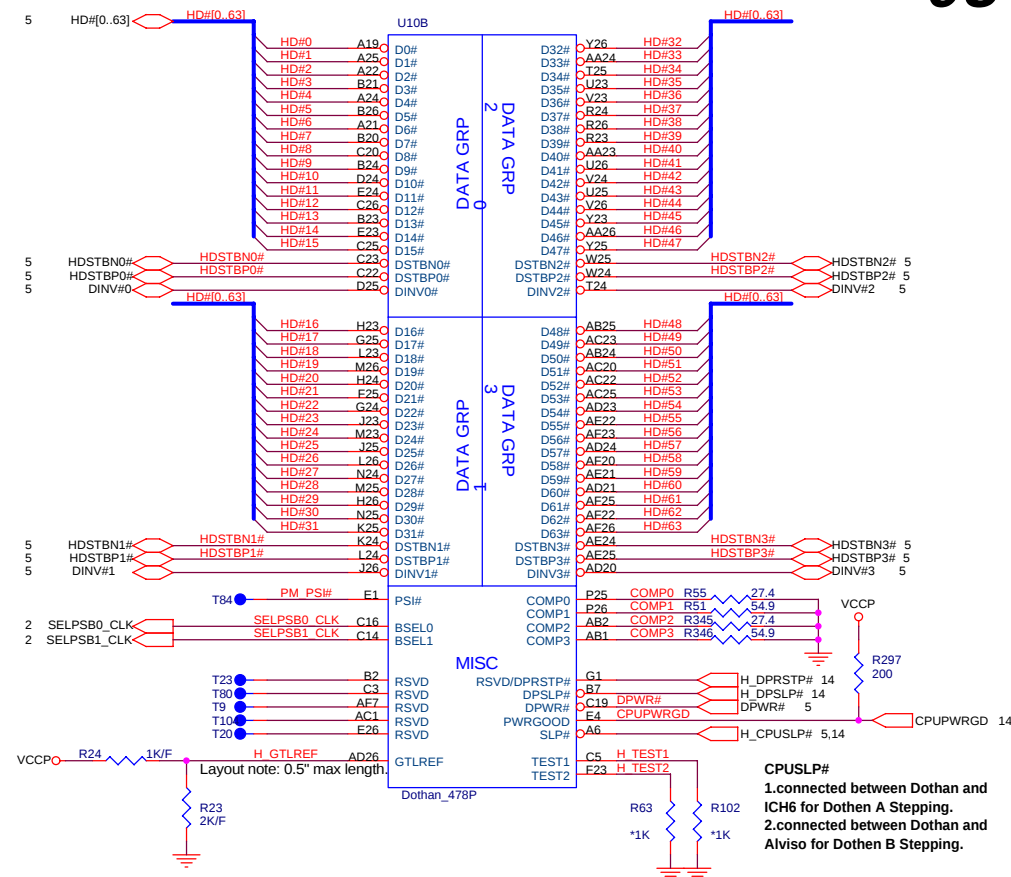
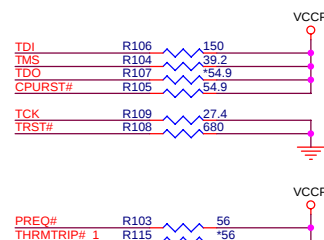
* Frequency select by CPU auto sense.

Place these termination to close ICS954226

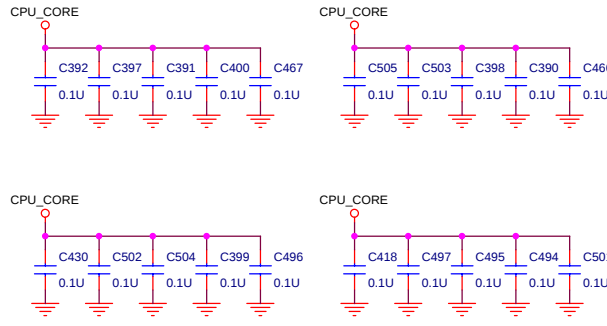
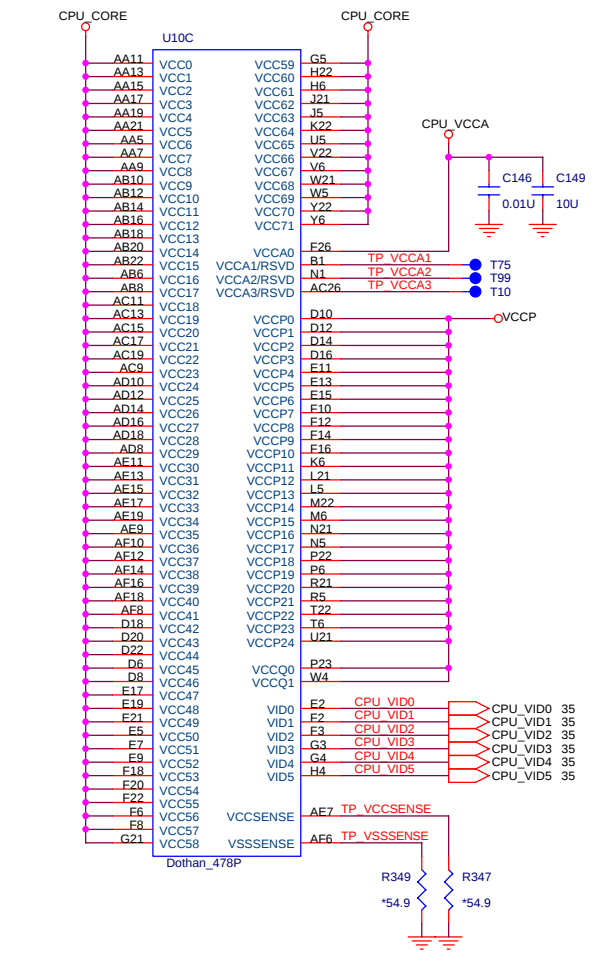
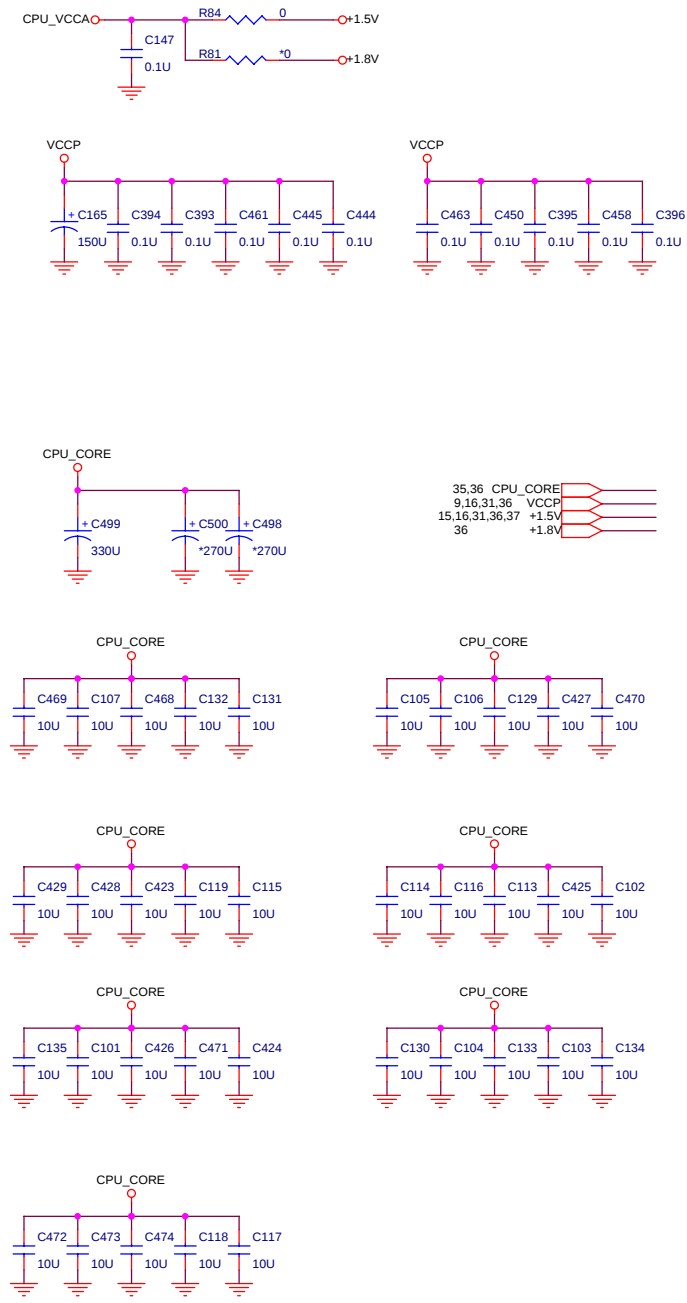


C TEST modified EMI

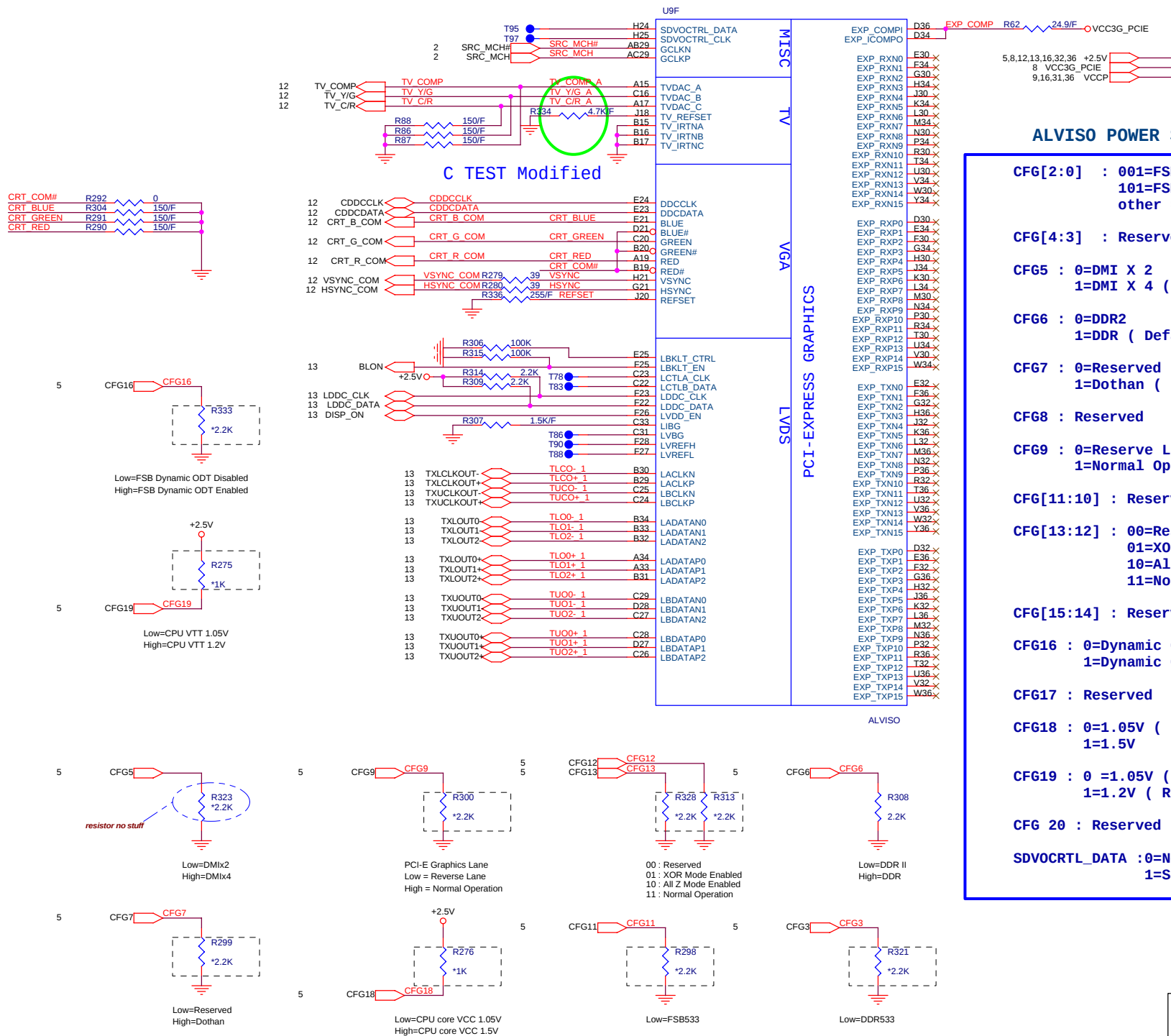


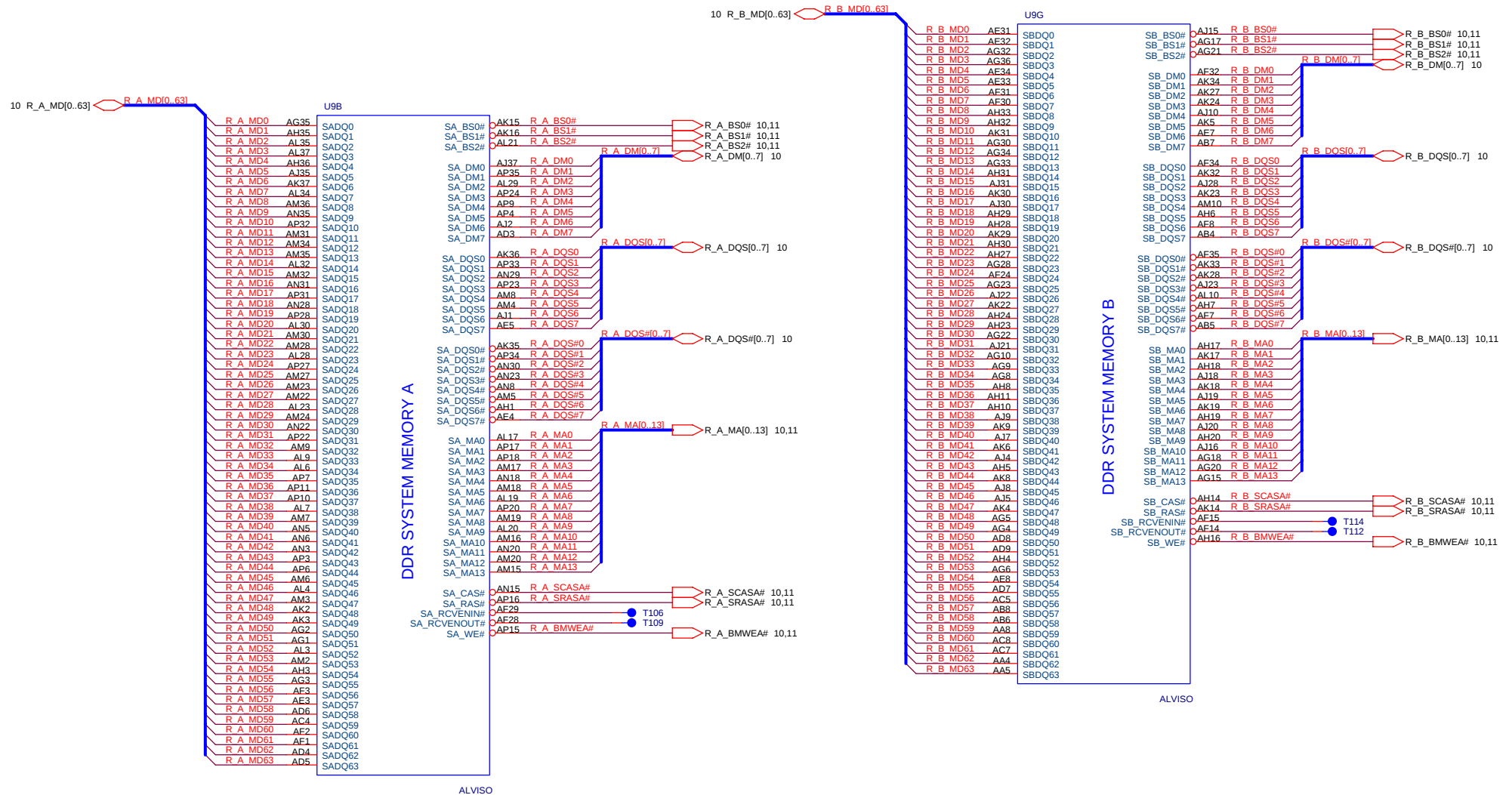


U10C		
A2	VSS0	D13
A5	VSS1	D15
A8	VSS2	D17
A11	VSS3	D19
A14	VSS4	D21
A17	VSS5	D23
A20	VSS6	D26
A23	VSS7	E3
A26	VSS8	E6
AA1	VSS9	E8
AA4	VSS10	E10
AA6	VSS11	E12
AA8	VSS12	E14
AA10	VSS13	E16
AA12	VSS14	E18
AA14	VSS15	E20
AA16	VSS16	E22
AA18	VSS17	E25
AA20	VSS18	F1
AA22	VSS19	F4
AA25	VSS20	F5
AB3	VSS21	F7
AB5	VSS22	F9
AB7	VSS23	F11
AB9	VSS24	F13
AB11	VSS25	F15
AB13	VSS26	F17
AB15	VSS27	F19
AB17	VSS28	F21
AB19	VSS29	F24
AB21	VSS30	G2
AB23	VSS31	G22
AB26	VSS32	G23
AC2	VSS33	G26
AC5	VSS34	H3
AC8	VSS35	H5
AC10	VSS36	H5
AC12	VSS37	H21
AC14	VSS38	H25
AC16	VSS39	J1
AC18	VSS40	J4
AC21	VSS41	J6
AC24	VSS42	J22
AD1	VSS43	K2
AD7	VSS44	K5
AD9	VSS45	K21
AD11	VSS46	K23
AD13	VSS47	K26
AD15	VSS48	L3
AD17	VSS49	L6
AD19	VSS50	L22
AD22	VSS51	L25
AD25	VSS52	M1
AE3	VSS53	M4
AE6	VSS54	M4
AE8	VSS55	M5
AE10	VSS56	M21
AE12	VSS57	M24
AE14	VSS58	N3
AE16	VSS59	N6
AE18	VSS60	N22
AE20	VSS61	N23
AE23	VSS62	N26
AE26	VSS63	P2
AF2	VSS64	P21
AF5	VSS65	P24
AF9	VSS66	R1
AF11	VSS67	R4
AF13	VSS68	R6
AF15	VSS69	R22
AF17	VSS70	R25
AF19	VSS71	T3
AF21	VSS72	T5
AF24	VSS73	T21
B3	VSS74	T23
B6	VSS75	T26
B9	VSS76	U2
B12	VSS77	U6
B16	VSS78	U22
B19	VSS79	U24
B22	VSS80	V1
B25	VSS81	V4
C1	VSS82	V5
C4	VSS83	V21
C7	VSS84	V25
C10	VSS85	W3
C13	VSS86	W6
C15	VSS87	W22
C18	VSS88	W23
C21	VSS89	W26
C24	VSS90	Y2
D2	VSS91	Y5
D5	VSS92	Y21
D7	VSS93	Y24
D9	VSS94	
D11	VSS95	
	VSS96	



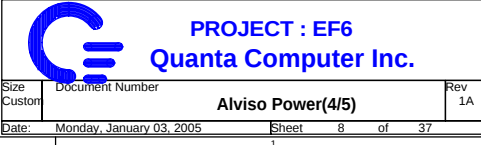


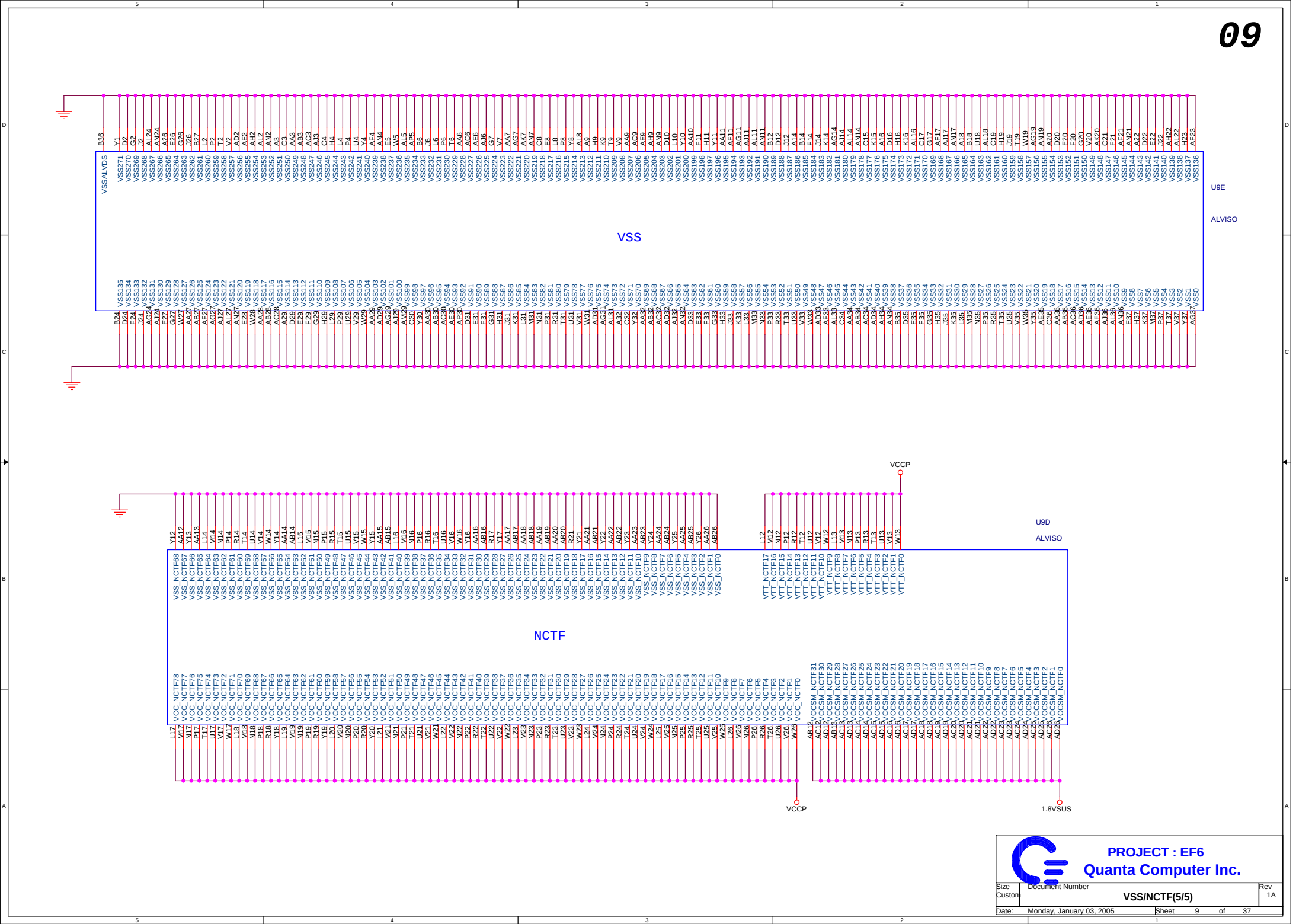


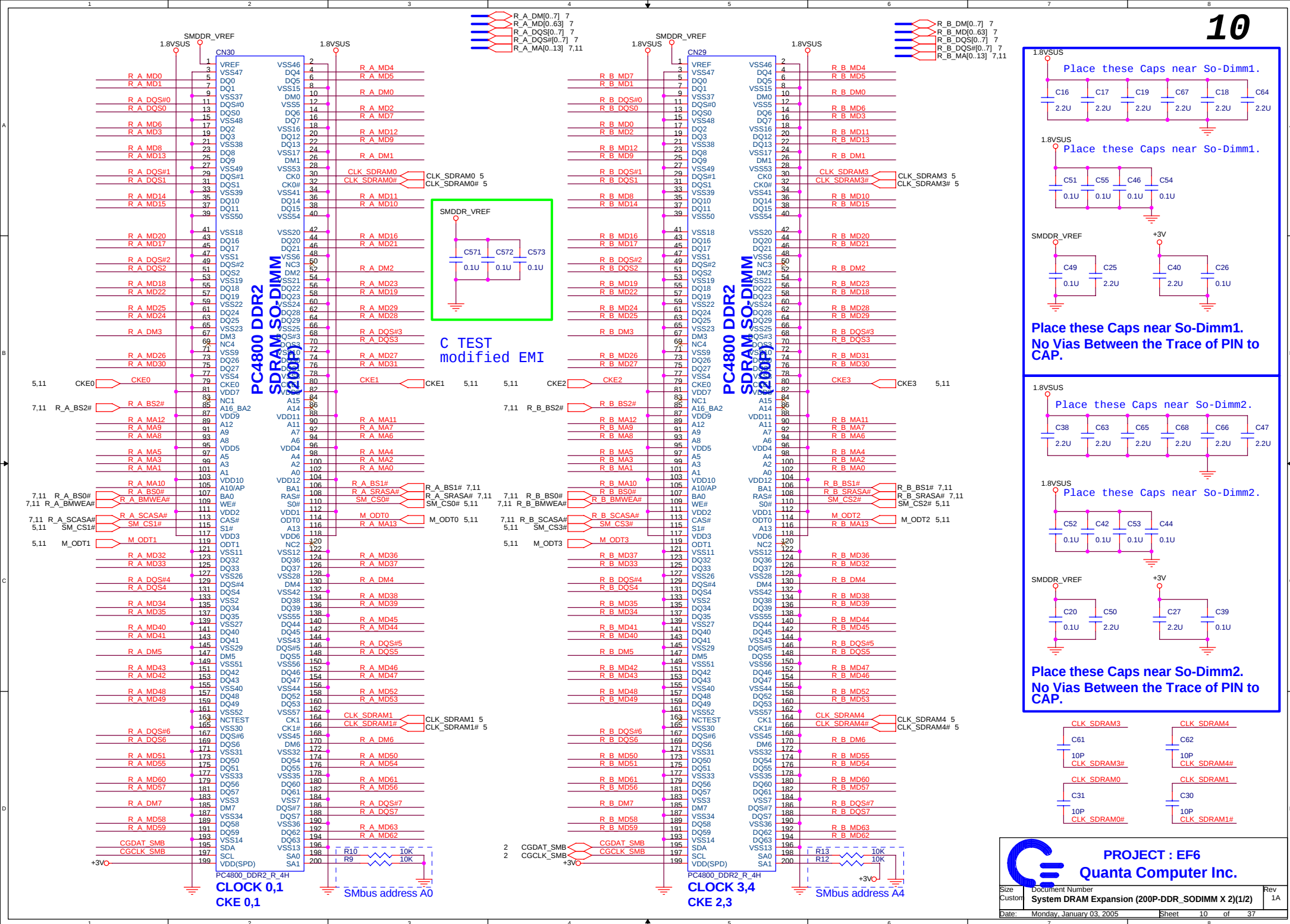


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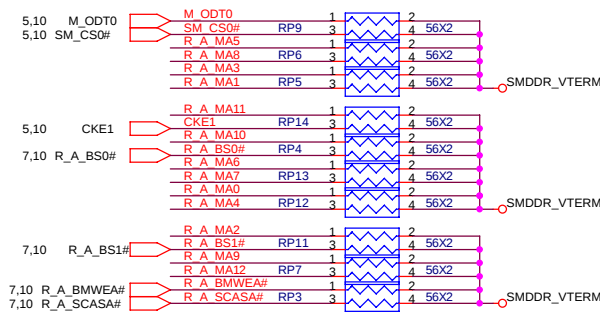
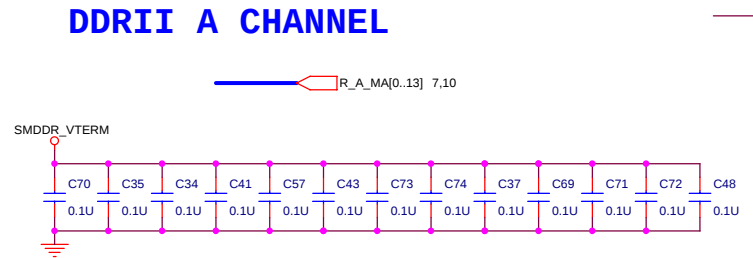




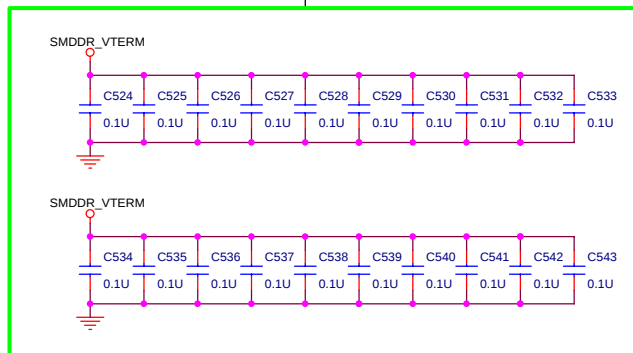


DDRII DUAL CHANNEL A, B.

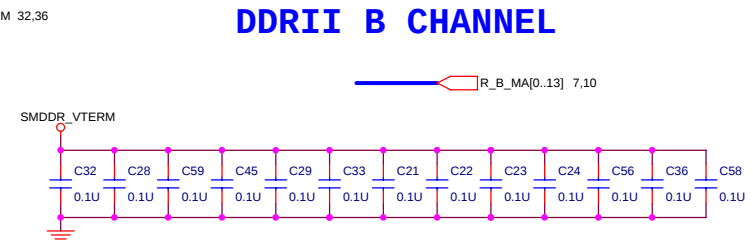
DDRII A CHANNEL



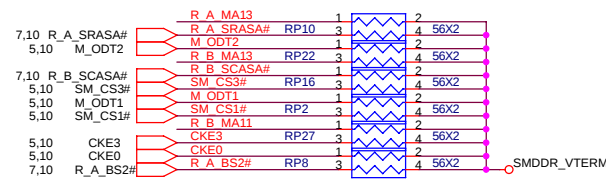
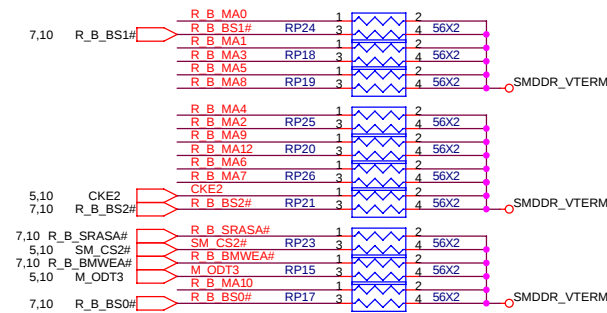
C TEST modified EMI



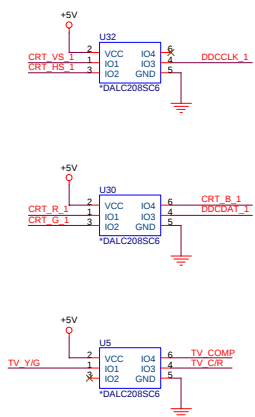
DDRII B CHANNEL



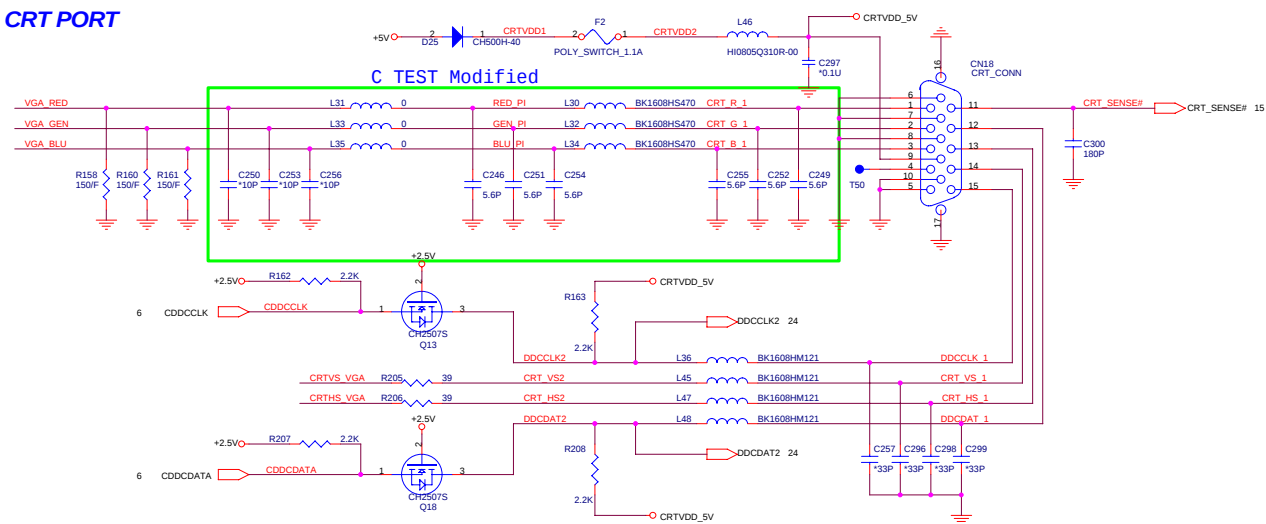
Layout note: Place one cap close to every 2 pullup resistors terminated to SMDDR_VTERM



ESD PROTECTION



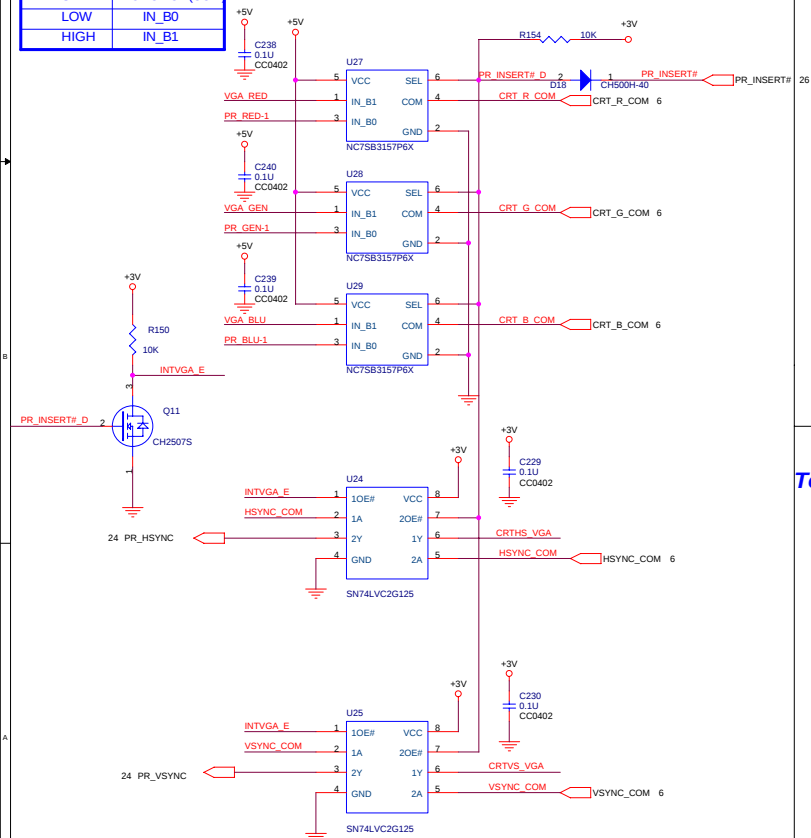
CRT PORT



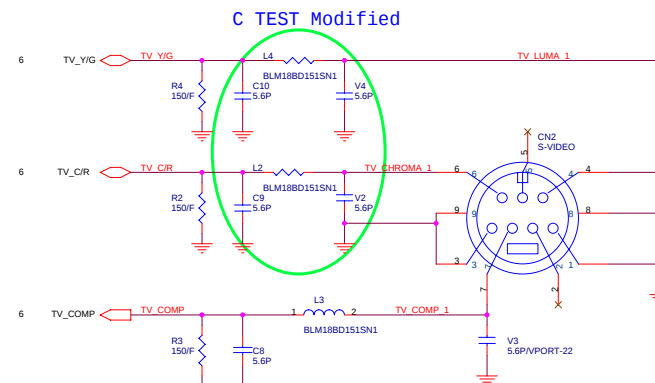
12

CRT SWITCH

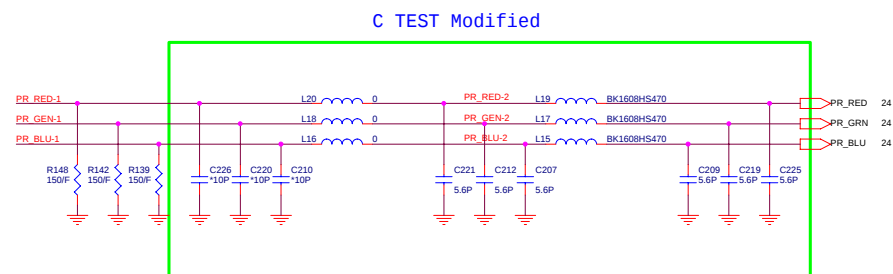
SEL	FUNCTION(COM
LOW	IN_B0
HIGH	IN_B1



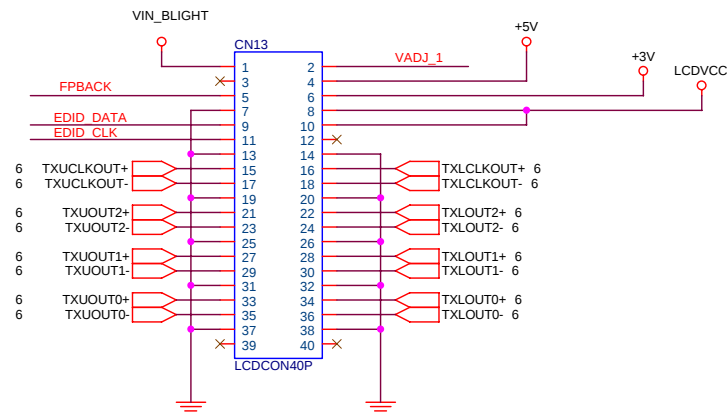
TV-OUT



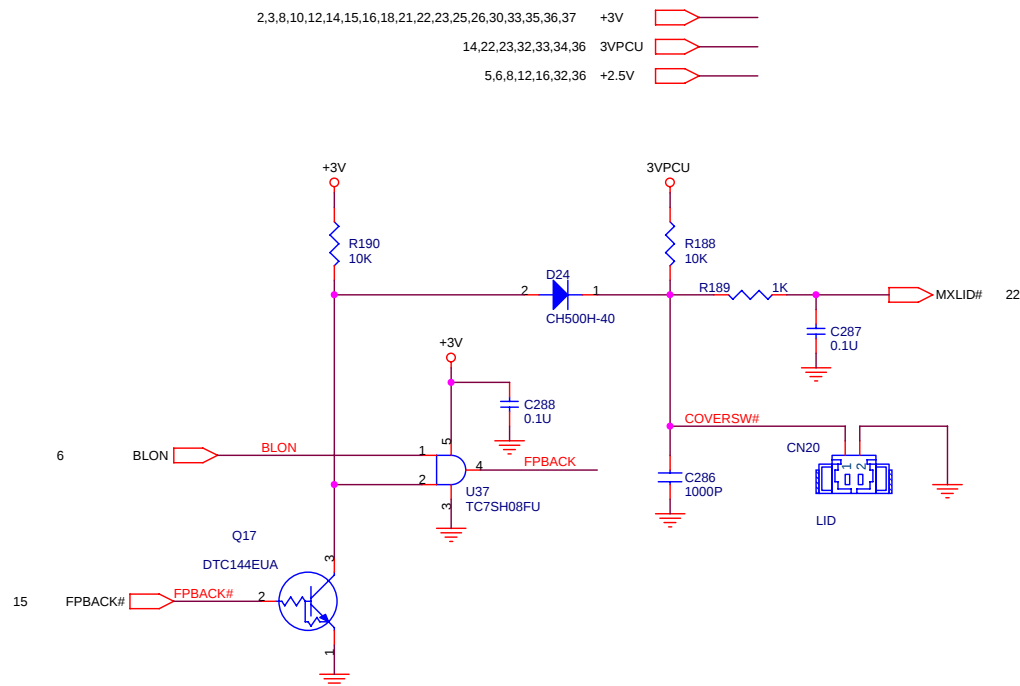
To port - replicator C.R.T

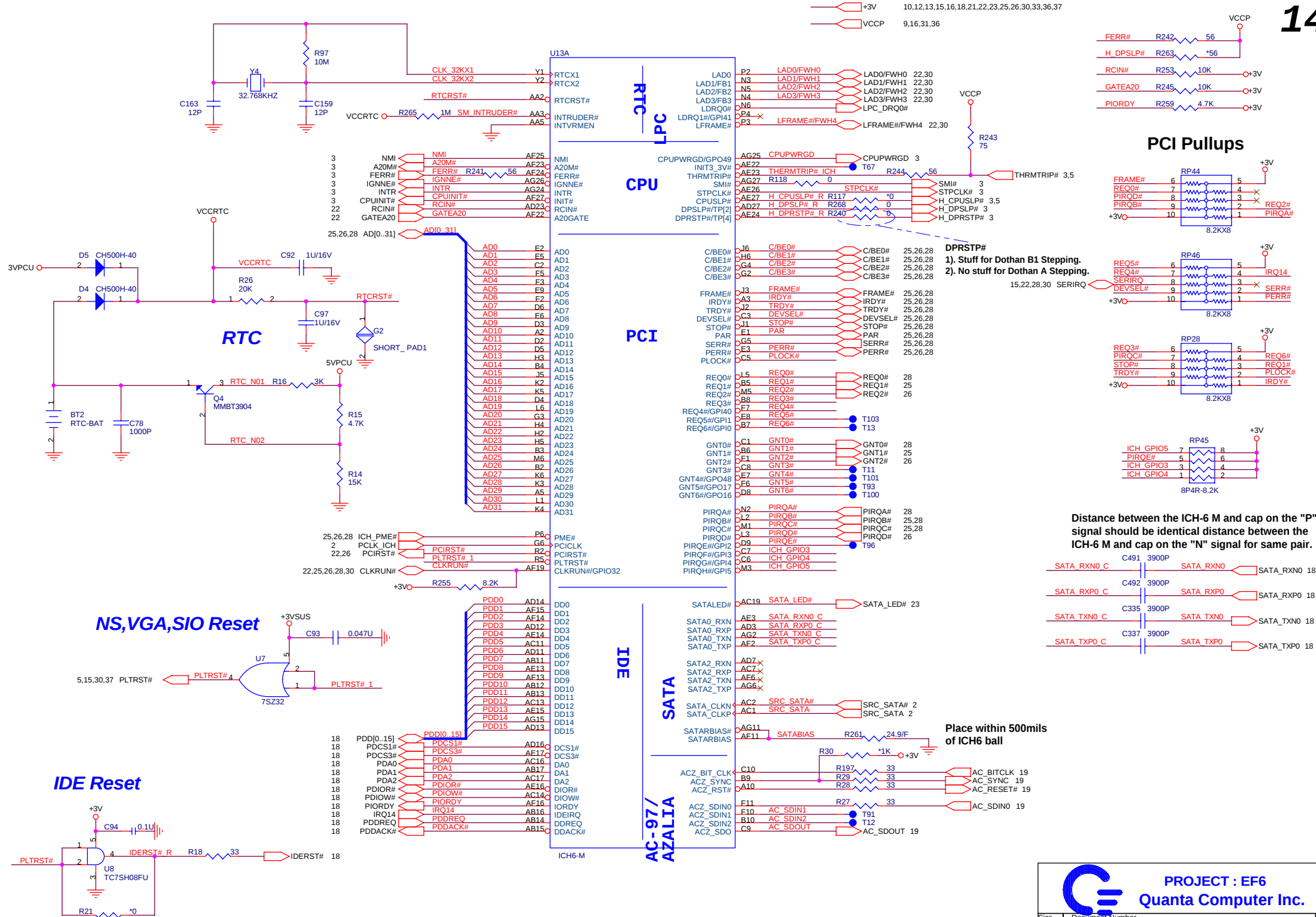


LCD CONNECTOR

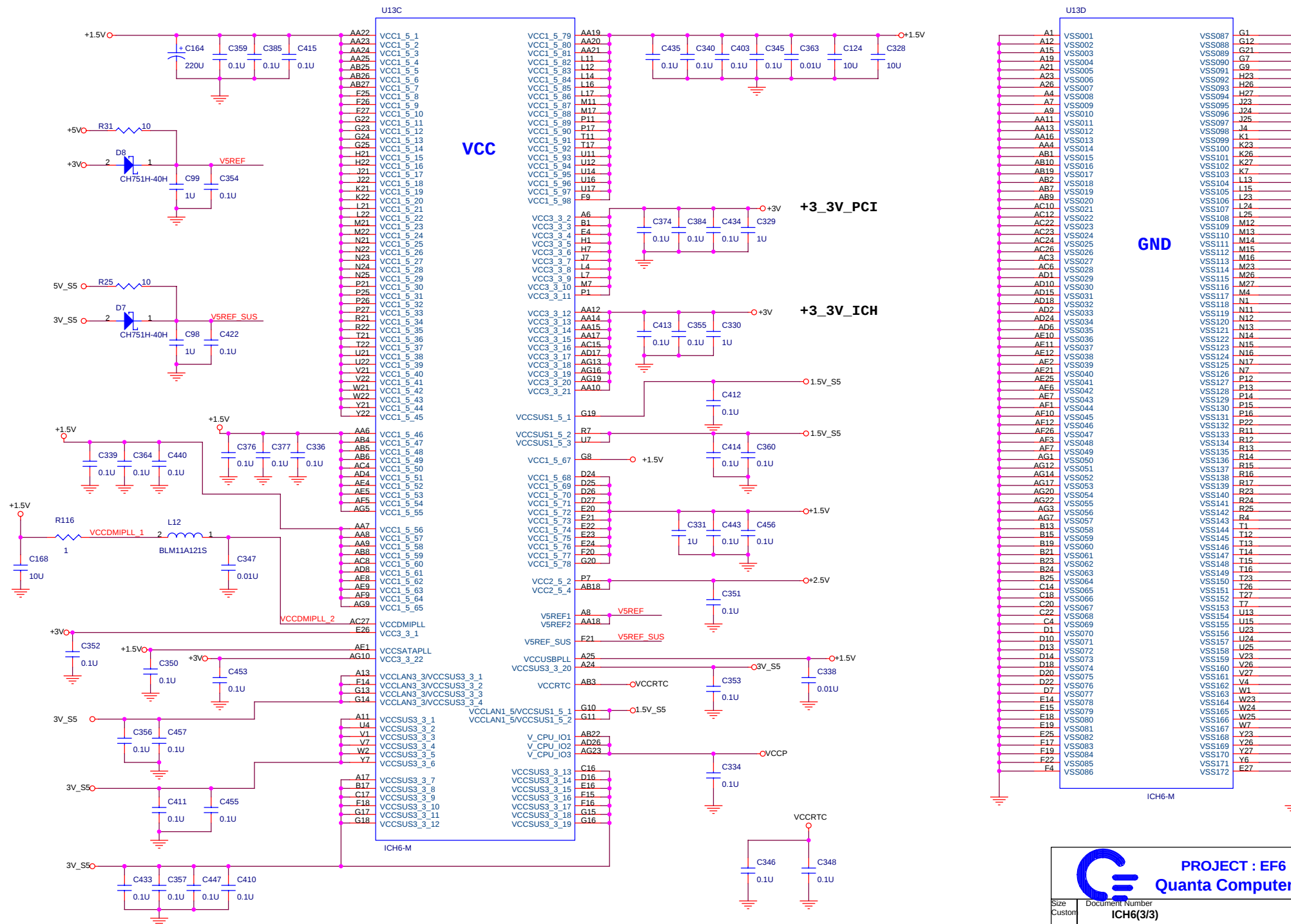


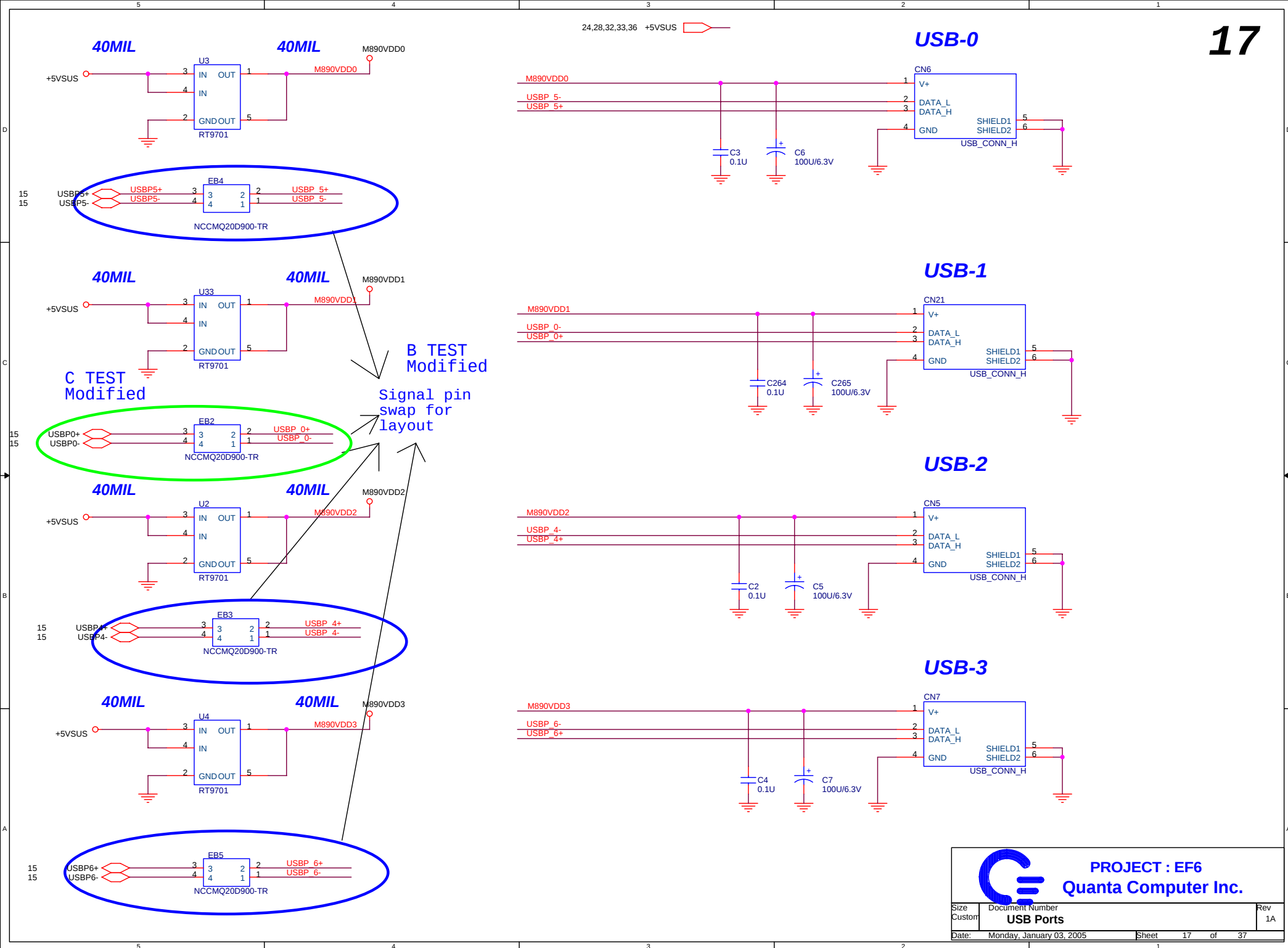
The schematic diagram illustrates the LED driver circuit for the HI0805Q310R-00. The circuit is powered by a +3V supply. Two MOSFETs, Q22 and Q23 (CH2507S), are used to drive the LED string. The gate of Q23 is connected to LDDC_CLK, and the gate of Q22 is connected to LDDC_DATA. The drain of Q23 is connected to the +3V supply, and the drain of Q22 is connected to the LED string (EDID_DATA, EDID_CLK). The source of Q23 is connected to the source of Q22, which is connected to ground. The LED string is connected to the +3V supply through a 2.2K resistor (R227) and to ground through a 2.2K resistor (R226). The pre-regulator stage consists of an inductor L13 and two capacitors, C170 and C181, both 1uF/50V CC0805. The input voltage is VIN, and the output voltage is VOUT. The feedback network includes an inductor L51 and a feedback voltage divider VADJ_1.

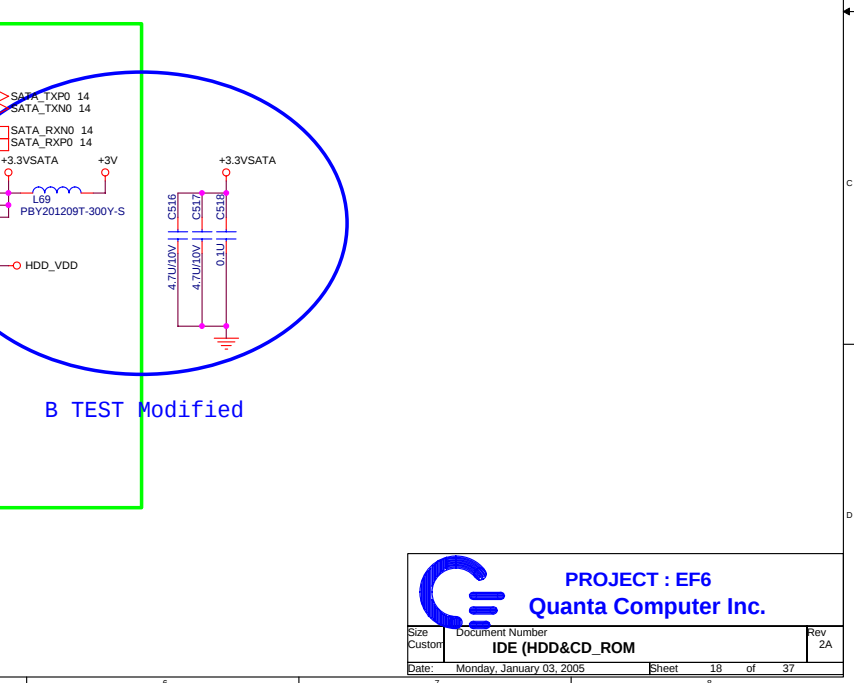
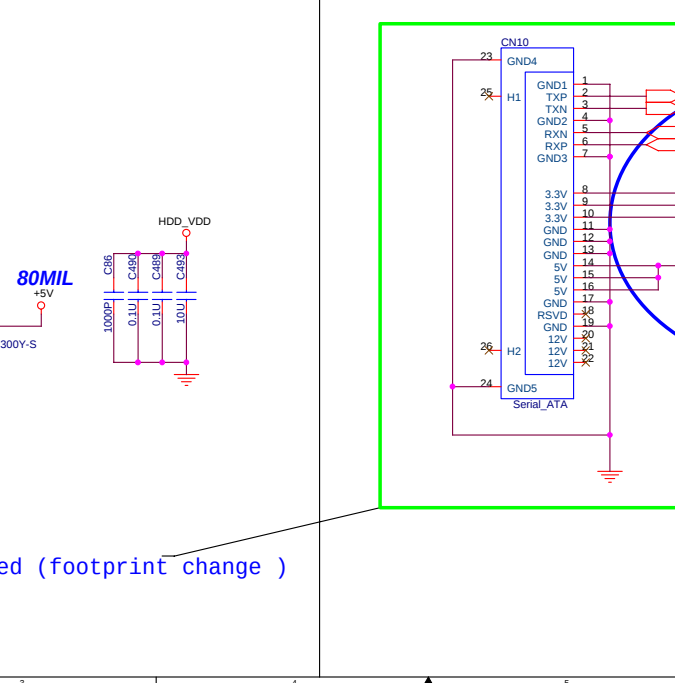
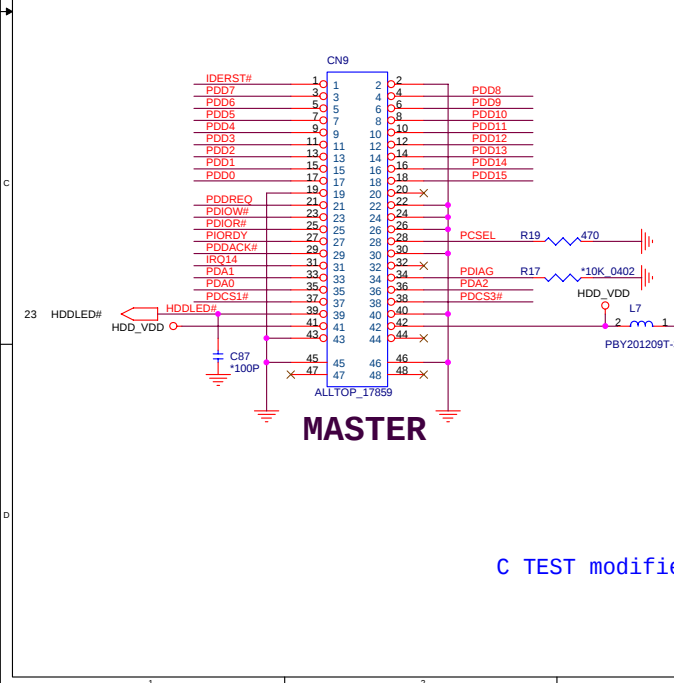




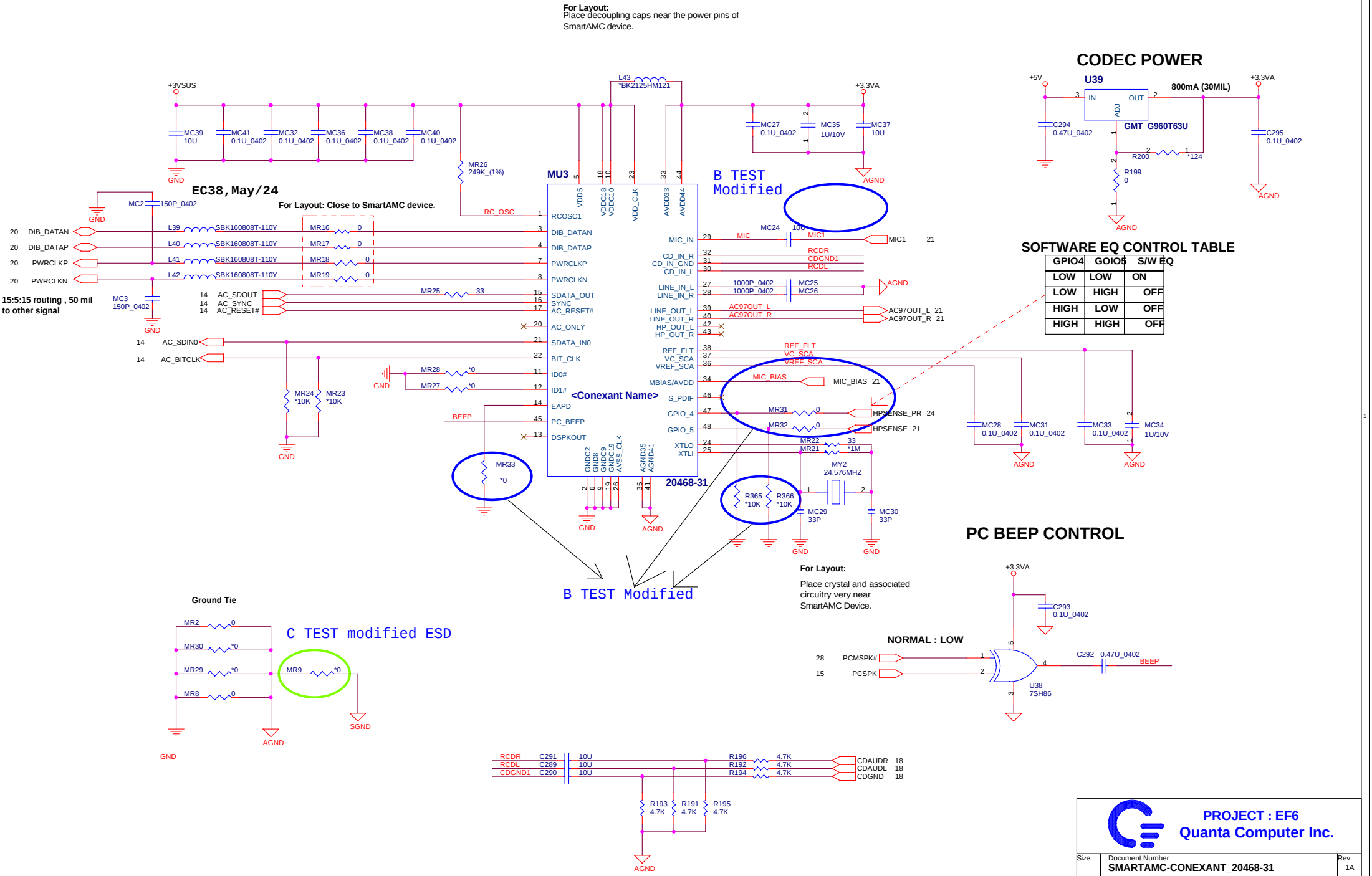
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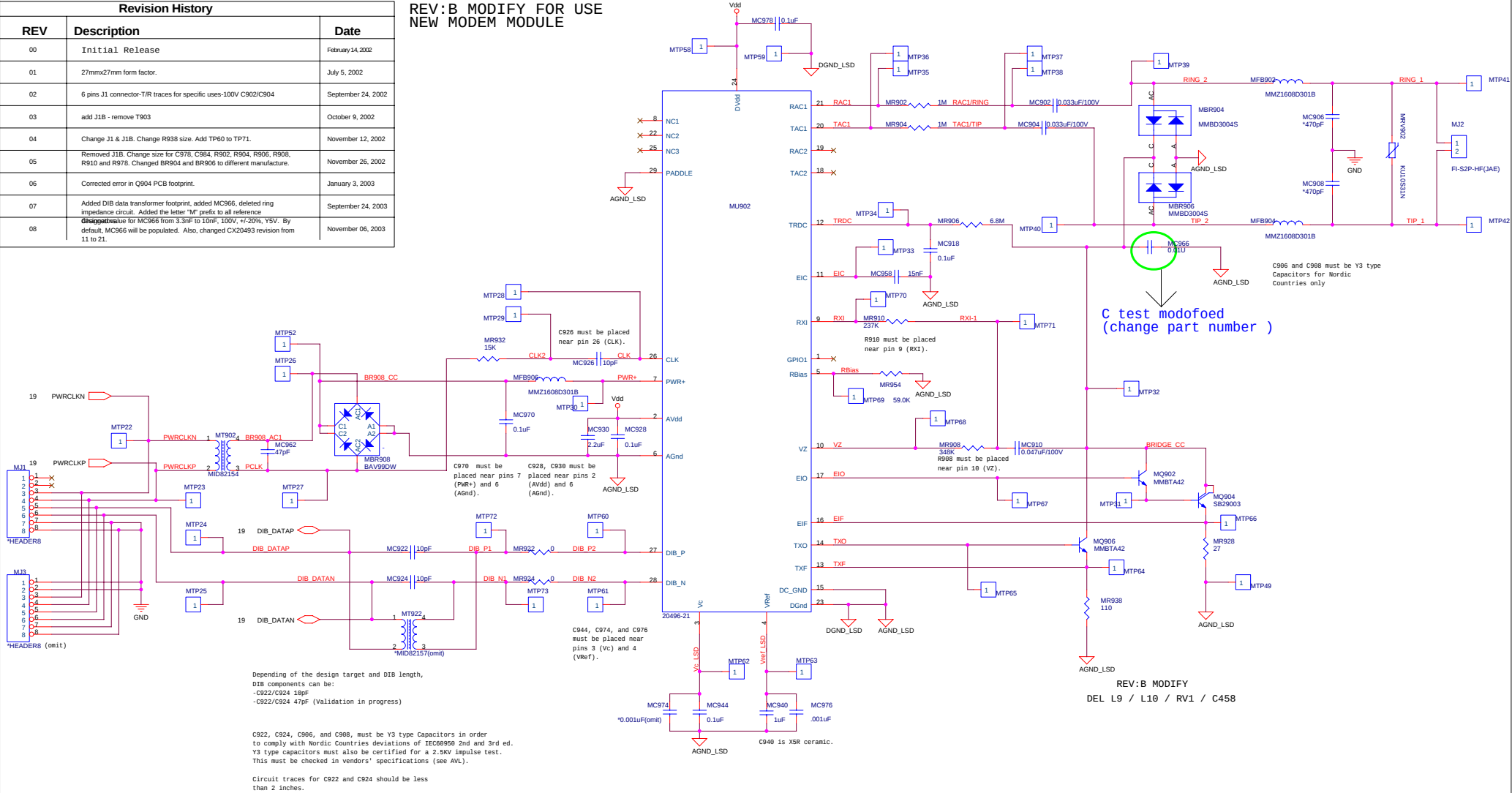


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	Quanta Computer Inc.		
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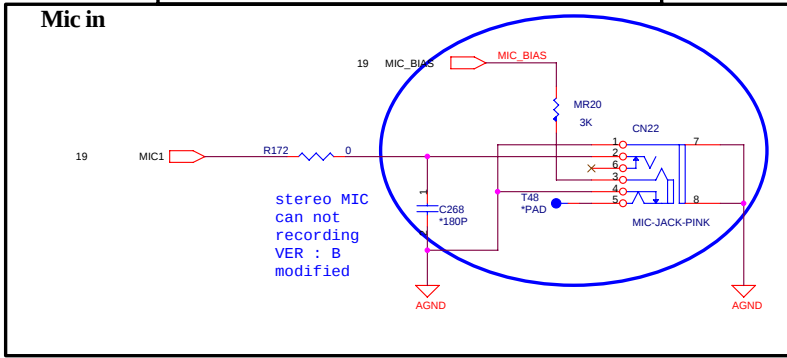
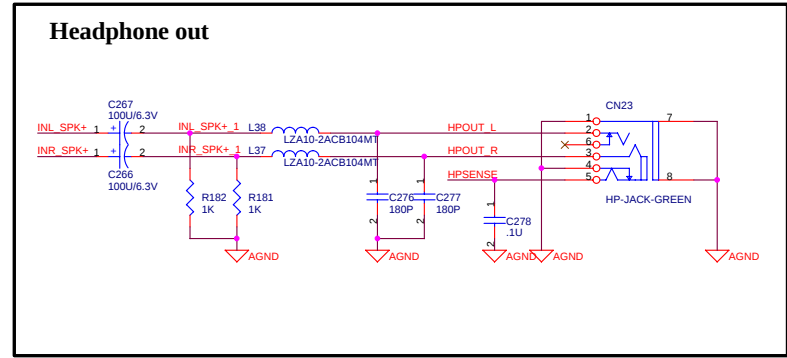
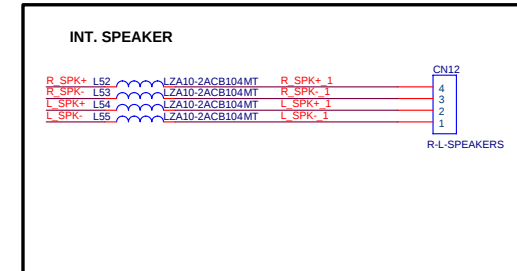
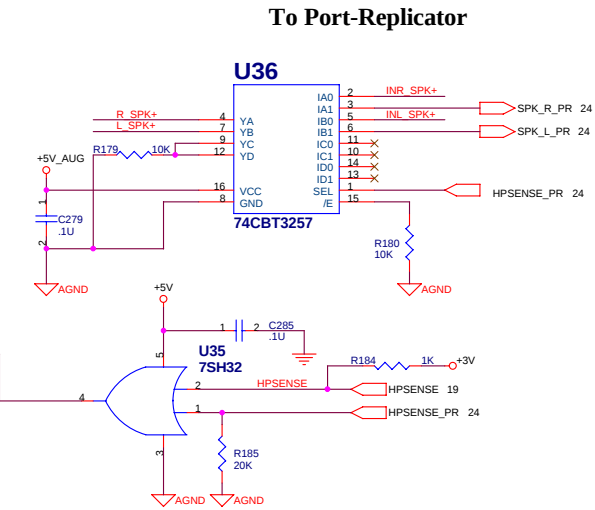
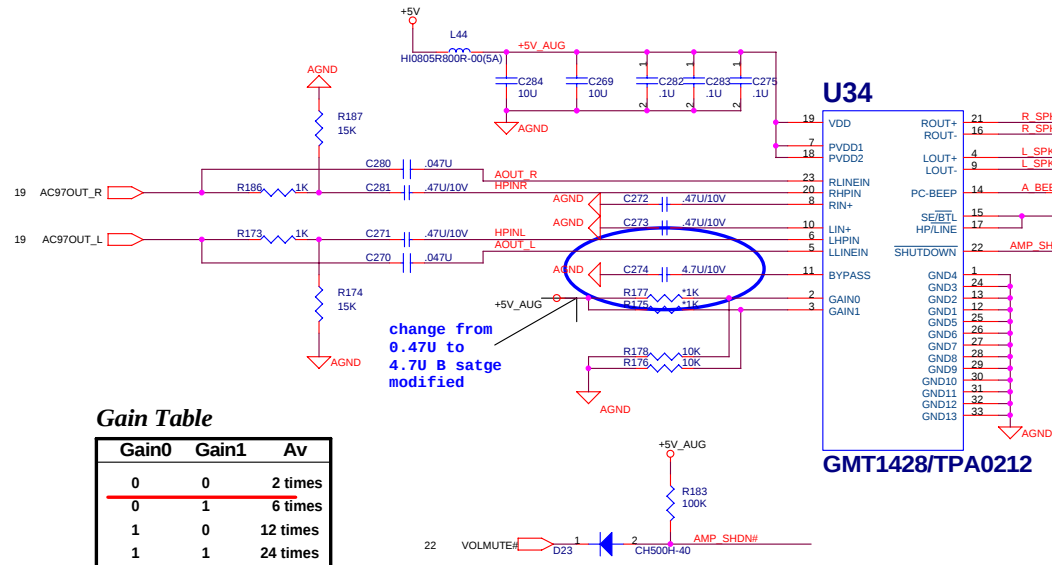
Revision History		
REV	Description	Date
00	Initial Release	February 14, 2002
01	27mmx27mm form factor.	July 5, 2002
02	6 pins J1 connector-T/R traces for specific uses-100V C902/C904	September 24, 2002
03	add J1B - remove T903	October 9, 2002
04	Change J1 & J1B. Change R938 size. Add TP60 to TP71.	November 12, 2002
05	Removed J1B. Change size for C978, C984, R902, R904, R906, R908, R910 and R978. Changed BR904 and BR906 to different manufacture.	November 26, 2002
06	Corrected error in Q904 PCB footprint.	January 3, 2003
07	Added DIB data transformer footprint, added MC966, deleted ring impedance circuit. Added the letter "M" prefix to all reference designators.	September 24, 2003
08	Changed value for MC966 from 3.3nF to 10nF, 100V, +/-20%, Y5V. By default, MC966 will be populated. Also, changed CX20493 revision from 11 to 21.	November 06, 2003

REV:B MODIFY FOR USE
NEW MODEM MODULE

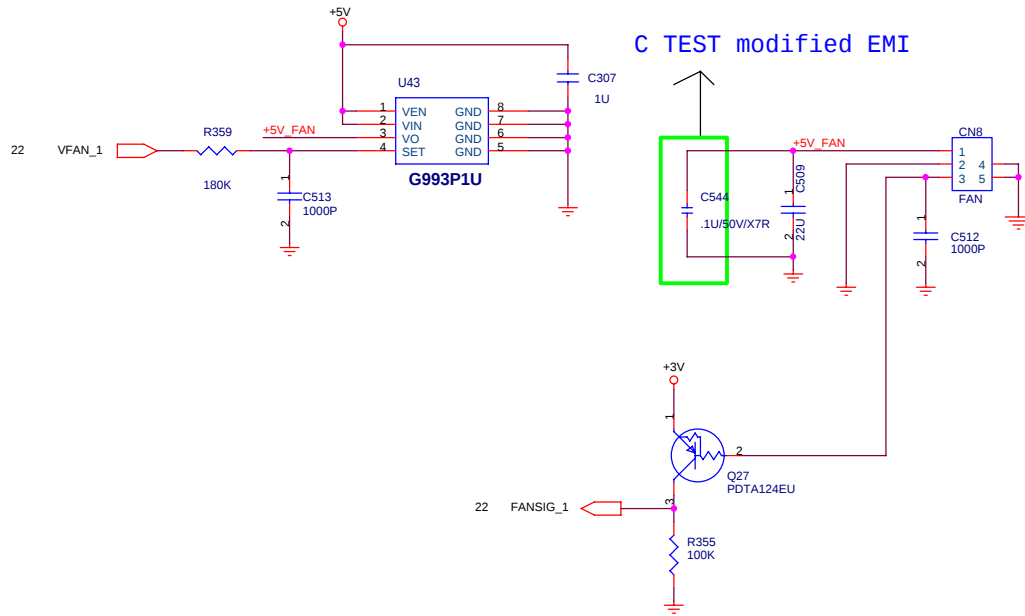


AUDIO AMPLIFIER-GMT_1428

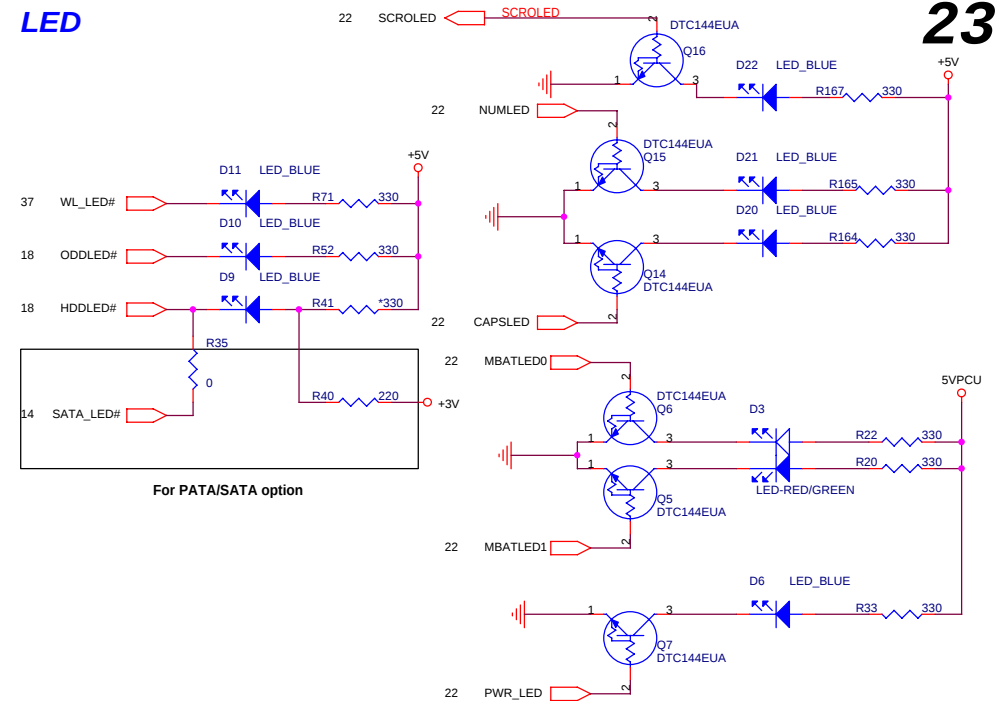
21



1st FAN OUT CONNECTOR

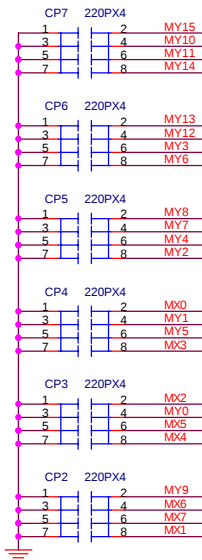
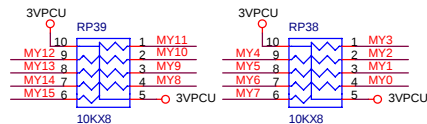
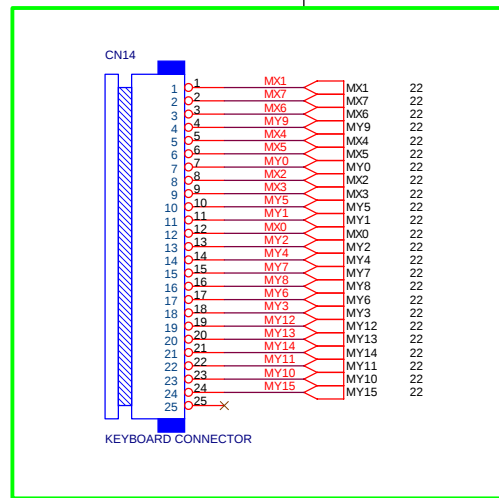


LED

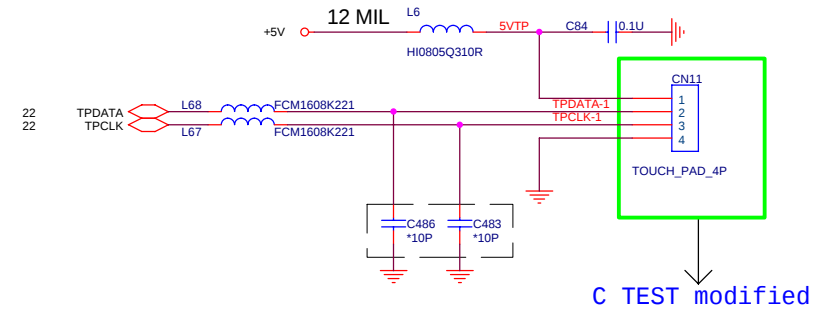


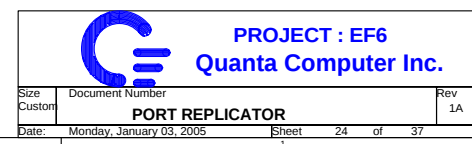
KEYBOARD

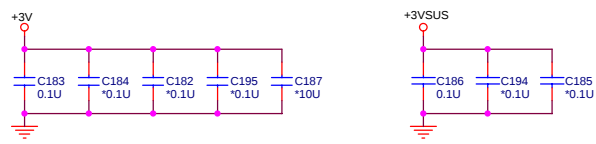
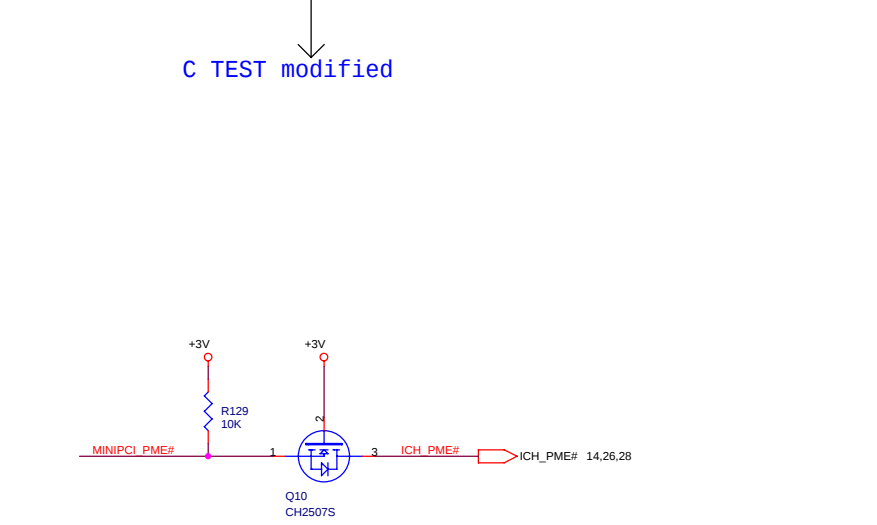
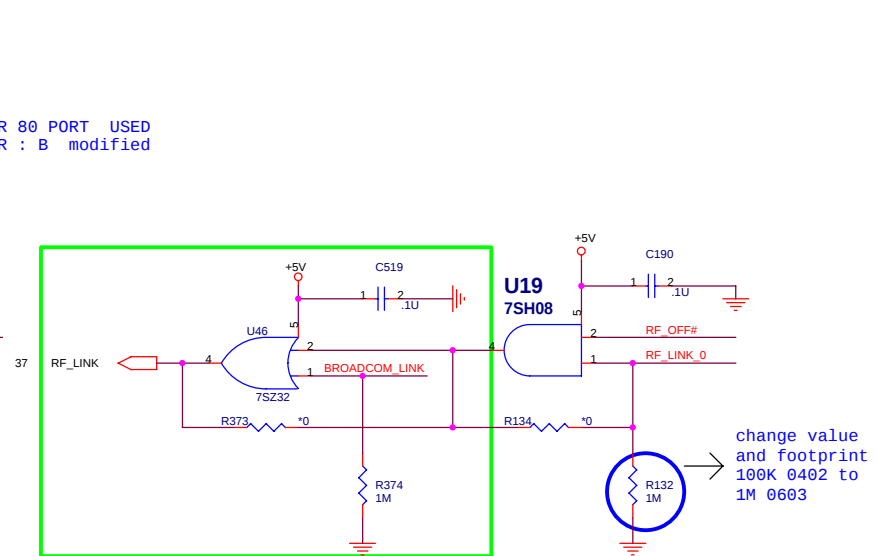
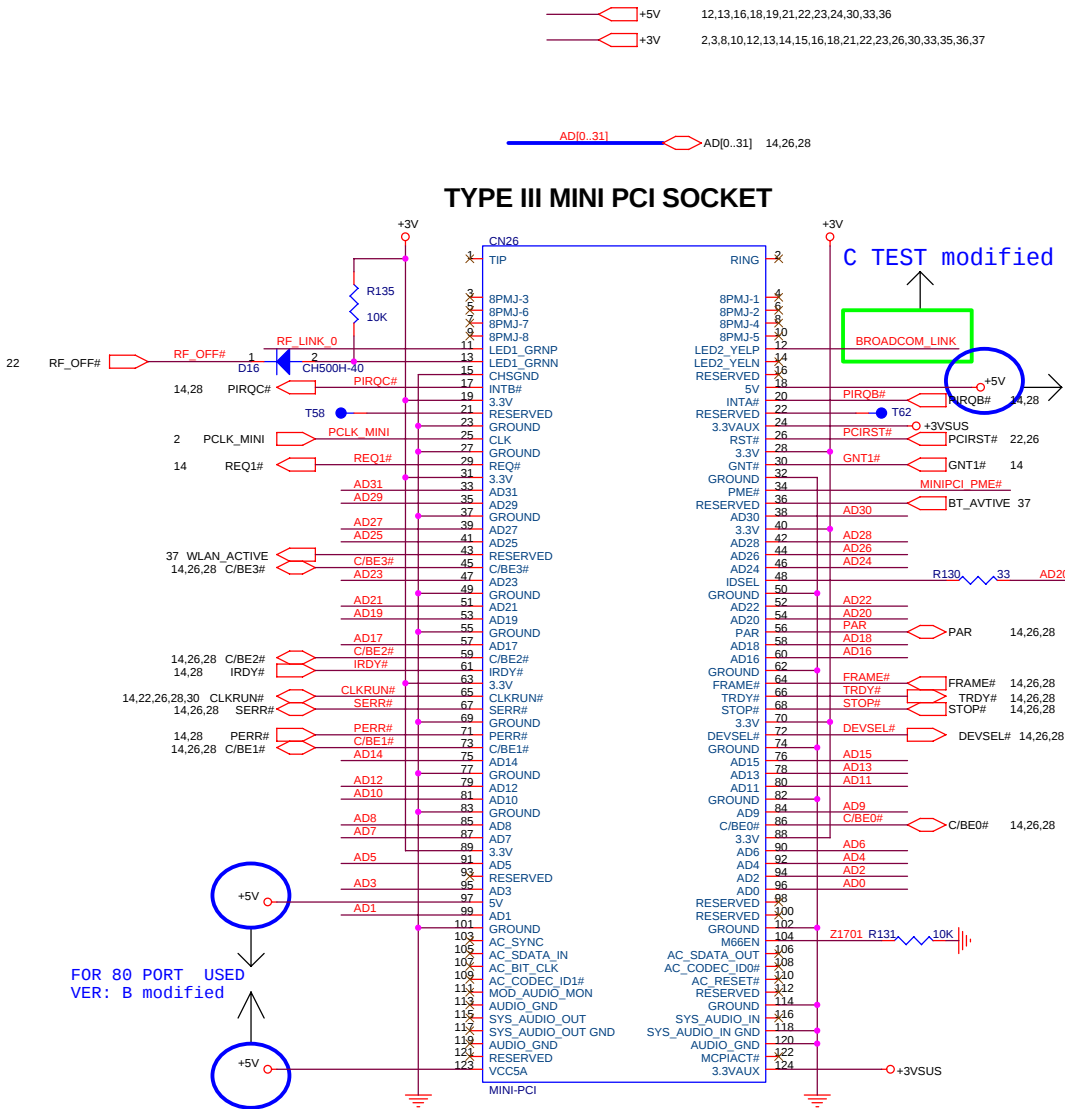
C TEST modified



TOUCHPAD



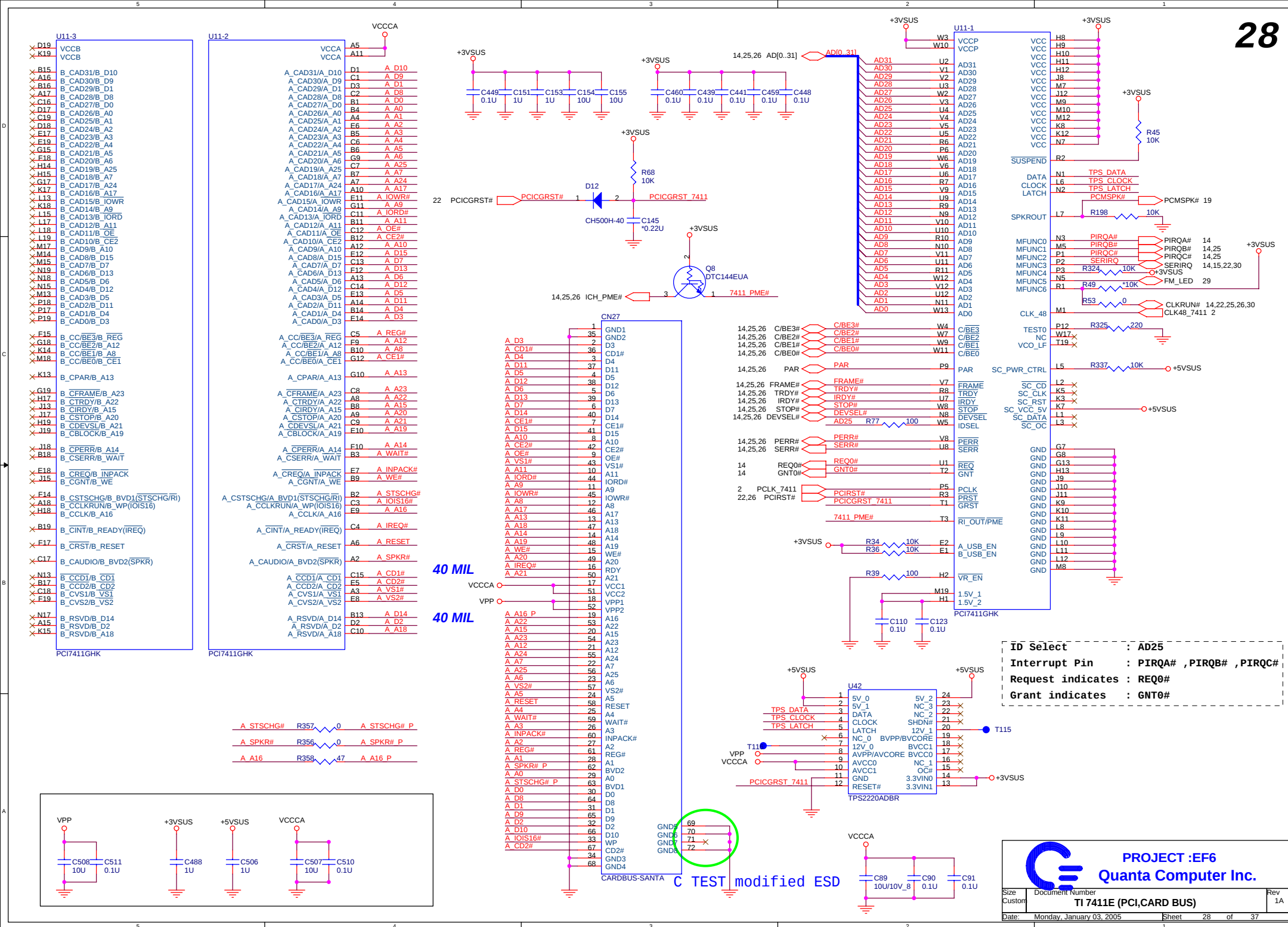






C TEST modified







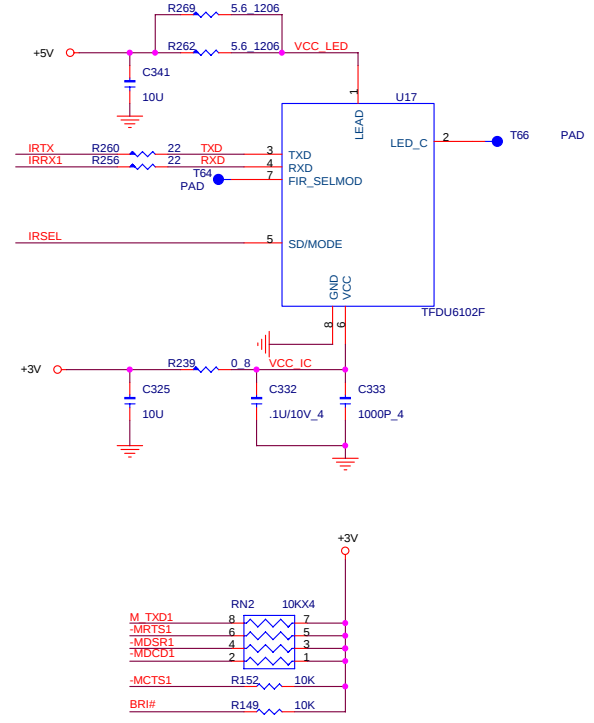
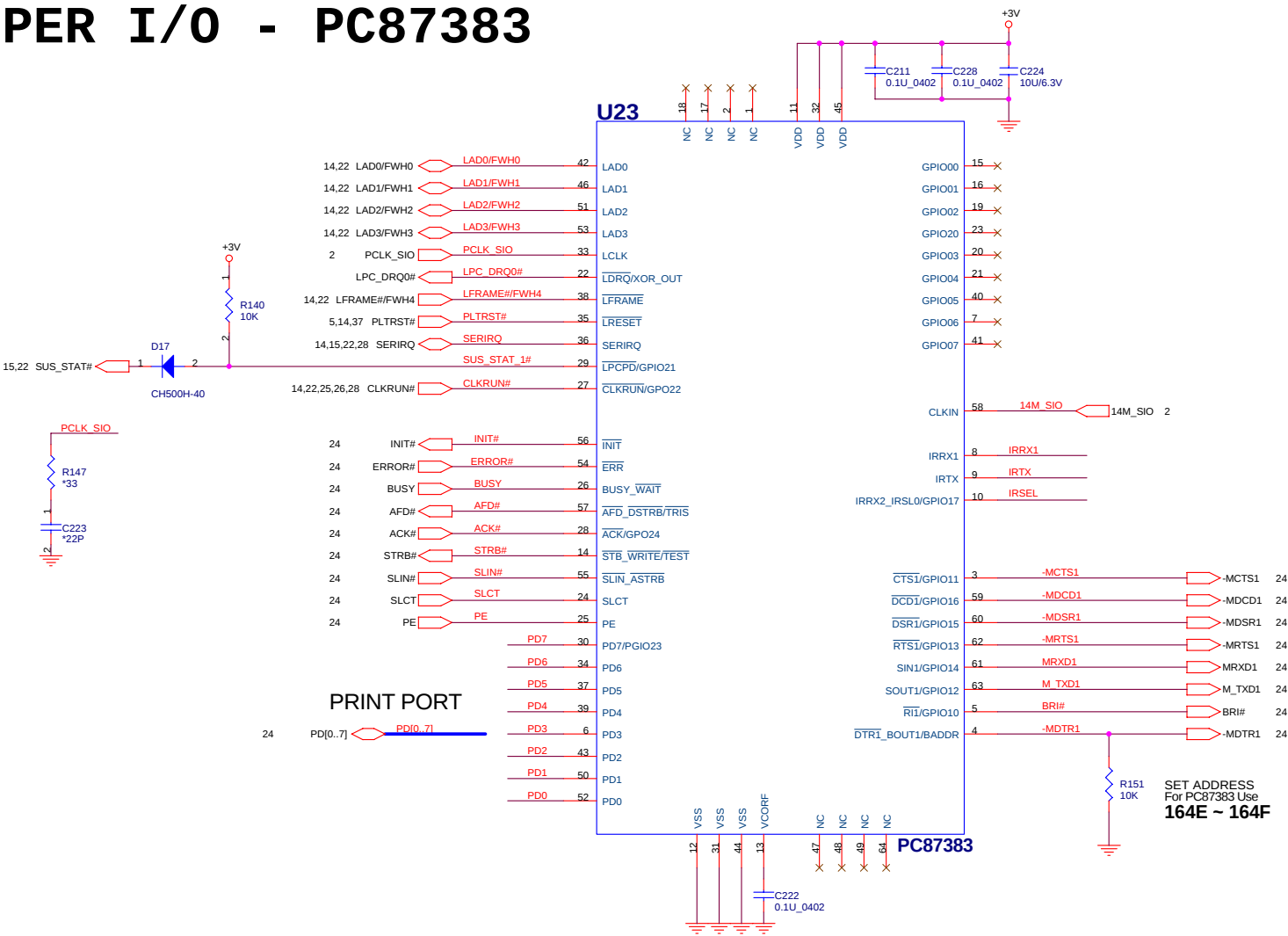
Supporting MMC/SD/MS Cards

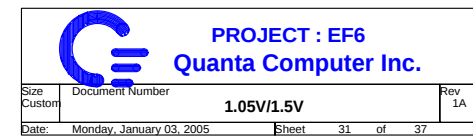


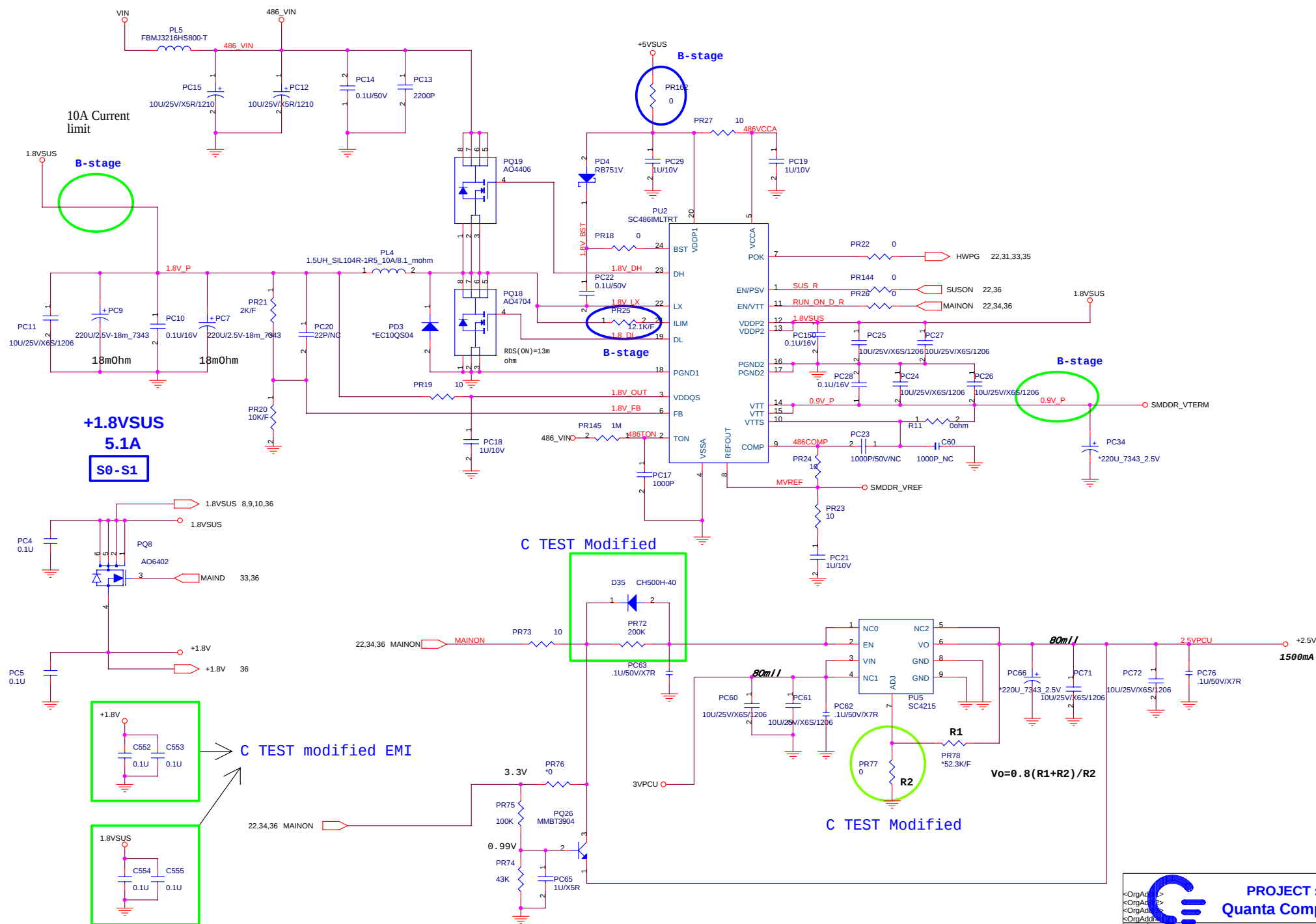
SUPER I/O - PC87383

FIR
40mil: Power/ GND
35mil: VCC_LED, VCC_IC, GND_IC
10mil: TXD, RXD, IRSEL, IRTX,
IRRX1

30





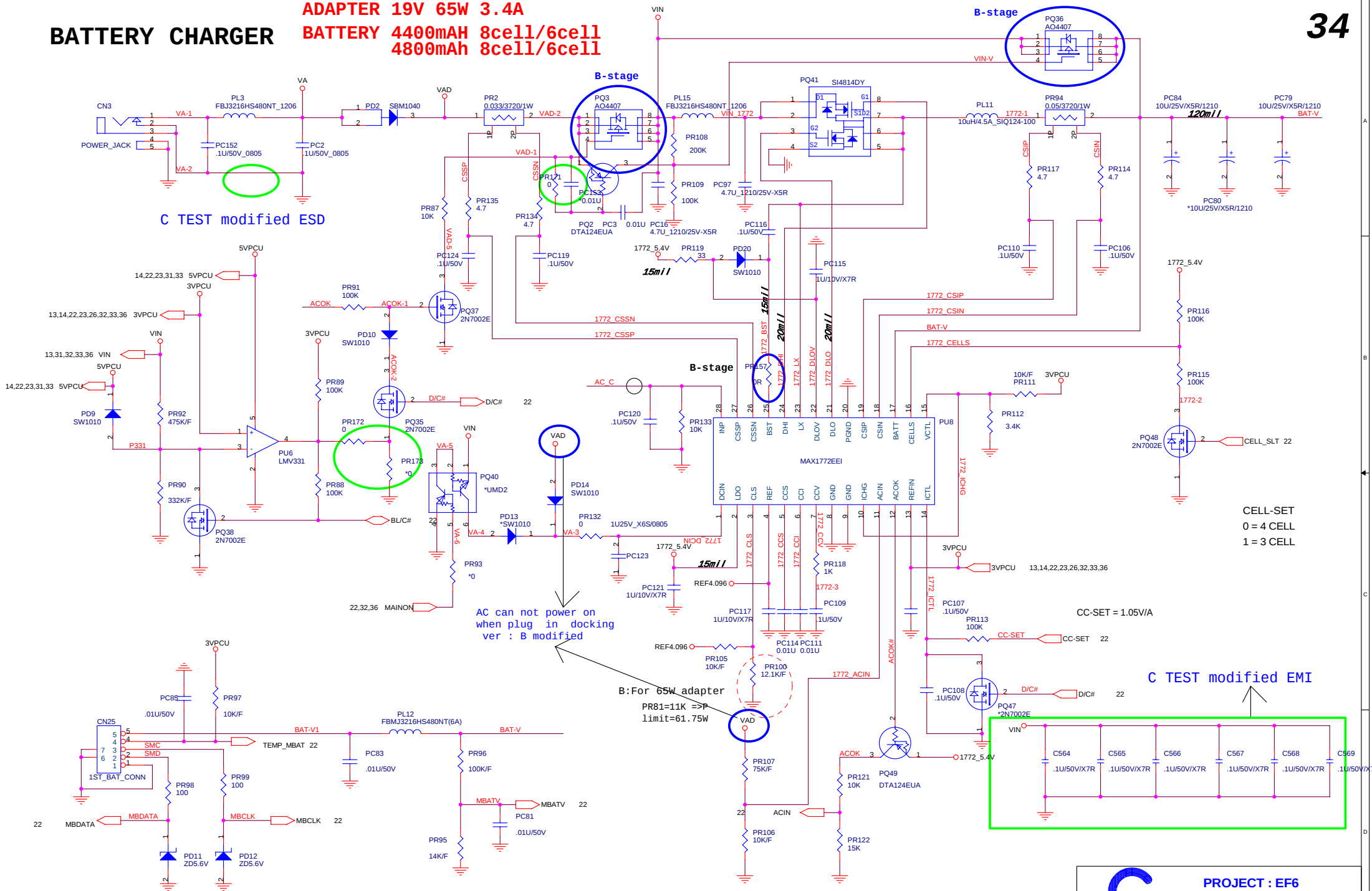




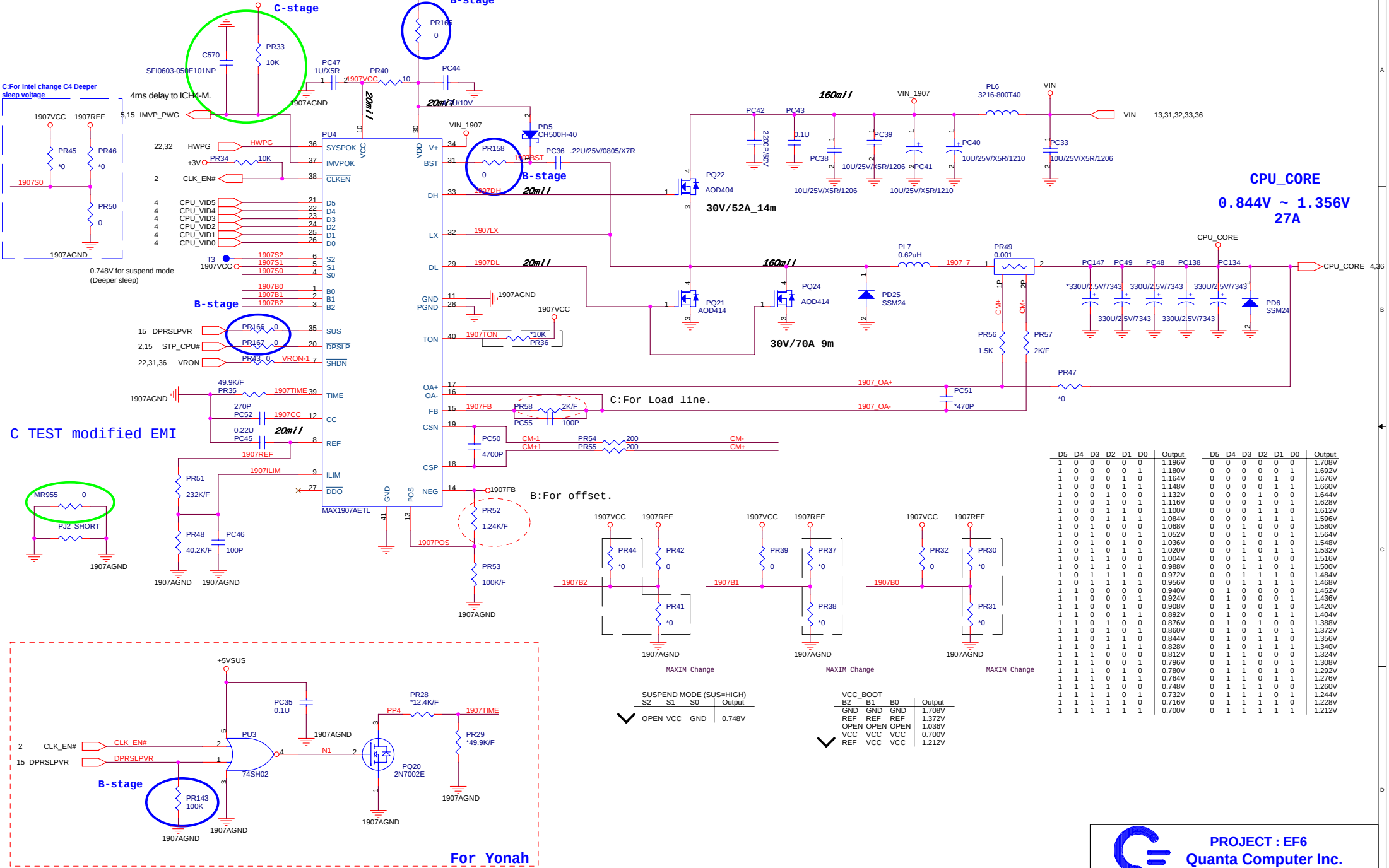
BATTERY CHARGER

ADAPTER 19V 65W 3.4A
BATTERY 4400mAh 8cell/6cell
4800mAh 8cell/6cell

34

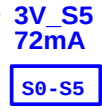


CPU VCC CORE (MAX1907)

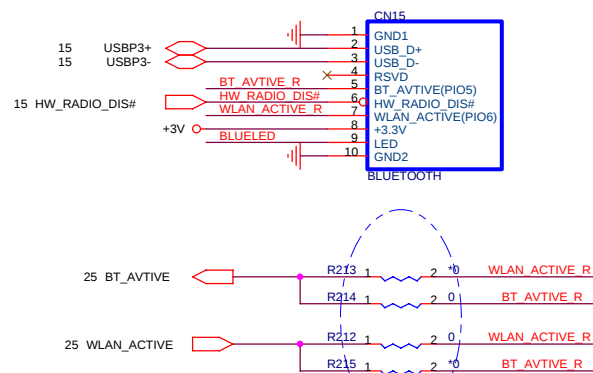


CPU_CORE
0.844V ~ 1.356V
27A

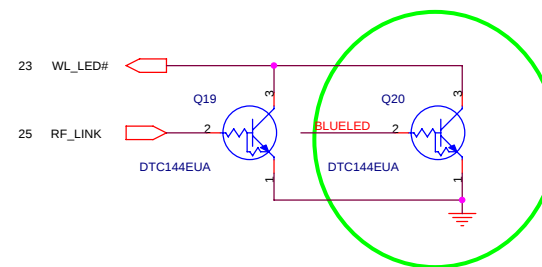
D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
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1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	0	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V



BLUETOOTH CONNECTOR

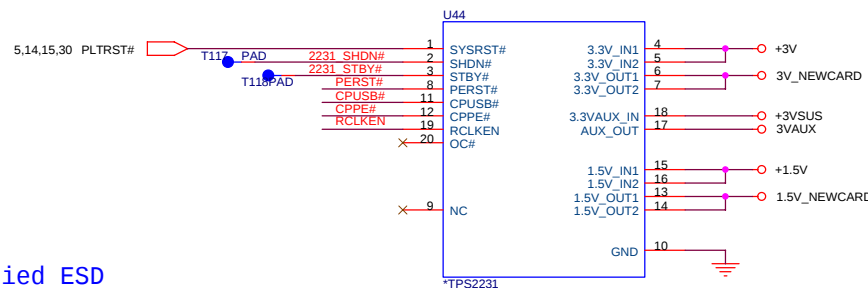
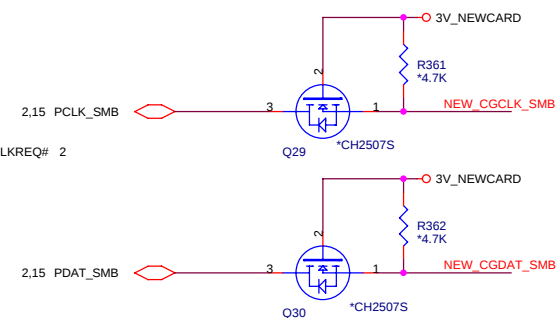
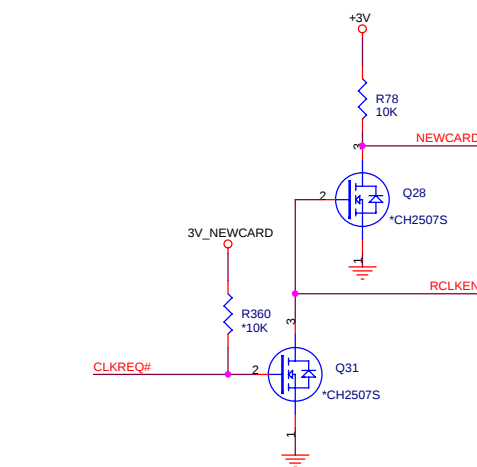
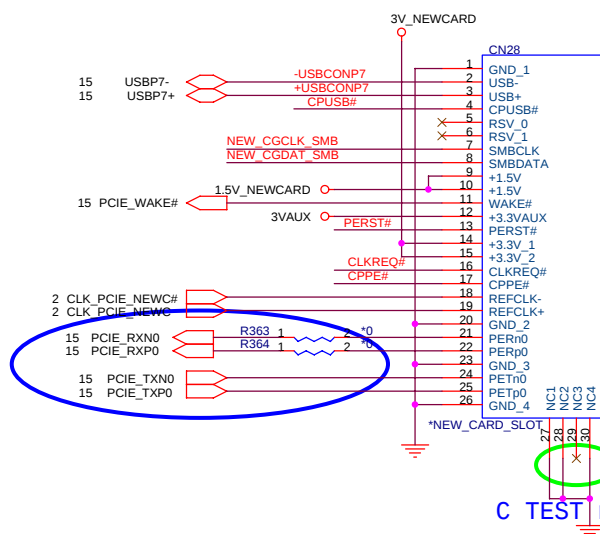


For bluetooth V1.2 spec option and combine intel wireless lan module use ,BC2-ext also provide the co-existence solution .



C TEST Modified

NEWCARD (PCIEXPRESS*1 + USB*1)



PROJECT : EF6
 Quanta Computer Inc.

Size Custom	Document Number	NEW CARD	Rev 1A
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