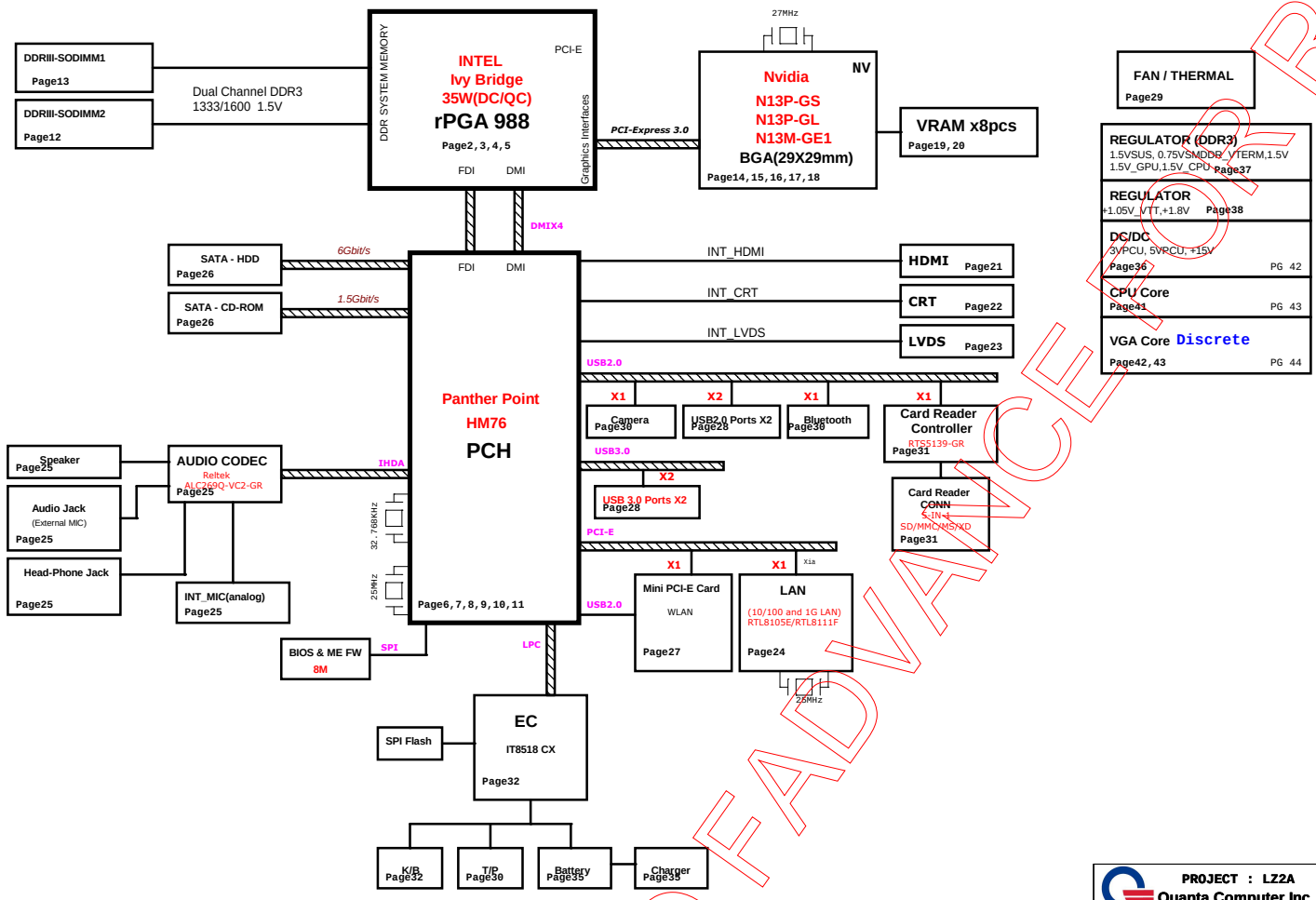


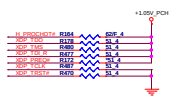
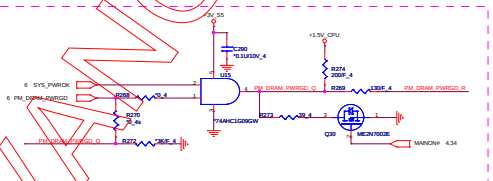
LZ3/LZ3A (Z580) Intel Chief River Platform (Optimus) Block Diagram

01



PEG_COMP connect to PIN J21 W:12mils/S:15mils/L: 500mils

PCI EXPRESS* - GRAPHICS

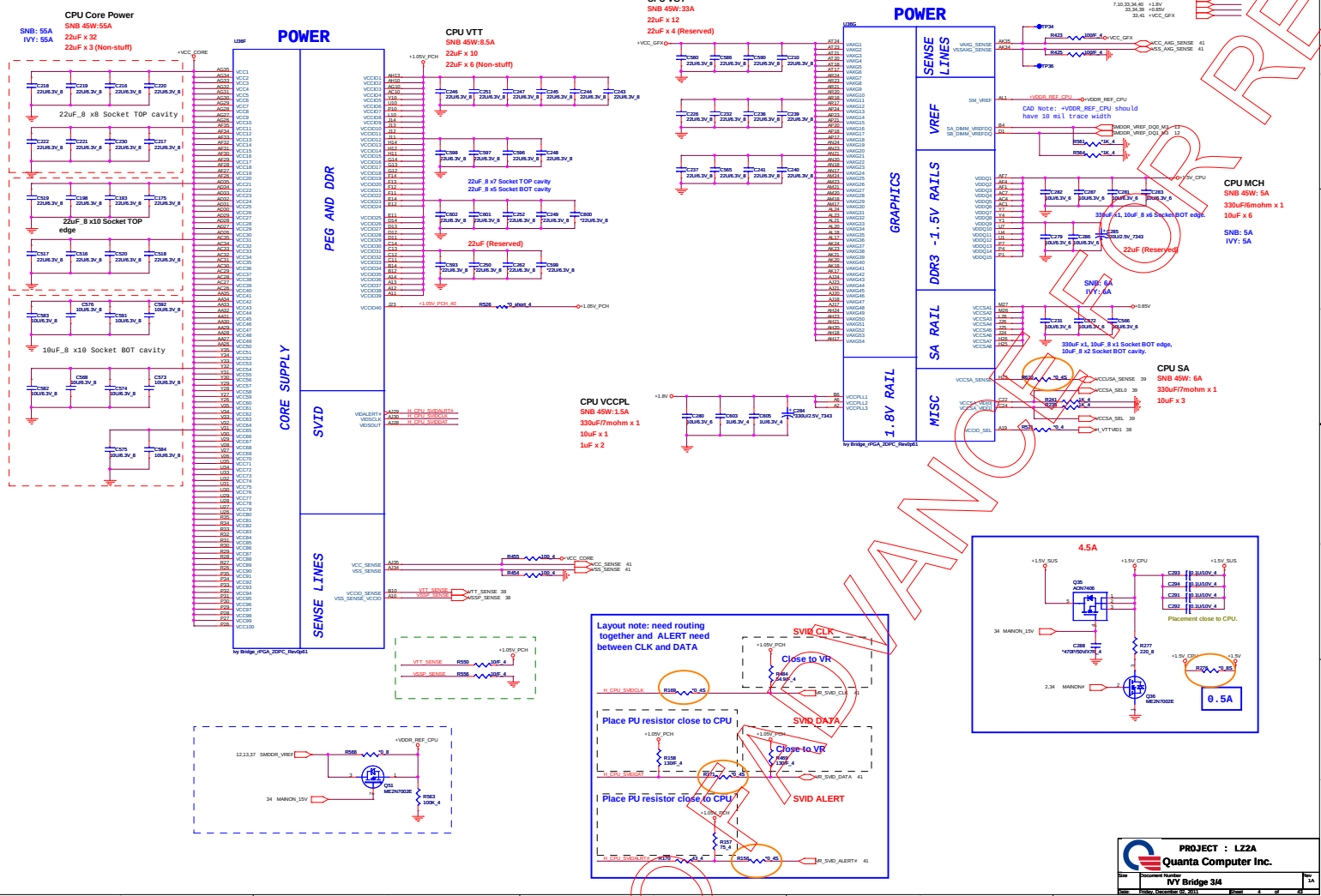


Ivy Bridge Processor (DDR3)

03

DDR SYSTEM MEMORY A

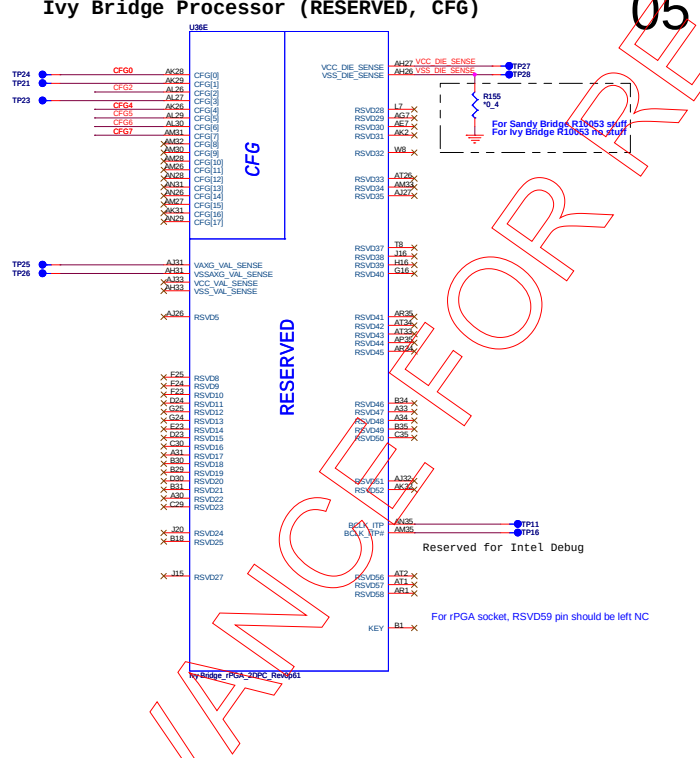
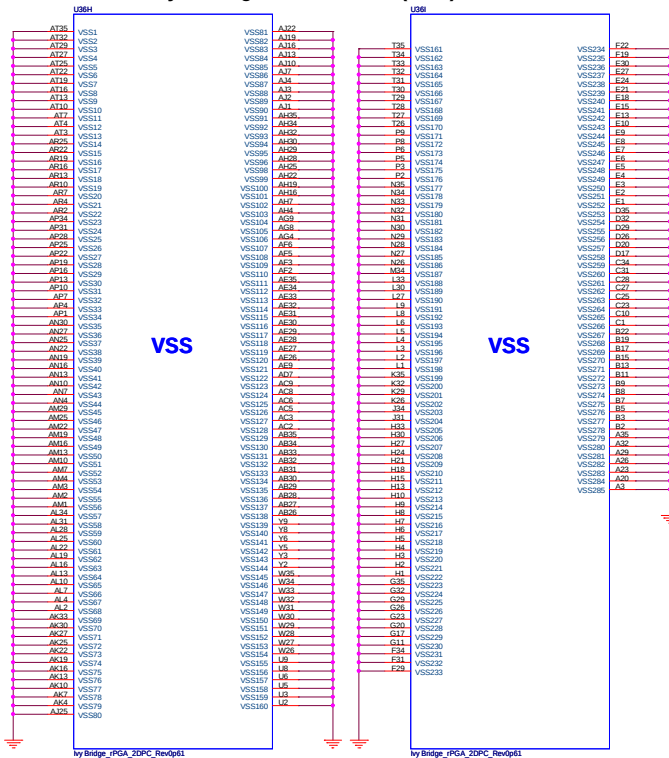
DDR SYSTEM MEMORY B



Ivy Bridge Processor (GND)

Ivy Bridge Processor (RESERVED, CFG)

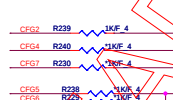
05



Processor Strapping

The CFG signals have a default value of '1' if not terminated on the board.

	1	0
CFG2 (PEG Static Lane Reversal)	Normal Operation	Lane Reversed
CFG4 (DP Presence Strap)	Disable; No physical DP attached to eDP	Enable; An ext DP device is connected to eDP
CFG7 (PEG Defer Training)	PEG train immediately following xxRESETB de assertion	PEG wait for BIOS training

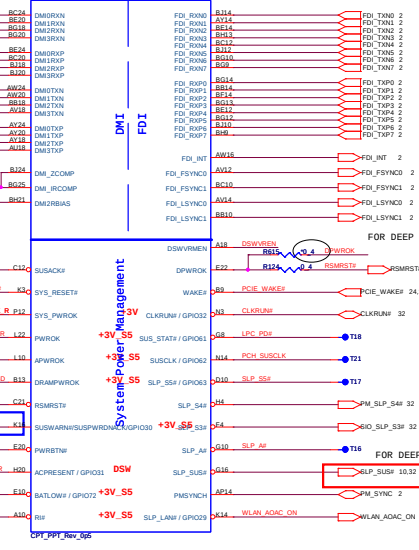


CFG[6:5] (PCIe Port Bifurcation Straps)

11: (Default) x16 - Device 1 functions 1 and 2 disabled
10: x8, x8 - Device 1 function 1 enabled; function 2 disabled
01: Reserved - (Device 1 function 1 disabled; function 2 enabled)
00: x8, x4, x4 - Device 1 functions 1 and 2 enabled

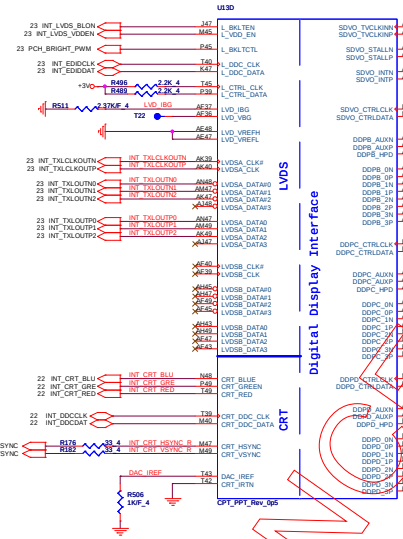
Cougar Point/Panther Point (DMI, FDI, PM)

U3C

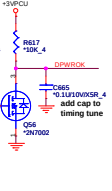


Cougar Point/Panther Point (LVDS, DDI)

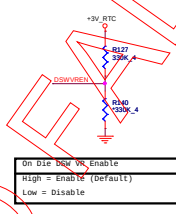
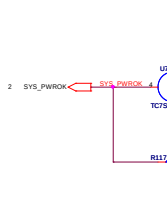
U3D



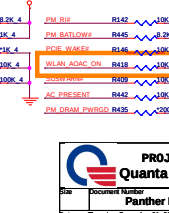
DPWROK FOR DSW (DEEP S3)



System PWR_OK(CLG)



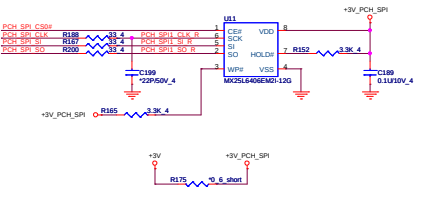
PCH Pull-high/low(CLG)



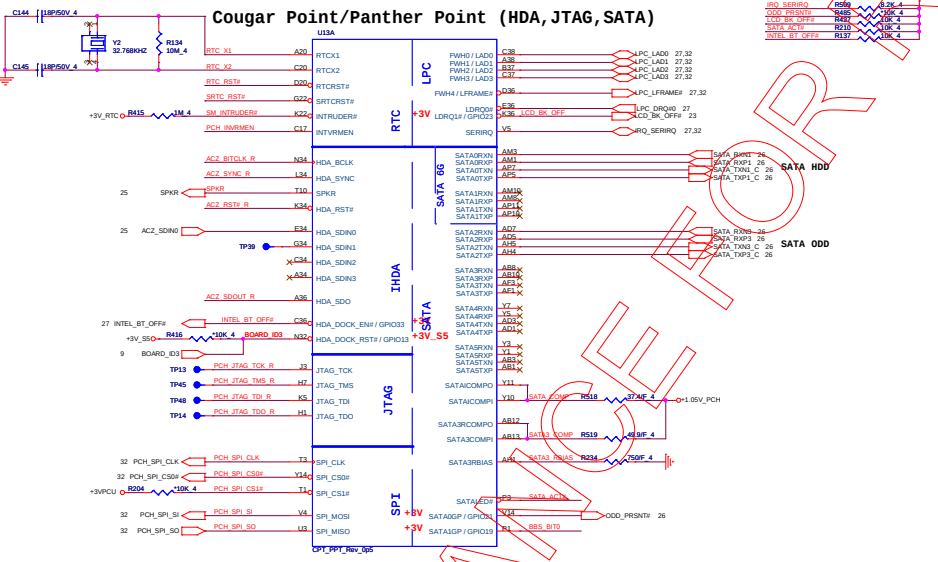
The diagram shows a battery charger circuit. A red oval highlights a loop containing the +5VPCU pin, the R54T resistor, and the +3V RTC 2 and +3V RTC 1 pins. A green label '20mils' points to this loop. Other components include a BAT54C diode, a 30mils loop with R40T, R41, and R42 resistors, and various capacitors (C404, C405, C406, C407, C408, C409, C410, C411, C412, C413, C414, C415, C416, C417, C418, C419, C420, C421, C422, C423, C424, C425, C426, C427, C428, C429, C430, C431, C432, C433, C434, C435, C436, C437, C438, C439, C440, C441, C442, C443, C444, C445, C446, C447, C448, C449, C450, C451, C452, C453, C454, C455, C456, C457, C458, C459, C460, C461, C462, C463, C464, C465, C466, C467, C468, C469, C470, C471, C472, C473, C474, C475, C476, C477, C478, C479, C480, C481, C482, C483, C484, C485, C486, C487, C488, C489, C490, C491, C492, C493, C494, C495, C496, C497, C498, C499, C500, C501, C502, C503, C504, C505, C506, C507, C508, C509, C510, C511, C512, C513, C514, C515, C516, C517, C518, C519, C520, C521, C522, C523, C524, C525, C526, C527, C528, C529, C530, C531, C532, C533, C534, C535, C536, C537, C538, C539, C540, C541, C542, C543, C544, C545, C546, C547, C548, C549, C550, C551, C552, C553, C554, C555, C556, C557, C558, C559, C560, C561, C562, C563, C564, C565, C566, C567, C568, C569, C570, C571, C572, C573, C574, C575, C576, C577, C578, C579, C580, C581, C582, C583, C584, C585, C586, C587, C588, C589, C590, C591, C592, C593, C594, C595, C596, C597, C598, C599, C600, C601, C602, C603, C604, C605, C606, C607, C608, C609, C610, C611, C612, C613, C614, C615, C616, C617, C618, C619, C620, C621, C622, C623, C624, C625, C626, C627, C628, C629, C630, C631, C632, C633, C634, C635, C636, C637, C638, C639, C640, C641, C642, C643, C644, C645, C646, C647, C648, C649, C650, C651, C652, C653, C654, C655, C656, C657, C658, C659, C660, C661, C662, C663, C664, C665, C666, C667, C668, C669, C670, C671, C672, C673, C674, C675, C676, C677, C678, C679, C680, C681, C682, C683, C684, C685, C686, C687, C688, C689, C690, C691, C692, C693, C694, C695, C696, C697, C698, C699, C700, C701, C702, C703, C704, C705, C706, C707, C708, C709, C710, C711, C712, C713, C714, C715, C716, C717, C718, C719, C720, C721, C722, C723, C724, C725, C726, C727, C728, C729, C730, C731, C732, C733, C734, C735, C736, C737, C738, C739, C740, C741, C742, C743, C744, C745, C746, C747, C748, C749, C750, C751, C752, C753, C754, C755, C756, C757, C758, C759, C760, C761, C762, C763, C764, C765, C766, C767, C768, C769, C770, C771, C772, C773, C774, C775, C776, C777, C778, C779, C780, C781, C782, C783, C784, C785, C786, C787, C788, C789, C790, C791, C792, C793, C794, C795, C796, C797, C798, C799, C800, C801, C802, C803, C804, C805, C806, C807, C808, C809, C810, C811, C812, C813, C814, C815, C816, C817, C818, C819, C820, C821, C822, C823, C824, C825, C826, C827, C828, C829, C830, C831, C832, C833, C834, C835, C836, C837, C838, C839, C840, C841, C842, C843, C844, C845, C846, C847, C848, C849, C850, C851, C852, C853, C854, C855, C856, C857, C858, C859, C860, C861, C862, C863, C864, C865, C866, C867, C868, C869, C870, C871, C872, C873, C874, C875, C876, C877, C878, C879, C880, C881, C882, C883, C884, C885, C886, C887, C888, C889, C890, C891, C892, C893, C894, C895, C896, C897, C898, C899, C900, C901, C902, C903, C904, C905, C906, C907, C908, C909, C910, C911, C912, C913, C914, C915, C916, C917, C918, C919, C920, C921, C922, C923, C924, C925, C926, C927, C928, C929, C930, C931, C932, C933, C934, C935, C936, C937, C938, C939, C940, C941, C942, C943, C944, C945, C946, C947, C948, C949, C950, C951, C952, C953, C954, C955, C956, C957, C958, C959, C960, C961, C962, C963, C964, C965, C966, C967, C968, C969, C970, C971, C972, C973, C974, C975, C976, C977, C978, C979, C980, C981, C982, C983, C984, C985, C986, C987, C988, C989, C990, C991, C992, C993, C994, C995, C996, C997, C998, C999, C1000, C1001, C1002, C1003, C1004, C1005, C1006, C1007, C1008, C1009, C1010, C1011, C1012, C1013, C1014, C1015, C1016, C1017, C1018, C1019, C1020, C1021, C1022, C1023, C1024, C1025, C1026, C1027, C1028, C1029, C1030, C1031, C1032, C1033, C1034, C1035, C1036, C1037, C1038, C1039, C1040, C1041, C1042, C1043, C1044, C1045, C1046, C1047, C1048, C1049, C1050, C1051, C1052, C1053, C1054, C1055, C1056, C1057, C1058, C1059, C1060, C1061, C1062, C1063, C1064, C1065, C1066, C1067, C1068, C1069, C1070, C1071, C1072, C1073, C1074, C1075, C1076, C1077, C1078, C1079, C1080, C1081, C1082, C1083, C1084, C1085, C1086, C1087, C1088, C1089, C1090, C1091, C1092, C1093, C1094, C1095, C1096, C1097, C1098, C1099, C1100, C1101, C1102, C1103, C1104, C1105, C1106, C1107, C1108, C1109, C1110, C1111, C1112, C1113, C1114, C1115, C1116, C1117, C1118, C1119, C1120, C1121, C1122, C1123, C1124, C1125, C1126, C1127, C1128, C1129, C1130, C1131, C1132, C1133, C1134, C1135, C1136, C1137, C1138, C1139, C1140, C1141, C1142, C1143, C1144, C1145, C1146, C1147, C1148, C1149, C1150, C1151, C1152, C1153, C1154, C1155, C1156, C1157, C1158, C1159, C1160, C1161, C1162, C1163, C1164, C1

[illegible]

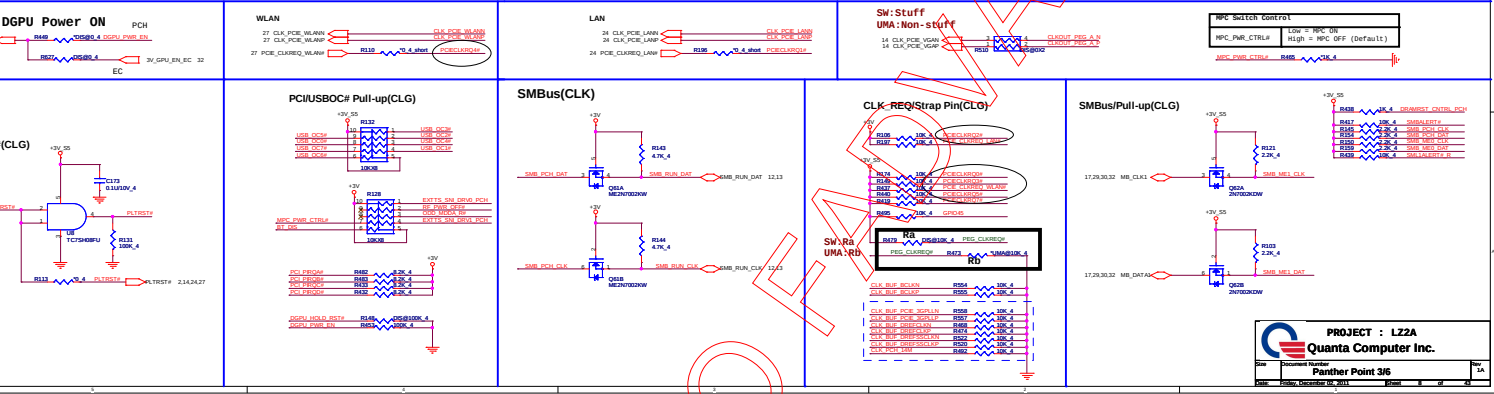
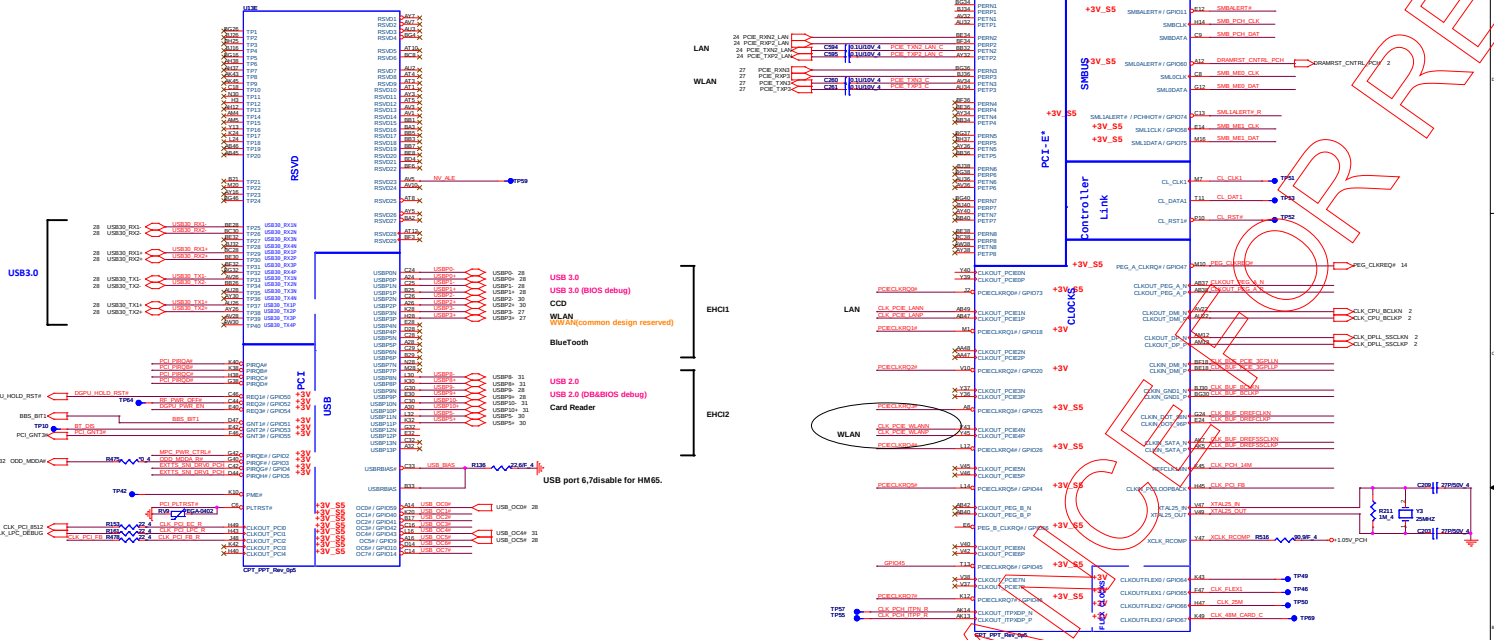
32Mbit (8M Byte), SPI



C144 HSP50V_4 Cougar Point/Panther Point (HDA, JTAG, SATA)



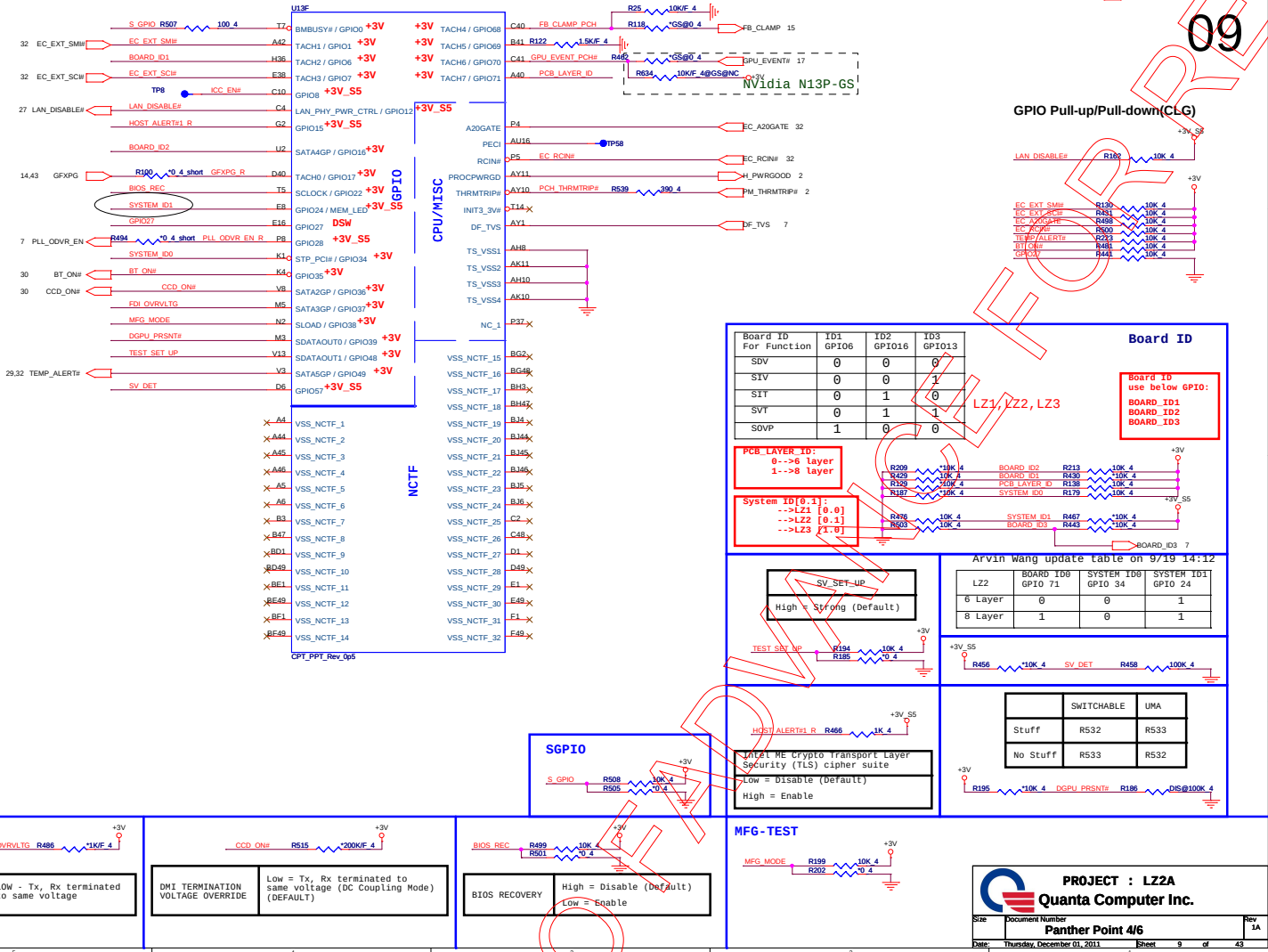
Pin Name	Strap description	Sampled	Configuration										
SPKR	No reboot mode setting	PWR0K	0 = Default (weak pull-down 20K) 1 = Setting to No-Reboot mode	+3V_5K  R514      R515									
GNT3# / GPIO55	Top Block Swap Override	PWR0K	0 = "Top-block swap" mode 1 = Default (weak pull-up 20K)	+3V_5K  R517      PC0_GNT3#									
INTRVMEN	Integrated 1.05V VRM enable	ALWAYS	Should be always pull-up	+3V_5K  R514      PC0_INTRVMEN									
GNT1# / GPIO1	Boot BIOS Selection 1 [bit-1]	PWR0K	<table border="1" data-bbox="809 840 949 875"> <thead> <tr> <th>GNT3#</th> <th>GNT1#</th> <th>Boot location</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>1</td> <td>SPI</td> </tr> <tr> <td>0</td> <td>0</td> <td>LPC</td> </tr> </tbody> </table> 0 = Default weak pull-up on GNT01# (Need external pull-down for LPC BIOS)	GNT3#	GNT1#	Boot location	1	1	SPI	0	0	LPC	+3V_5K  R514      PC0_GNT1#
GNT3#	GNT1#	Boot location											
1	1	SPI											
0	0	LPC											
GPIO19	Boot BIOS Selection 0 [bit-0]	PWR0K		+3V_5K  R513      R512									
HDA_SDO	Flash Descriptor Security	RSMRST	0 = Override 1 = Default (weak pull-up 20K)	+3V_5K  R411      AC2_SDO0T_B									
DF_VSS	DMI/FDI Termination voltage	PWR0K	0 = Set to Vss 1 = Set to Vcc (weak pull-down 20K)	+3V_5K  R514      DF_VSS_9									
GPIO28	On-die PLL Voltage Regulator	RSMRST#	0 = Disable 1 = Enable (Default)	+3V_5K  R490      PLL_D0VR_EN_9									
HDA_SYNC	On-Die PLL VR Voltage Select	RSMRST	0 = Support by 1.8V (weak pull-down) 1 = Support by 1.5V	+3V_5K  R413      AC2_SYNC_B									
GPIO8	Integrated Clock Chip Enable	RSMRST	Should be pull-down (weak pull-up 20K)										
SPI_MOSI	ITPM function Disable	APW0K	0 = Default (weak pull-down 20K) 1 = Enable	+3V_5K  R516      PC0_SPI_SI									
NV_ALE	Intel Anti-Theft HDD protection	PWR0K	0 = Disable (Internal pull-down 20kohm)										



Cougar Point/Panther Point (GPIO,VSS_NCTF,RSVD)

6,7,8,10,12,13,14,15,17,21,22,23,24,25,26,27,29,30,31,32,33,34,37,38,41,42,43 2,6,7,8,10,27,31,34 +3V_SS +3V

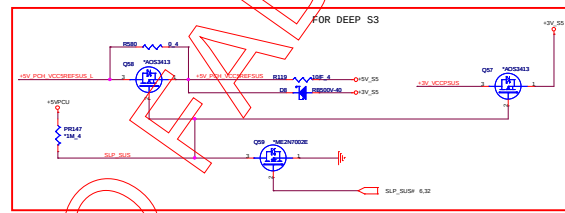
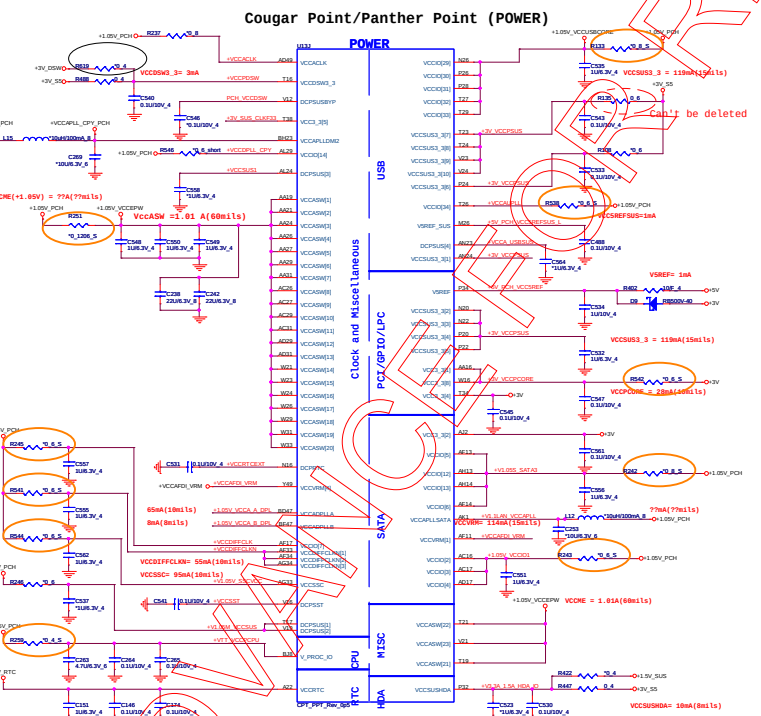
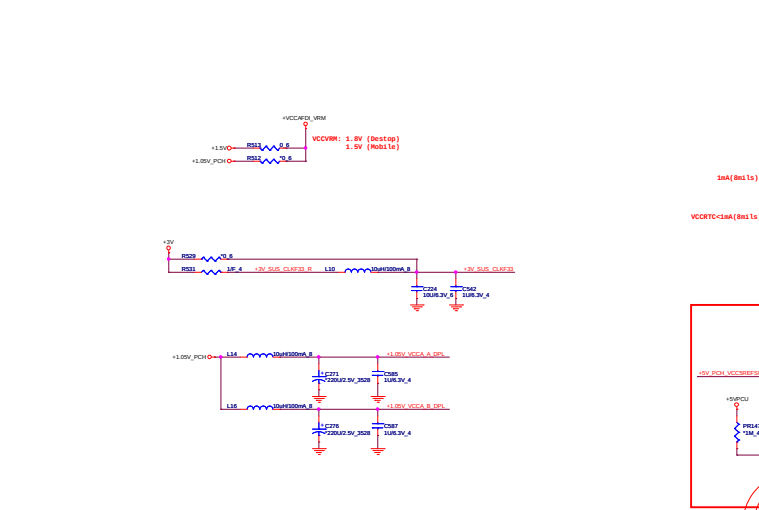
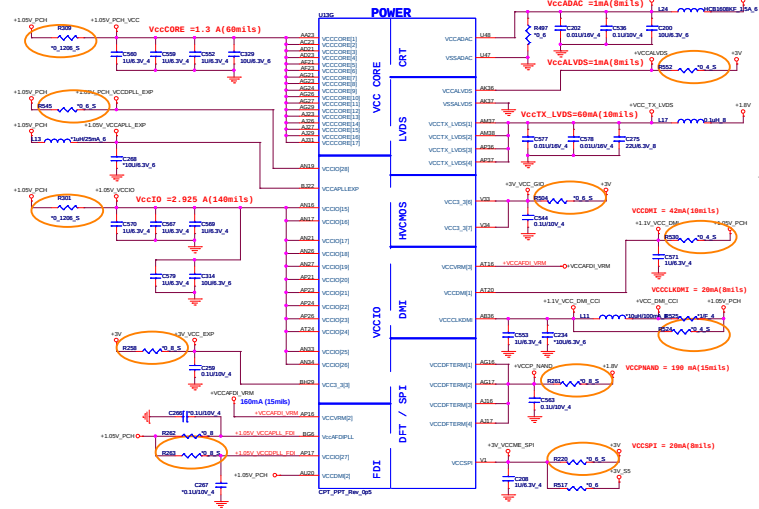
09



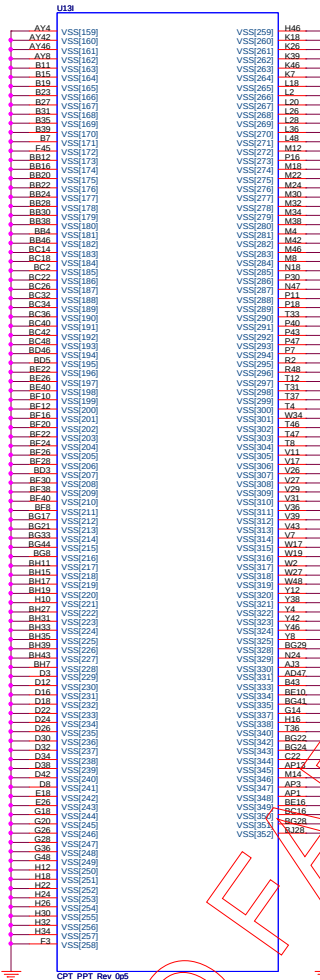
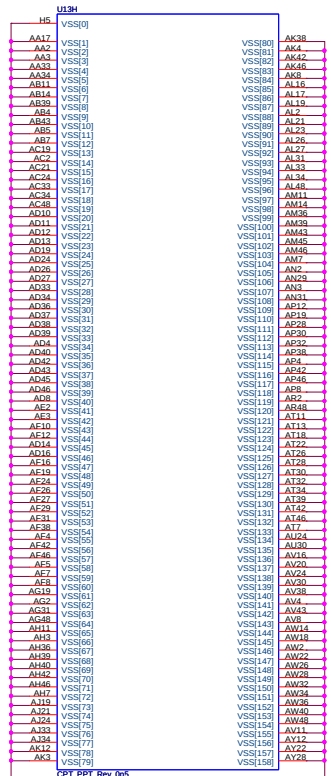
CONFIDENTIAL

Cougar Point/Panther Point (POWER)

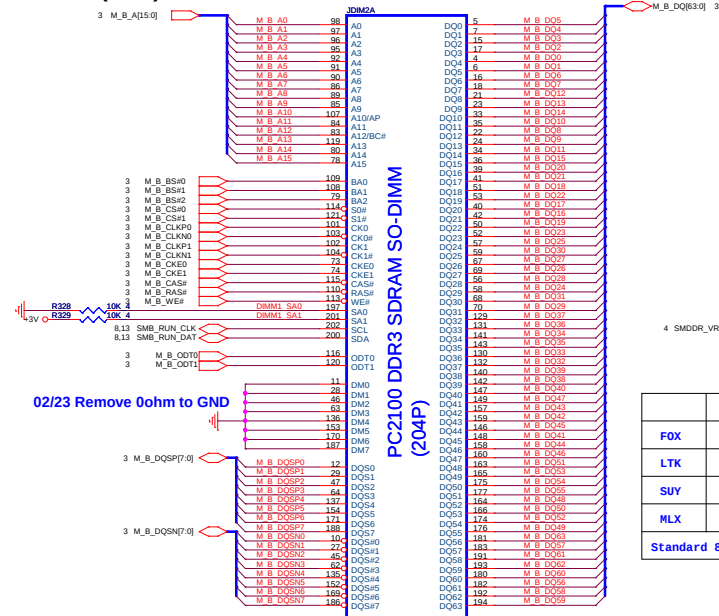
2.457,8,3134,35,43 +1.05V_PCH
6,7,8,12,13,14,15,17,21,22,23,24,25,26,27,29,30,31,32,33,34,37,38,41,42,43 +3V
12,18,19,20,22,32,33,34,37,38,41,42,43 +5V_RTC
22,26,28,31,33,34 +5V_SS
1,2,3,22,31,32,33,34,35 +5V



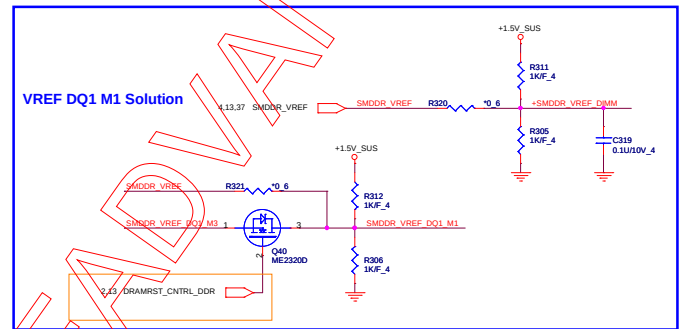
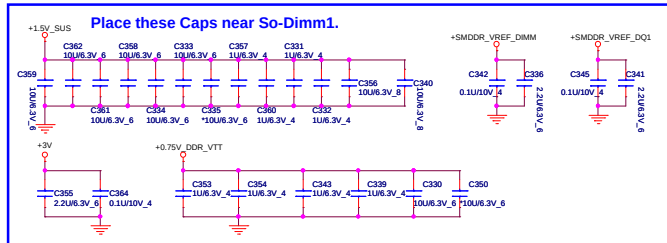
Cougar Point/Panther Point (GND)



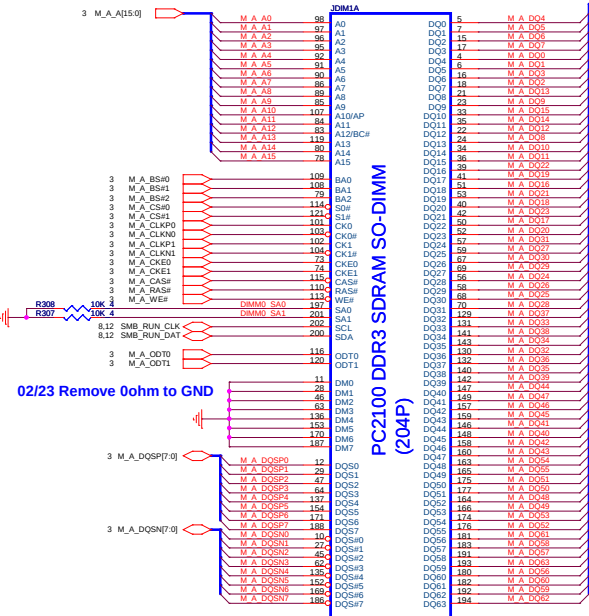
DDR_RVS (DDR)



	STD 4H	STD 8H
FOX		
LTK	DGMK4000004	DGMK4000097
SUY		
MLX	DGMK4000011	DGMK4000080
Standard 8H type:DDR-C-2013310-204p-1		



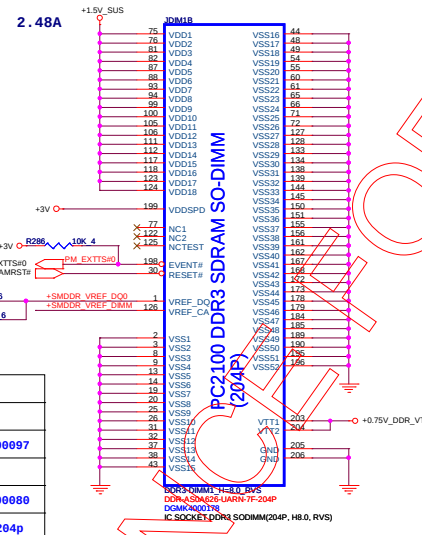
DDR_RVS (DDR)



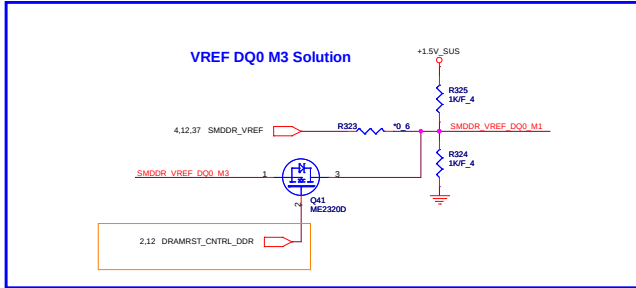
02/23 Remove 0ohm to GND

	STD 4H	STD 8H
FOX		
LTX	DGMK4000004	DGMK4000007
SUY		
MLX	DGMK4000011	DGMK4000080

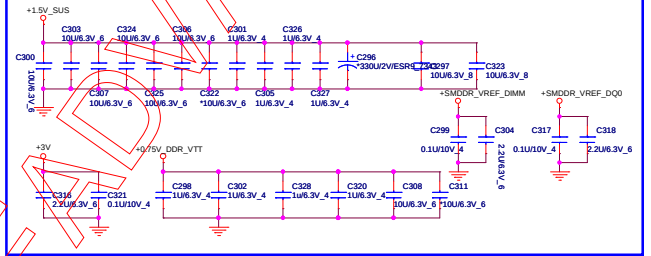
Standard 4H type:DDR-C-2013289-204p

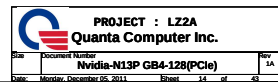
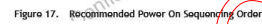
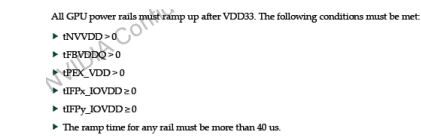


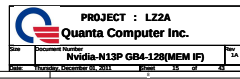
VREF DQ0 M3 Solution

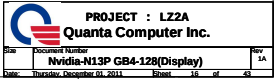


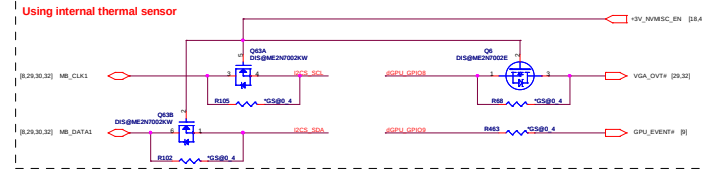
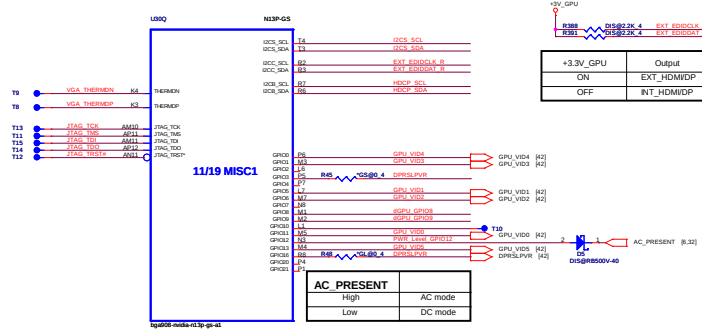
Place these Caps near So-Dimm0.











	Q6, Q63, R81, R104	R68, R102, R105, R463
N13P-GS	Unstuff	Stuff
N13P-GL		
N13M-GE1	Stuff	Unstuff

Table 5. Stuffing Options

GPU	signal/Rail	Stuffing Option
H13P-GT/-GS/-LP, H14P-Q1-Q3	I2C and GPIO	No stuff FET Stuff Q0 bypass resistor
	3V3MISC	Stuff FET No stuff Q0 bypass resistor
Other H13P and H13M	I2C and GPIO	Stuff FET No stuff Q0 bypass resistor
	3V3MISC	No stuff FET Stuff Q0 bypass resistor

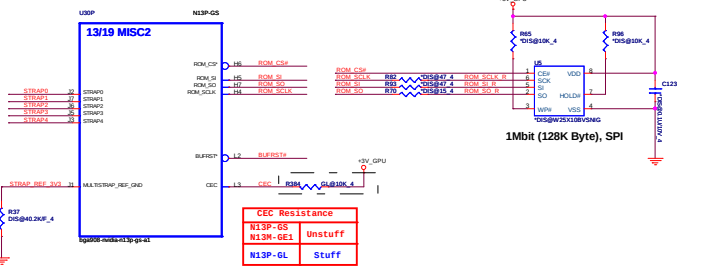


Table 2. GB4-128 Ballout Compatibility

Ball Number	N13P-PES/-GL/-N51 Signal Names	N13M-GE1 Signal Names	N13P-GV / N13M-GS Signal Names	N13P-GT/-GS/-LP and N14P-Q1/-Q3 Signal Names	Comment
L3	CEC	NC	NC	NC	Place a 10k pull-up to 3V3 on N13P-PES/-GL/-N51.

	Logical Strapping B#3	Logical Strapping B#4	Logical Strapping B#1	Logical Strapping B#0
ROM_50	SCUR_4[2:0]	PE_3[2:0]	SRM_7[2:0]	XXXX
ROM_SCLK	PCI_DEVCFG[4]	SUB_VEN[2]	SLOT_CLK_CFG_0	REL_PLX_ERM_TERM
ROM_S1	RAMCFG[0]	RAMCFG[0]	RAMCFG[0]	RAMCFG[0]
STRAP0	USER[0]	USER[2]	USER[1]	USER[0]
STRAP1	PCI_DEVCFG[0]	PCI_DEVCFG[0]	PCI_DEVCFG[0]	PCI_DEVCFG[0]
STRAP2	PCI_DEVCFG[1]	PCI_DEVCFG[2]	PCI_DEVCFG[1]	PCI_DEVCFG[0]
STRAP3	S0R0_EXPOSED	S0R2_EXPOSED	S0R0_EXPOSED	S0R0_EXPOSED
STRAP4	RESERVED	PCI_EE_SPEED_CHANGE_GEN3	PCI_EE_MAX_SPEED	DR_PLL_VDD3V3

VRAM Configuration Table

ROM_SI Strap Bit for RAM Mapping

VRAM Configure	Quanta PN(Q buy)	Quanta PN(W buy)	Vendor PN	RAMCFG [3,0]	
900MHz 2GB(128*16) Samsung	AKDSMGWT500		K4W2G1646C-HC11	0x7(0111)	R8
900MHz 2GB(128*16) Hynix	AKDSMGWTW00		H5TQ2G63BFR-11C	0x6(0110)	R8
900MHz 1GB(64*16) Samsung	AKDSEGT500		K4W1G1646G-BC11	0x3(0011)	R8
900MHz 1GB(64*16) Hynix	AKDSLZWTW02		H5TQ1G63DFR-11C	0x2(0010)	R8

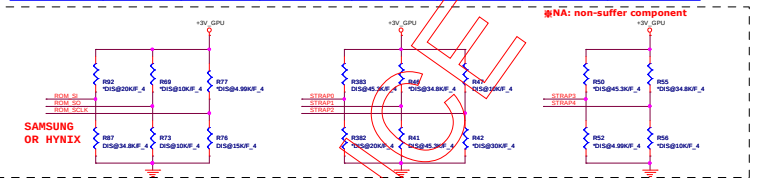
Resistance	Quanta PN	DESCRIPTION
4.99K/F	CS2490ZF026	RES CHIP 4.99K 1/16W +1% (0402)
10K/F	CS3100ZF026	RES CHIP 10K 1/16W +1% (0402)
15K/F	CS3150ZF024	RES CHIP 15K 1/16W +1% (0402)
20K/F	CS3200ZF026	RES CHIP 20K 1/16W +1% (0402)
24.9K/F	CS3249ZF016	RES CHIP 24.9K 1/16W +1% (0402)
30K/F	CS3300ZF013	RES CHIP 30K 1/16W +1% (0402)
34.8K/F	CS3348ZF022	RES CHIP 34.8K 1/16W +1% (0402)
45.3K/F	CS3453ZF016	RES CHIP 45.3K 1/16W +1% (0402)

Res	PU	PD
5K	1000	0000
10K	1001	0001
15K	1010	0010
20K	1011	0011
25K	1100	0100
30K	1101	0101
35K	1110	0110
45K	1111	0111

GPU Model Strap Table

CPU Model	ROM_IO	ROM_SCLK	STRAP0	STRAP1	STRAP2	STRAP3	STRAP4
N13P-GE-A1 (GF119)	R73 (30K ohm) PD	R77 (4.95K ohm) PU	R303 (45.3K ohm) PU	R41 (34.8K ohm) PD	R47 (4.99K ohm) PU	R52 (4.95K ohm) PD	R56 (10K ohm) PD
N13P-GL-A1 (GF106)	R73 (10K ohm) PD	R76 (15K ohm) PD	R303 (45.3K ohm) PU	R41 (45.3K ohm) PU	R47 (10K ohm) PU	NA	NA
R69 (GS-A2) (GK107)	R69 (10K ohm) PD	R77 (4.95K ohm) PU	R303 (45.3K ohm) PU	R41 (34.8K ohm) PD	R42 (15K ohm) PU	R52 (4.95K ohm) PD	R56 (10K ohm) PD

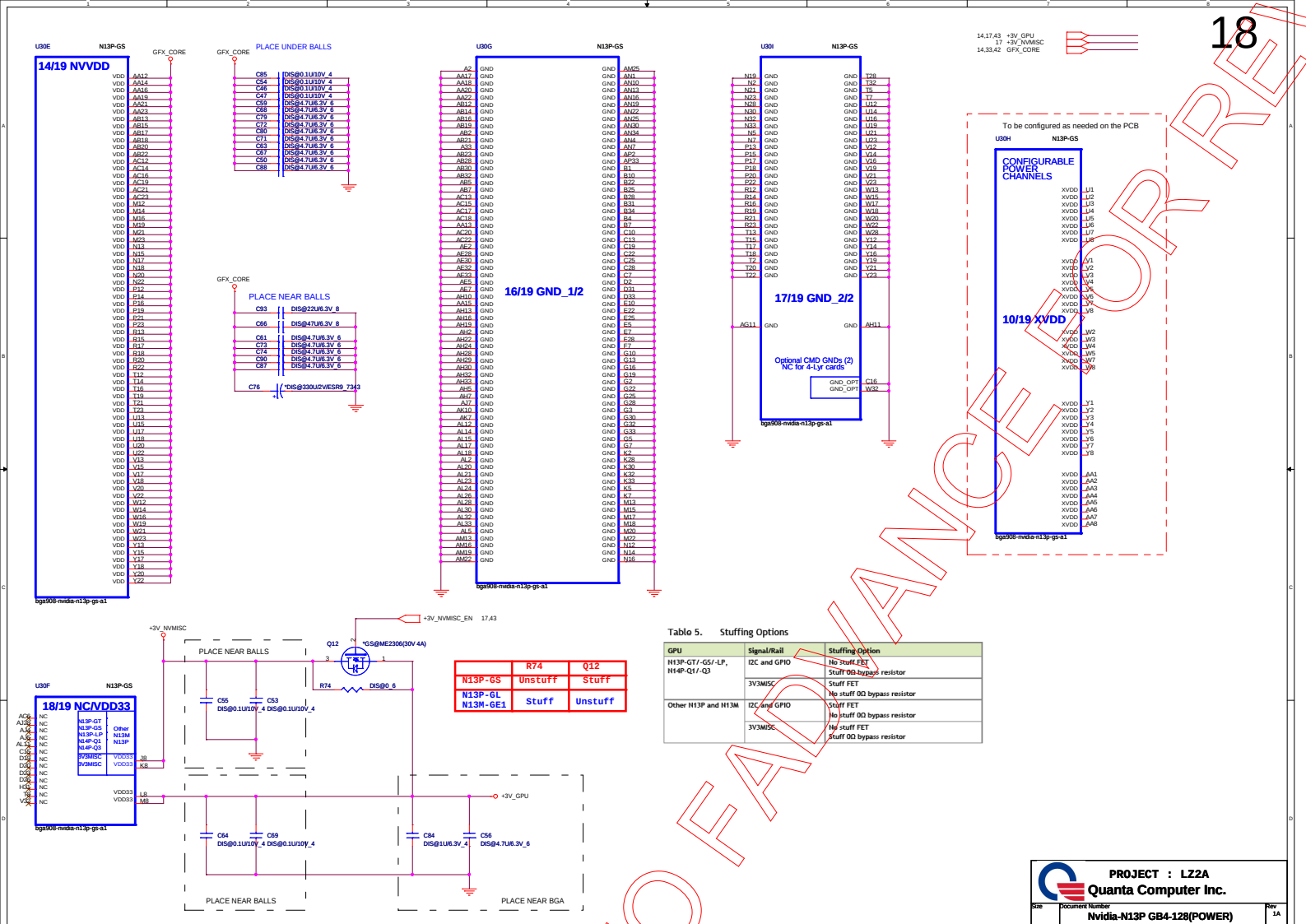
GPU Model Strap Table

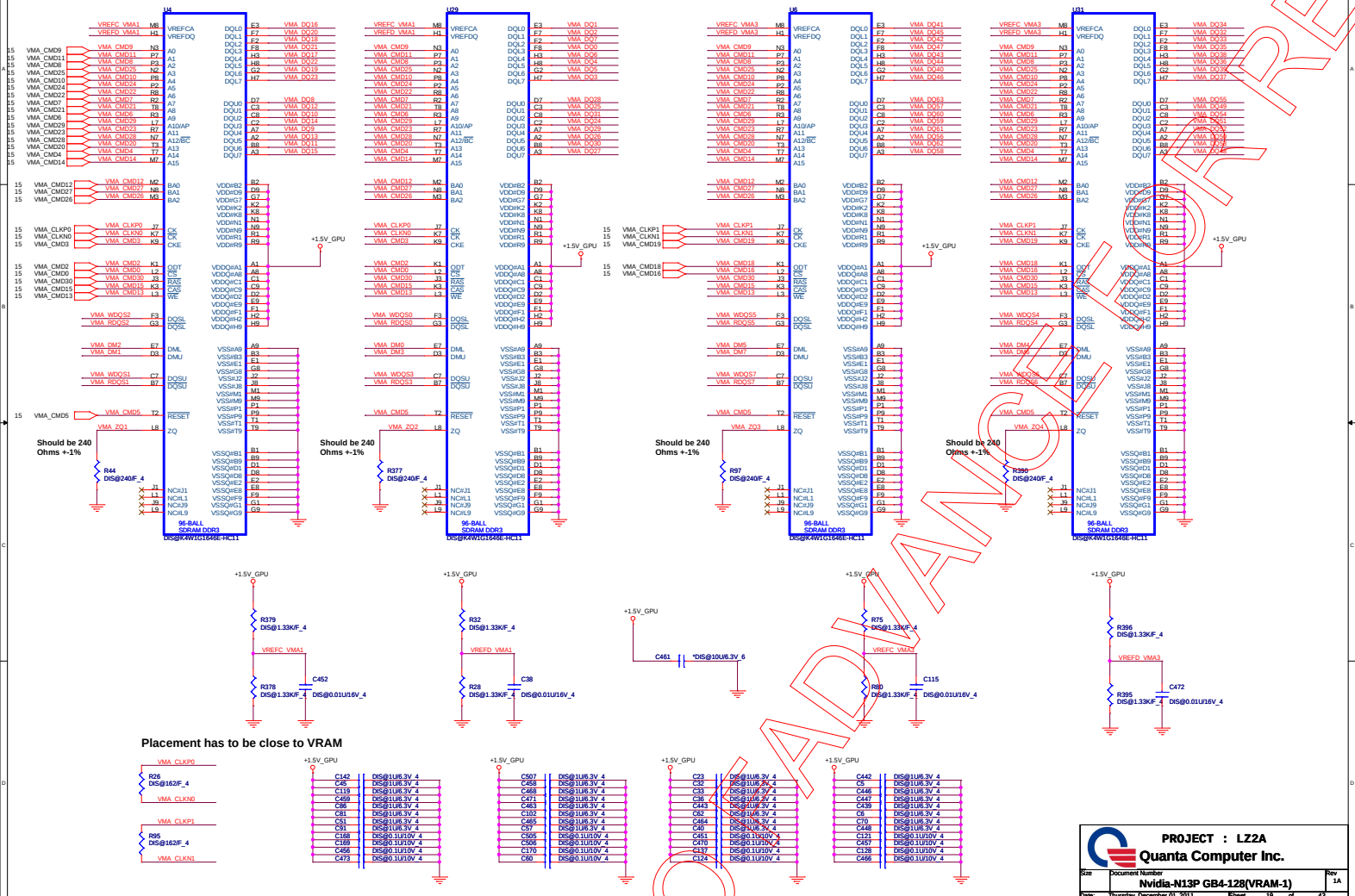


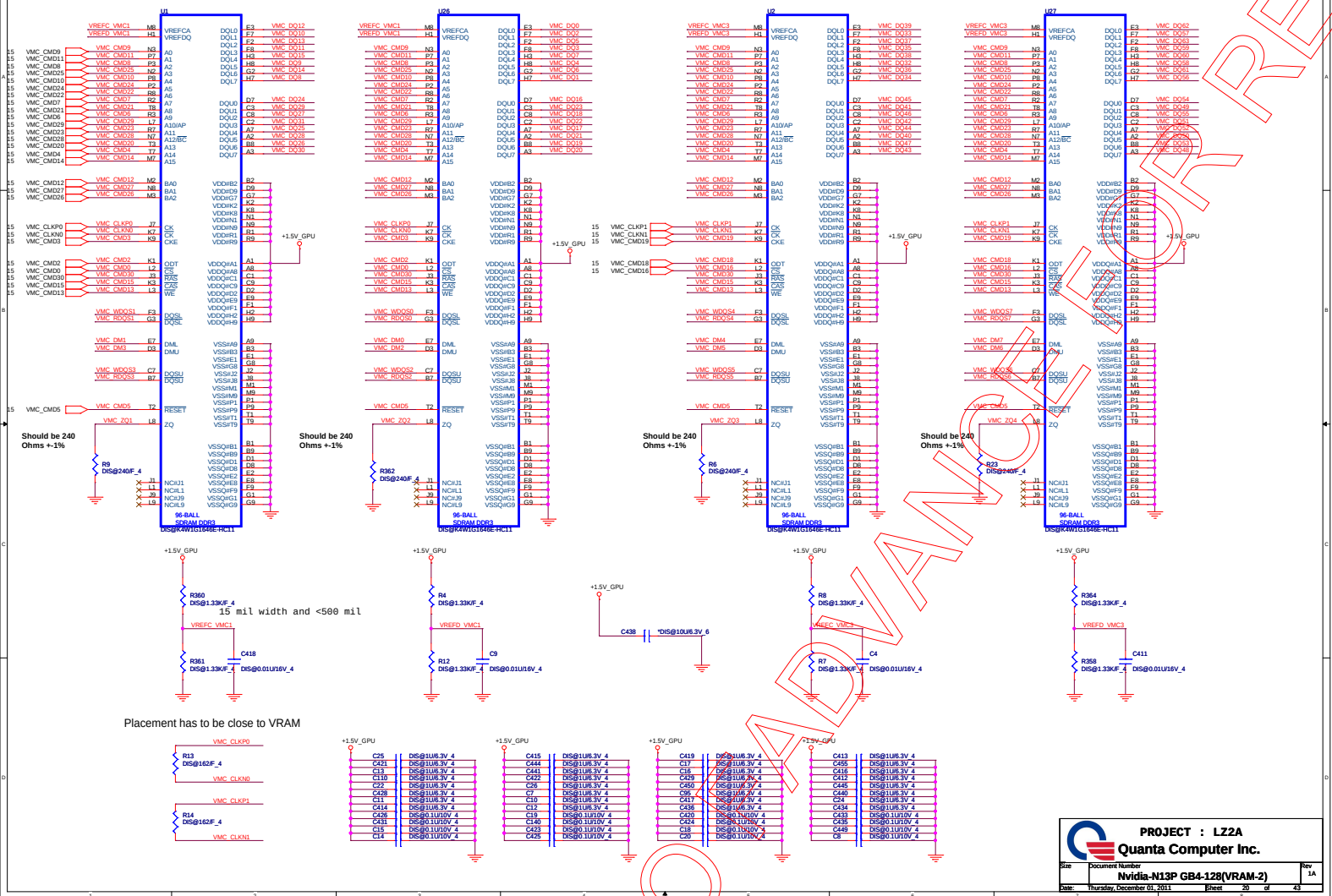
GPIO ASSIGNMENTS

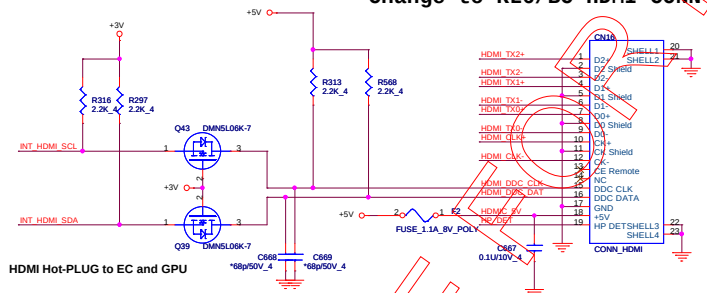
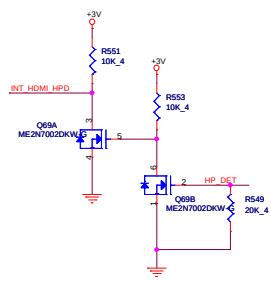
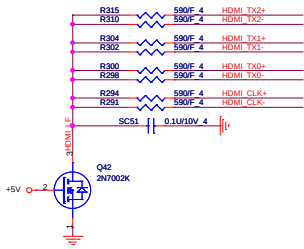
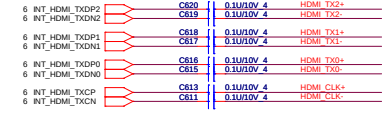
NVDD Table	N13M-GE1-A1 (GF119)	N13P-GL-A1 (GF108)	N13P-GS-A1 (GF107)
	NVDD (8.8V)	NVDD (8.8V)	NVDD (8.8V)
GPU_Y84	0 (R46)	0 (R46)	0 (R66)
GPU_Y91	0 (R52)	0 (R52)	0 (R52)
GPU_Y92	0 (R56)	1 (R56)	0 (R56)
GPU_Y93	0 (R57)	1 (R54)	0 (R57)
GPU_Y94	1 (R71)	0 (R40)	1 (R71)
GPU_YD7	1 (R385)	1 (R385)	1 (R385)

GPIO pin	Normal Function	I/O	Functional Description
GPIO0	GPU_VSD1	O	GPU Core VSD1
GPIO1	GPU_VSD3	O	GPU Core VSD3
GPIO2	LCD_BB_PWM	O	Panel Backlight PWM Brightness Control
GPIO3	LCD_BECH or PSI	O	Panel Power Enable or Phase Spreading
GPIO4	LCD_BECH	O	Panel Backlight Enable
GPIO5	GPU_VSD1	O	GPU Core VSD1
GPIO6	GPU_VSD2	O	GPU Core VSD2
GPIO7	3D Vision	O	3D Vision Left/Right signal
GPIO8	OVERT	O	Active Low Thermal Catastrophic Over Temperature
GPIO9	ALERT	O	Active Low Thermal Alert
GPIO10	MEM_VREF_CTL	O	Memory VREF Control
GPIO11	GPU_VSD0	O	GPU Core VSD0
GPIO12	PWR_LED	I	LED power detect or power supply over/under input
GPIO13	GPU_VSD5	O	GPU Core VSD5
GPIO14	HPF_AB	I	Hot Plug Detect for HPFAB
GPIO15	HPF_BB	I	Hot Plug Detect for HPFBB
GPIO16	PSI or MEM_VSD_CTL	O	Phase Spreading or Memory VSD Control
GPIO17	HPF_D	I	Hot Plug Detect for HPFD
GPIO18	HPF_E	I	Hot Plug Detect for HPFE
GPIO19	HPF_F	I	Hot Plug Detect for HPFF
GPIO20	Reserved		
GPIO21	Reserved		



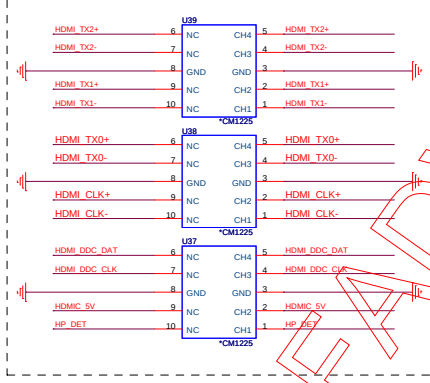




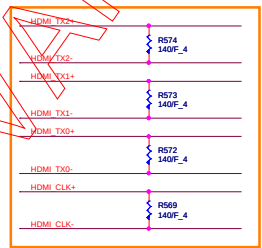


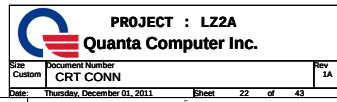
HDMI Hot-PLUG to EC and GPU

For ESD

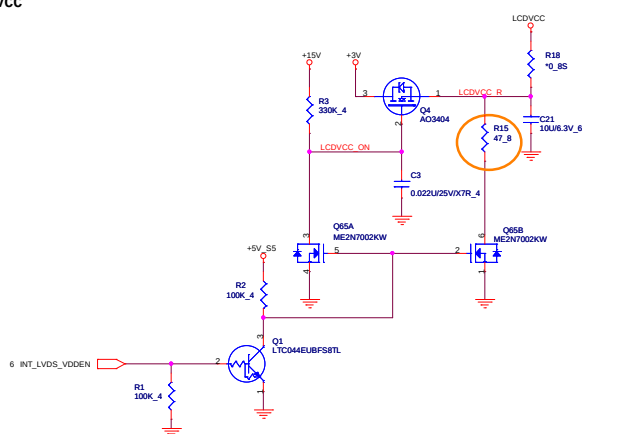


EMI reserve for HDMI

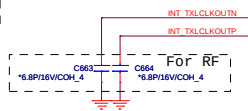
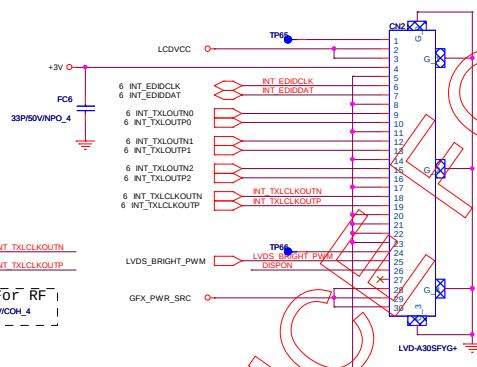




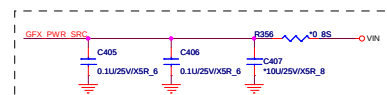
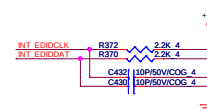
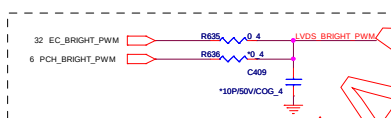
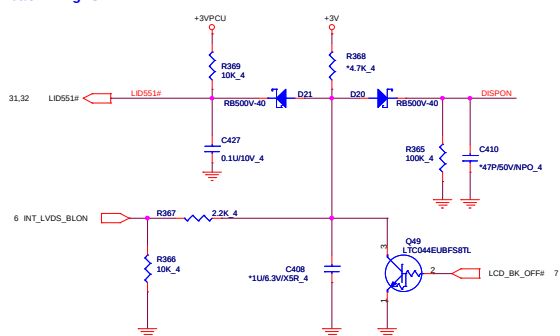
LCDVCC

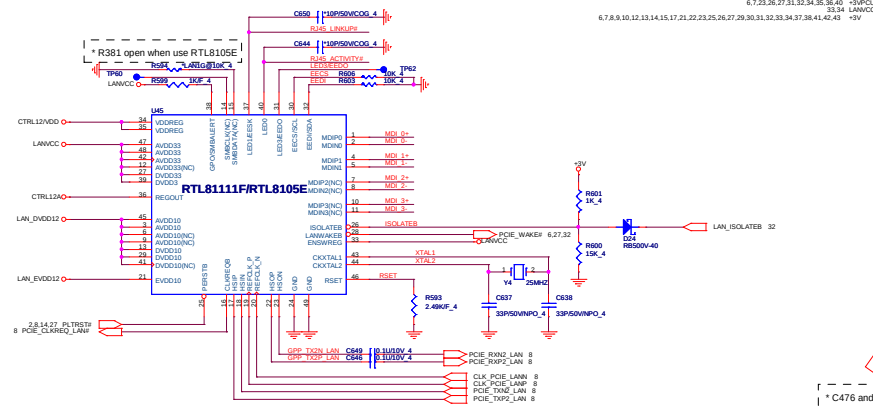
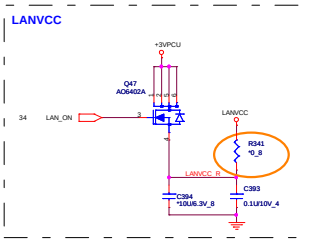


26,30,34,36,43 +15V
37,38,41,42,43 +3V
10,26,28,31,33,34 +5V_S5
6,7,24,26,27,31,32,34,35,36,40 +3VPCU
33,35,36,37,38,39,41,42 VIN

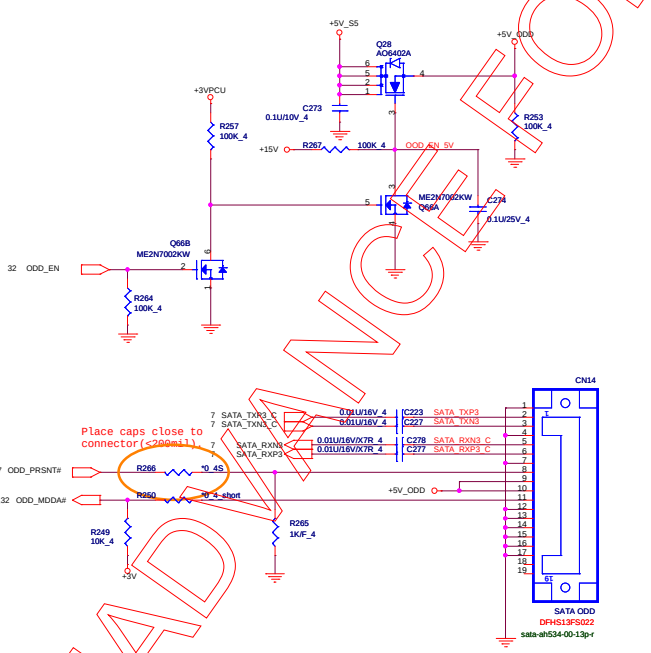
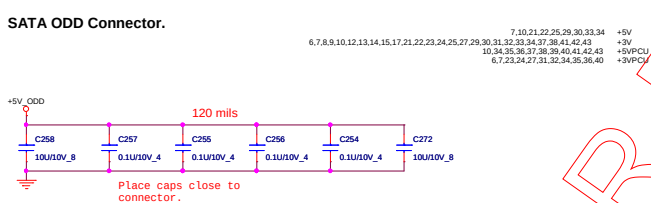


back light

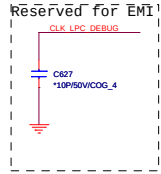
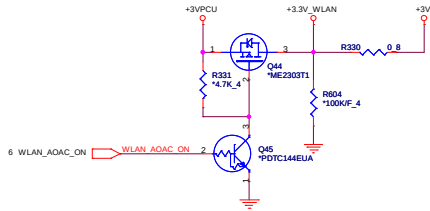
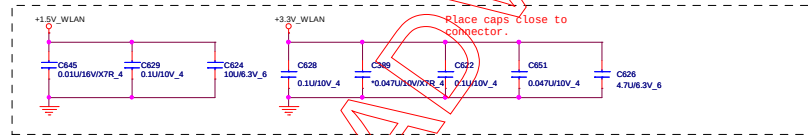
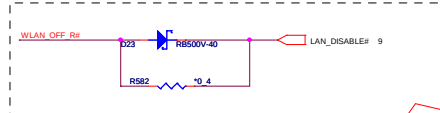
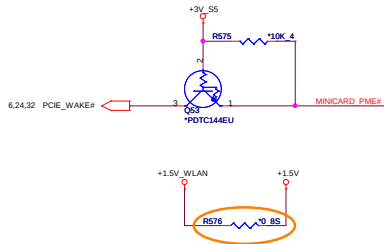




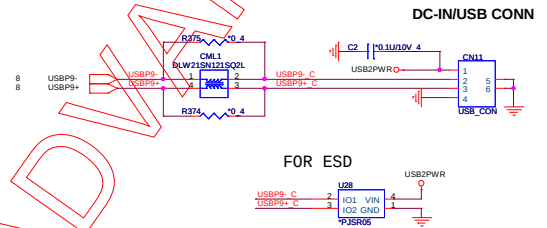
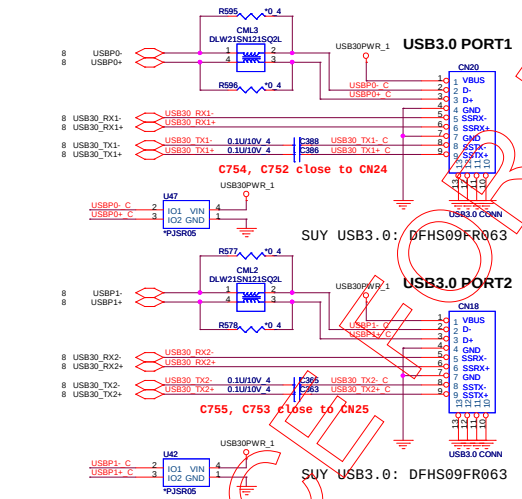
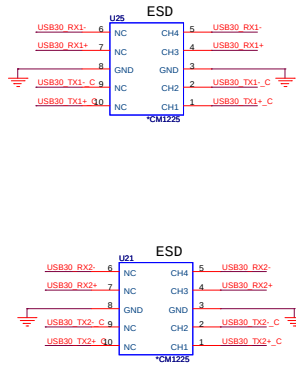
~~26~~



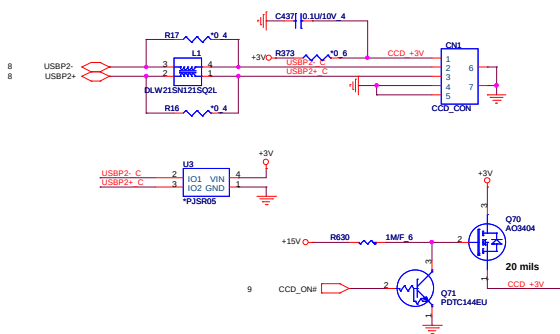
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	Quanta Computer Inc.		
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PCI-Express TX and RX
direct to connector

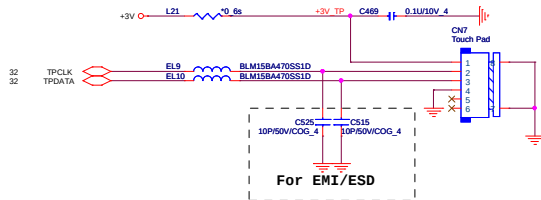
USB2.0*1



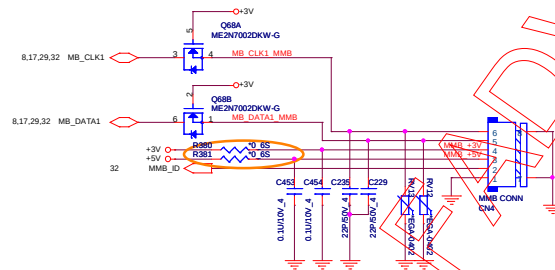
CCD BOARD



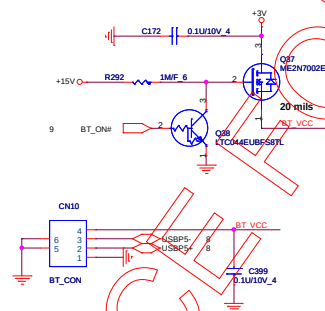
Touch pad



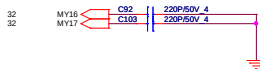
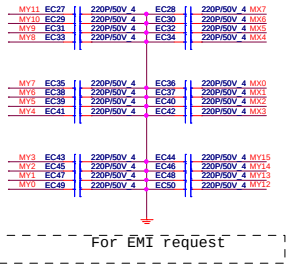
MMB



BLUETOOTH



MX1	MX1	30	30
MX5	MX5	28	28
MX6	MX6	29	29
MY9	MY9	27	27
MX4	MX4	27	27
MX5	MX5	25	25
MY0	MY0	24	24
MX5	MX5	23	23
MX3	MX3	22	22
MX5	MX5	21	21
MY1	MY1	20	20
MX0	MX0	19	19
MX2	MX2	18	18
MY4	MY4	17	17
MX3	MX3	16	16
MY8	MY8	15	15
MY9	MY9	14	14
MY5	MY5	13	13
MY12	MY12	12	12
MY13	MY13	11	11
MY14	MY14	10	10
MY11	MY11	9	9
MY10	MY10	8	8
MY15	MY15	7	7
MY16	MY16	6	6
MY17	MY17	5	5
		4	4
		3	3
		2	2
		1	1
		0	0



The schematic diagram illustrates the electrical connections for the Card Reader BOARD. Key components and connections include:

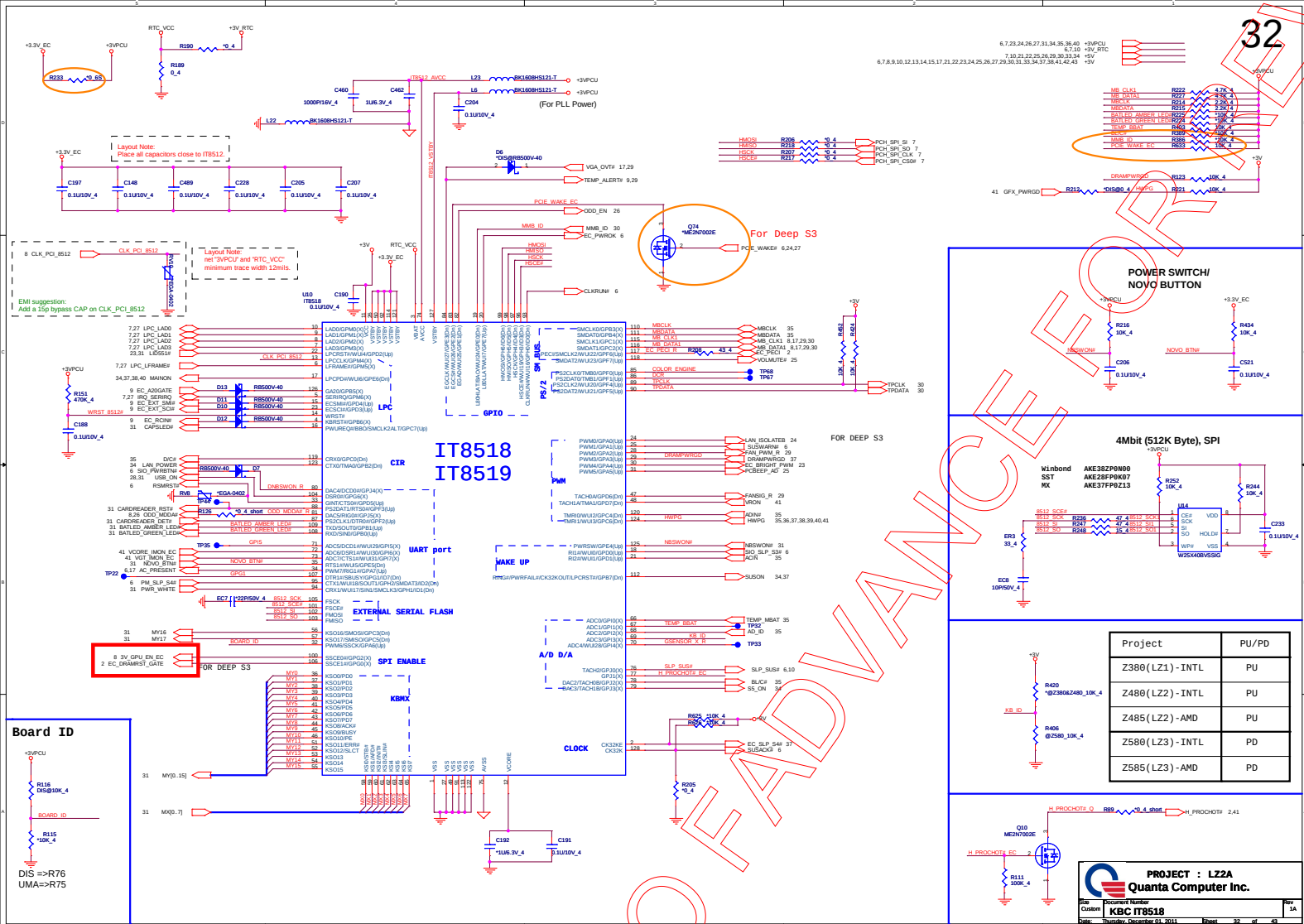
- Power Section:** A +5V SS power source is connected to the USB_ON signal and the VIN pin of the U18 (G547G1PRL) voltage regulator. The regulator's output (VOUT) is connected to the USBP5V signal. A 40 mΩ load (IOUT=1A) is connected to the output. The regulator's GND pin is connected to the common ground.
- USB Section:** The USB_ON signal is connected to the USBP5V signal. The USBP5V signal is connected to the USBP5V pin of the USBPWR1 connector. The USBPWR1 connector also provides USBP5V, USBP9V, and USBP8V signals. The USBP5V signal is connected to the USBP5V pin of the USBP10 connector. The USBP9V and USBP8V signals are connected to the USBP9V and USBP8V pins of the USBP10 connector. The USBP10 connector also provides USBP10 and USBP10+ signals.
- Card Interface Section:** The card interface is connected to the CARDREADER_RST# signal. The CARDREADER_RST# signal is connected to the CARDREADER_RST# pin of the CARDREADER_DET# connector. The CARDREADER_DET# connector also provides MIC1_VREF0, MIC1_VREF0+, MIC1_3D, MIC1_L1, MIC1_R1, LINE_3D, HP_OUT_L1, and HP_OUT_R1 signals.
- Other Components:** The diagram includes several capacitors (C346, C625, C626, C627, C628, C629, C630, C631, C632, C633, C634, C635, C636, C637, C638, C639, C640, C641, C642, C643, C644, C645, C646, C647, C648, C649, C650, C651, C652, C653, C654, C655, C656, C657, C658, C659, C660, C661, C662, C663, C664, C665, C666, C667, C668, C669, C670, C671, C672, C673, C674, C675, C676, C677, C678, C679, C680, C681, C682, C683, C684, C685, C686, C687, C688, C689, C690, C691, C692, C693, C694, C695, C696, C697, C698, C699, C700, C701, C702, C703, C704, C705, C706, C707, C708, C709, C710, C711, C712, C713, C714, C715, C716, C717, C718, C719, C720, C721, C722, C723, C724, C725, C726, C727, C728, C729, C730, C731, C732, C733, C734, C735, C736, C737, C738, C739, C740, C741, C742, C743, C744, C745, C746, C747, C748, C749, C750, C751, C752, C753, C754, C755, C756, C757, C758, C759, C760, C761, C762, C763, C764, C765, C766, C767, C768, C769, C770, C771, C772, C773, C774, C775, C776, C777, C778, C779, C780, C781, C782, C783, C784, C785, C786, C787, C788, C789, C790, C791, C792, C793, C794, C795, C796, C797, C798, C799, C800, C801, C802, C803, C804, C805, C806, C807, C808, C809, C810, C811, C812, C813, C814, C815, C816, C817, C818, C819, C820, C821, C822, C823, C824, C825, C826, C827, C828, C829, C830, C831, C832, C833, C834, C835, C836, C837, C838, C839, C840, C841, C842, C843, C844, C845, C846, C847, C848, C849, C850, C851, C852, C853, C854, C855, C856, C857, C858, C859, C860, C861, C862, C863, C864, C865, C866, C867, C868, C869, C870, C871, C872, C873, C874, C875, C876, C877, C878, C879, C880, C881, C882, C883, C884, C885, C886, C887, C888, C889, C890, C891, C892, C893, C894, C895, C896, C897, C898, C899, C900, C901, C902, C903, C904, C905, C906, C907, C908, C909, C910, C911, C912, C913, C914, C915, C916, C917, C918, C919, C920, C921, C922, C923, C924, C925, C926, C927, C928, C929, C930, C931, C932, C933, C934, C935, C936, C937, C938, C939, C940, C941, C942, C943, C944, C945, C946, C947, C948, C949, C950, C951, C952, C953, C954, C955, C956, C957, C958, C959, C960, C961, C962, C963, C964, C965, C966, C967, C968, C969, C970, C971, C972, C973, C974, C975, C976, C977, C978, C979, C980, C981, C982, C983, C984, C985, C986, C987, C988, C989, C990, C991, C992, C993, C994, C995, C996, C997, C998, C999, C1000) and a diode (D18).

For EMI request



PROJECT : LZ2A
Quanta Computer Inc.

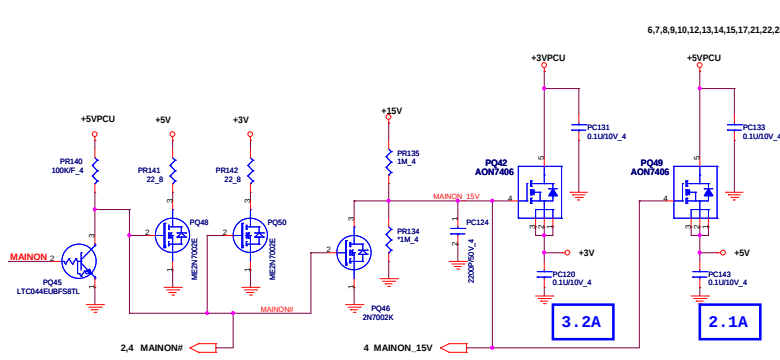
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Date: Friday, December 02, 2011	Sheet 31 of 43	



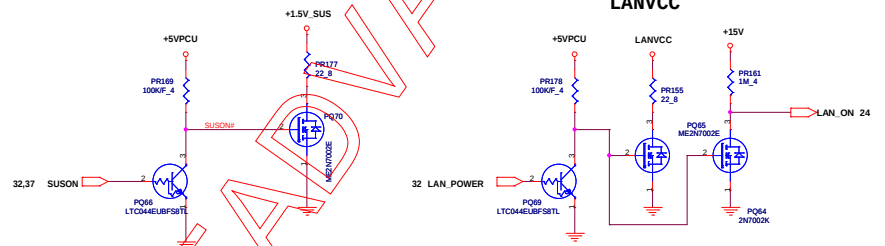
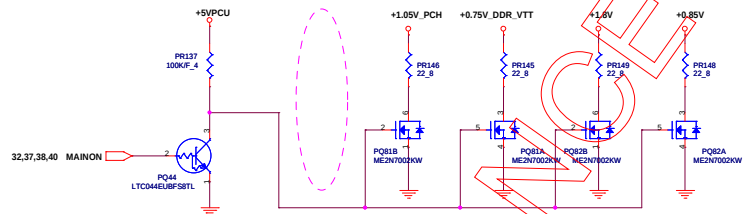
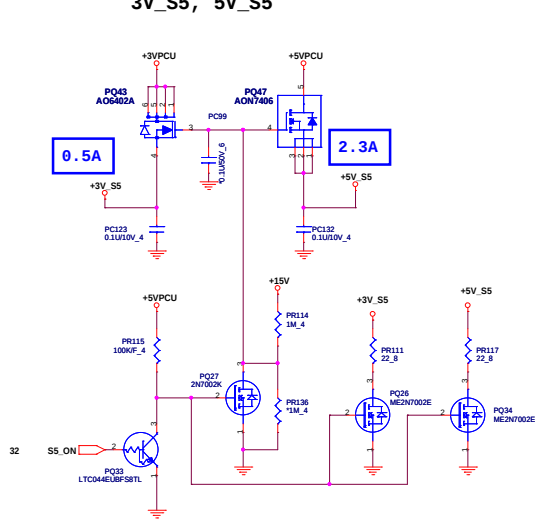
DISCHARGE

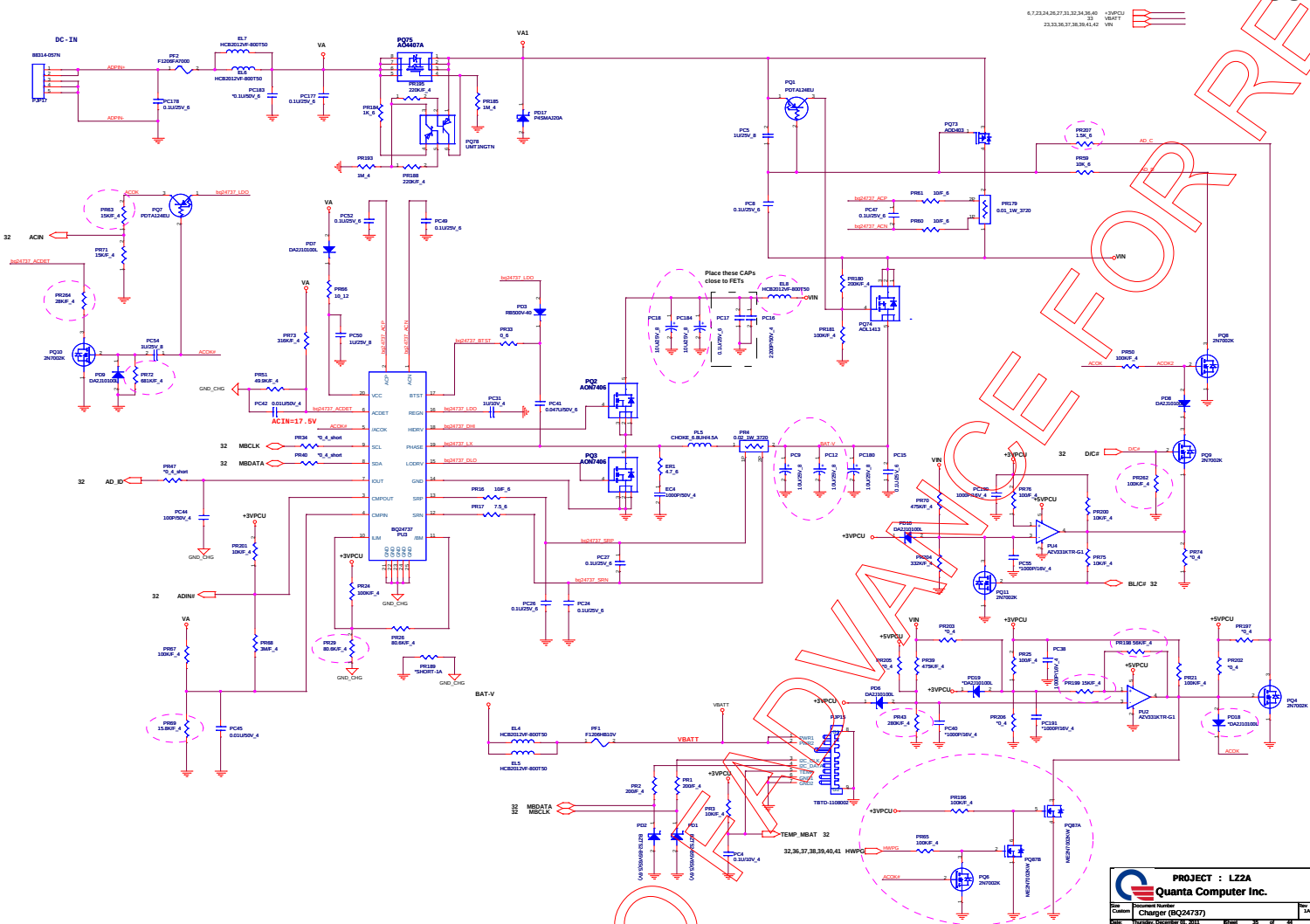
+3V, +5V

34

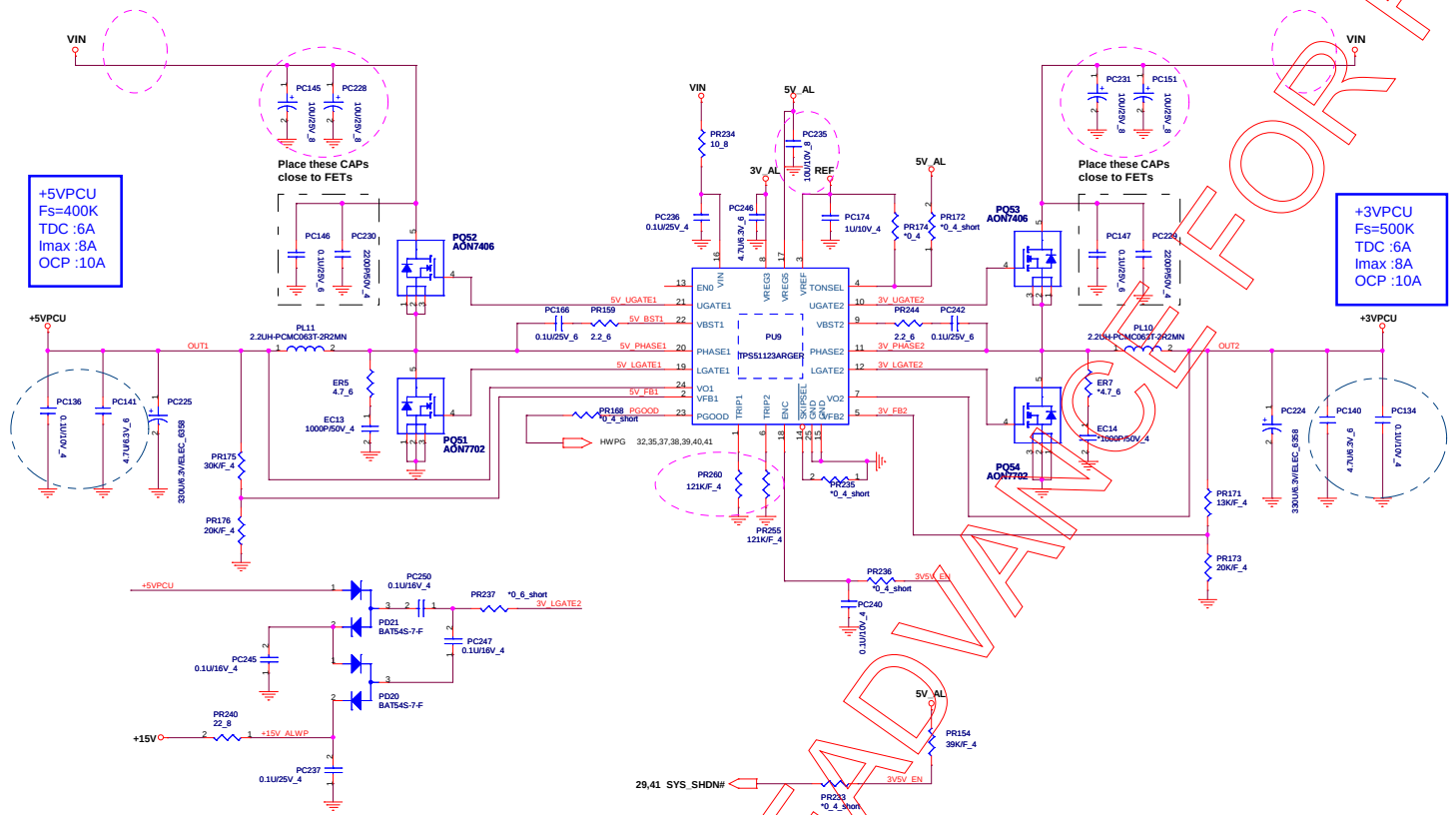


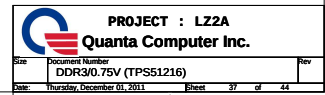
3V_S5, 5V_S5

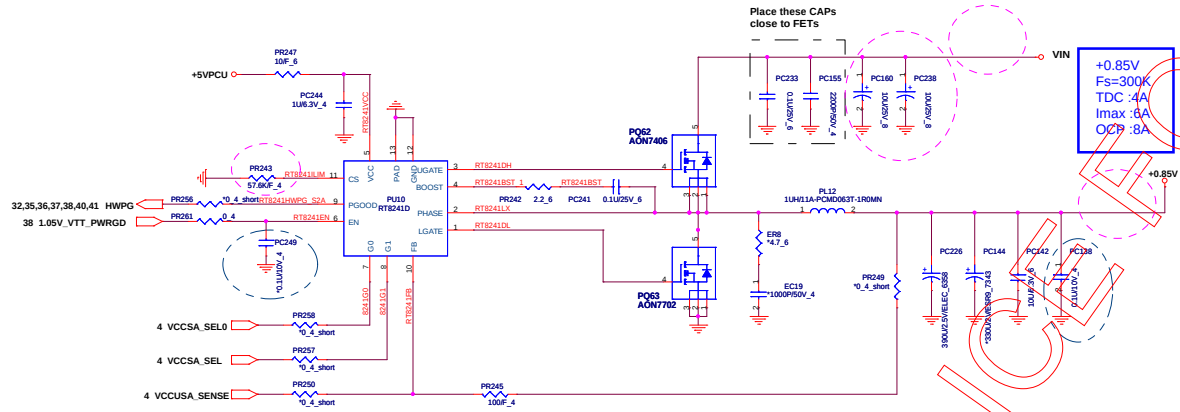




23,33,35,37,38,39,41,42 VIN
 5V_AL
 3V_AL
 REF
 +5VPCU
 23,26,30,34,43
 +15V
 6,7,23,24,26,27,31,32,34,35,40 +3VPCU

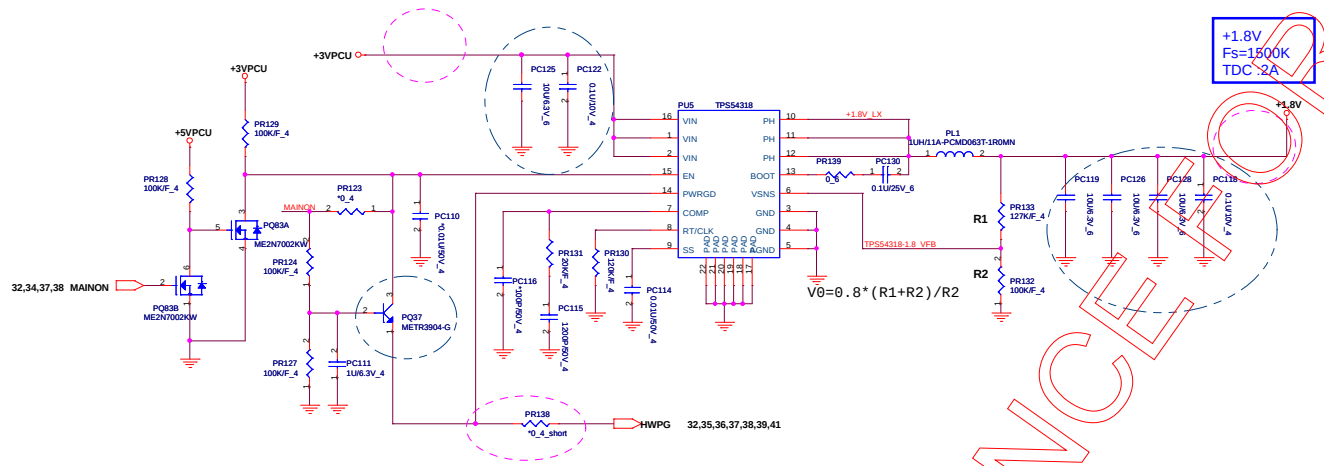






60	61	VCCSA
0	0	0.9V
0	1	0.8V
1	0	0.725V
1	1	0.675V

default 0.9V



6,7,8,9,10,12,13,14,15,17,21,22,23,24,25,26,27,29,30,31,32,33,34,37,38,40,43
10,34,35,36,37,38,39,40,42,43
23,33,35,36,37,38,39,42
4,39 +VCC_GFX
139
+5VPCU
+VCC_CORE
VN
+VCC_GFX

