

COMPAL CONFIDENTIAL

MODEL NAME : QAL70
PCB NO : LA-7741P(DAB00000800)
BOM P/N : 4619EO31L01 TPM ;4519EO31L02 TPM/TAA
GPIO MAP: Rev0.9

Dalmore 13 UMA

Ivy Bridge + Panther POINT

2011-06-23
REV : 0.1 (X00)
@ : Nopop Component
CONN@ : Connector Component

MB Type	BOM P/N	
TPM EN/ TCM DIS		1@ 3@
TPM DIS/ TCM EN		2@ 4@
TPM DIS/ TCM DIS		2@ 3@
TAA		@TAA

SPI ON BOARD

@SPI

MB PCB	
Part Number	Description
DA800001700	PCB OFH LA-6562P REV0 M/B UMA

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Title

Cover Sheet

Size

Document Number

LA-7741

Rev

0.1

Date

Thursday, June 23, 2011

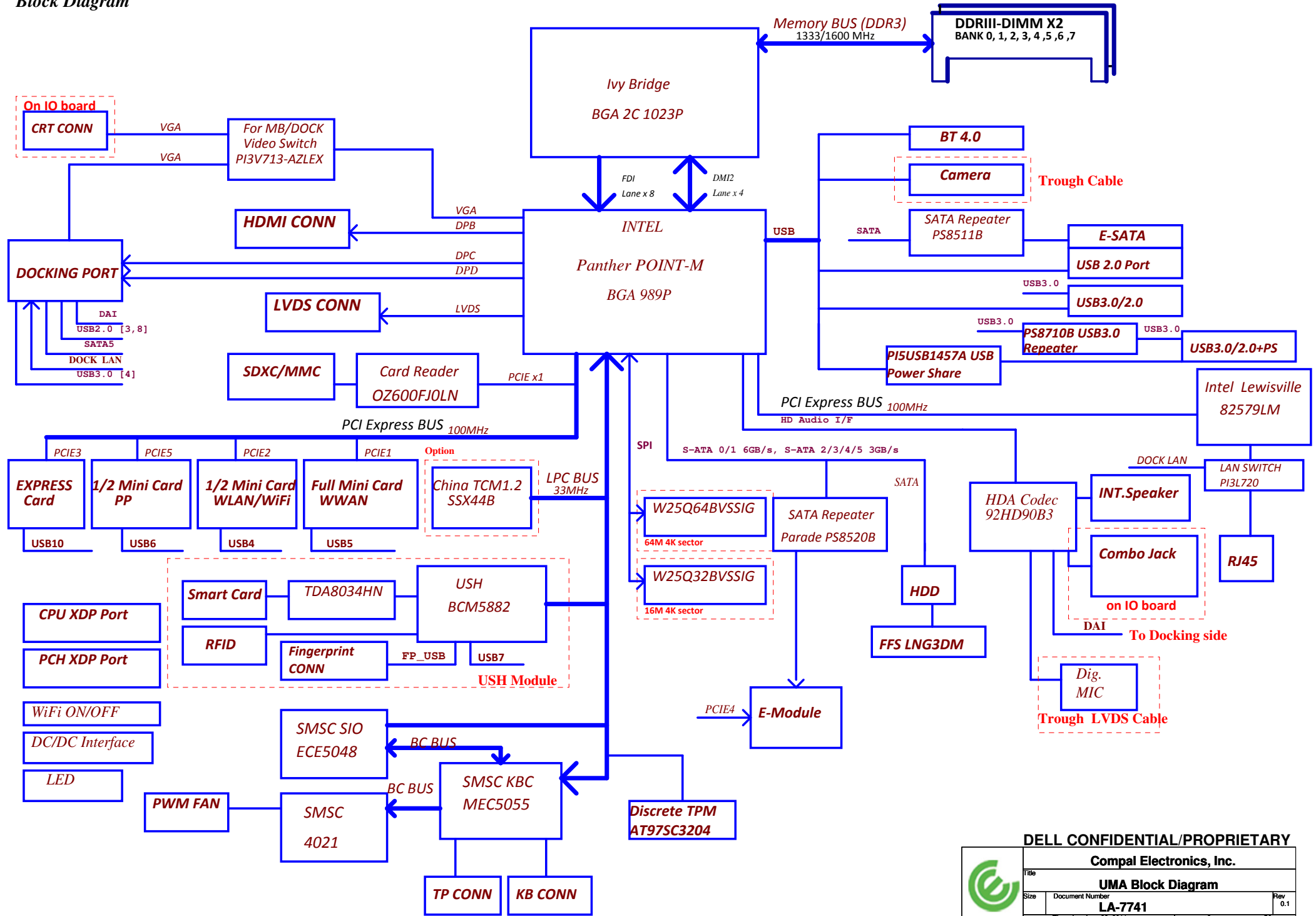
Sheet

1

of

56

Block Diagram



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Compal Electronics, Inc.			
Title UMA Block Diagram			
Size	Document Number LA-7741	Rev 0.1	
Date: Thursday, June 23, 2011	Sheet 2	of 56	

POWER STATES

Signal State	SLP S3#	SLP S4#	SLP S5#	SLP A#	ALWAYS PLANE	M PLANE	SUS PLANE	RUN PLANE	CLOCKS
S0 (Full ON) / M0	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON	ON
S3 (Suspend to RAM) / M3	LOW	HIGH	HIGH	HIGH	ON	ON	ON	OFF	OFF
S4 (Suspend to DISK) / M3	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF	OFF
S5 (SOFT OFF) / M3	LOW	LOW	LOW	HIGH	ON	ON	OFF	OFF	OFF
S3 (Suspend to RAM) / M-OFF	LOW	HIGH	HIGH	LOW	ON	OFF	ON	OFF	OFF
S4 (Suspend to DISK) / M-OFF	LOW	LOW	HIGH	LOW	ON	OFF	OFF	OFF	OFF
S5 (SOFT OFF) / M-OFF	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF	OFF

PM TABLE

power plane State	+15V_ALW +5V_ALW +3.3V_ALW_PCH +3.3V_RTC_LDO	+3.3V_SUS +1.5V_MEM	+5V_RUN +3.3V_RUN +1.8V_RUN +1.5V_RUN +0.75V_DDR_VTT +VCC_CORE +1.05V_RUN_VTT +1.05V_RUN	+3.3V_M +1.05V_M (M-OFF)	+3.3V_M +1.05V_M (M-OFF)
S0	ON	ON	ON	ON	ON
S3	ON	ON	OFF	ON	OFF
S5 S4/AC	ON	OFF	OFF	ON	OFF
S5 S4/AC don't exist	OFF	OFF	OFF	OFF	OFF

need to update Power Status and PM Table

SATA	DESTINATION
SATA 0	HDD
SATA 1	ODD/ E3 Module Bay
SATA 2	NA
SATA 3	NA
SATA 4	ESATA
SATA 5	Dock

UMA DP/HDMI Port	Connetion
Port B	MB HDMI Conn
Port C	Dock DP port 2
Port D	Dock DP port 1

PCH	USB PORT#	DESTINATION
	0	JUSB1 (Right side)
	1	JUSB2 (Rear Left side)
	2	NA
	3	MLK DOCK
	4	WLAN
	5	WWAN
	6	JMINI3(PP)
	7	USH->BIO
	8	DOCKING
	9	JESATA1 (right side)
	10	Express card
	11	Bluetooth
	12	Camera
	13	NA

USH	0	BIO
	1	NA

PCI EXPRESS	DESTINATION
Lane 1	MINI CARD-1 WWAN
Lane 2	MINI CARD-2 WLAN
Lane 3	Express card
Lane 4	E3 Module Bay (USB3)
Lane 5	1/2vMINI CARD-3 PCIE
Lane 6	MMI
Lane 7	10/100/1G LOM
Lane 8	None

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Index and Config.

LA-7741

Title

Size

Document Number

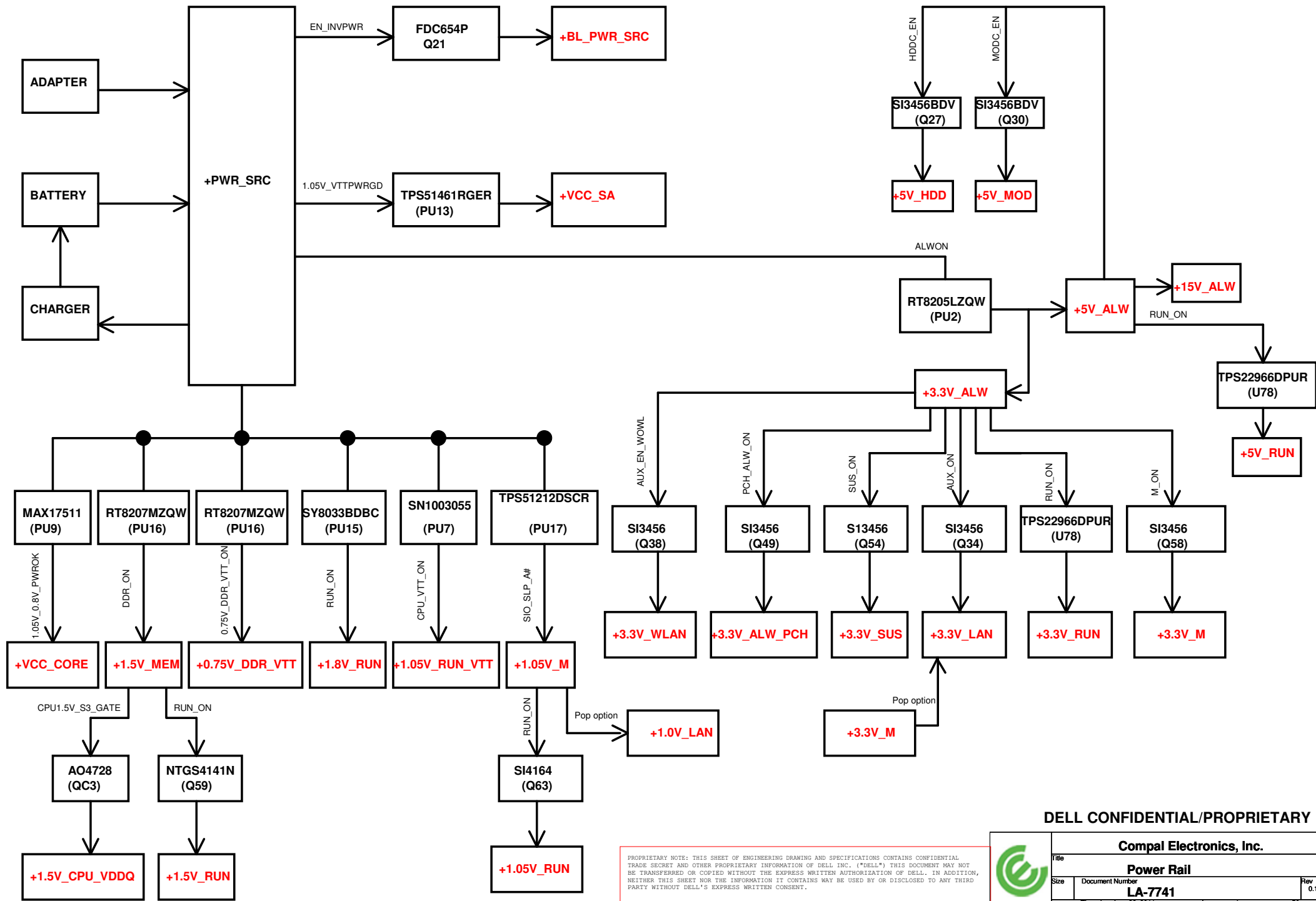
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Rev

0.1

Sheet 3 of 56

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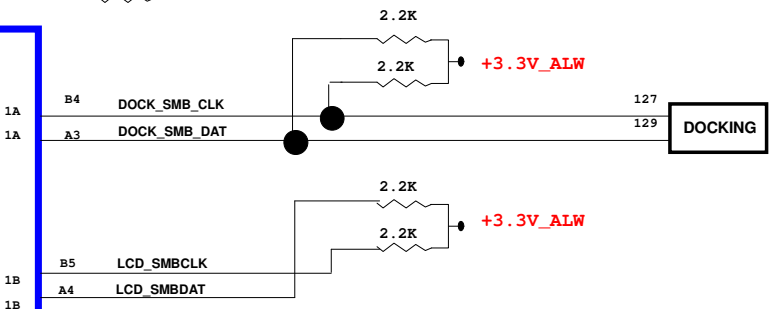
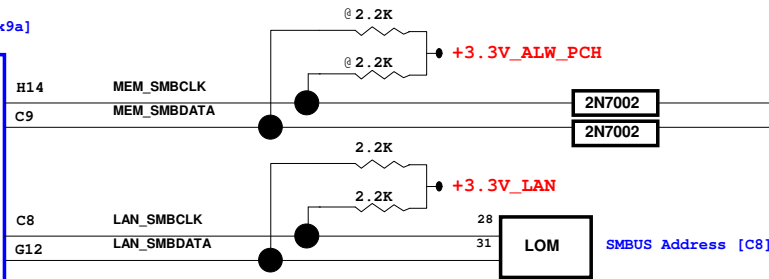
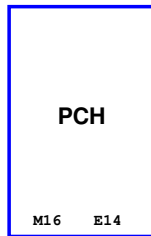


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Title	Power Rail		
Size	Document Number	Rev	
	LA-7741	0.1	
Date:	Thursday, June 23, 2011	Sheet	4 of 56

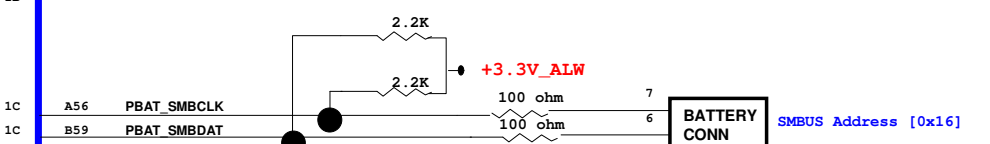
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SMBUS Address [0x9a]

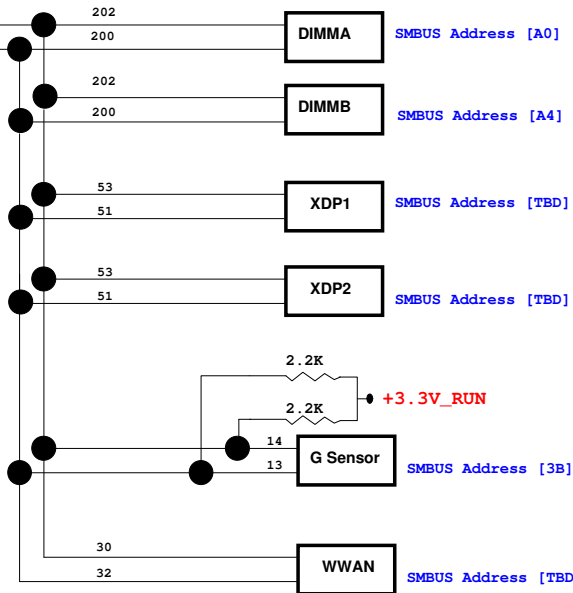
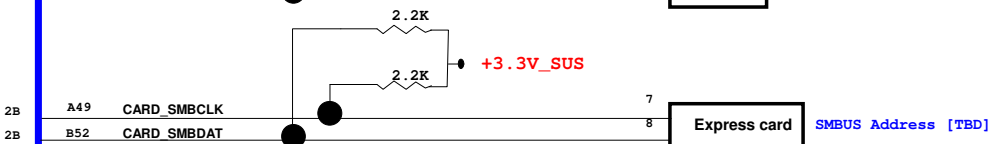
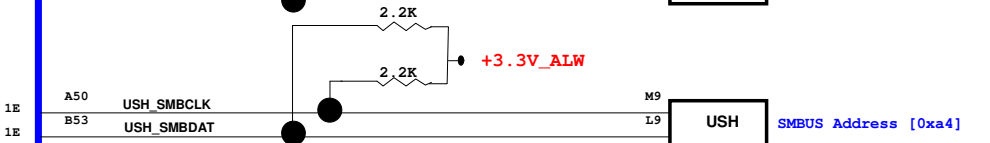


SMBUS Address
APR_EC: 0x48
SPR_EC: 0x70
MSLICE_EC: 0x72
USB: 0x59
AUDIO: 0x34
SLICE_BATTERY: 0x17
SLICE_CHARGER: 0x13

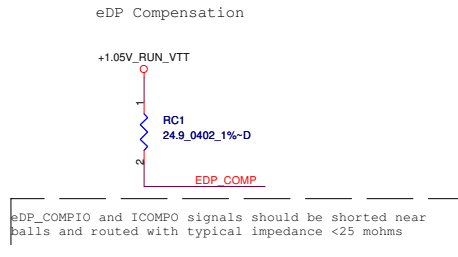
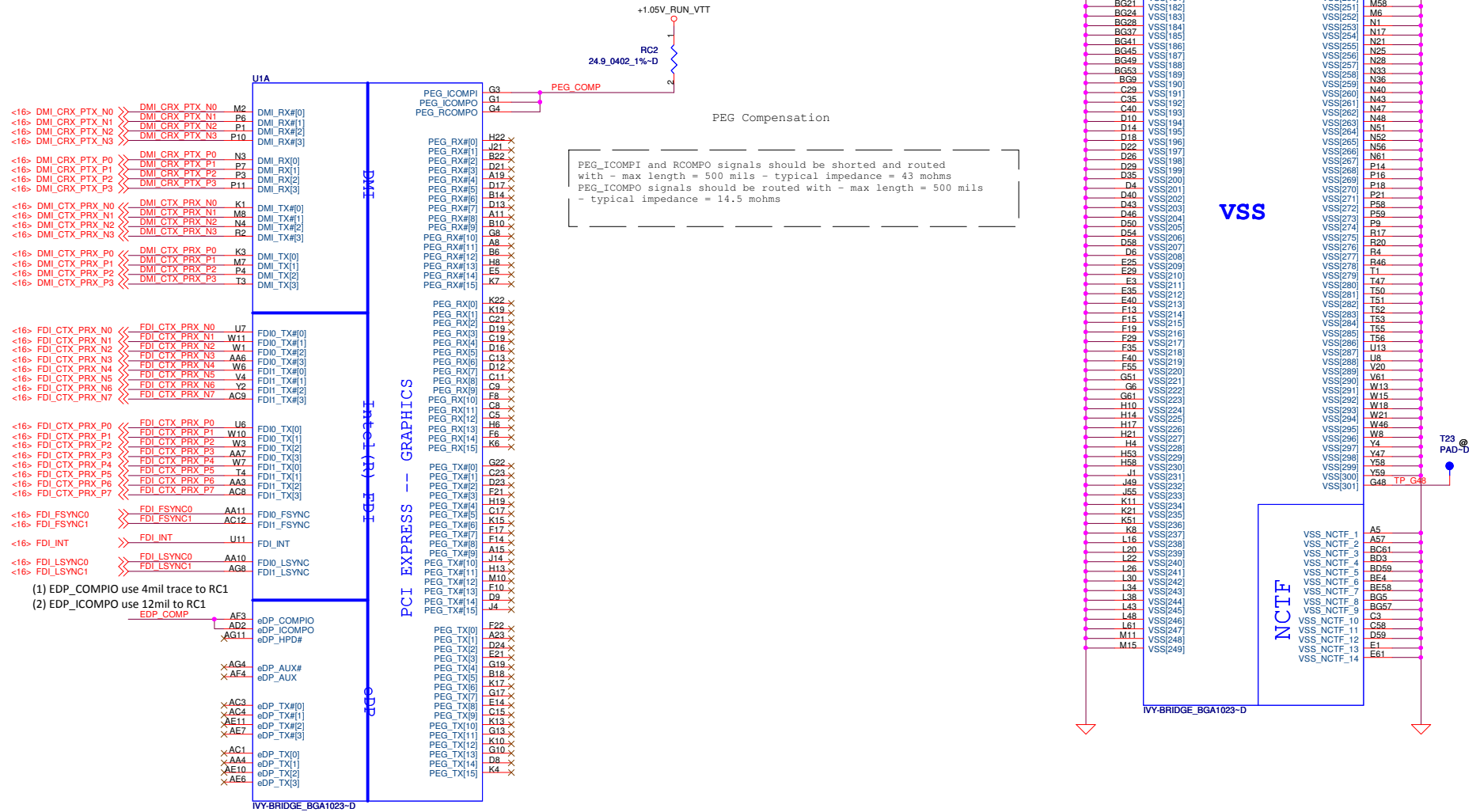
KBC



MEC 5065



(1) PEG_RCOMP (G4) use 4mil connect to PEG_ICOMPI, then use 4mil connect to RC2.
(2) PEG_ICOMPO use 12mil connect to RC2

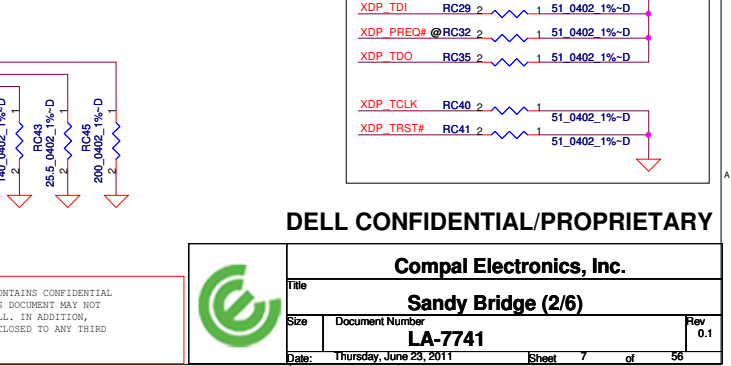
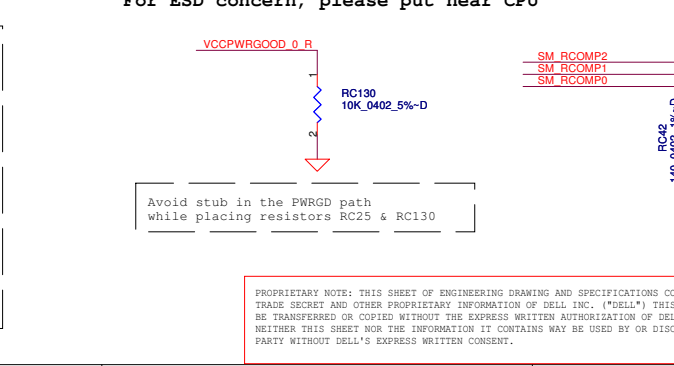
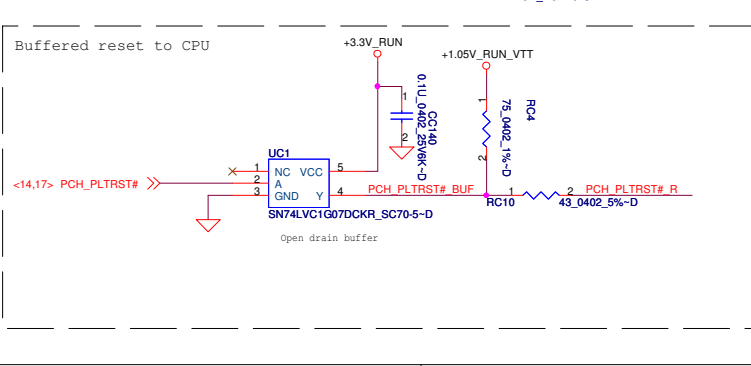
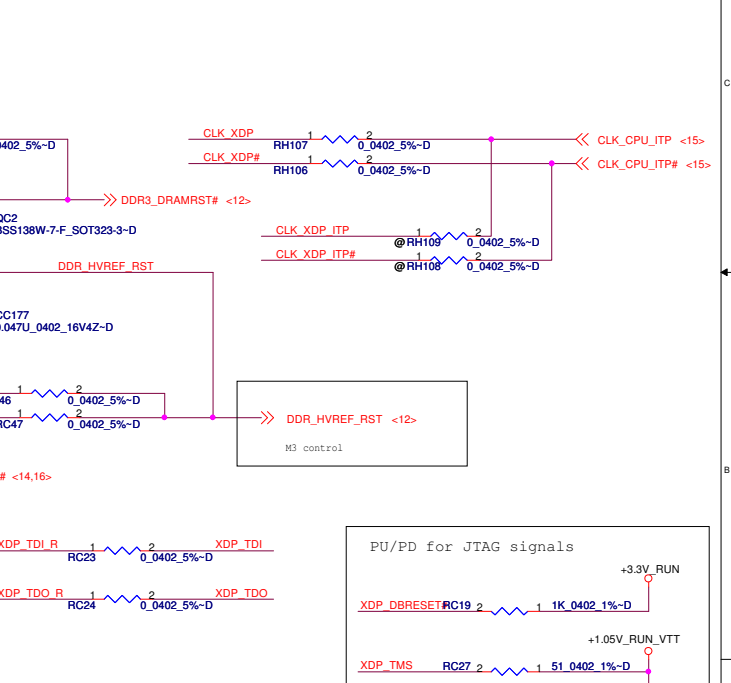
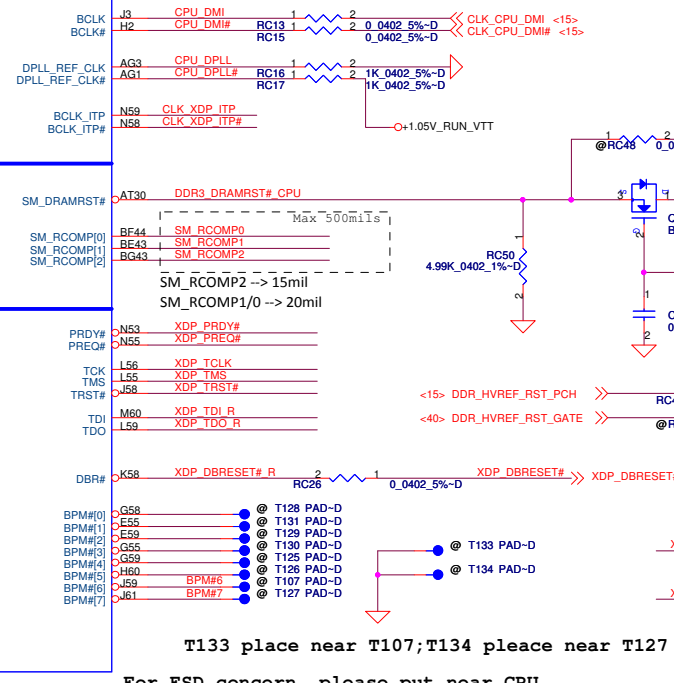
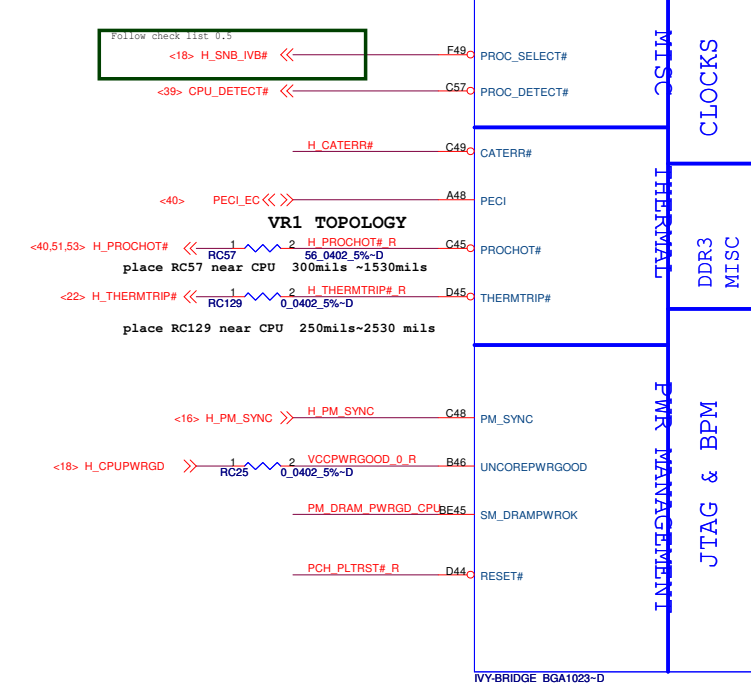
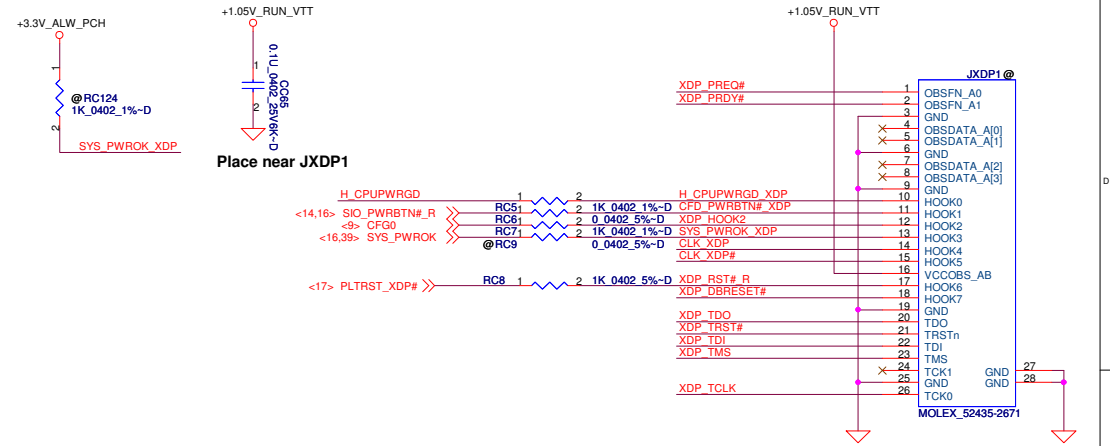
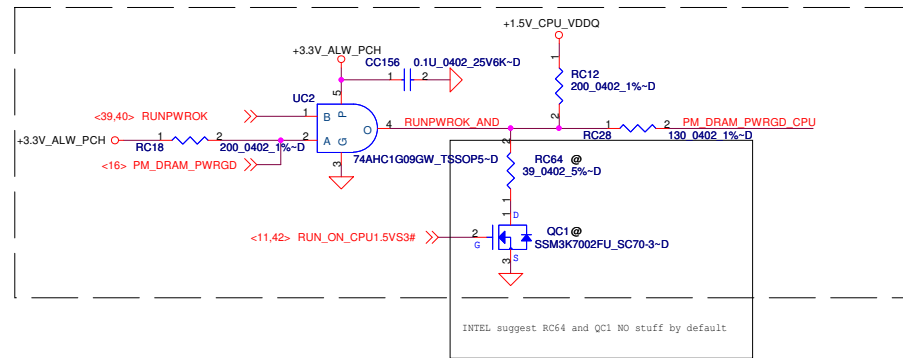


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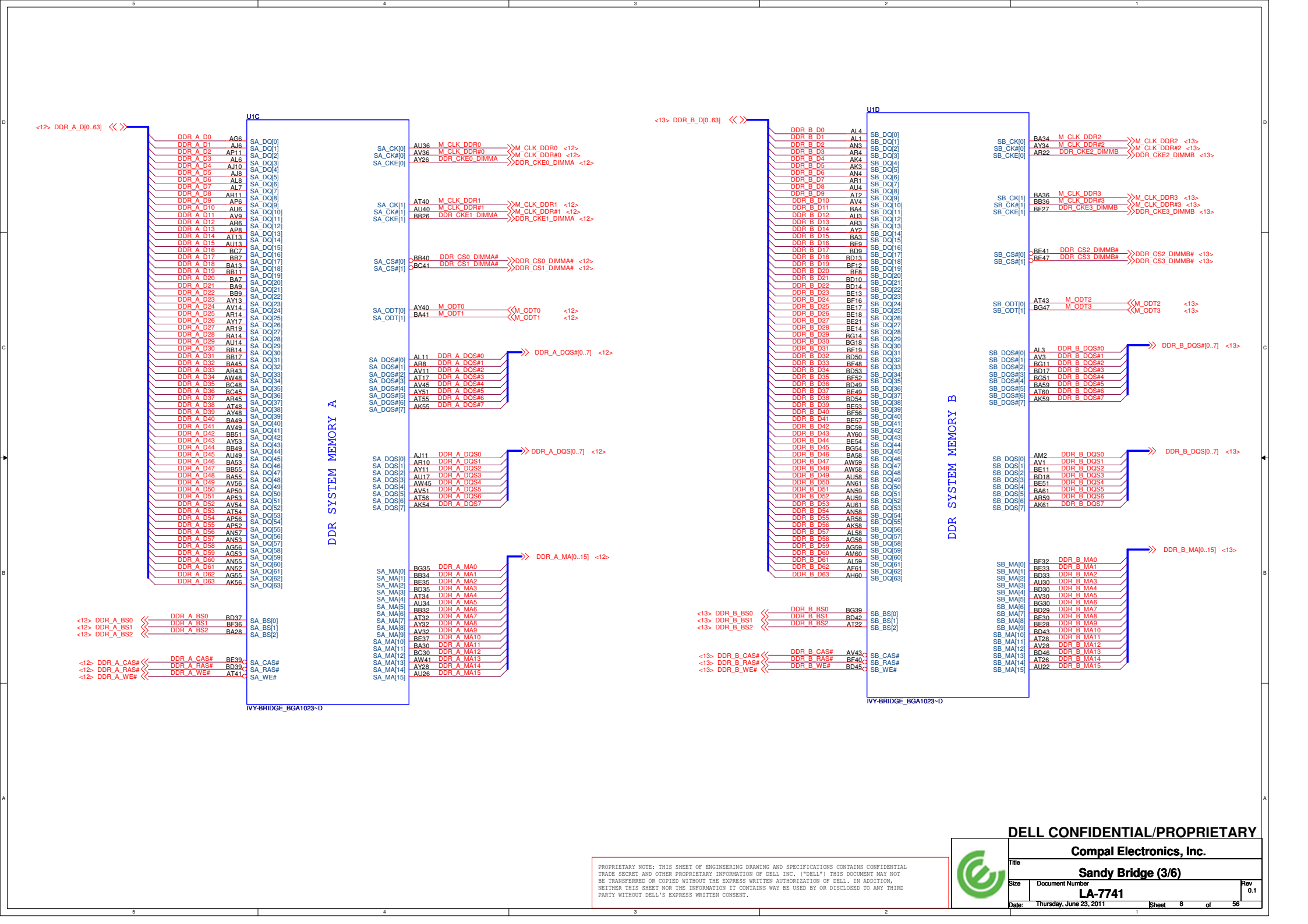
Compal Electronics, Inc.			
Title			
Sandy Bridge (1/6)			
Size	Document Number	Rev	
	LA-7741	0.1	
Date:	Thursday, June 23, 2011	Sheet	6 of 56



DELL CONFIDENTIAL/PROPRIETARY

Compal Electronics, Inc.			
Sandy Bridge (2/6)			
Title	Size	Document Number	Rev
Date	Thursday, June 23, 2011	Sheet	7 of 56

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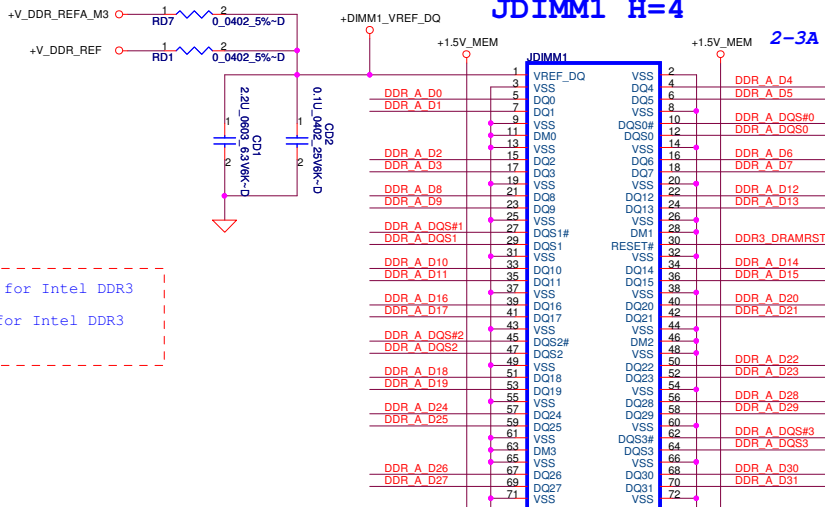
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Title					Rev 0.1
Sandy Bridge (3/6)					
Size	Document Number				Rev 0.1
	LA-7741				
Date:	Thursday, June 23, 2011		Sheet	8	of 56

JDIMM1 H=4

2-3A to 1 DIMMs/channel



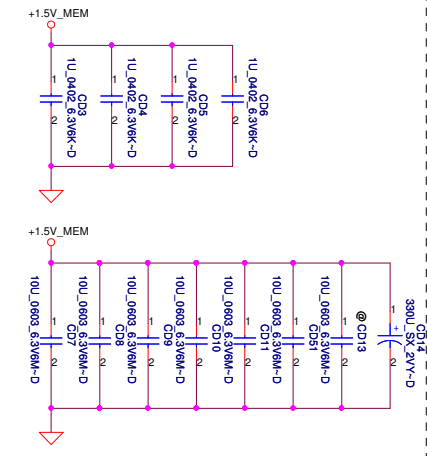
Populate RD1, De-Populate RD7 for Intel DDR3 VREFDQ multiple methods M1
Populate RD7, De-Populate RD1 for Intel DDR3 VREFDQ multiple methods M3

All VREF traces should have 10 mil trace width

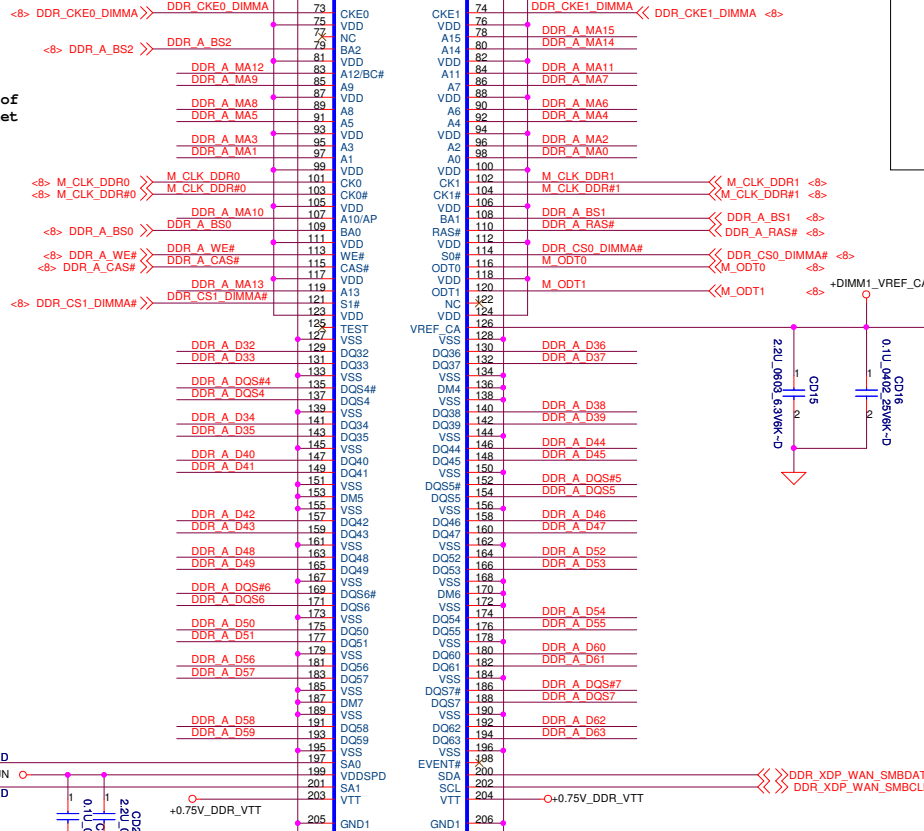
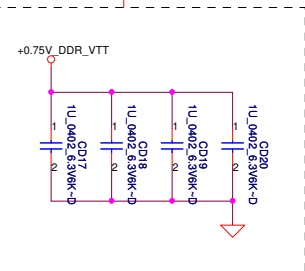
<8> DDR_A_DQS#0[0.7] <<>>
<8> DDR_A_D[0.63] <<>>
<8> DDR_A_DQS#0[0.7] <<>>
<8> DDR_A_MA[0.15] <<>>

Layout Note:
Place near JDIMM1

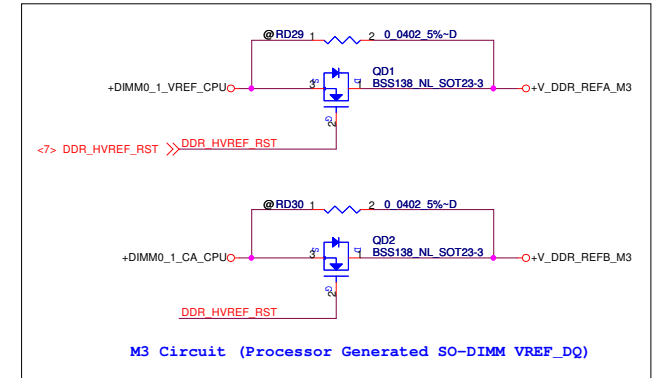
Note:
Check voltage tolerance of VREF_DQ at the DIMM socket



Layout Note:
Place near JDIMM1.203,204



<13> DDR3_DRAMRST#_R <<DDR3_DRAMRST#_R 1 RD28 2 0.0402 5%-D >> DDR3_DRAMRST# <7>



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DDRIII-SODIMM SLOT1

LA-7741

Rev 0.1

Date: Thursday, June 23, 2011 Sheet 12 of 56

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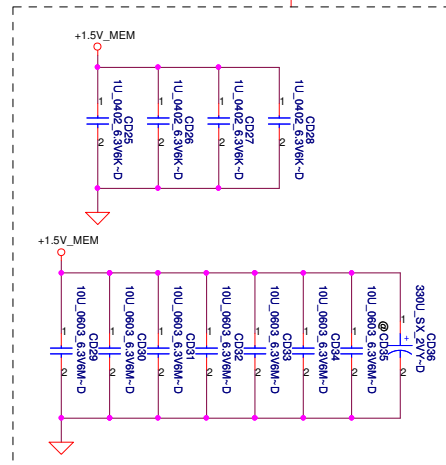
Note:
Check voltage tolerance of
VREF_DQ at the DIMM socket

```
Populate RD4, De-Populate RD8 for Intel DDR3
VREFDQ multiple methods M1
Populate RD8, De-Populate RD4 for Intel DDR3
VREFDQ multiple methods M3
```

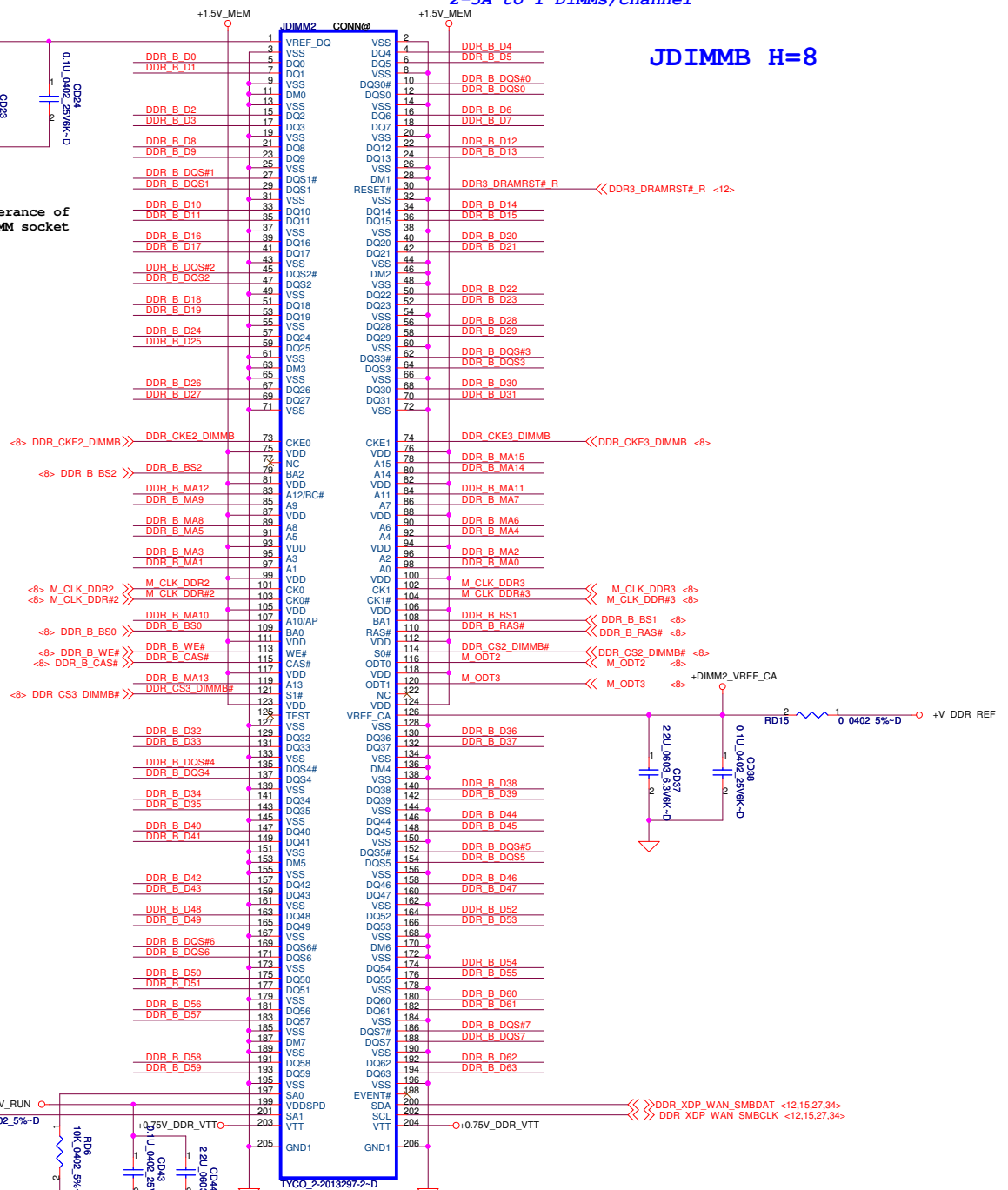
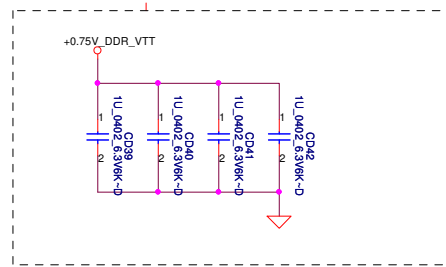
```
<8> DDR_B_DQS#[0..7] << >>
<8> DDR_B_D[0..63] << >>
<8> DDR_B_DQS[0..7] << >>
<8> DDR_B_MA[0..15] >>
```

All VREF traces should have 10 mil trace width

Layout Note:
Place near JDIMM2



Layout Note:
Place near JDIMM2.203,204



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DDRIII-SODIMM SLOT2

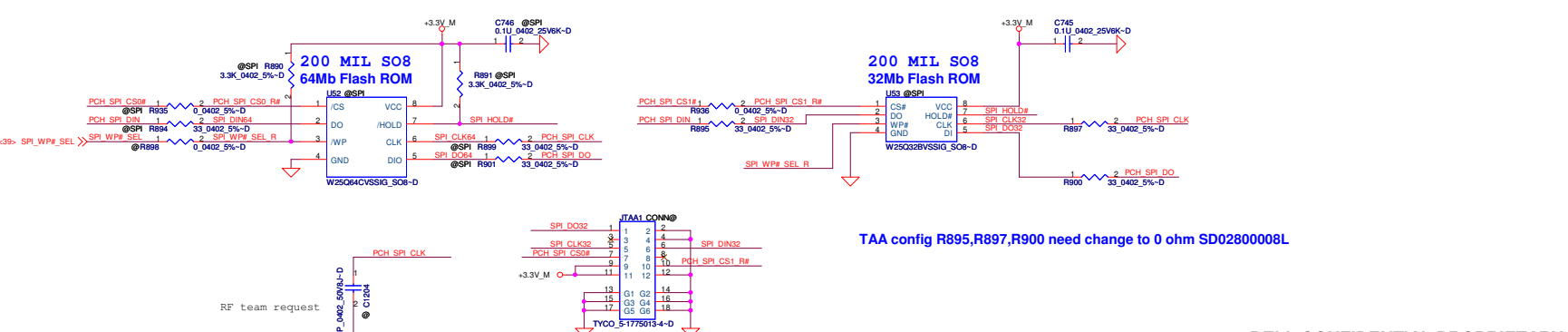
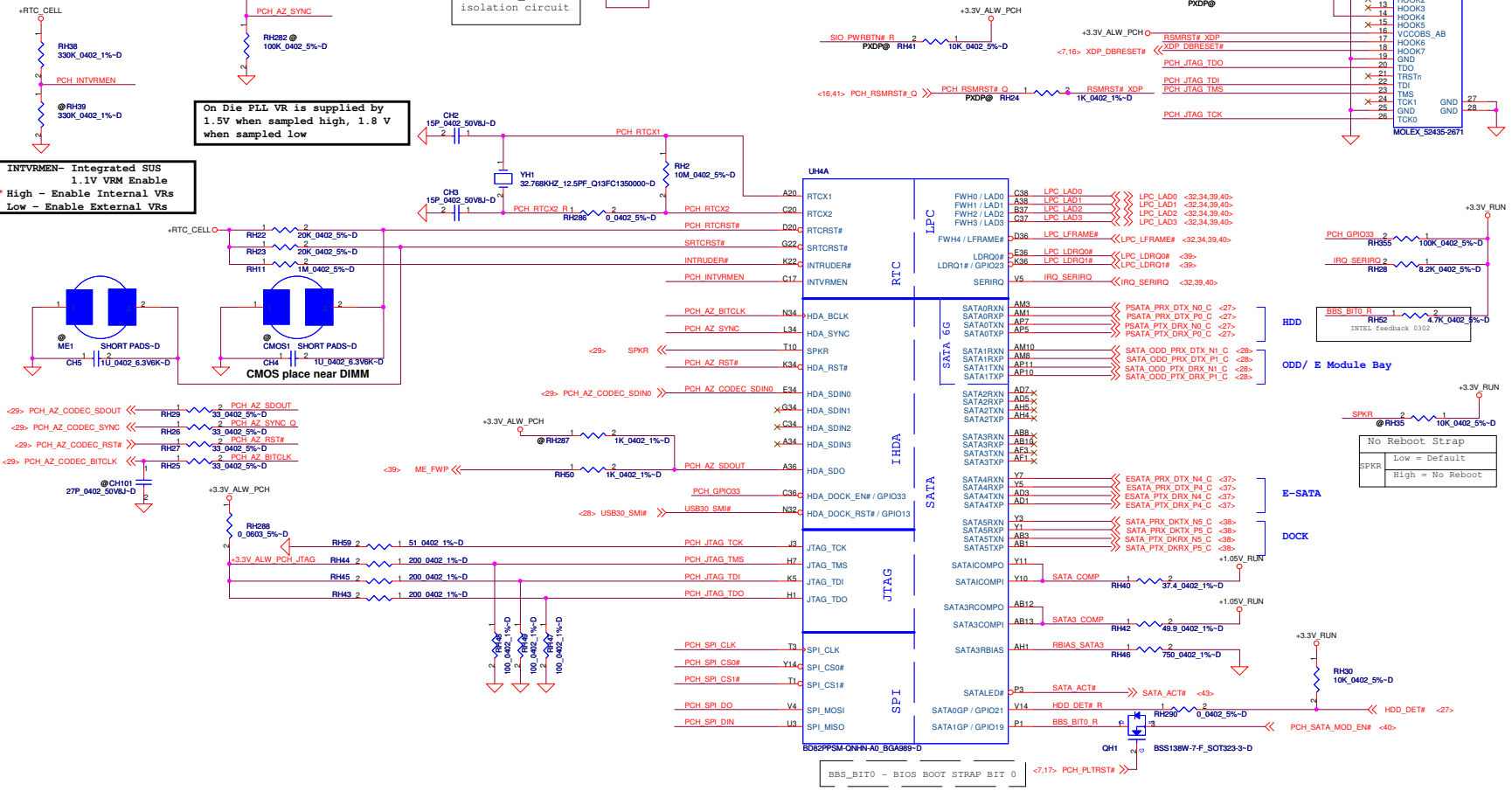
LA-7741

0.1

Date: Thursday, June 23, 2011 Sheet 13 of 56

CMOS CLR1	CMOS setting
Shunt	Clear CMOS
Open	Keep CMOS

ME CLR1	TPM setting
Shunt	Clear ME RTC Registers
Open	Keep ME RTC Registers



Follow DG0.9 Device
down & Express/Mini
card topology

MiniWWAN (Mini Card 1)---->

MiniWLAN (Mini Card 2)---->

EXPRESS Card---->

E3 Module Bay---->

1/2vMINI CARD-3 PCIE
(Mini Card 3)---->

MMI ---->

10/100/1G LAN ---->

MiniWWAN (Mini Card 1)---->

10/100/1G LAN ---->

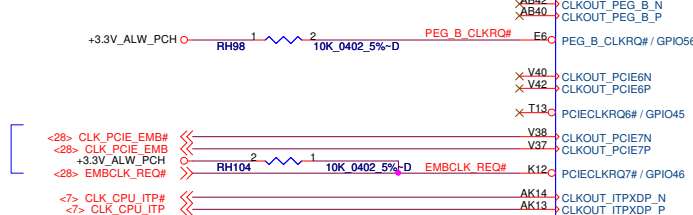
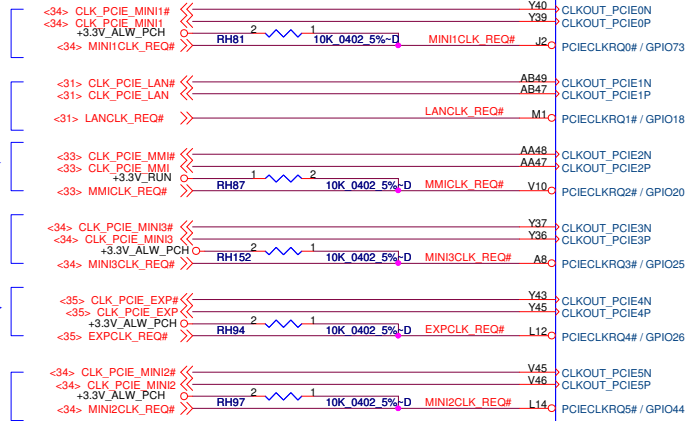
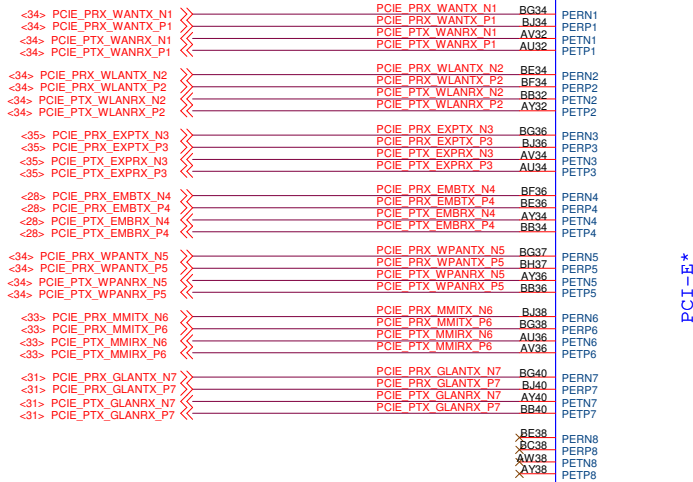
MMI Card---->

MiniWPAN (Mini Card 3)---->

Express card---->

MiniWLAN (Mini Card 2)---->

eModule Bay---->



U4B8

PCI-E *

CLOCKS

FLEX CLOCKS

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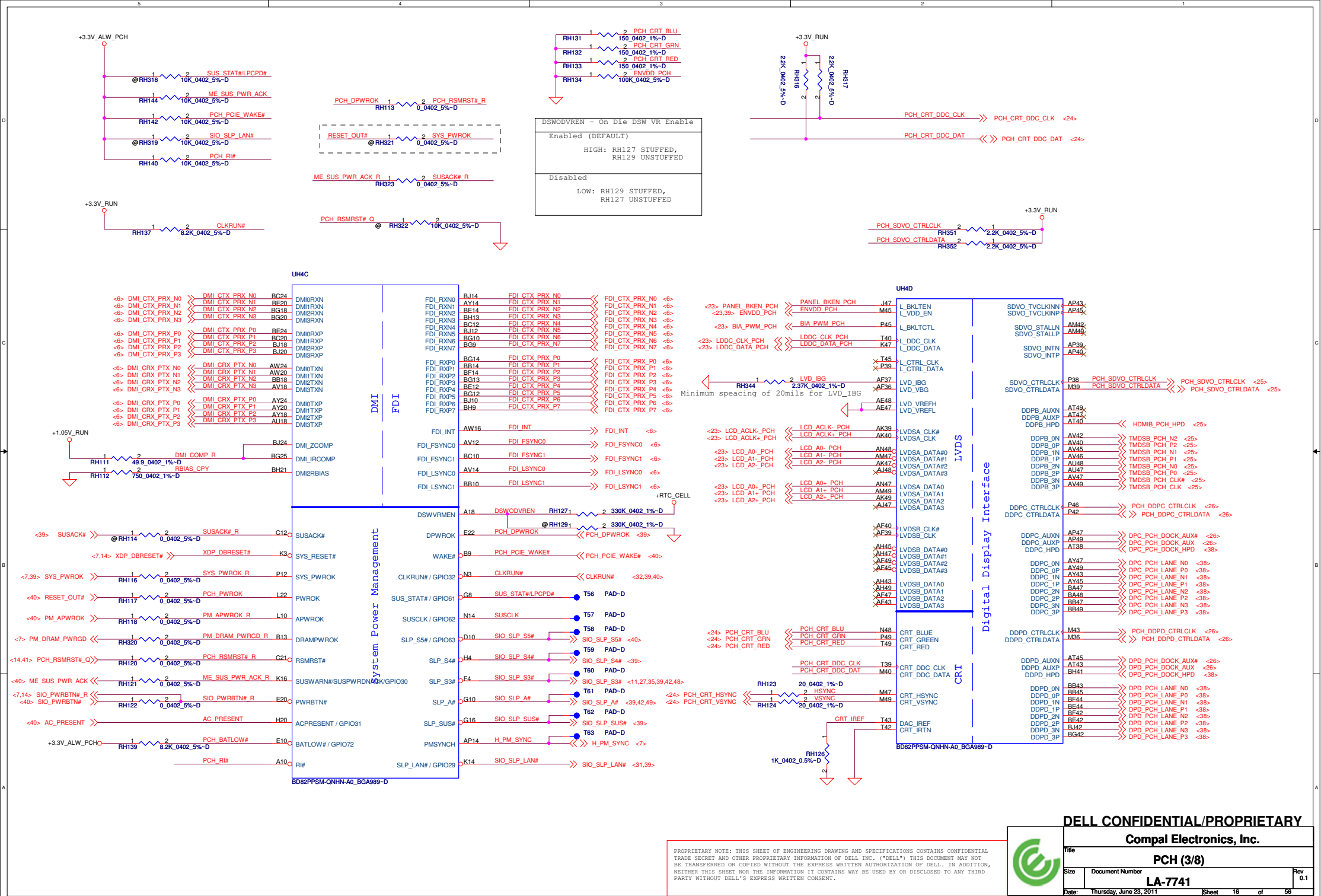
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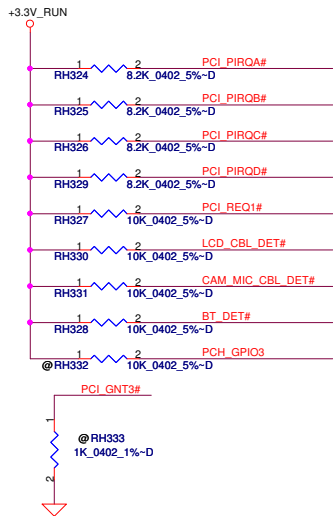
PCH (2/8)

LA-7741

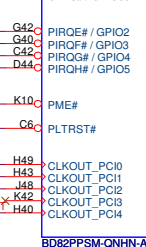
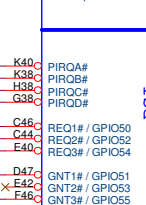
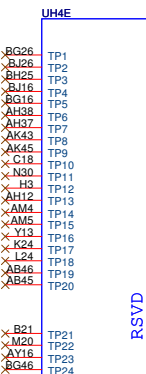
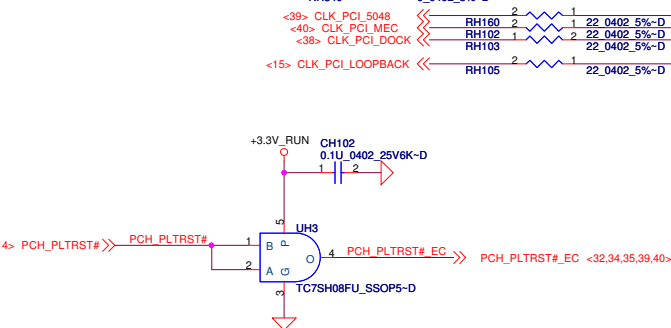
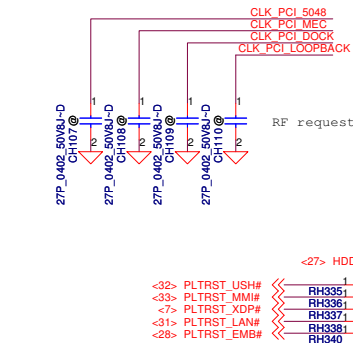
Rev 0.1

Sheet 15 of 56



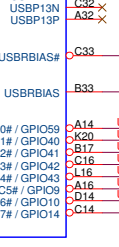
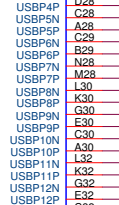
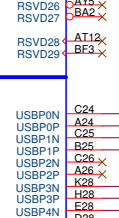
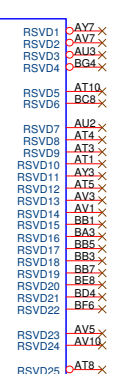


A16 swap override Strap/Top-Block Swap Override jumper	
PCI_GNT#3	Low = A16 swap High = Default

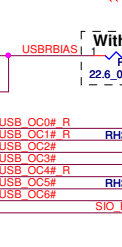
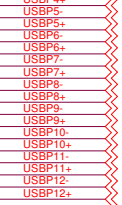
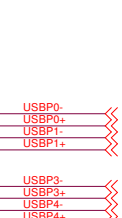


BD82PPSM-QNHN-A0_BGA989-D

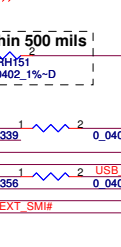
Boot BIOS Strap		
BBS_BIT1	SATA_SLPD (BBS_BIT0)	Boot BIOS Location
0	0	LPC
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI



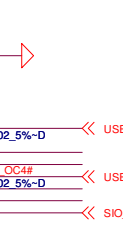
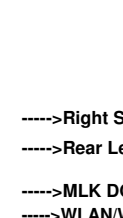
BD82PPSM-QNHN-A0_BGA989-D



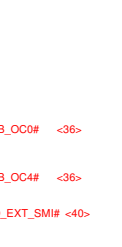
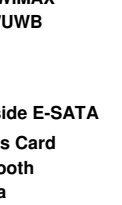
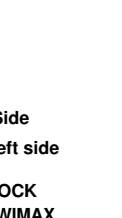
BD82PPSM-QNHN-A0_BGA989-D



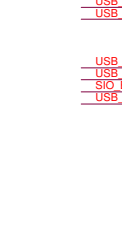
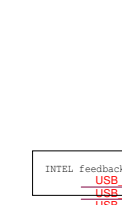
BD82PPSM-QNHN-A0_BGA989-D



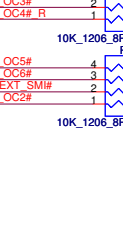
BD82PPSM-QNHN-A0_BGA989-D



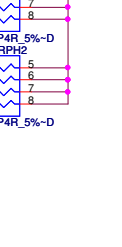
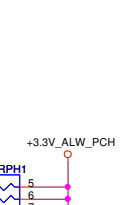
BD82PPSM-QNHN-A0_BGA989-D



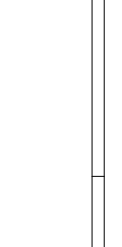
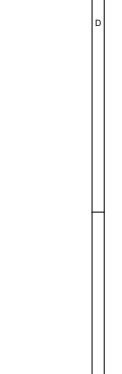
BD82PPSM-QNHN-A0_BGA989-D



BD82PPSM-QNHN-A0_BGA989-D

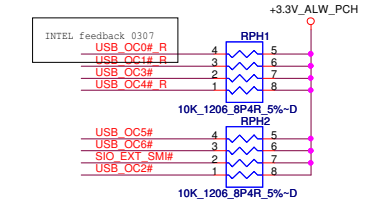


BD82PPSM-QNHN-A0_BGA989-D



BD82PPSM-QNHN-A0_BGA989-D

- >Right Side
- >Rear Left side
- >MLK DOCK
- >WLAN/WIMAX
- >WWAN/UWB
- >PP
- >USH
- >DOCK
- >Right side E-SATA
- >Express Card
- >Blue Tooth
- >Camera



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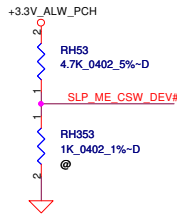
Compal Electronics, Inc.

PCH (4/8)

LA-7741

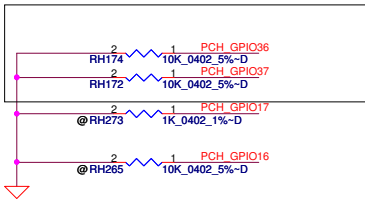
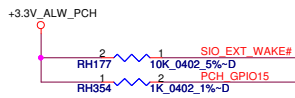
Rev 0.1

Date: Thursday, June 23, 2011 Sheet 17 of 56

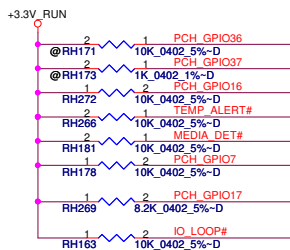
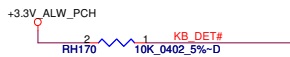


Note: PCH has internal pull up 20k ohm on E3_PAID_TS_DET# (GPIO27)

SLP_ME_CSW_DEV#	PLL ON DIE VR ENABLE
ENABLED - HIGH DEFAULT	
DISABLED - LOW	



Layout note:
Trace wide 10mil & length 30mil
All NCTF pins should have thick traces at 45° from the pad.



VSS_NCTF_1	A4	VSS_NCTF_1
VSS_NCTF_2	A44	VSS_NCTF_2
VSS_NCTF_3	A45	VSS_NCTF_3
VSS_NCTF_4	A46	VSS_NCTF_4
VSS_NCTF_5	A5	VSS_NCTF_5
VSS_NCTF_6	A6	VSS_NCTF_6
VSS_NCTF_7	B3	VSS_NCTF_7
VSS_NCTF_8	B47	VSS_NCTF_8
VSS_NCTF_9	BD1	VSS_NCTF_9
VSS_NCTF_10	BD49	VSS_NCTF_10
VSS_NCTF_11	BE1	VSS_NCTF_11
VSS_NCTF_12	BE49	VSS_NCTF_12
VSS_NCTF_13	BF1	VSS_NCTF_13
VSS_NCTF_14	BF49	VSS_NCTF_14

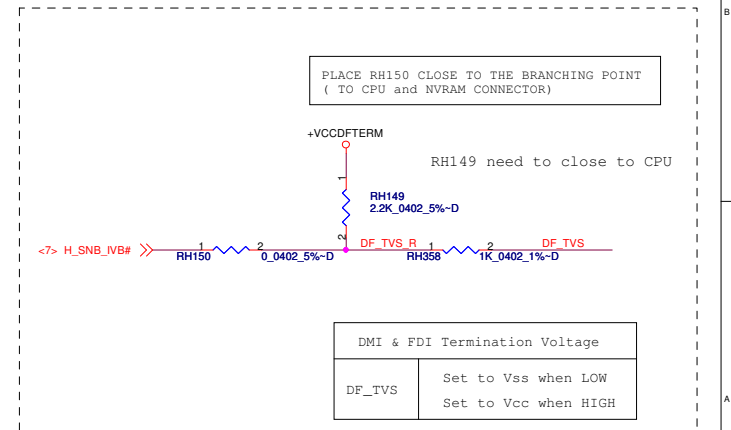
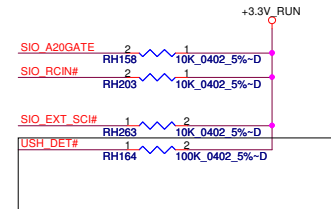
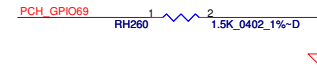
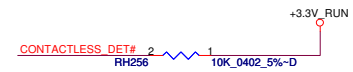
BD82PPSM-QNH-N-A0_BGA989-D

GPIO

NCTF

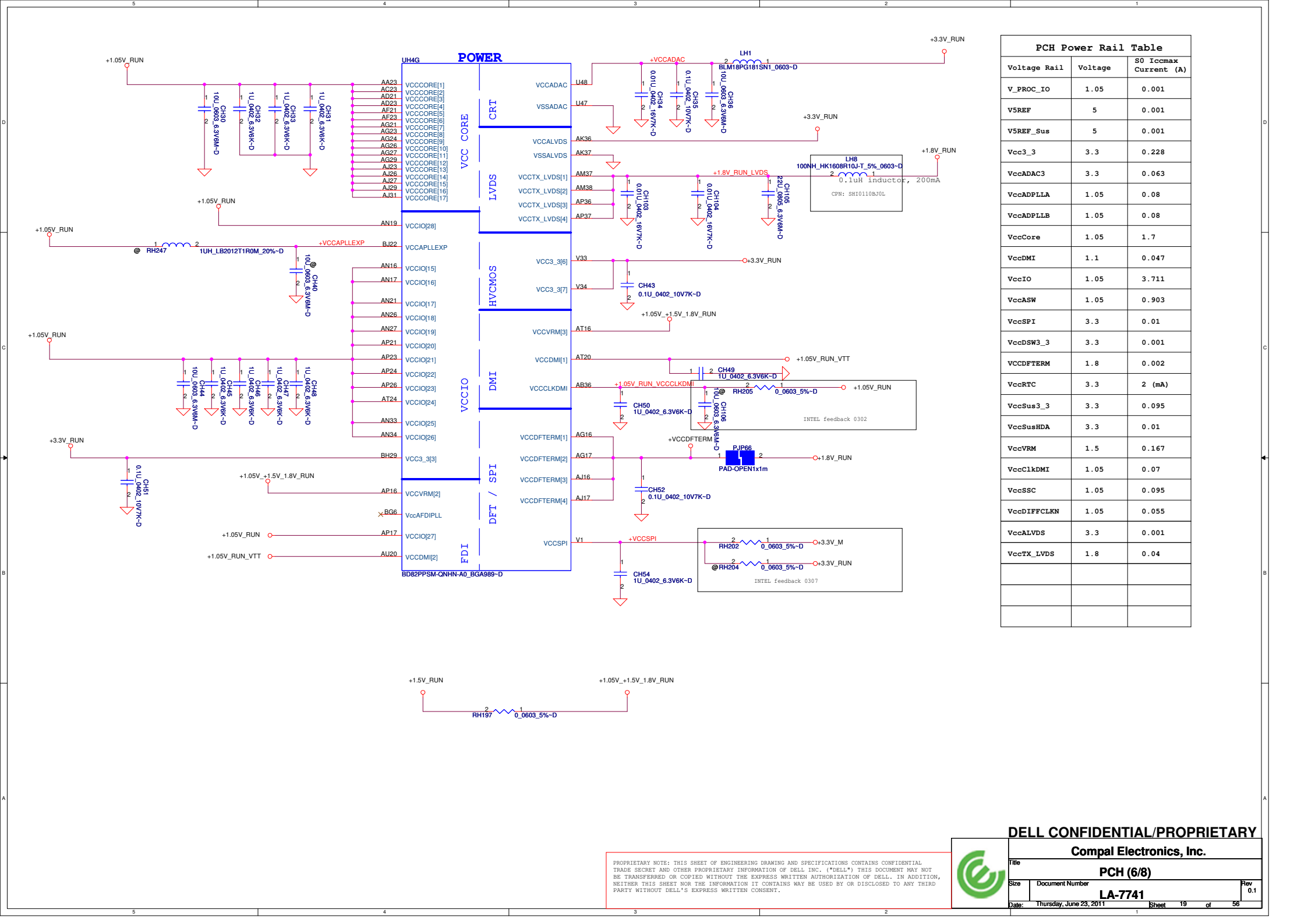


Layout note:
Trace wide 10mil & length 30mil
All NCTF pins should have thick traces at 45° from the pad.

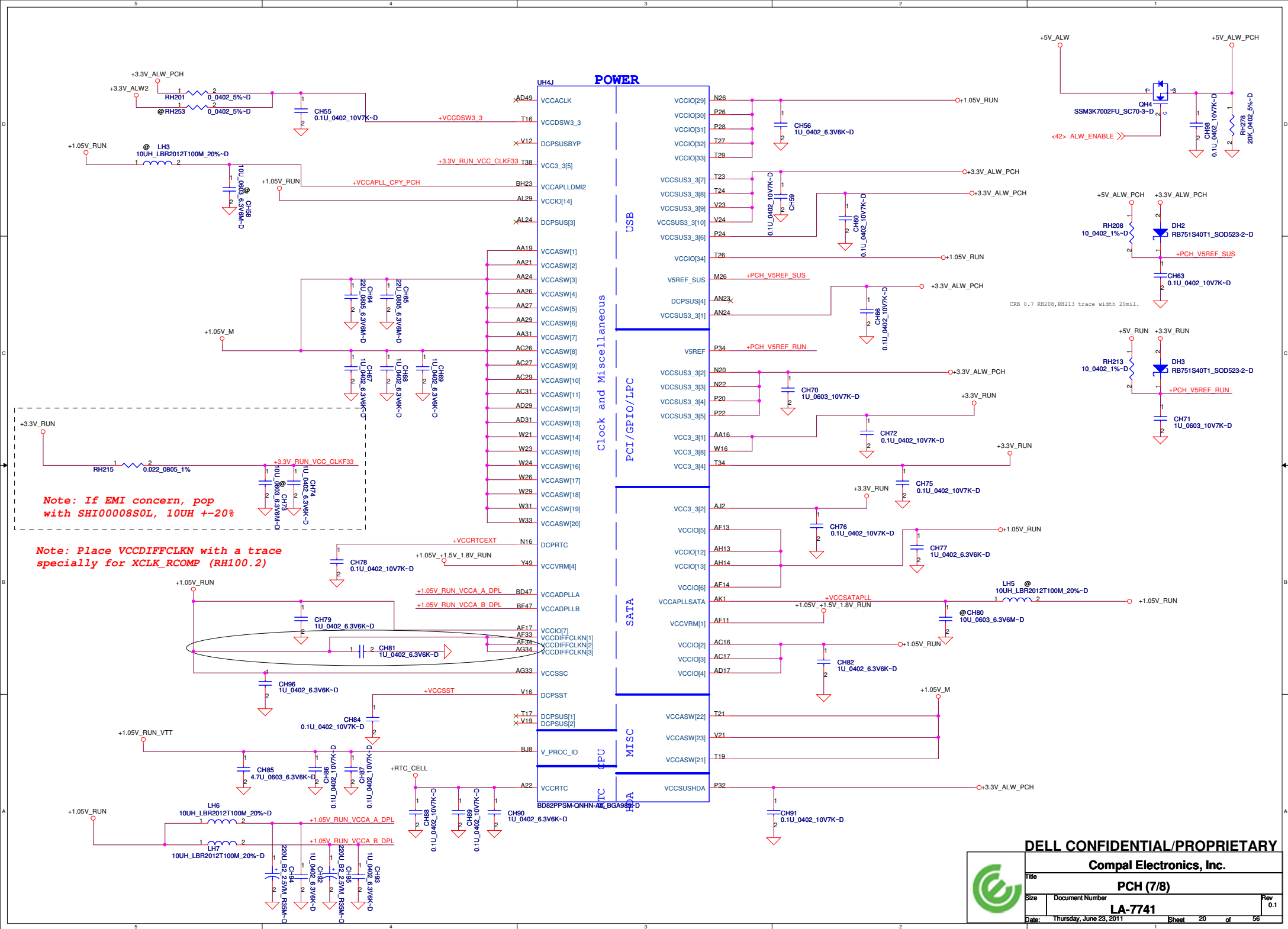


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Compal Electronics, Inc.			
PCH (5/8)			
Title	Document Number	Rev	
	LA-7741	0.1	
Date	Thursday, June 23, 2011	Sheet	18 of 56



PCH Power Rail Table		
Voltage Rail	Voltage	S0 Iccmax Current (A)
V_PROC_IO	1.05	0.001
V5REF	5	0.001
V5REF_Sus	5	0.001
Vcc3_3	3.3	0.228
VccADAC3	3.3	0.063
VccADPLLA	1.05	0.08
VccADPLLB	1.05	0.08
VccCore	1.05	1.7
VccDMI	1.1	0.047
VccIO	1.05	3.711
VccASW	1.05	0.903
VccSPI	3.3	0.01
VccDSW3_3	3.3	0.001
VCCDFTERM	1.8	0.002
VccRTC	3.3	2 (mA)
VccSus3_3	3.3	0.095
VccSusHDA	3.3	0.01
VccVRM	1.5	0.167
VccClkDMI	1.05	0.07
VccSSC	1.05	0.095
VccDIFFCLKN	1.05	0.055
VccALVDS	3.3	0.001
VccTX_LVDS	1.8	0.04



UH4H		
H5	VSS[0]	
AA17	VSS[1]	VSS[80] AK38
AA2	VSS[2]	VSS[81] AK4
AA3	VSS[3]	VSS[82] AK46
AA33	VSS[4]	VSS[83] AK5
AA34	VSS[5]	VSS[84] AK8
AB11	VSS[6]	VSS[85] AL16
AB14	VSS[7]	VSS[86] AL17
AB39	VSS[8]	VSS[87] AL19
AB4	VSS[9]	VSS[88] AL2
AB43	VSS[10]	VSS[89] AL21
AB5	VSS[11]	VSS[90] AL23
AB7	VSS[12]	VSS[91] AL26
AC19	VSS[13]	VSS[92] AL27
AC2	VSS[14]	VSS[93] AL31
AC21	VSS[15]	VSS[94] AL33
AC24	VSS[16]	VSS[95] AL34
AC33	VSS[17]	VSS[96] AL48
AC34	VSS[18]	VSS[97] AM11
AC48	VSS[19]	VSS[98] AM14
AD10	VSS[20]	VSS[99] AM36
AD11	VSS[21]	VSS[100] AM39
AD12	VSS[22]	VSS[101] AM43
AD13	VSS[23]	VSS[102] AM45
AD19	VSS[24]	VSS[103] AM46
AD24	VSS[25]	VSS[104] AM7
AD26	VSS[26]	VSS[105] AN2
AD27	VSS[27]	VSS[106] AN29
AD33	VSS[28]	VSS[107] AN3
AD34	VSS[29]	VSS[108] AN31
AD36	VSS[30]	VSS[109] AP12
AD37	VSS[31]	VSS[110] AP19
AD38	VSS[32]	VSS[111] AP28
AD39	VSS[33]	VSS[112] AP29
AD4	VSS[34]	VSS[113] AP32
AD40	VSS[35]	VSS[114] AP38
AD42	VSS[36]	VSS[115] AP4
AD43	VSS[37]	VSS[116] AP42
AD45	VSS[38]	VSS[117] AP46
AD46	VSS[39]	VSS[118] AP8
AD8	VSS[40]	VSS[119] AR2
AE2	VSS[41]	VSS[120] AR48
AE3	VSS[42]	VSS[121] AT11
AF10	VSS[43]	VSS[122] AT13
AF12	VSS[44]	VSS[123] AT22
AD14	VSS[45]	VSS[124] AT26
AD16	VSS[46]	VSS[125] AT28
AF16	VSS[47]	VSS[126] AT28
AF19	VSS[48]	VSS[127] AT30
AF24	VSS[49]	VSS[128] AT32
AF26	VSS[50]	VSS[129] AT34
AF27	VSS[51]	VSS[130] AT39
AF29	VSS[52]	VSS[131] AT42
AF31	VSS[53]	VSS[132] AT46
AF38	VSS[54]	VSS[133] AT7
AF4	VSS[55]	VSS[134] AU24
AF42	VSS[56]	VSS[135] AU30
AF46	VSS[57]	VSS[136] AV16
AF5	VSS[58]	VSS[137] AV20
AF7	VSS[59]	VSS[138] AV24
AF8	VSS[60]	VSS[139] AV30
AG19	VSS[61]	VSS[140] AV38
AG2	VSS[62]	VSS[141] AV4
AG31	VSS[63]	VSS[142] AV8
AG48	VSS[64]	VSS[143] AW14
AH11	VSS[65]	VSS[144] AW18
AH3	VSS[66]	VSS[145] AW2
AH36	VSS[67]	VSS[146] AW22
AH39	VSS[68]	VSS[147] AW26
AH40	VSS[69]	VSS[148] AW28
AH42	VSS[70]	VSS[149] AW32
AH46	VSS[71]	VSS[150] AW36
AH7	VSS[72]	VSS[151] AW40
AJ19	VSS[73]	VSS[152] AW48
AJ21	VSS[74]	VSS[153] AV11
AJ24	VSS[75]	VSS[154] AY12
AJ33	VSS[76]	VSS[155] AY22
AJ34	VSS[77]	VSS[156] AY28
AK12	VSS[78]	VSS[157]
AK3	VSS[79]	VSS[158]

BD82PPSM-QNHN-A0_BGA989-D

UH4I		
AY4	VSS[159]	VSS[259] H46
AY42	VSS[160]	VSS[260] K18
AY46	VSS[161]	VSS[261] K26
AY8	VSS[162]	VSS[262] K39
B11	VSS[163]	VSS[263] K46
B15	VSS[164]	VSS[264] K7
B19	VSS[165]	VSS[265] L18
B23	VSS[166]	VSS[266] L2
B27	VSS[167]	VSS[267] L20
B31	VSS[168]	VSS[268] L26
B39	VSS[169]	VSS[269] L38
B7	VSS[170]	VSS[270] L48
F45	VSS[171]	VSS[271] M12
B12	VSS[172]	VSS[272] P16
BB16	VSS[173]	VSS[273] M18
BB20	VSS[174]	VSS[274] M22
BB22	VSS[175]	VSS[275] M22
BB24	VSS[176]	VSS[276] M24
BB28	VSS[177]	VSS[277] M30
BB30	VSS[178]	VSS[278] M32
BB38	VSS[179]	VSS[279] M34
BB4	VSS[180]	VSS[280] M38
BB46	VSS[181]	VSS[281] M4
BC14	VSS[182]	VSS[282] M42
BC18	VSS[183]	VSS[283] M46
BC2	VSS[184]	VSS[284] M8
BC22	VSS[185]	VSS[285] N18
BC26	VSS[186]	VSS[286] N47
BC32	VSS[187]	VSS[287] P11
BC34	VSS[188]	VSS[288] P18
BC36	VSS[189]	VSS[289] T33
BC40	VSS[190]	VSS[290] P40
BC42	VSS[191]	VSS[291] P43
BC48	VSS[192]	VSS[292] P43
BC48	VSS[193]	VSS[293] P47
BD46	VSS[194]	VSS[294] P7
BE22	VSS[195]	VSS[295] R2
BE26	VSS[196]	VSS[296] R48
BE26	VSS[197]	VSS[297] T12
BE40	VSS[198]	VSS[298] T31
BF10	VSS[199]	VSS[299] T37
BF12	VSS[200]	VSS[300] T4
BF16	VSS[201]	VSS[301] W34
BF20	VSS[202]	VSS[302] T46
BF22	VSS[203]	VSS[303] T47
BF24	VSS[204]	VSS[304] T8
BF26	VSS[205]	VSS[305] V11
BF28	VSS[206]	VSS[306] V17
BF30	VSS[207]	VSS[307] V26
BF38	VSS[208]	VSS[308] V27
BF38	VSS[209]	VSS[309] V29
BF40	VSS[210]	VSS[310] V31
BF48	VSS[211]	VSS[311] V38
BG17	VSS[212]	VSS[312] V43
BG21	VSS[213]	VSS[313] V43
BG33	VSS[214]	VSS[314] V7
BG44	VSS[215]	VSS[315] W17
BH11	VSS[216]	VSS[316] W19
BH15	VSS[217]	VSS[317] W2
BH17	VSS[218]	VSS[318] W27
BH19	VSS[219]	VSS[319] W48
H10	VSS[220]	VSS[320] Y12
BH27	VSS[221]	VSS[321] Y4
BH31	VSS[222]	VSS[322] Y42
BH33	VSS[223]	VSS[323] Y46
BH35	VSS[224]	VSS[324] Y8
BH39	VSS[225]	VSS[325] BG29
BH43	VSS[226]	VSS[326] N24
BH47	VSS[227]	VSS[327] A13
D1	VSS[228]	VSS[328] AD47
D12	VSS[229]	VSS[329] B43
D16	VSS[230]	VSS[330] BE10
D18	VSS[231]	VSS[331] BG41
D22	VSS[232]	VSS[332] G14
D24	VSS[233]	VSS[333] H16
D26	VSS[234]	VSS[334] T36
D30	VSS[235]	VSS[335] BG22
D32	VSS[236]	VSS[336] BG24
D34	VSS[237]	VSS[337] C22
D38	VSS[238]	VSS[338] AP13
D42	VSS[239]	VSS[339] M14
D8	VSS[240]	VSS[340] AP3
E18	VSS[241]	VSS[341] AP1
E26	VSS[242]	VSS[342] BE16
G18	VSS[243]	VSS[343] BC16
G20	VSS[244]	VSS[344] BG28
G26	VSS[245]	VSS[345] B128
G28	VSS[246]	VSS[346]
G36	VSS[247]	VSS[347]
G48	VSS[248]	VSS[348]
H12	VSS[249]	VSS[349]
H18	VSS[250]	VSS[350]
H22	VSS[251]	VSS[351]
H24	VSS[252]	VSS[352]
H26	VSS[253]	
H28	VSS[254]	
H30	VSS[255]	
H32	VSS[256]	
H34	VSS[257]	
F3	VSS[258]	

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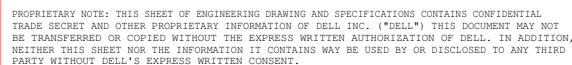
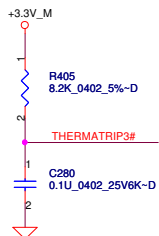
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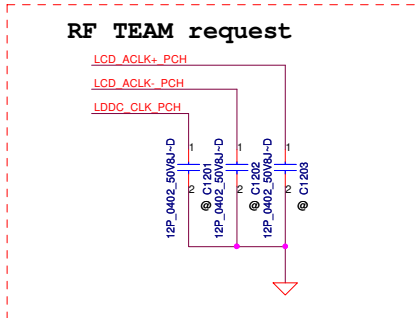
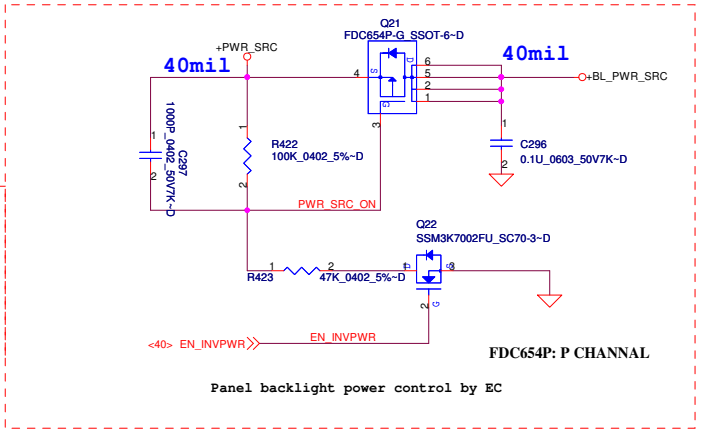
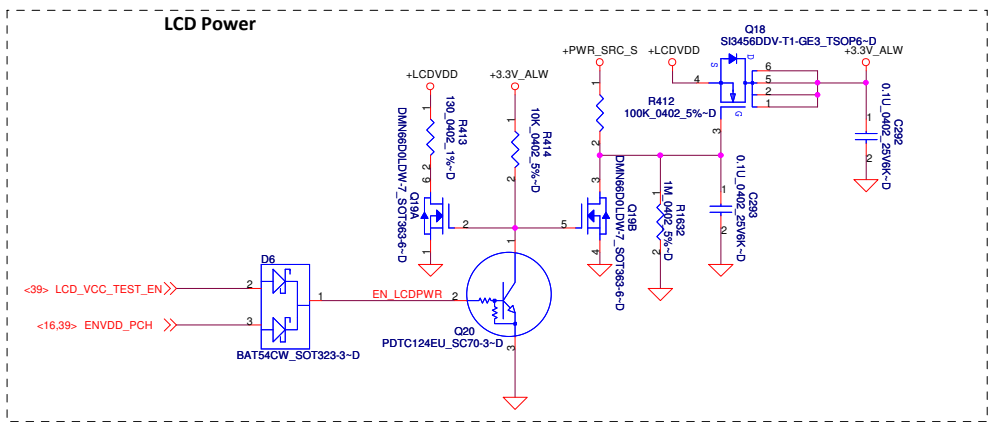
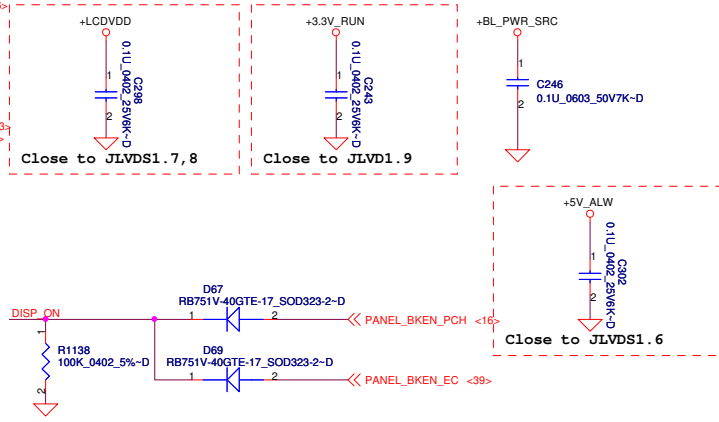
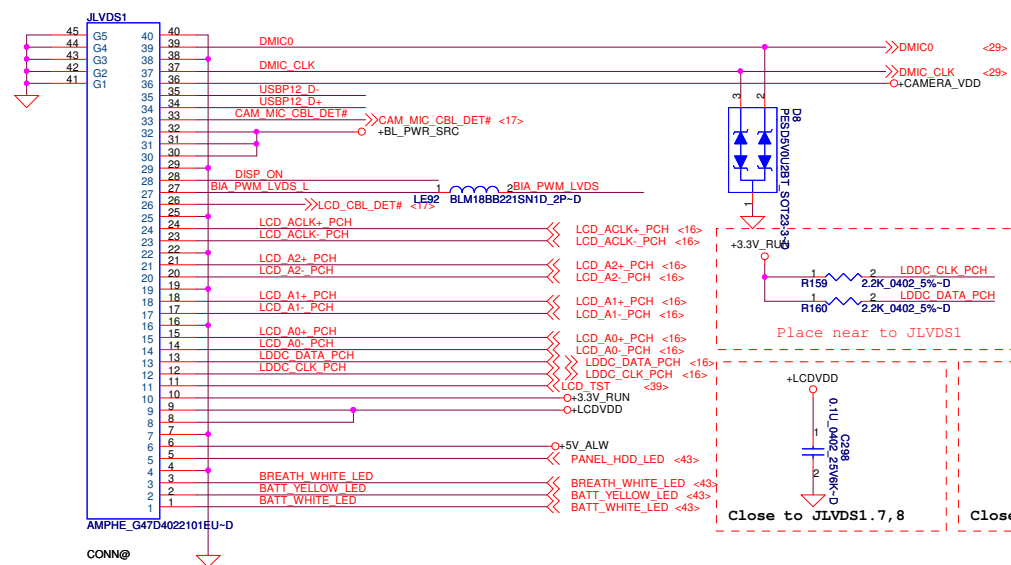


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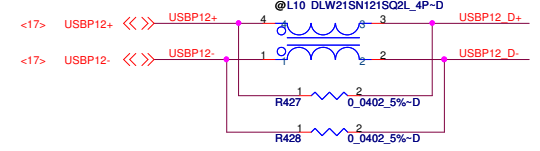
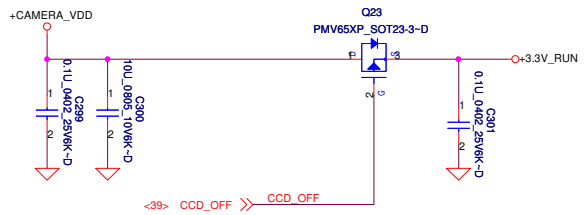
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Title			
PCH (8/8)			
Size	Document Number	Rev	
	LA-7741	0.1	
Date	Thursday, June 23, 2011	Sheet	21 of 56





For Webcam

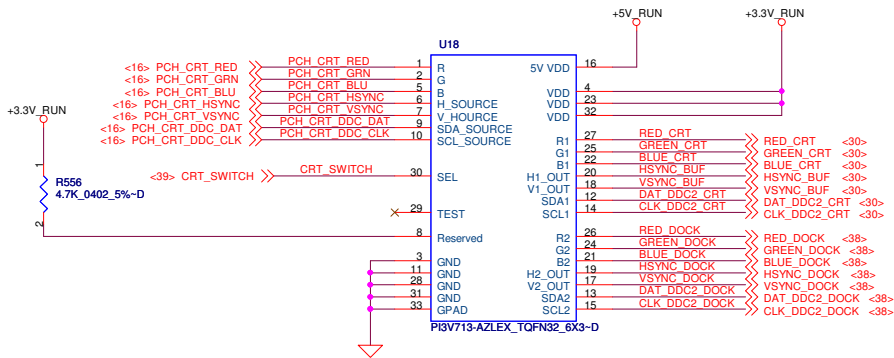


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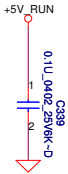
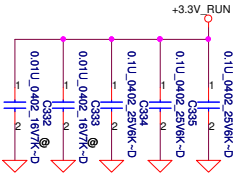
Compal Electronics, Inc.			
Title			
LVDS			
Size			
Document Number			
LA-7741			
Date			
Thursday, June 23, 2011			
Sheet			
23 of 56			

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SW for MB/DOCK

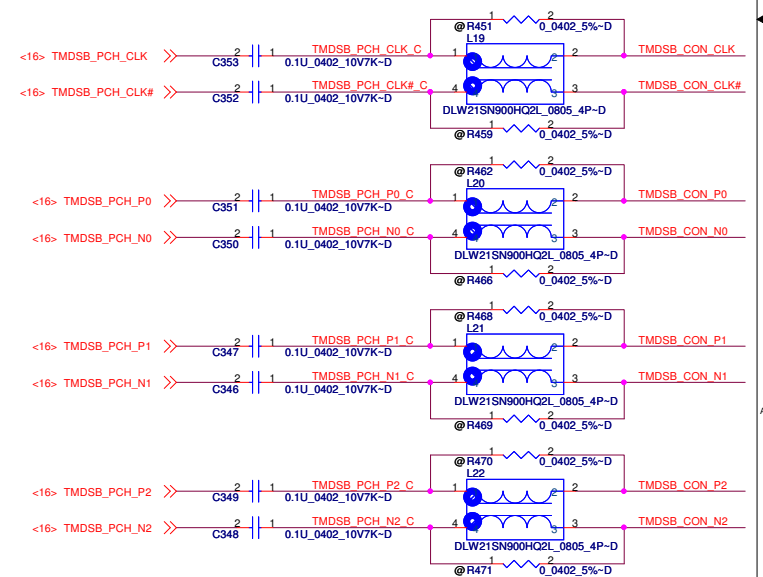
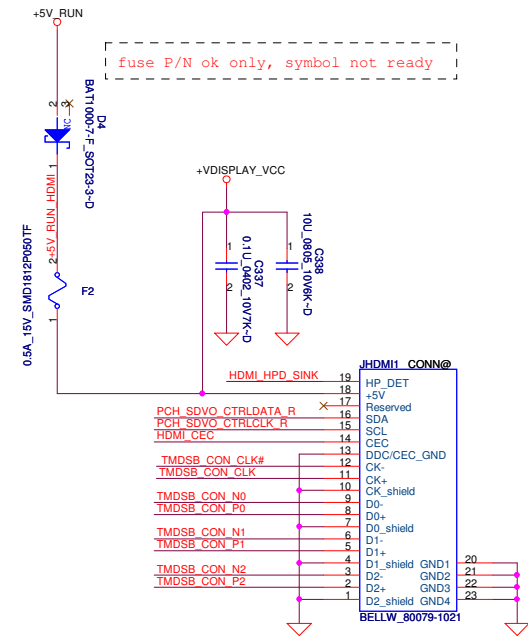
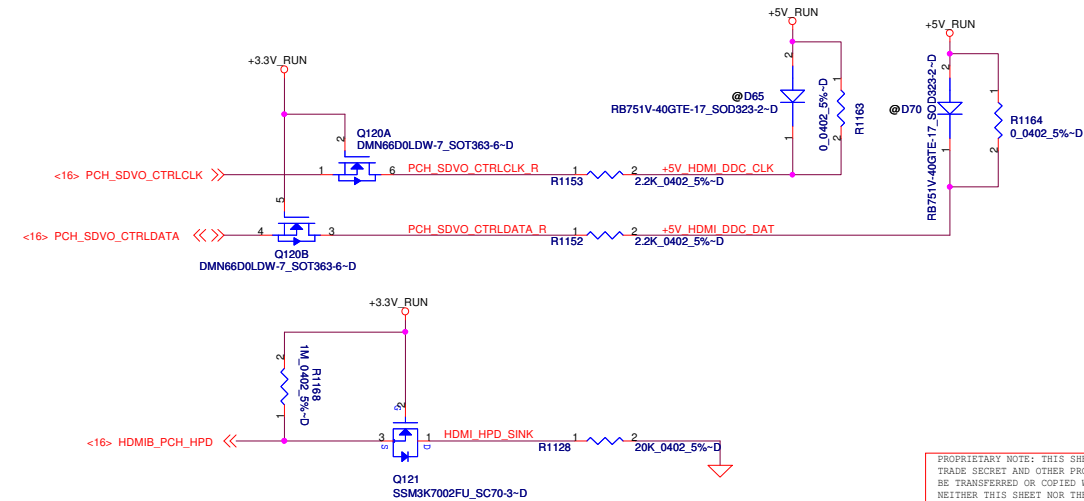
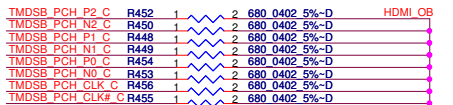
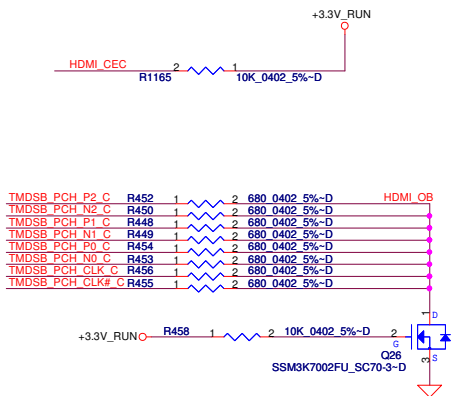


SEL1/SEL2	Chanel	Source
0	A=B1	MB
1	A=B2	APR/SPR



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	Compal Electronics, Inc.					
	Title					
	CRT/Video switch					
	Size	Document Number			Rev	
		LA-7741			0.1	
Date:	Thursday, June 23, 2011		Sheet	24	of	56

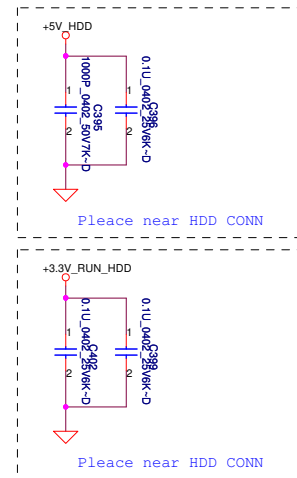
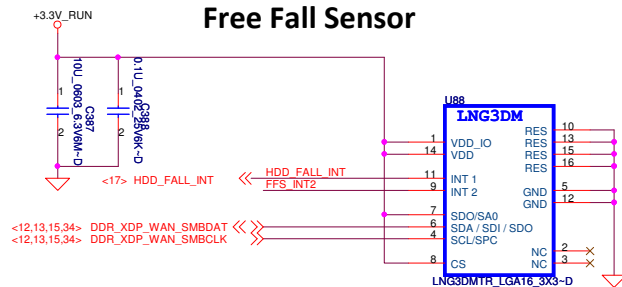


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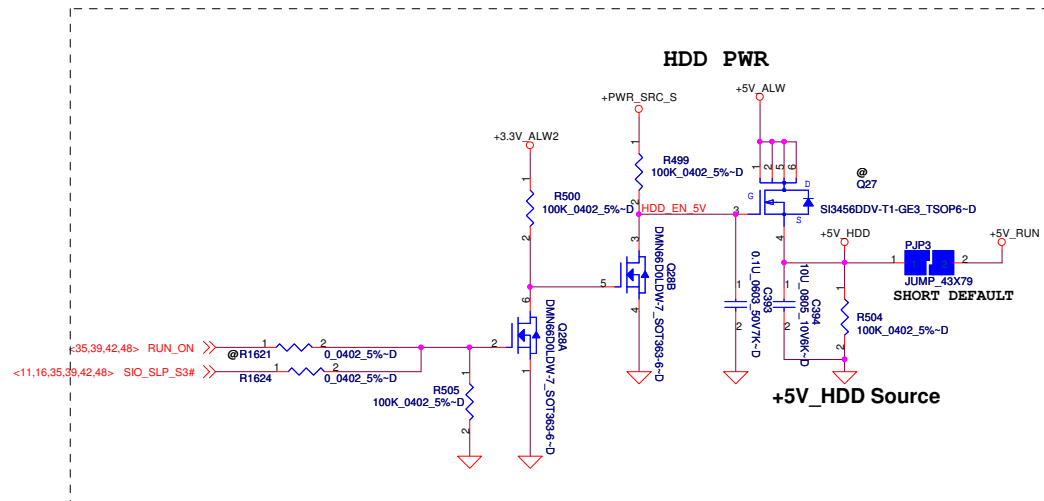
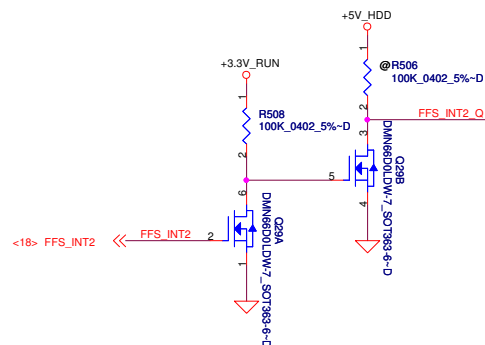
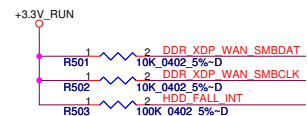
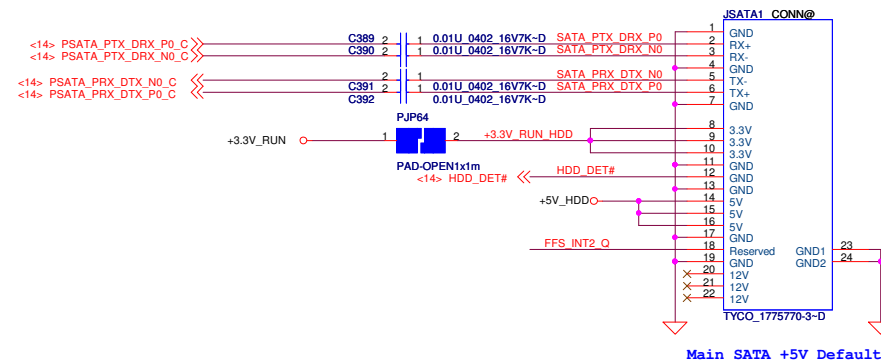
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Compal Electronics, Inc.		
HDMI port		
Size	Document Number	Rev
	LA-7741	0.1
Date:	Thursday, June 23, 2011	Sheet 25 of 56

Free Fall Sensor



For HDD Temp.



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HDD CONNECTOR			
Title	Document Number	Rev	0.1
Date	Thursday, June 23, 2011	Sheet	27 of 56

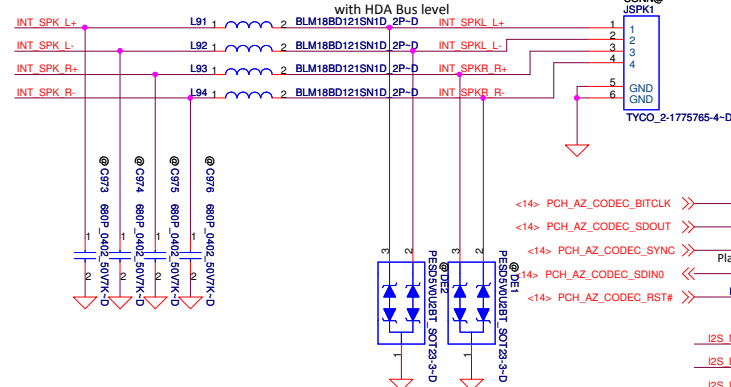
PROPRIETARY NOTE: THIS SHEET OF ENGINEERING DRAWING AND SPECIFICATIONS CONTAINS CONFIDENTIAL TRADE SECRET AND OTHER PROPRIETARY INFORMATION OF DELL INC. ("DELL") THIS DOCUMENT MAY NOT BE TRANSFERRED OR COPIED WITHOUT THE EXPRESS WRITTEN AUTHORIZATION OF DELL. IN ADDITION, NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT DELL'S EXPRESS WRITTEN CONSENT.

Internal Speakers Header

15 mils trace

DVDD_IO should match

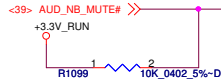
with HDA Bus level



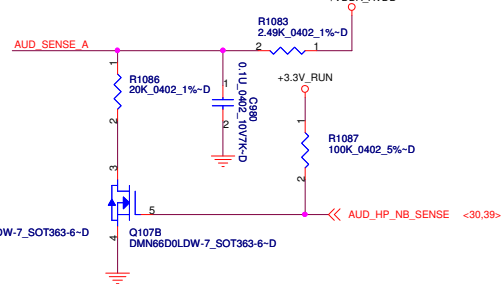
Close to U72 pin5

Close to U72 pin6

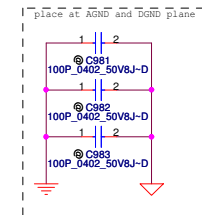
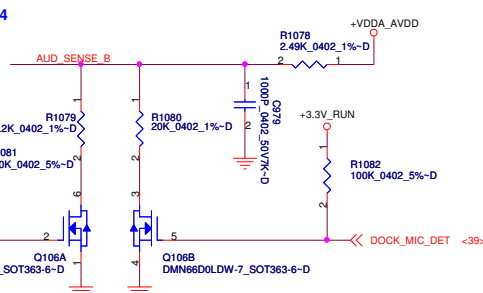
BCLK: Audio serial data bus bit clock input/output
LRCLK: Audio serial data bus word clock input/output



Place closely to Pin 13.



Place closely to Pin 14

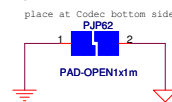


Resistor	SENSE_A	SENSE_B
39.2K	PORT A	PORT E
20K	PORT B	PORT F
10K	NA	DMIC0
5.11K	SPDIFOUT0	SPDIFOUT1 (DMIC1)
2.49K	Pull-up to AVDD	

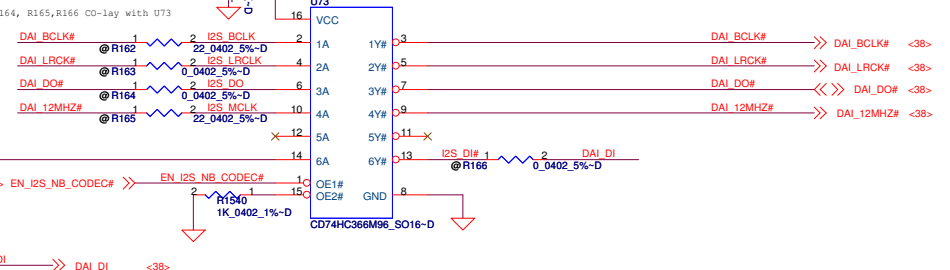
PORT A	External MIC
PORT B	HeadPhone Out
PORT C	Dock Audio
PORT D	Internal SPK

Notes:

Keep PVDD supply and speaker traces routed on the DGND plane.
Keep away from AGND and other analog signals



R162, R163, R164, R165, R166 CO-LAY WITH U73



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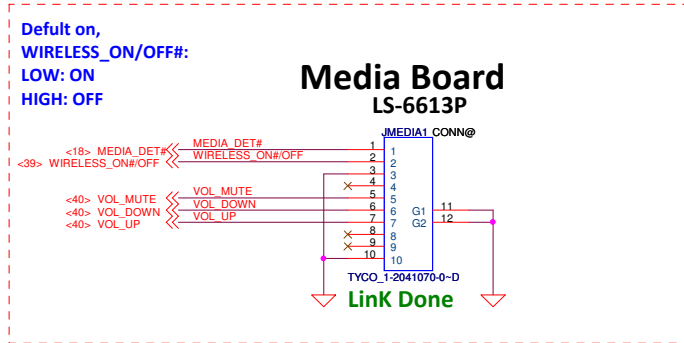
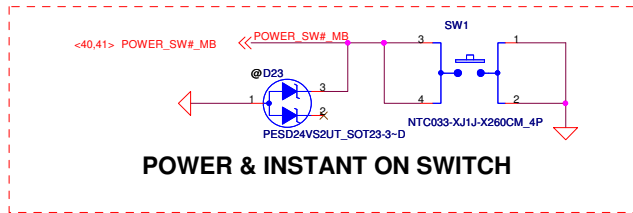
Compal Electronics, Inc.

Azalia (HD) Codec

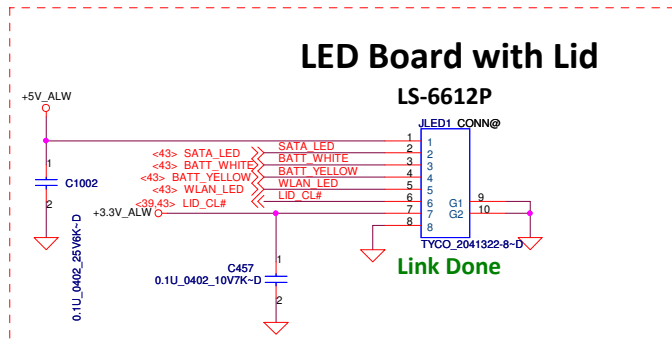
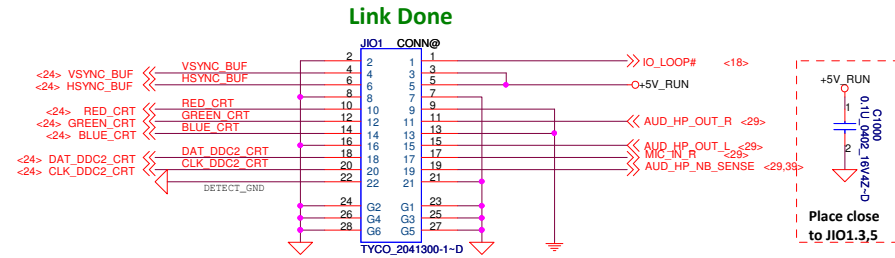
Title	Document Number	Rev
LA-7741		0.1

Date: Thursday, June 23, 2011 Sheet 29 of 56

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I/O board CONN.

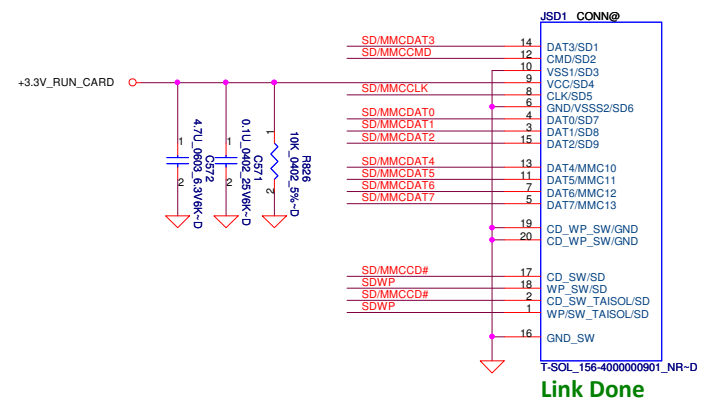
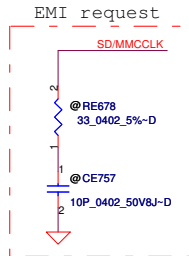
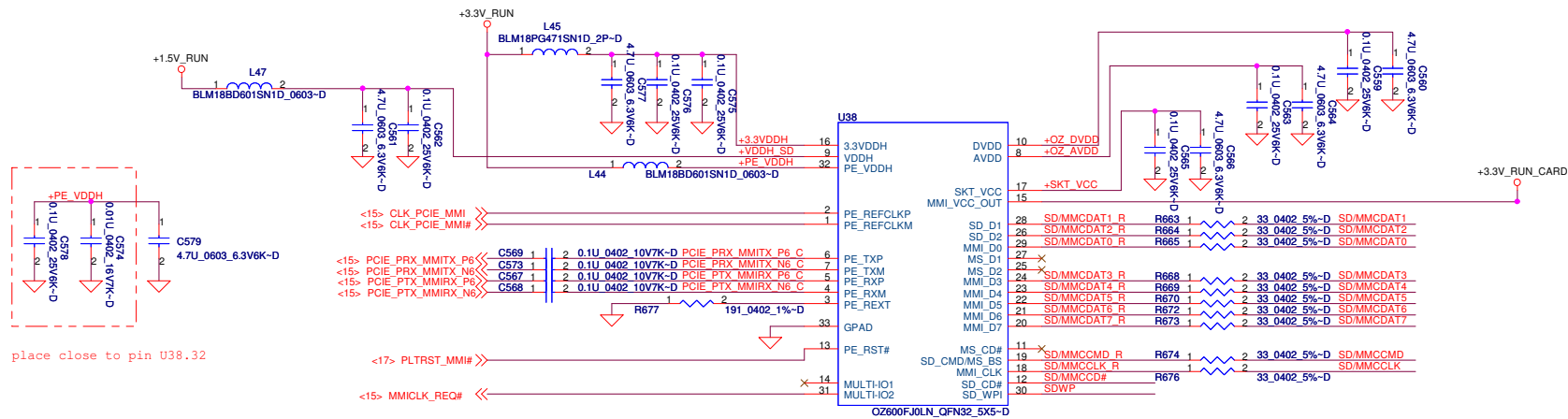


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Title		PWR SW/Sub-board Connector	
Size	Document Number	LA-7741	Rev 0.1
Date:	Thursday, June 23, 2011	Sheet 30	of 56

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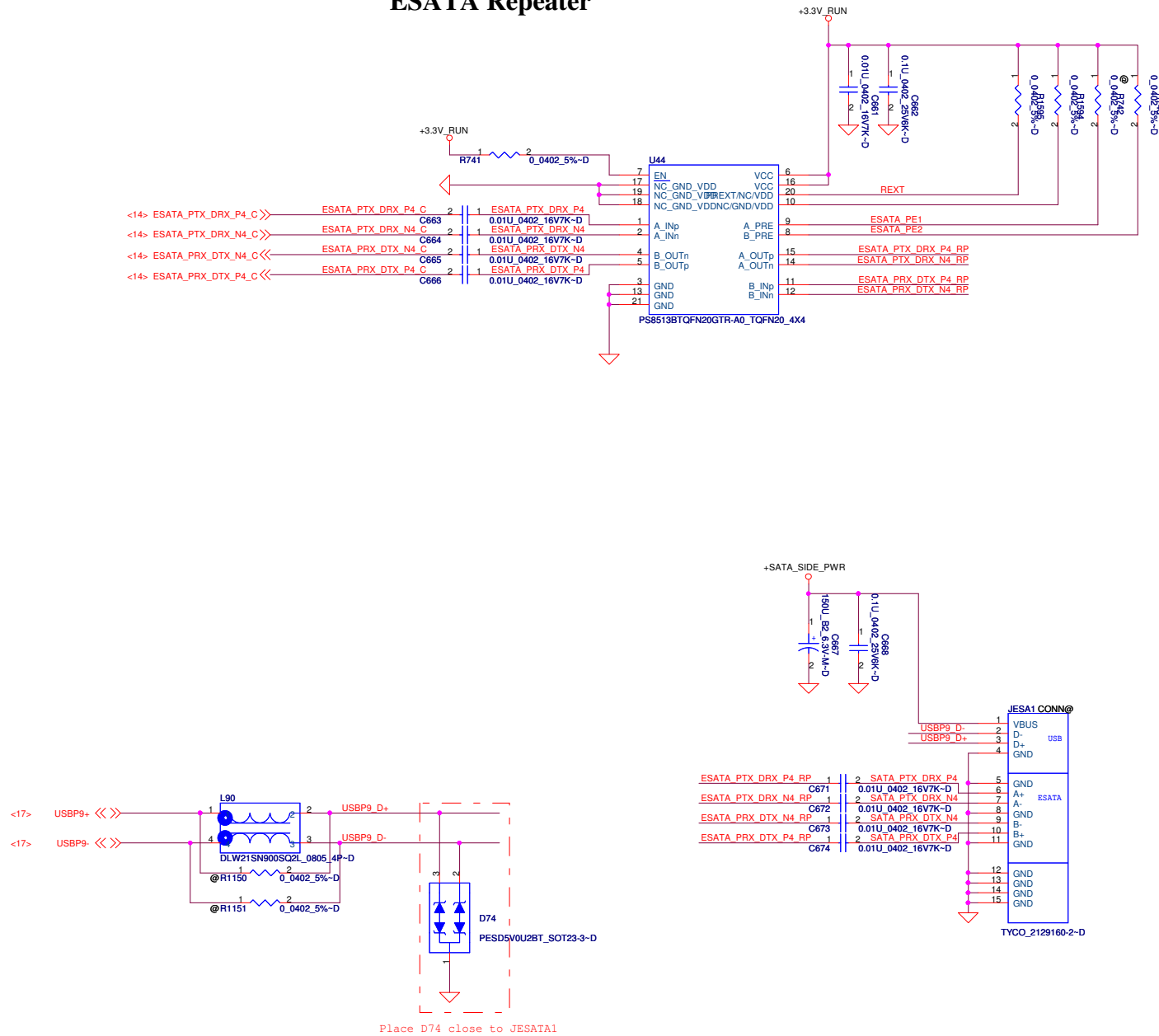
Card Reader

LA-7741

Title	LA-7741
Size	Document Number
Date	Thursday, June 23, 2011
Sheet	33 of 56
Rev	0.1

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ESATA Repeater



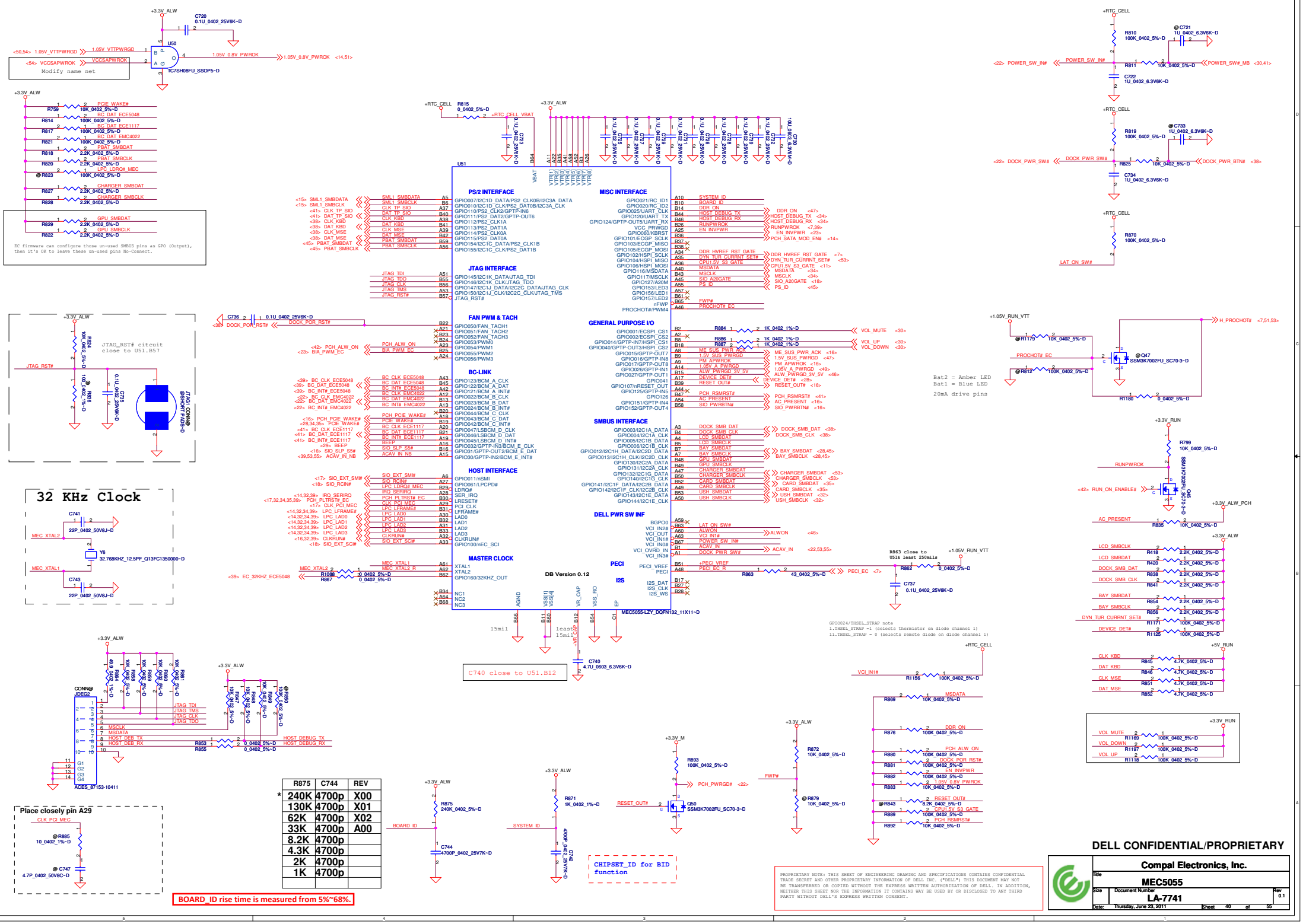
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Compal Electronics, Inc.

Title				
USB/ESATA/IO/MDC				
Size	Document Number			Rev
	LA-7741			0.1
Date:	Thursday, June 23, 2011		Sheet 37 of 56	

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BOARD_ID rise time is measured from 5%~68%.

R875	C744	REV
240K	4700p	X00
130K	4700p	X01
62K	4700p	X02
33K	4700p	A00
8.2K	4700p	
4.3K	4700p	
2K	4700p	
1K	4700p	

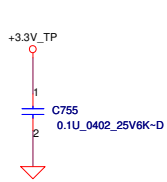
CHIPSET_ID for BD function

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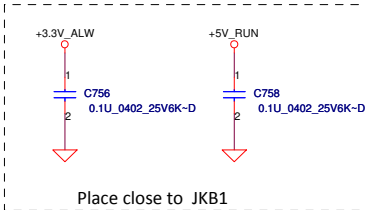
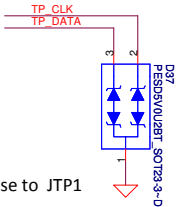
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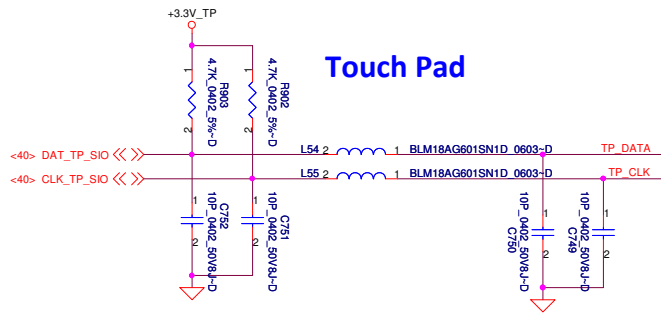
File	MEC5055		
Size	Document Number	LA-7741	Rev 0.1
Date:	Thursday, June 23, 2011	Sheet 40	of 55



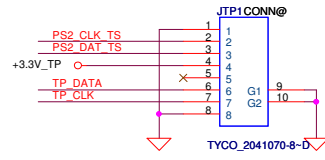
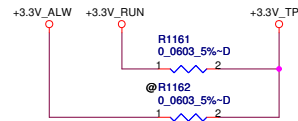
Place close to JTP1



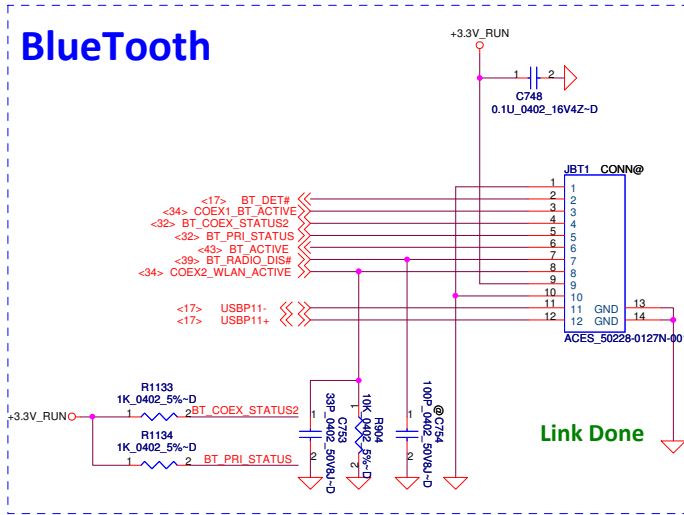
Place close to JKB1



Touch Pad



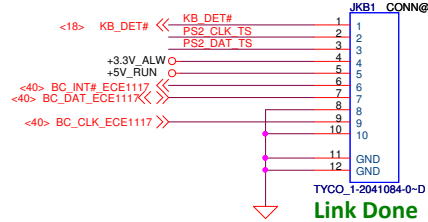
Link Done



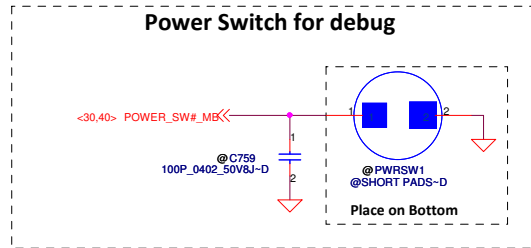
BlueTooth

Link Done

KB Conn. Pitch=1.0mm

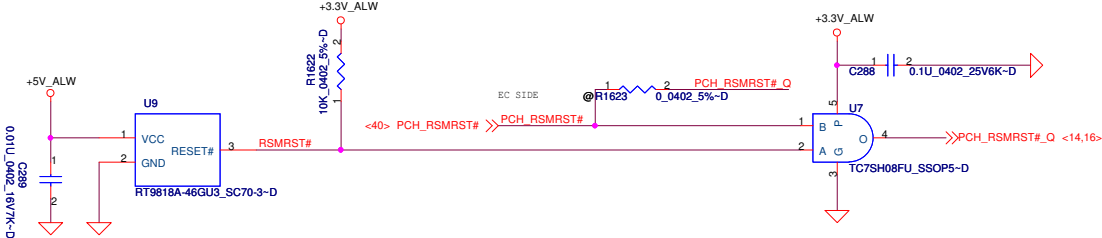


Link Done



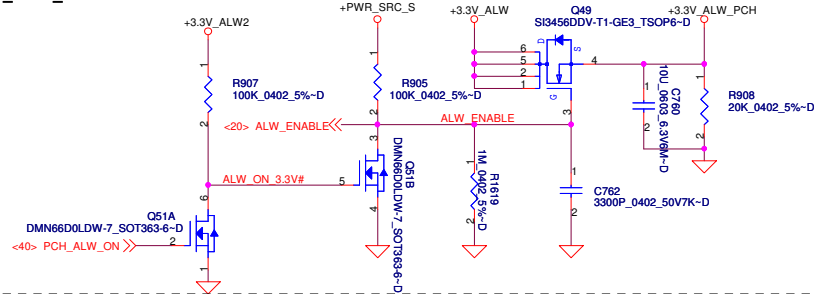
Power Switch for debug

Place on Bottom

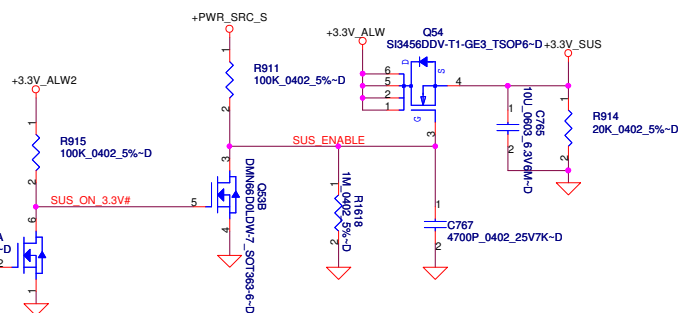


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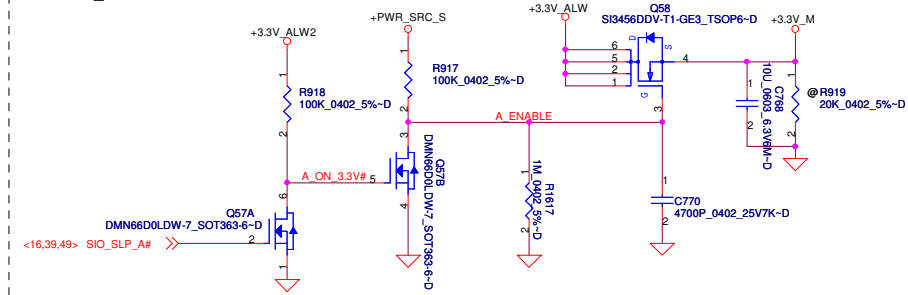
+3.3V_ALW_PCH Source



+3.3V_SUS Source



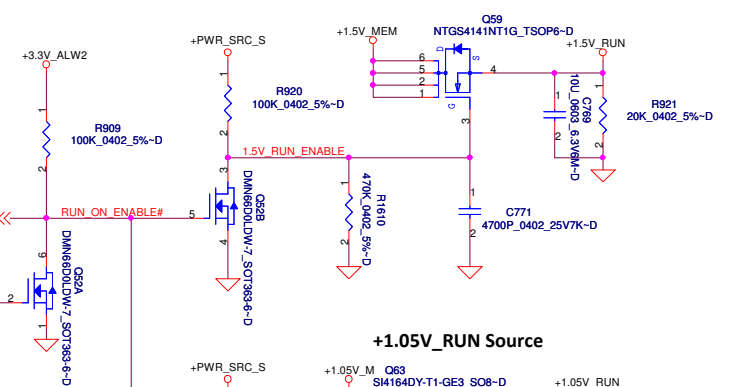
+3.3V_M Source



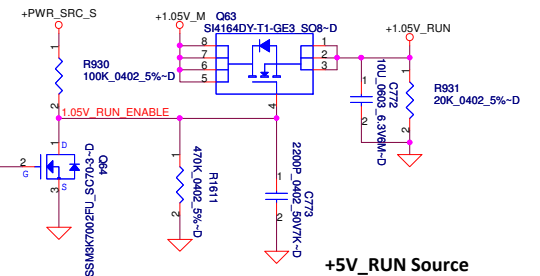
DC/DC Interface

<11,16,27,35,39,48> SIO_SLP_S3#
<27,35,39,48> RUN_ON

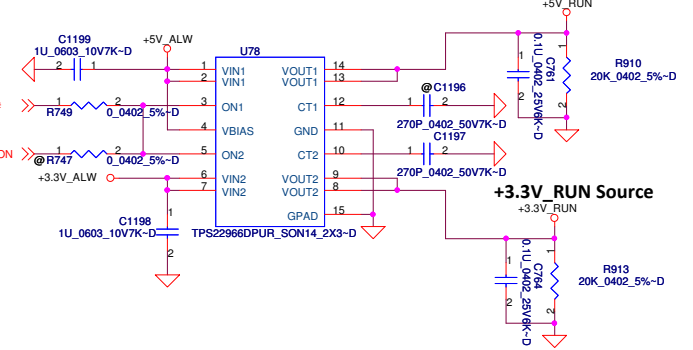
+1.5V_RUN Source



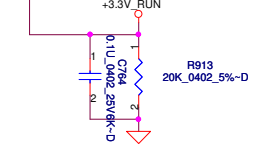
+1.05V_RUN Source



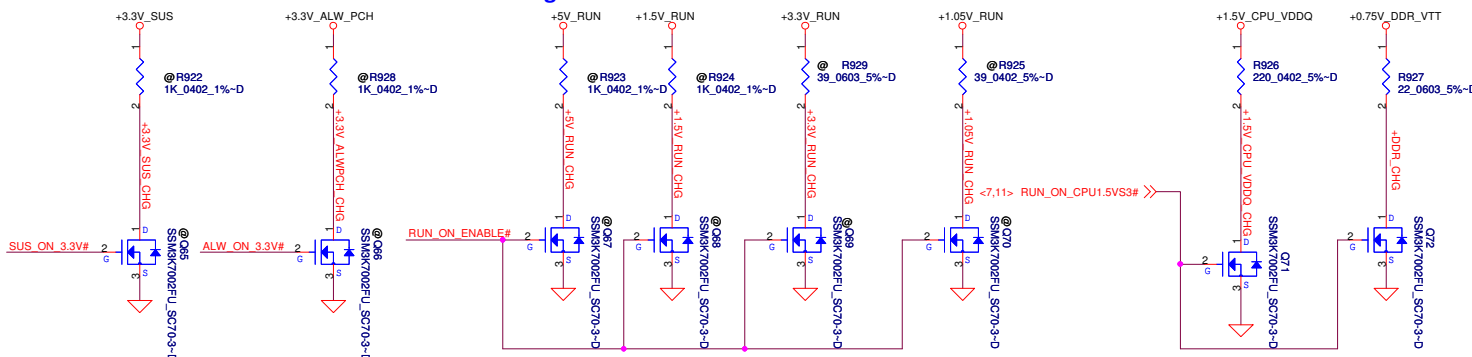
+5V_RUN Source



+3.3V_RUN Source



Discharg Circuit



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POWER CONTROL

LA-7741



Size

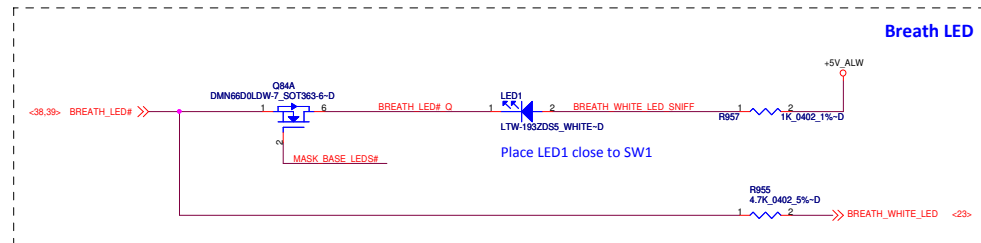
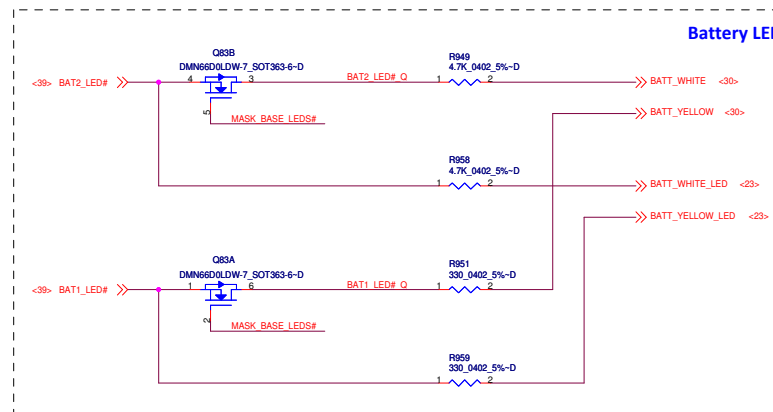
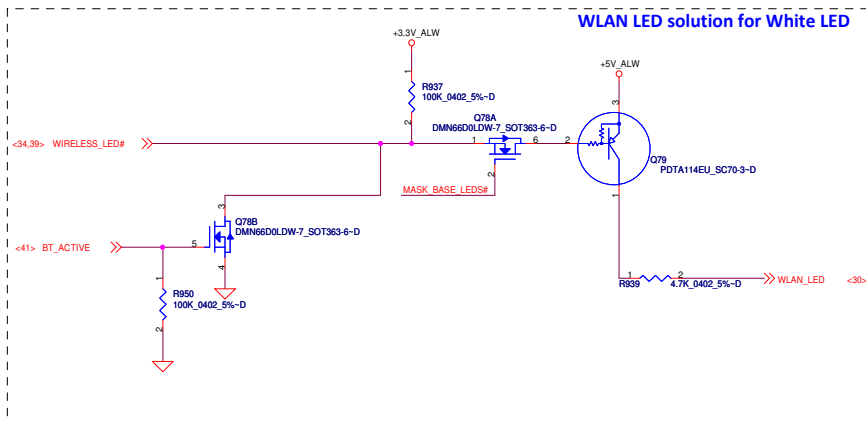
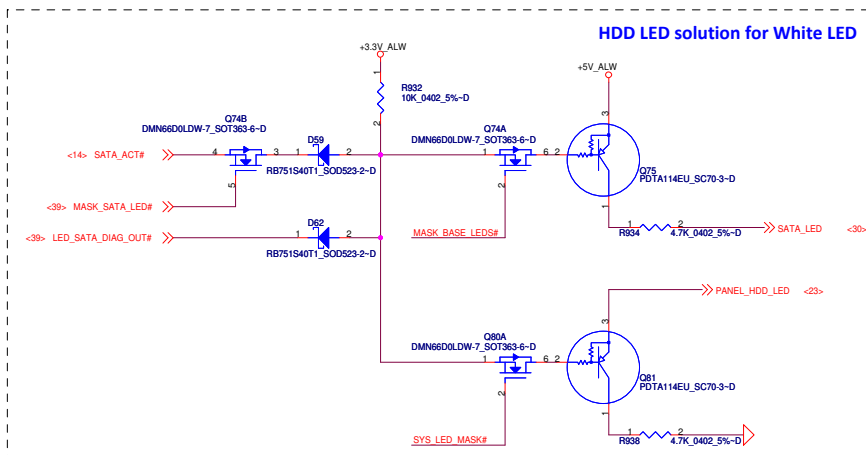
Document Number

LA-7741

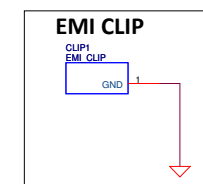
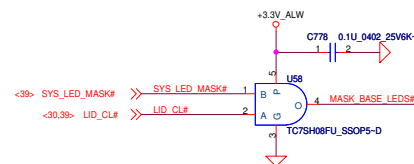
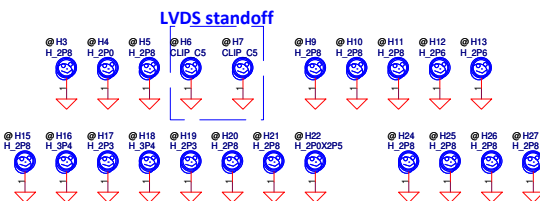
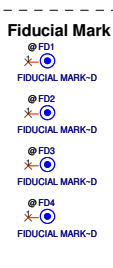
Date: Thursday, June 23, 2011

Sheet 42 of 56

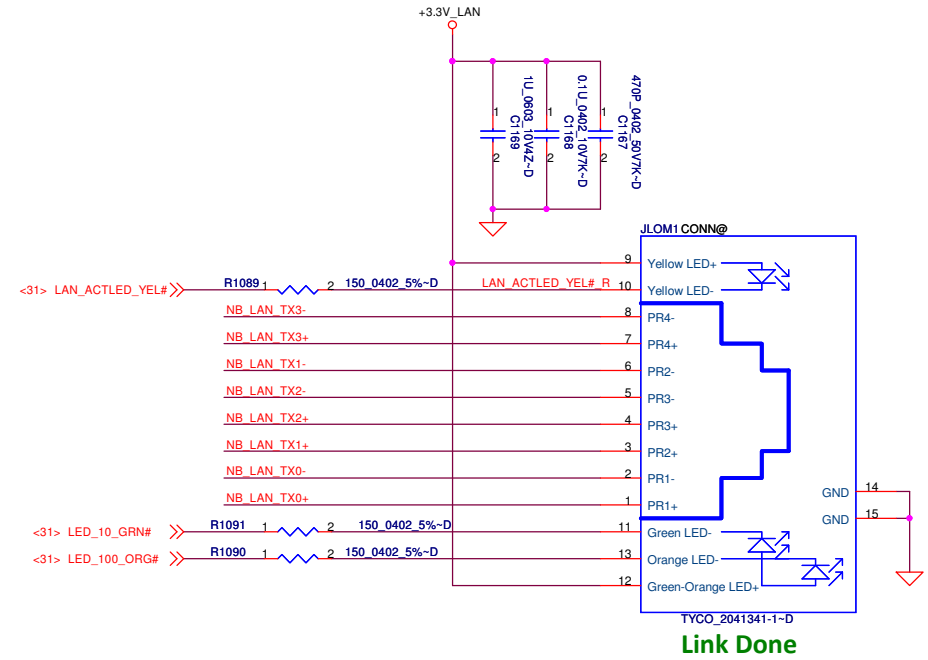
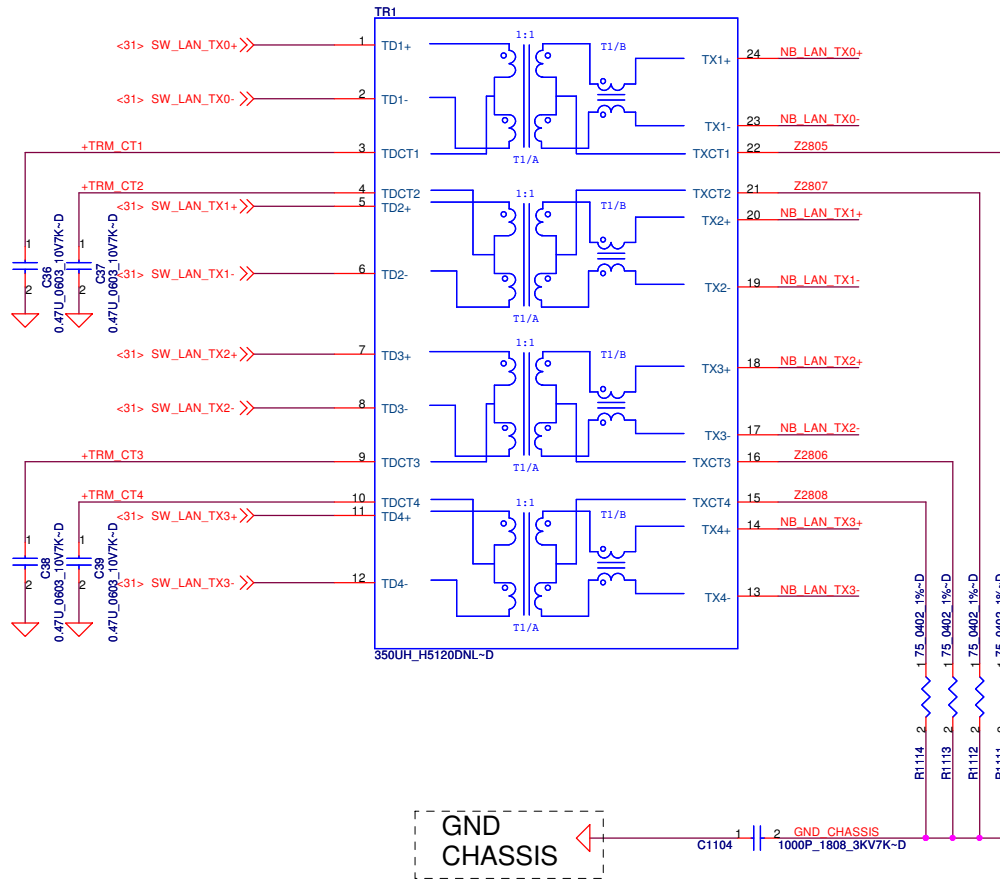
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LED Circuit Control Table		
	SYS_LED_MASK#	LID_CL#
Mask All LEDs (Sniffer Function)	0	X
Mask Base MB LEDs (Lid Closed)	1	0
Do not Mask LEDs (Lid Opened)	1	1



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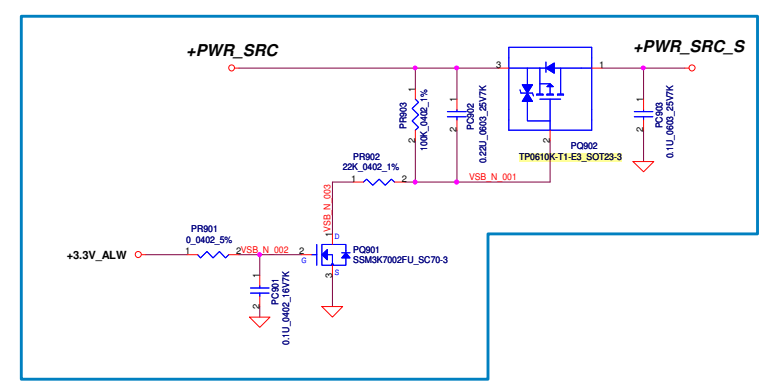
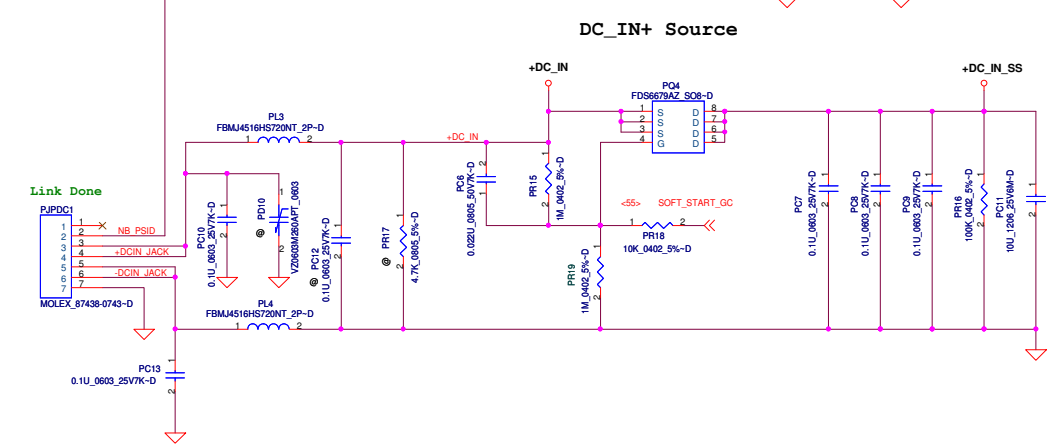
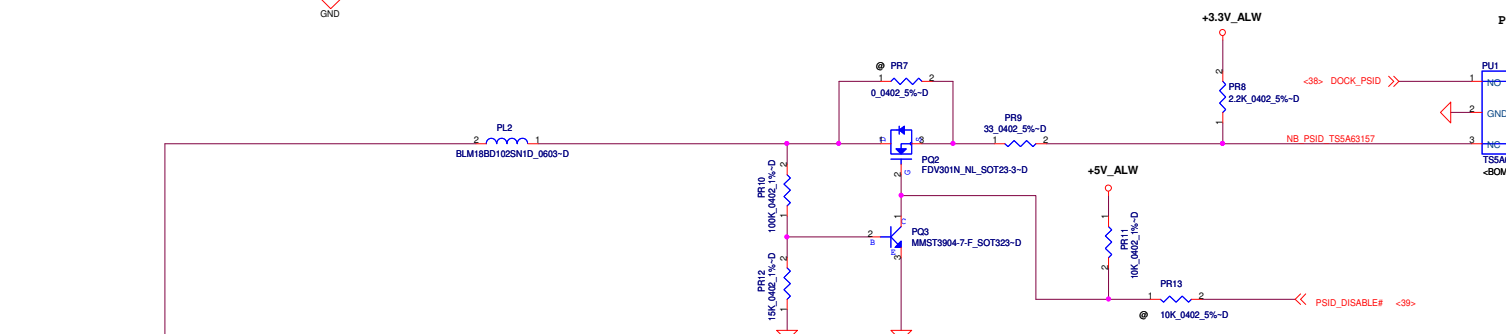
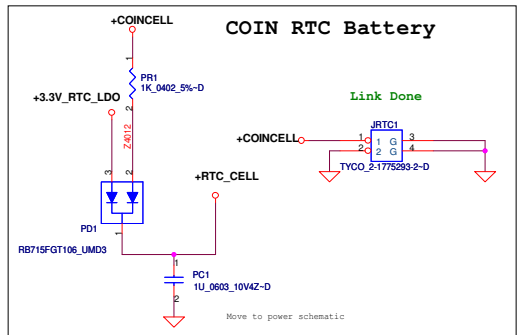
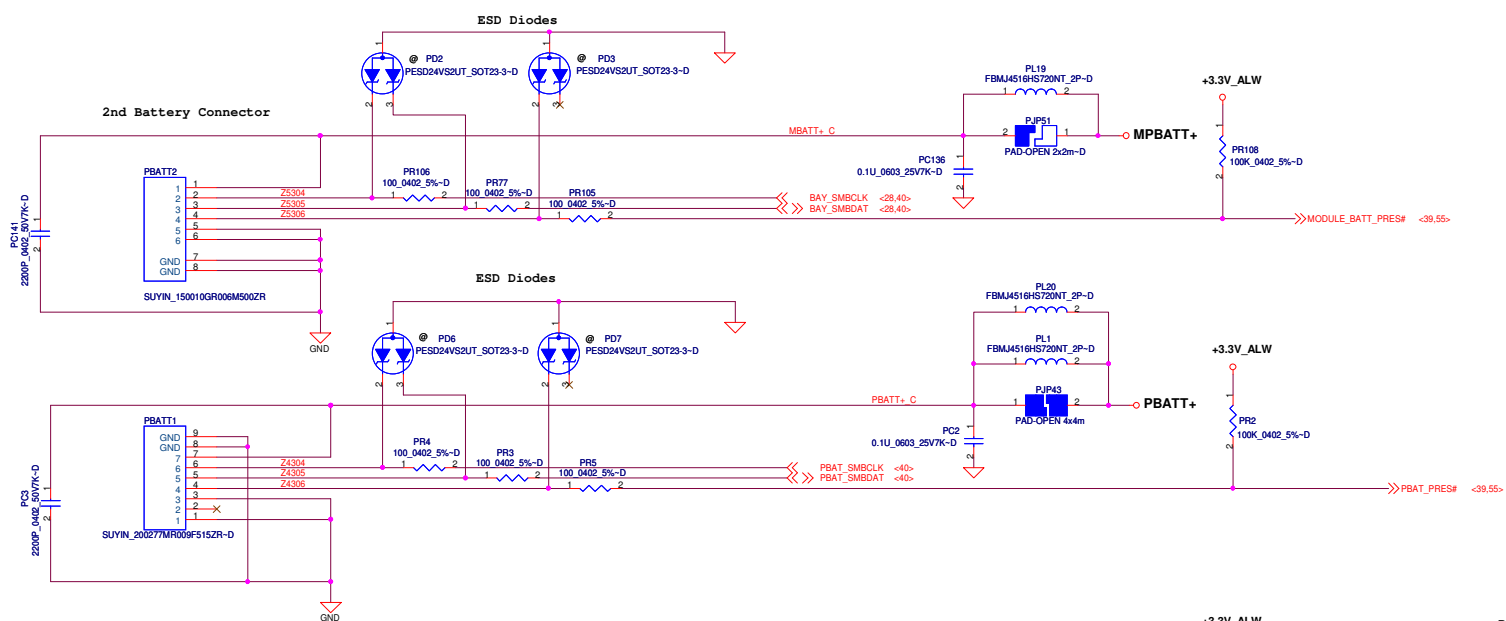
Compal Electronics, Inc.

Title		RJ45
Size	Document Number	LA-7741
Date:	Thursday, June 23, 2011	Sheet 44 of 56

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Rev 0.3



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		Compal Electronics, Inc.	
		+DCIN	
Size	Document Number	LA-7741	
Date:	Thursday, June 23, 2011	Sheet	45 of 56

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3.3VALWP +/- 5%
TDC=5.45A
Peak Current=7.786A
OCP min=10.122A
L/S RDS(on) 14.2m ohm(typ), 17.5m ohm(max)
FSW=375KHz
Delta_Iin=1.246A
Delta_Io=3.3231A

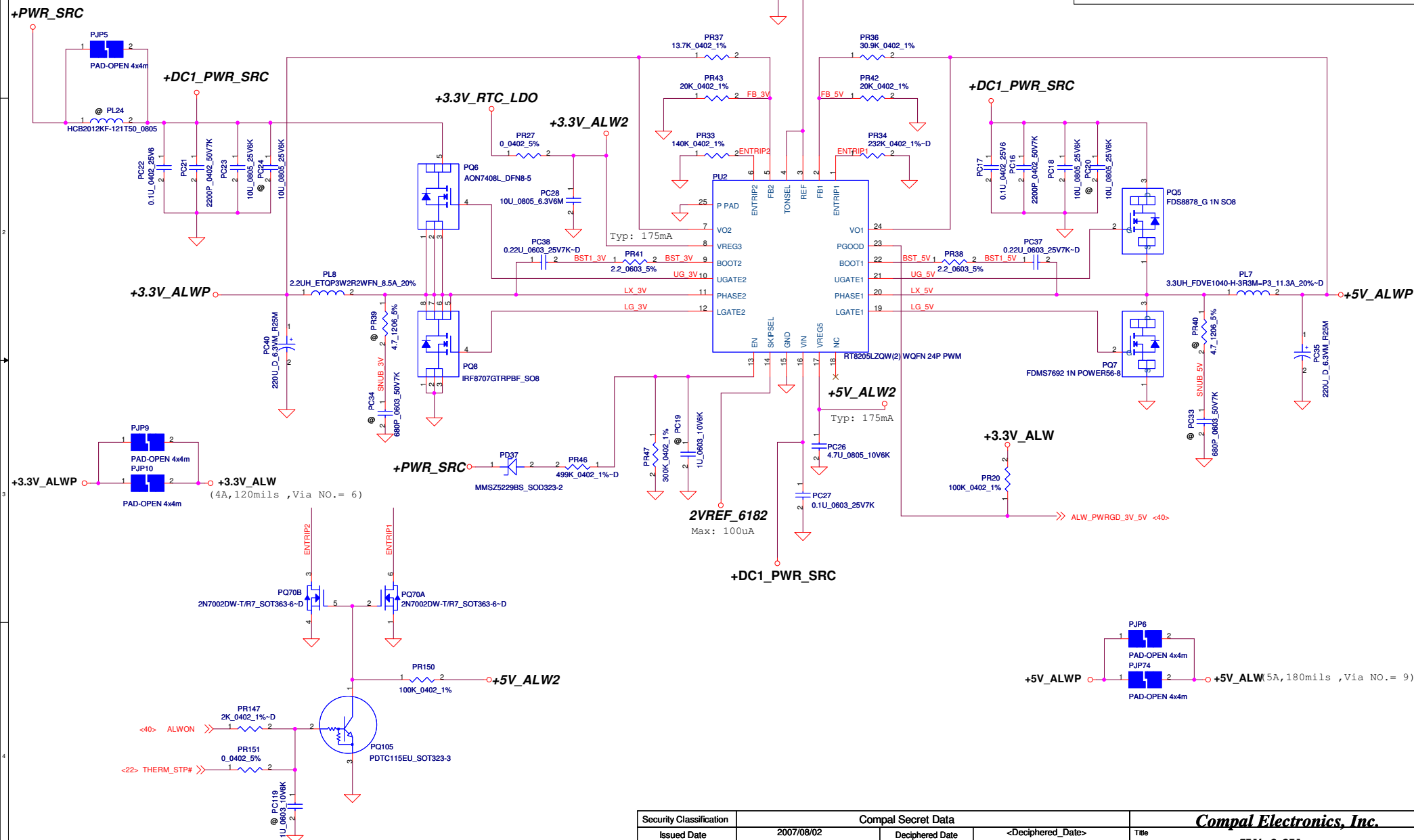
+5V_ALWP / +3.3V_ALWP

Charlie_note :

SKIPSEL Connect to REF : DEM Mode
@DEM VFBx=2.0V

TONSEL
Frequency Selectable Input for VOUT1(+5v)/VOUT2(+3.3v)
respectively.
300kHz/375kHz : Connect to REF

5VALWP +/- 5%
TDC=8.41A
Peak Current=12.012A
OCP min=15.62A
L/S RDS(on) 14.2m ohm(typ), 17.5m ohm(max)
FSW=300KHz
Delta_Iin=1.64A
Delta_Io=3.756A

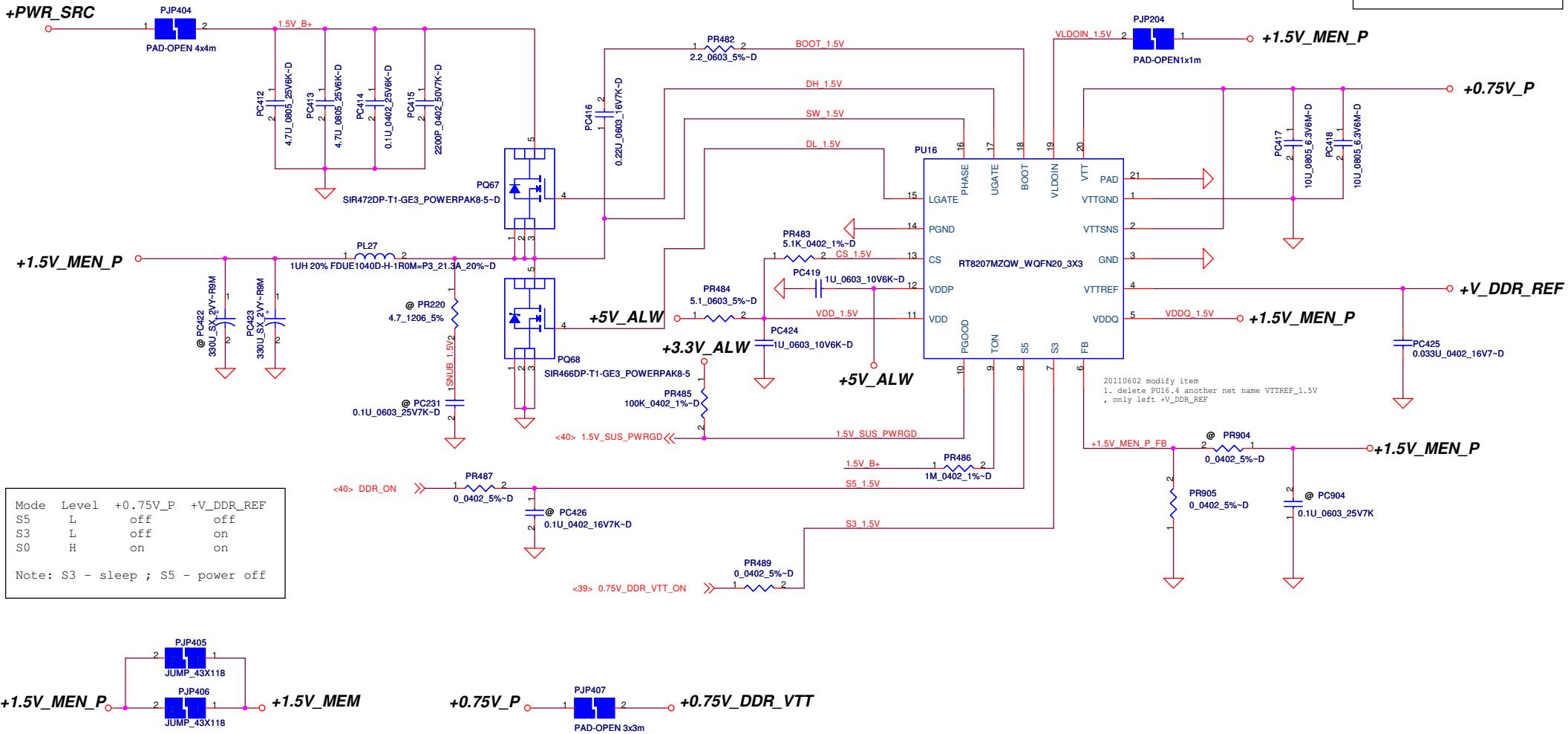


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				Custor	LA-7741
				Date	Thursday, June 23, 2011
				Sheet	46 of 56

1.5Volt +/- 5%
TDC=7.18A
Peak Current=10.25A
OCP min=13.33A
L/S RDS(on) 3.8m ohm(typ), 4.8m ohm(max)
FSW=253KHz for RTON=1M ohm, spec. On-Time =303.947ns
Delta_Iin=1.458A
Delta_Io=5.4728A

+1.5V_MEN_P/ +0.75V_P

0.75Volt +/- 5%
TDC=0.525A
Peak Current=0.75A
OCP min=0.975A



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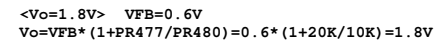
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+1.5V MEN/+0.75V DDR VTT

Title	LA-7741	Rev	0.1
Size	Document Number	Date	Thursday, June 23, 2011
Sheet	47	of	56

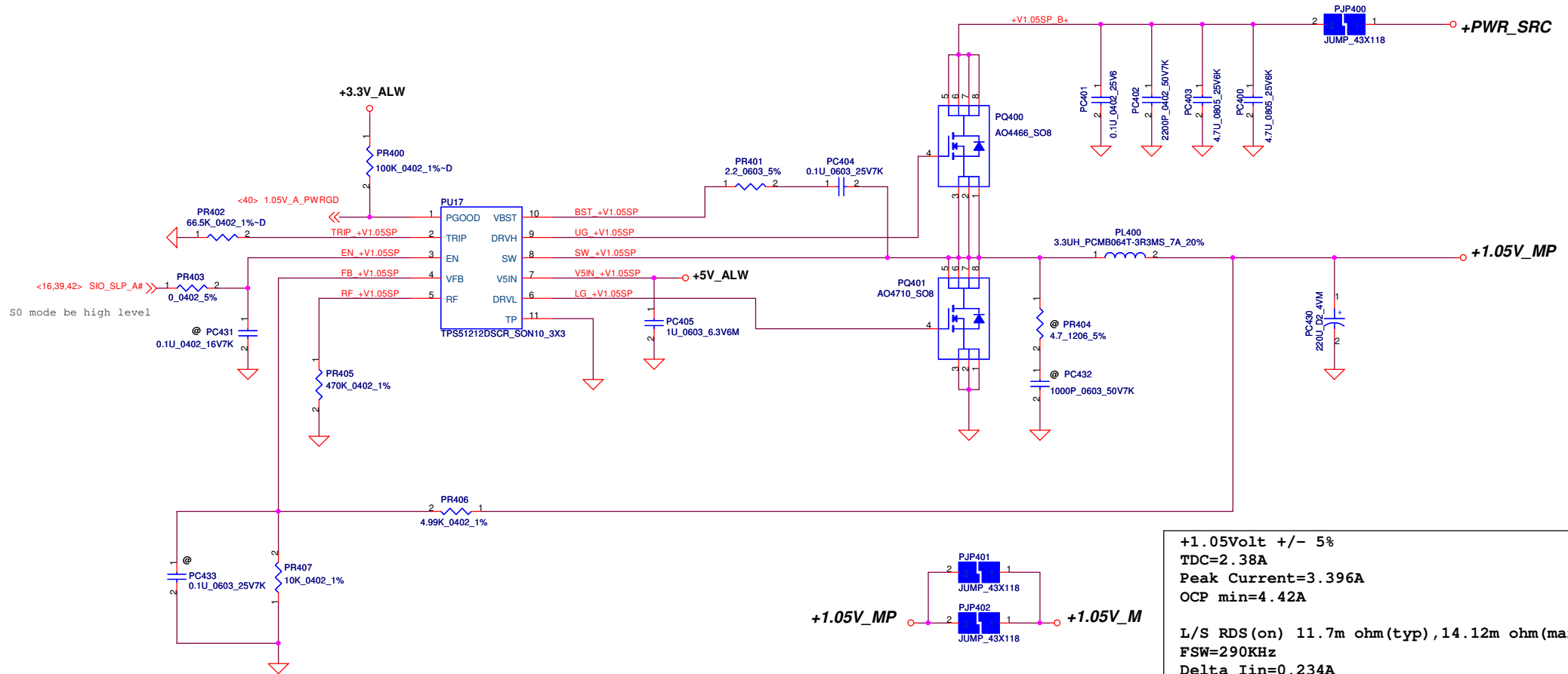
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```
FSW=1MHz
Delta_Iin=0.407A
Delta_Io=0.8182A
```



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+1.05V_MP



+1.05Volt +/- 5%
TDC=2.38A
Peak Current=3.396A
OCP min=4.42A
L/S RDS(on) 11.7m ohm(typ), 14.12m ohm(max)
FSW=290KHz
Delta I_{in}=0.234A
Delta I_o=1.038A

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Title		+1.05V_M	
Size	Document Number		Rev
	LA-7741		0.1
Date:	Thursday, June 23, 2011	Sheet	49 of 56

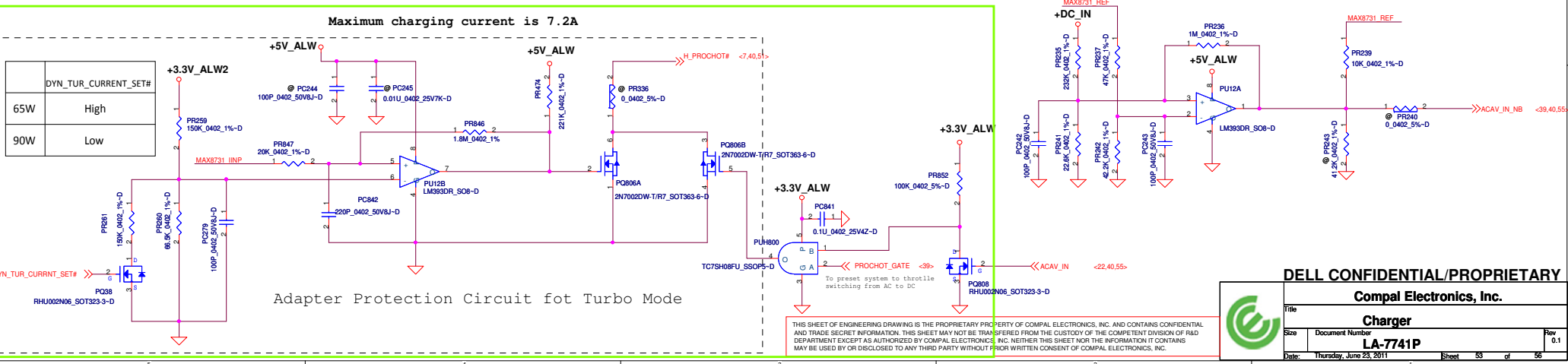
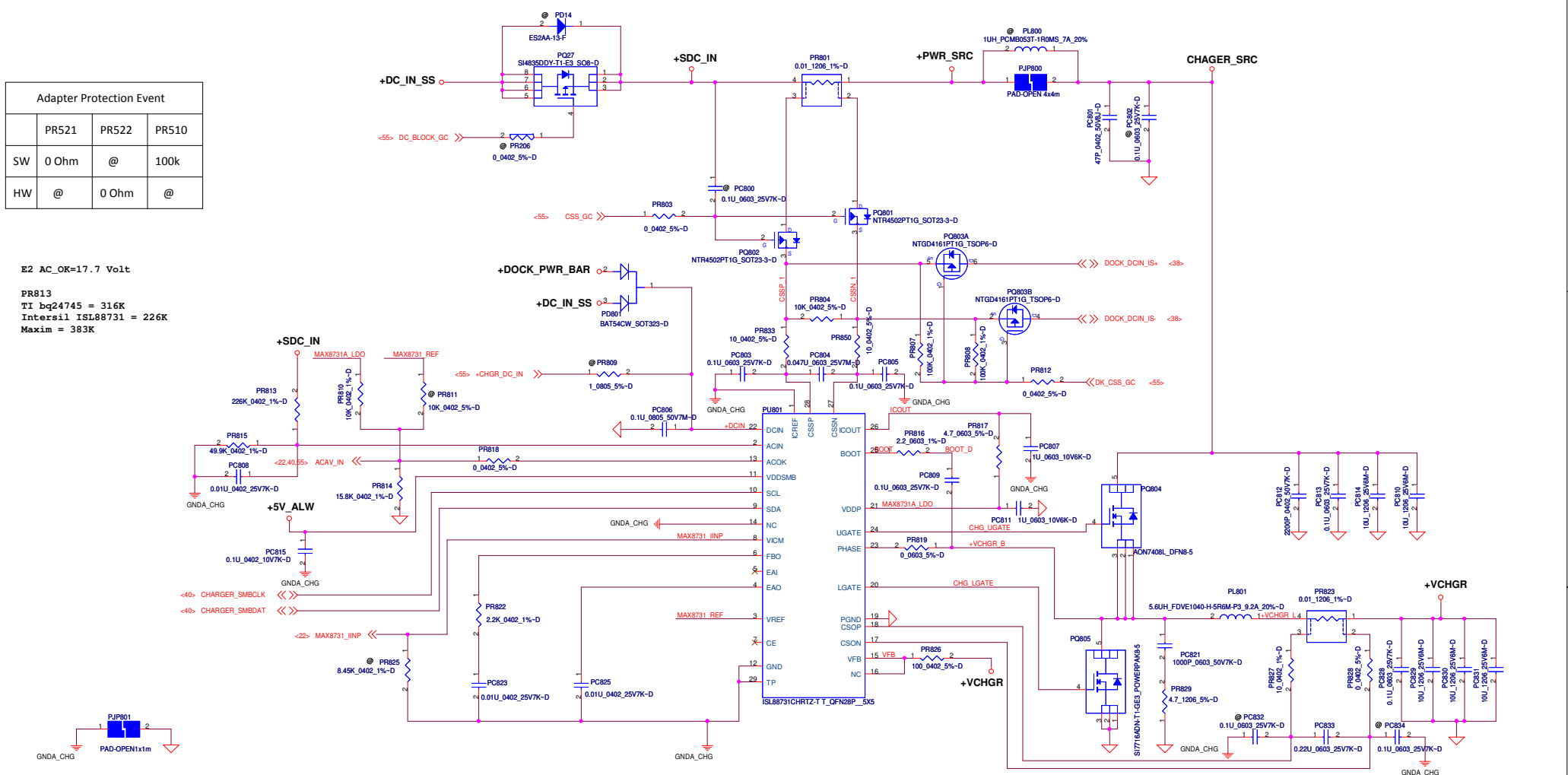
E2 AC_OK=17.7 Volt

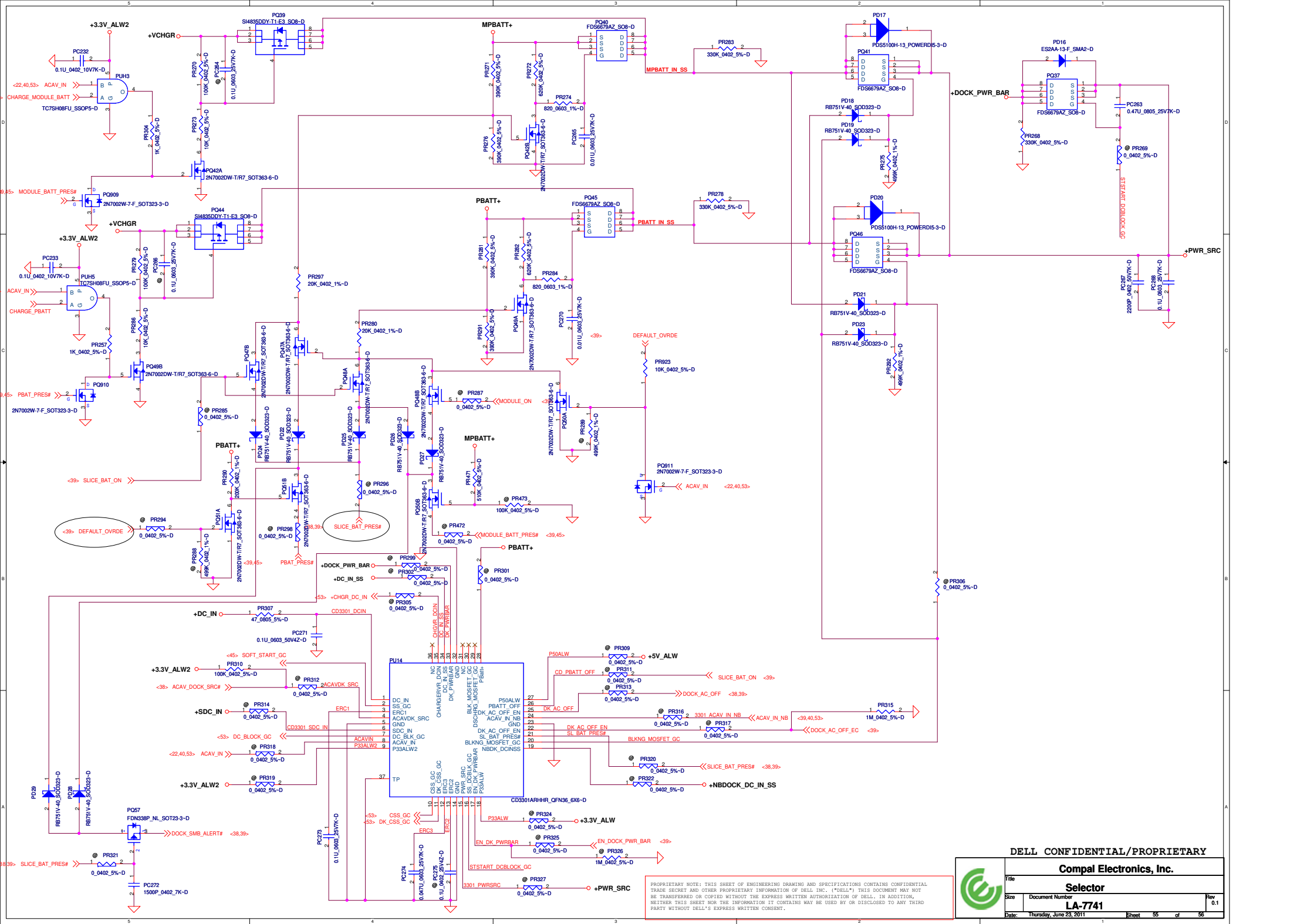
PR813

TI bq24745 = 316K

Intersil ISL88731 = 226K

Maxim = 383K

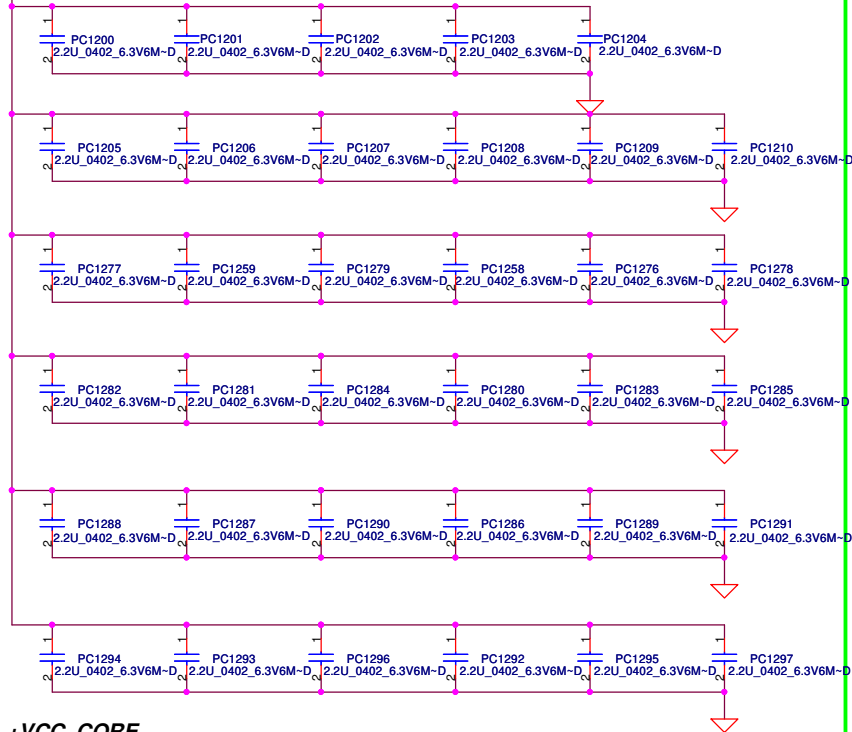




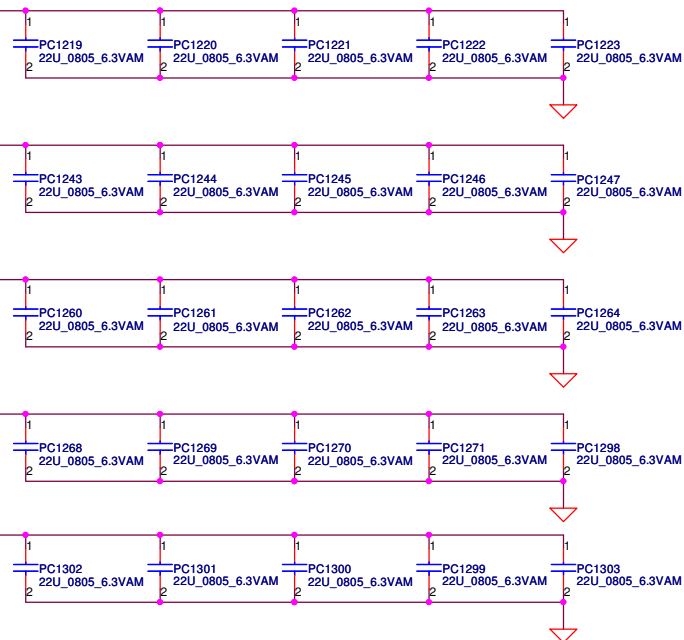
+VCC_CORE

Charlie note: Vcore_Cout1
1.22uF*35 (SE00000888L)
2.22uF*25 (SE00000888L)
Vcore_Cout2
1.470uF 4.5m *4 (SGA00004X80)

+VCC_CORE



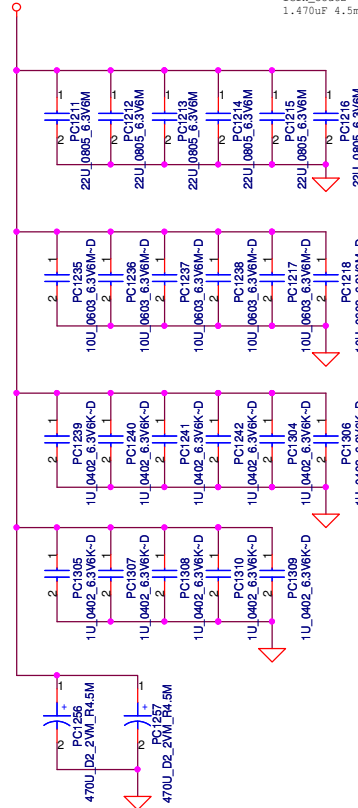
+VCC_CORE



+VCC_GFXCORE

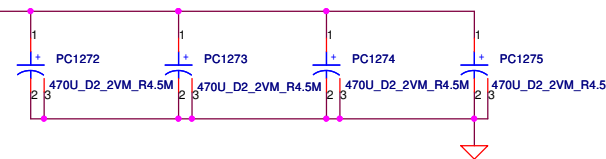
Charlie note:
iGfx_Cout1
1.22uF*6 (SE000000110)
2.10uF*6 (SE00000578L)
3.1uF*11 (SE000000K8L)
iGfx_Cout2
1.470uF 4.5m *2 (SGA00004200)

+VCC_GFXCORE



+VCC_CORE

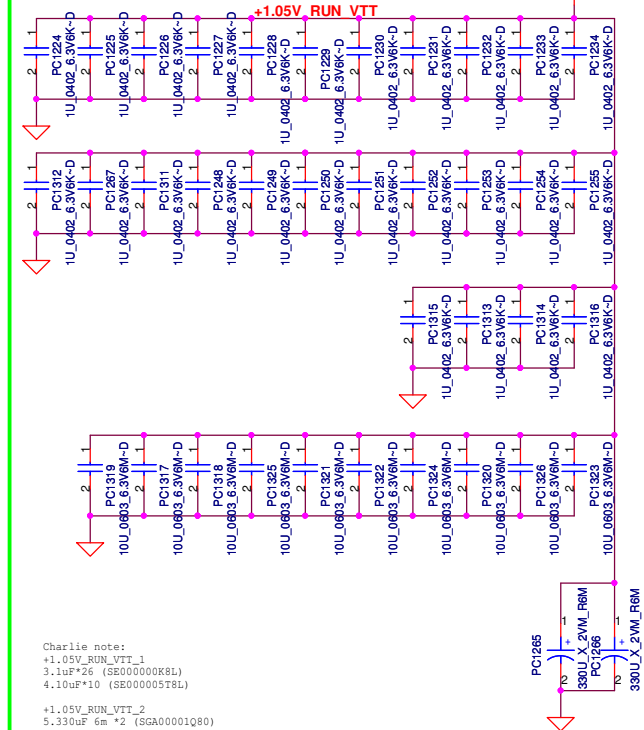
+VCC_CORE



Below is 458544_CRV_PDDG_0.5 Table 5-8.

Socket Bottom	5 x 22 μ F (0805) 5 x (0805) no-stuff sites
Socket Top	7 x 22 μ F (0805) 2 x (0805) no-stuff sites

+1.05V_RUN_VTT



Charlie note:
+1.05V_RUN_VTT_1
3.1uF*26 (SE000000K8L)
4.10uF*10 (SE00000578L)
+1.05V_RUN_VTT_2
5.330uF 6m *2 (SGA00001Q80)

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PROCESSOR DECOUPLING

Title	LA-7741	Rev	0.1
Size	Document Number		
Date	Thursday, June 23, 2011	Sheet	56 of 56

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