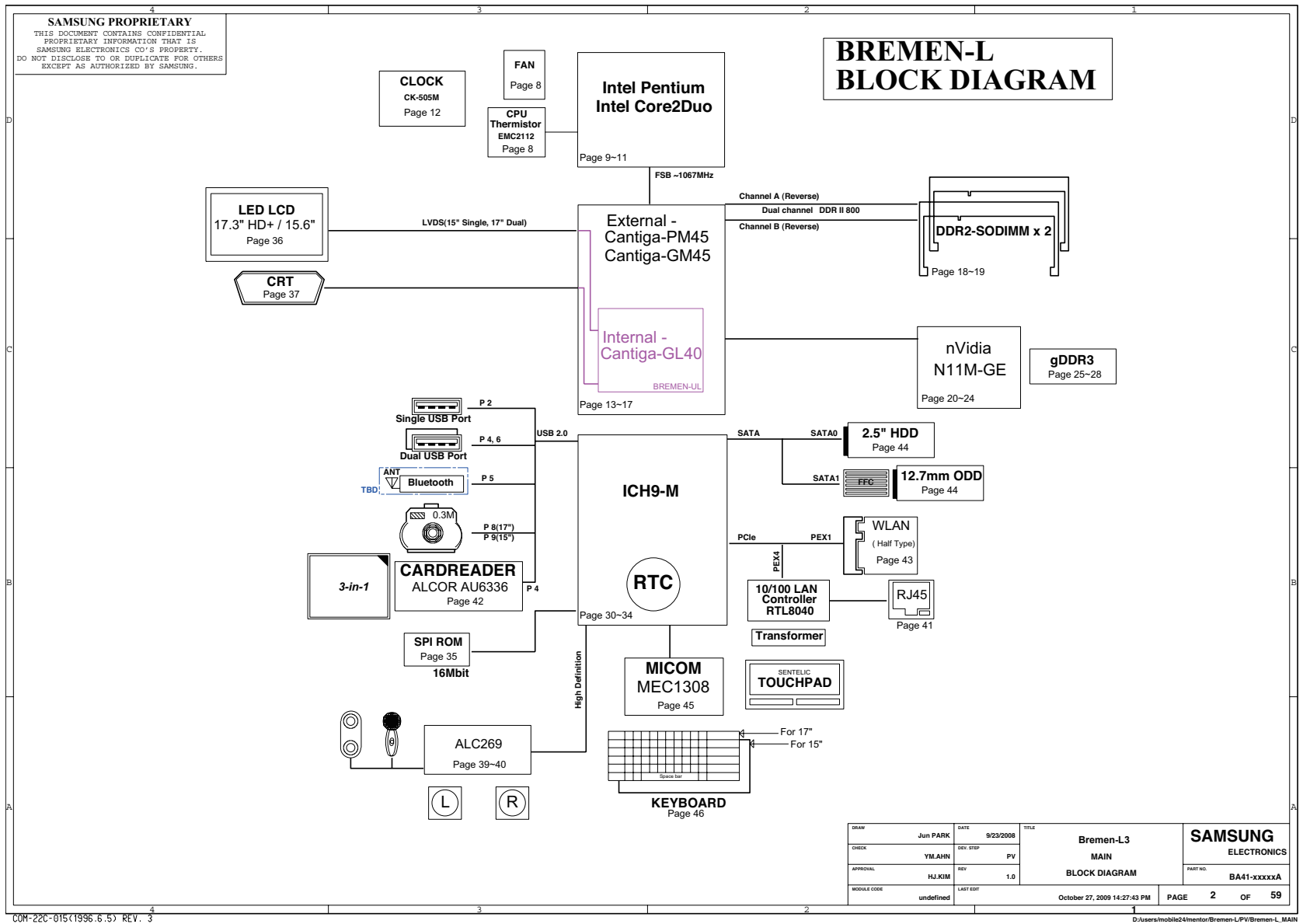


8. Block Diagram and Schematic

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Bremen-L3																							
CPU : Intel Penryn																							
Chip Set : Intel Cantiga & ICH9M																							
Remarks : Montevina Platform																							
Model Name : Bremen-L3 PBA Name : MAIN PCB Code : GCE : BA41-01226A NY : BA41-01227A HST : BA41-01228A Dev. Step : PV Revision : 1.0 T.R. Date : 2009.10.27																							
<table border="1"><thead><tr><th>Design</th><th>CHECK</th><th>APPROVAL</th></tr></thead><tbody><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr></tbody></table>				Design	CHECK	APPROVAL																	
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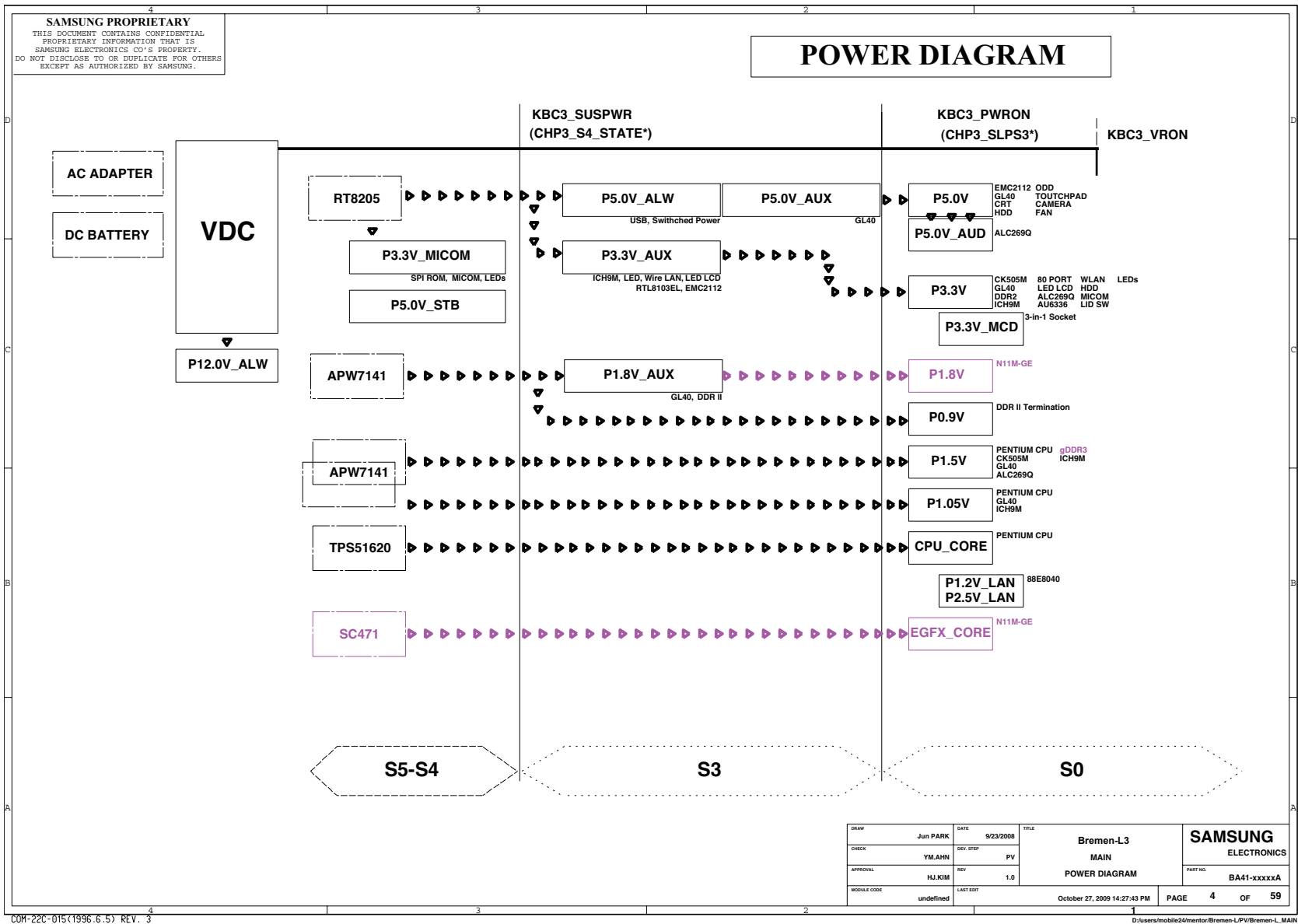
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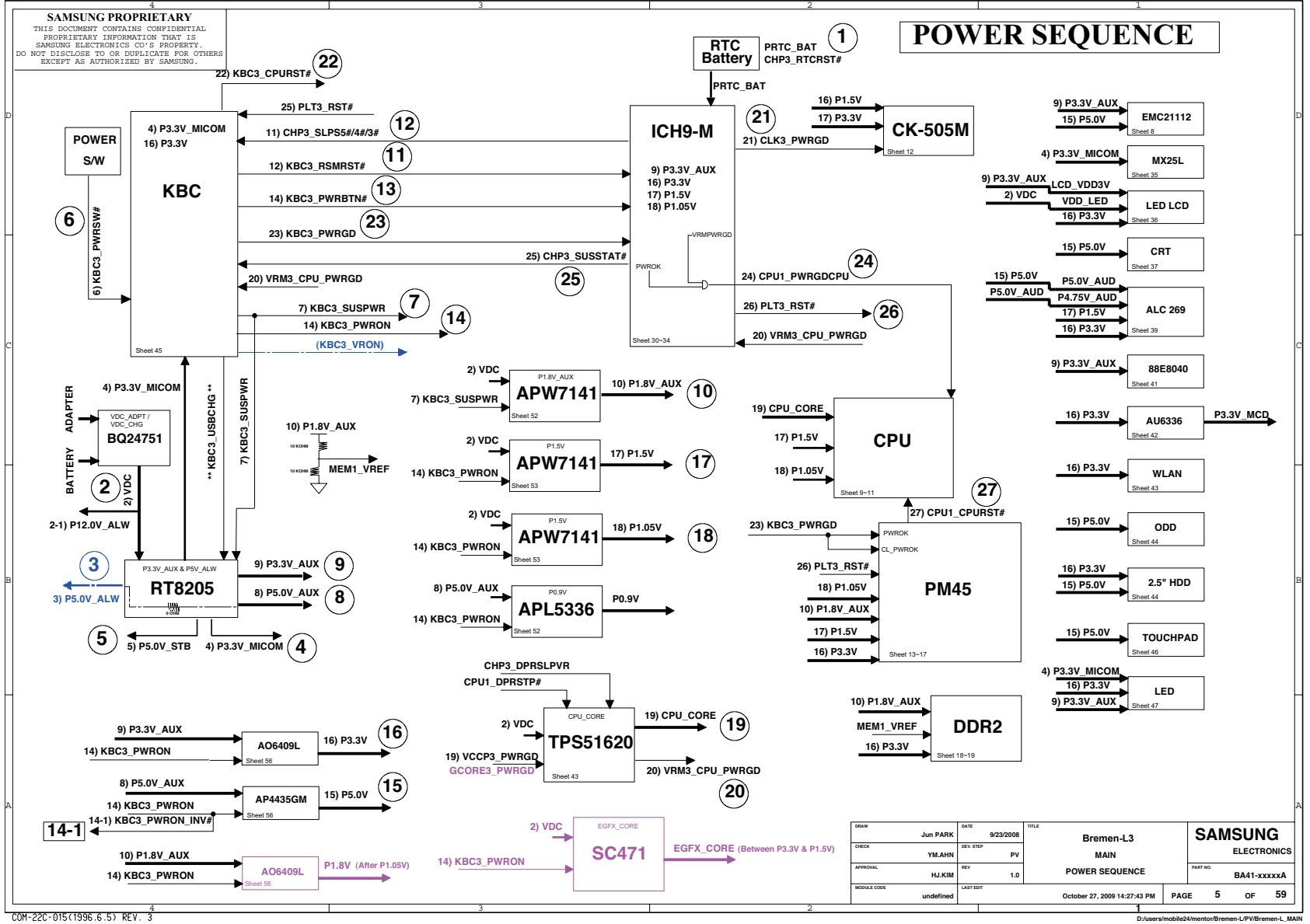
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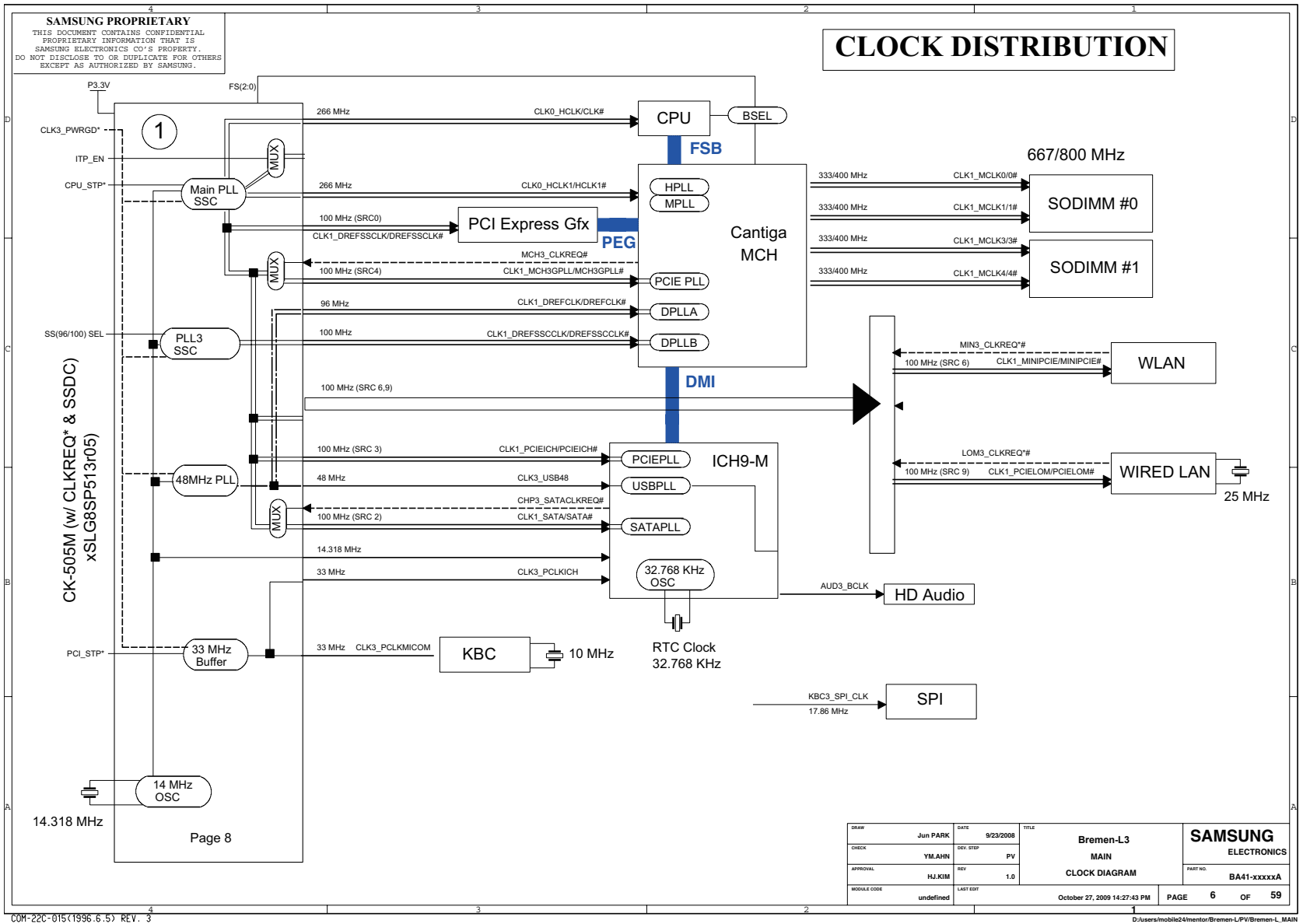
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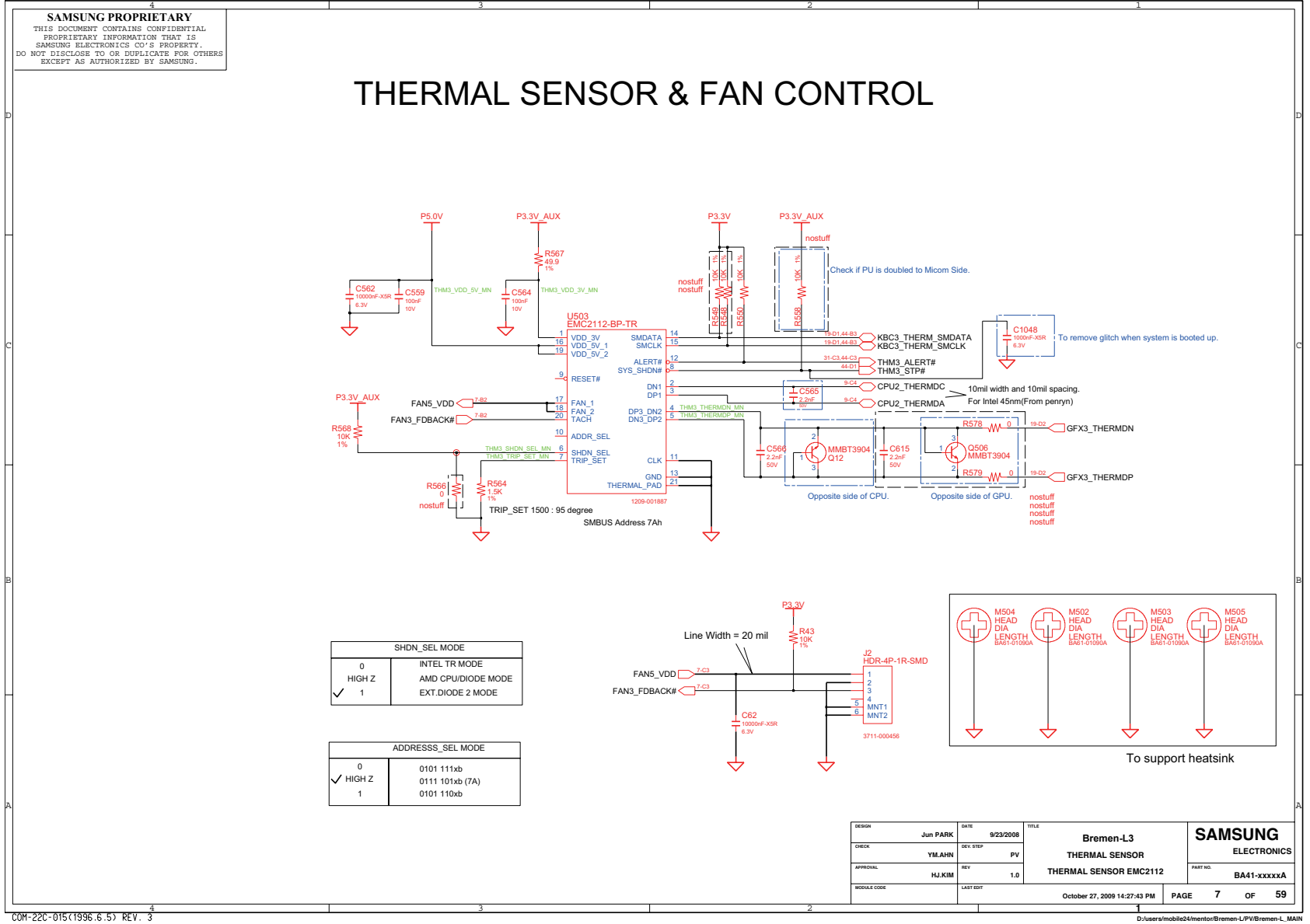
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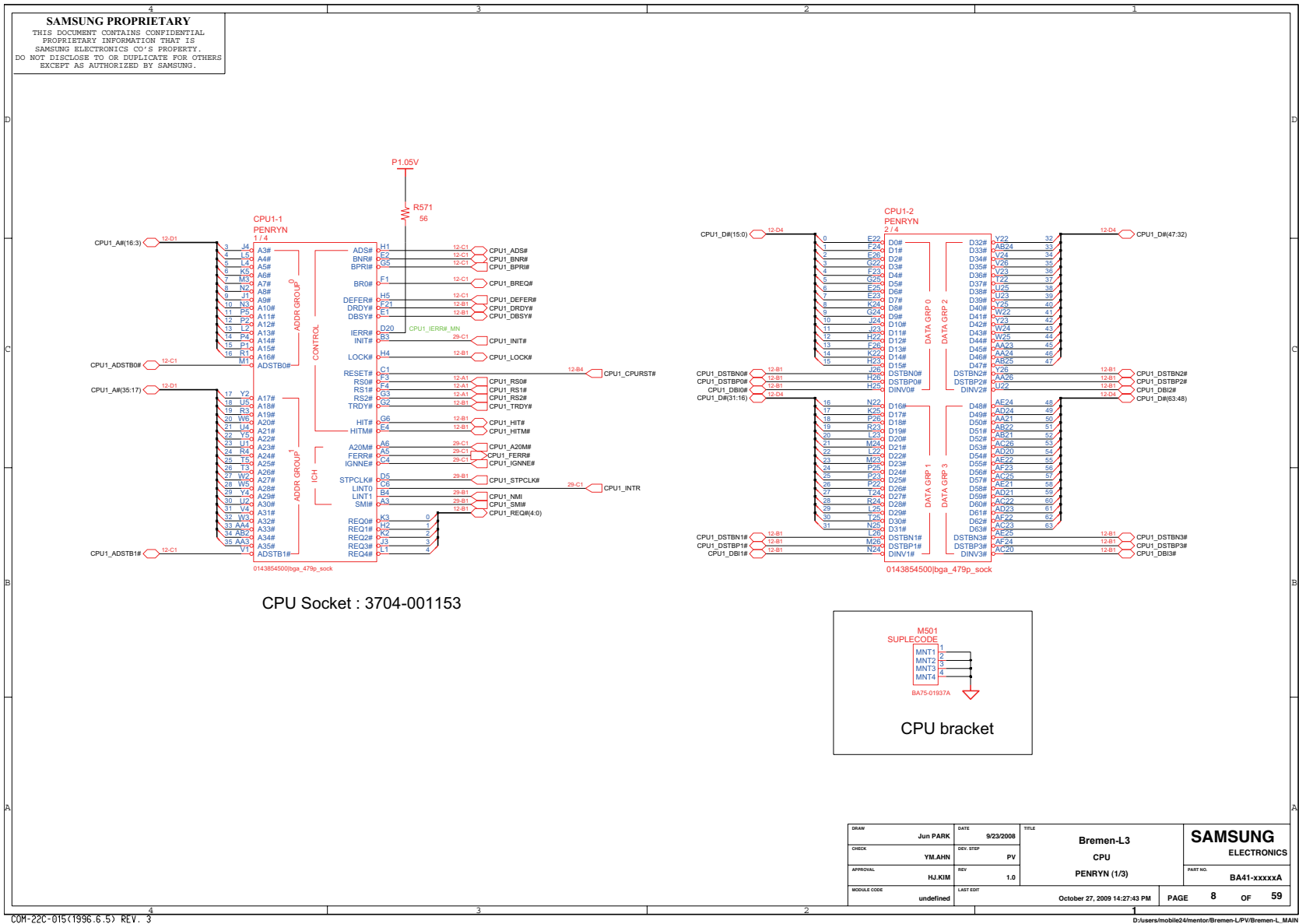
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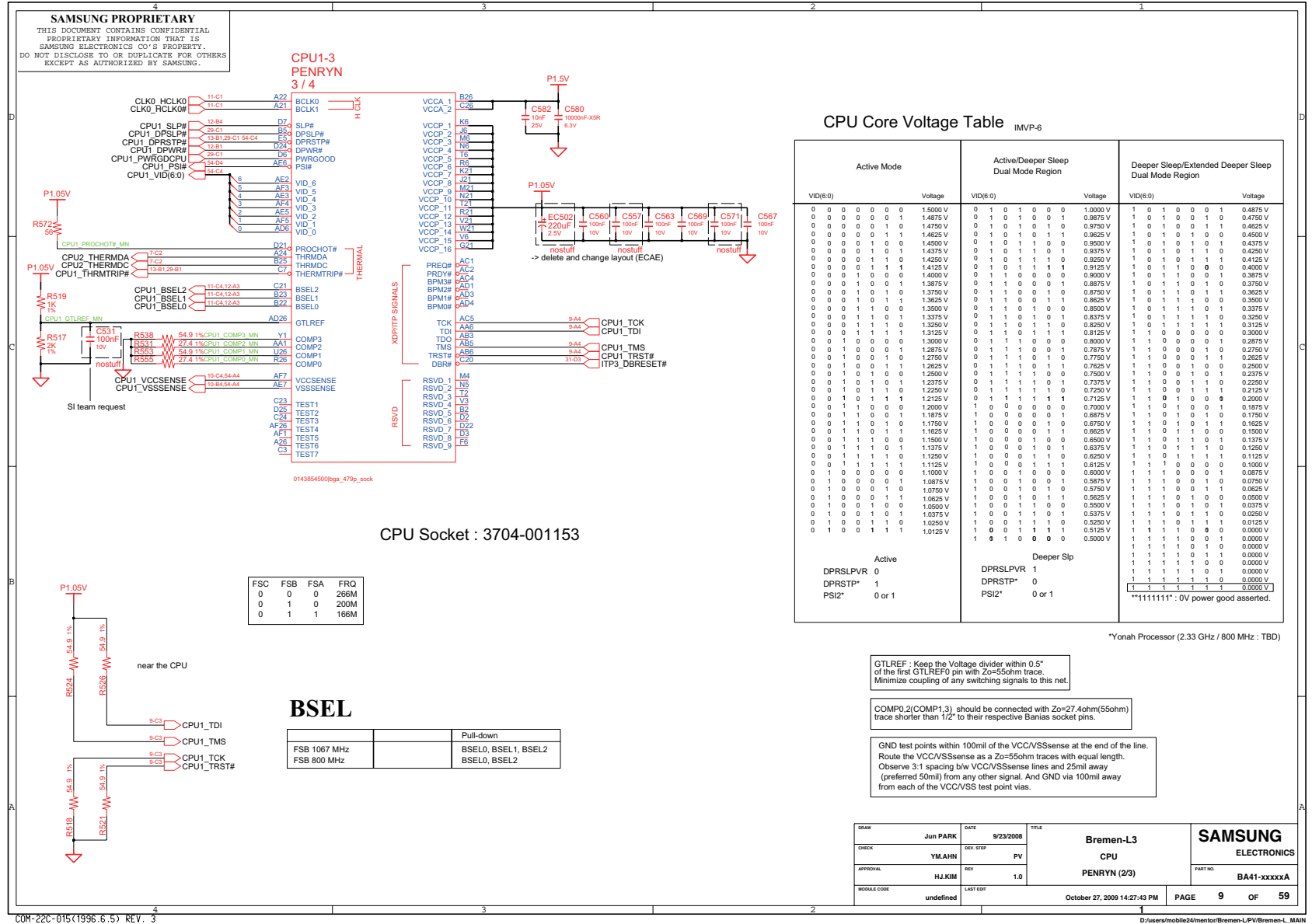
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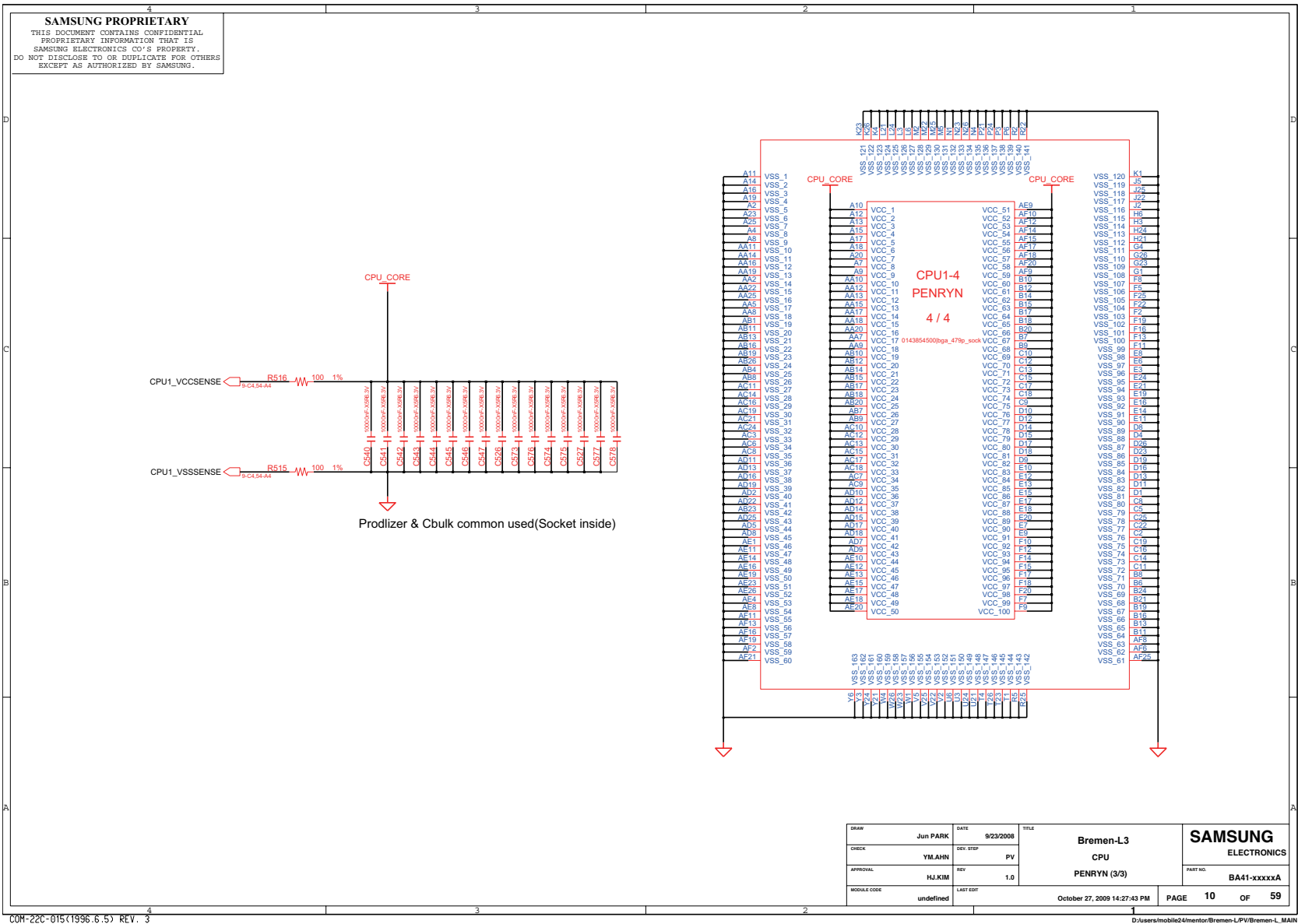
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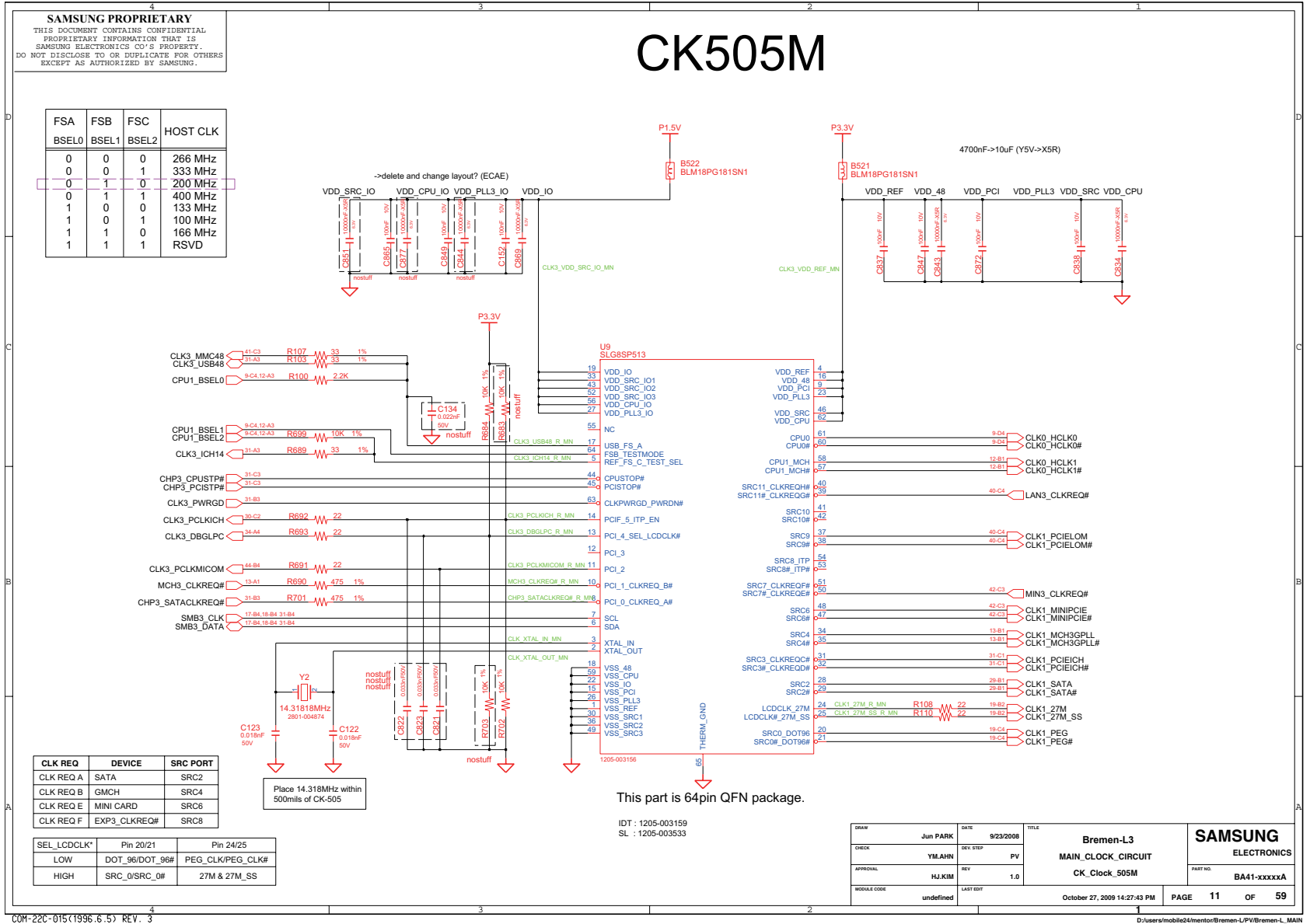
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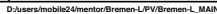
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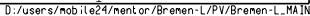


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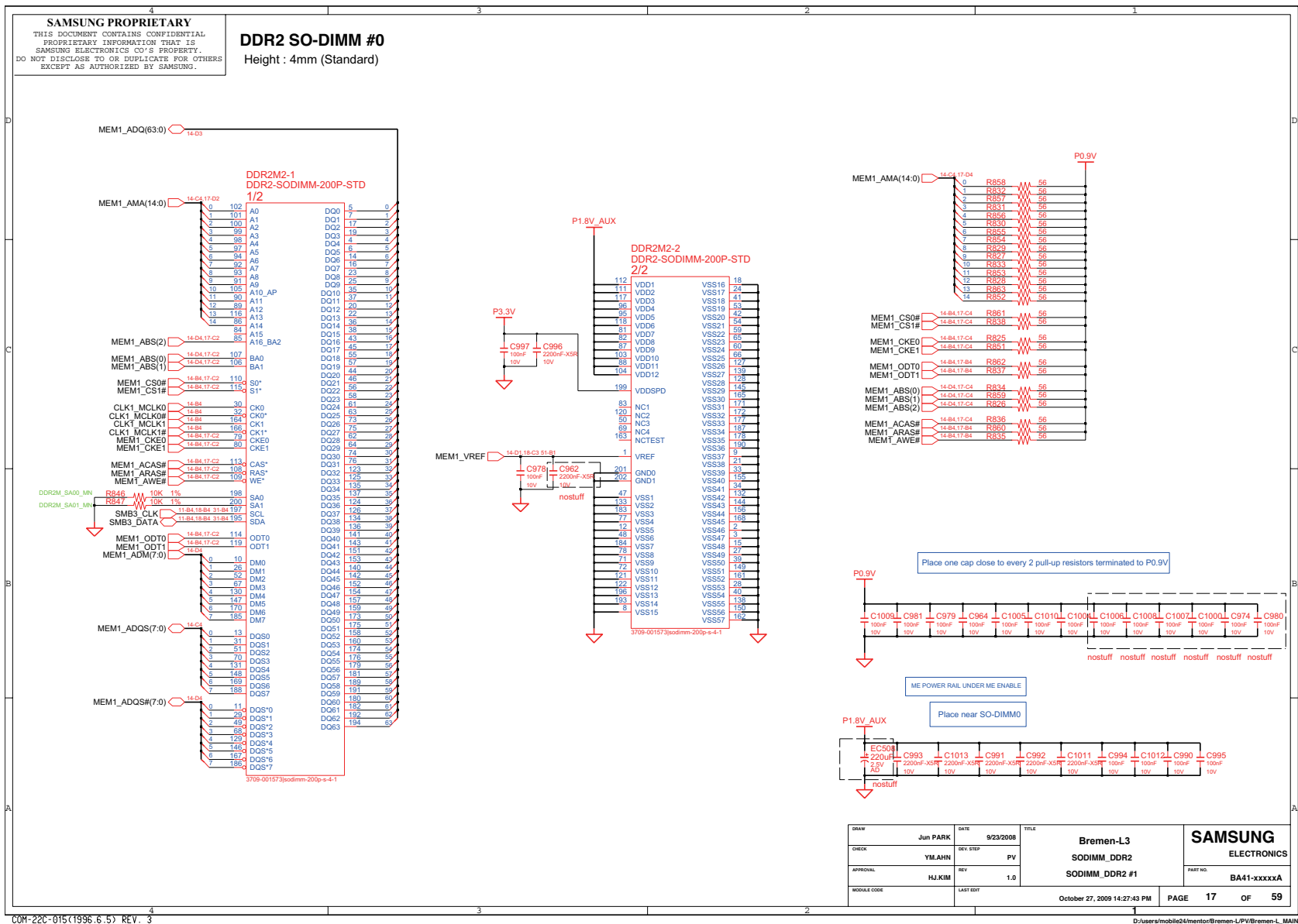




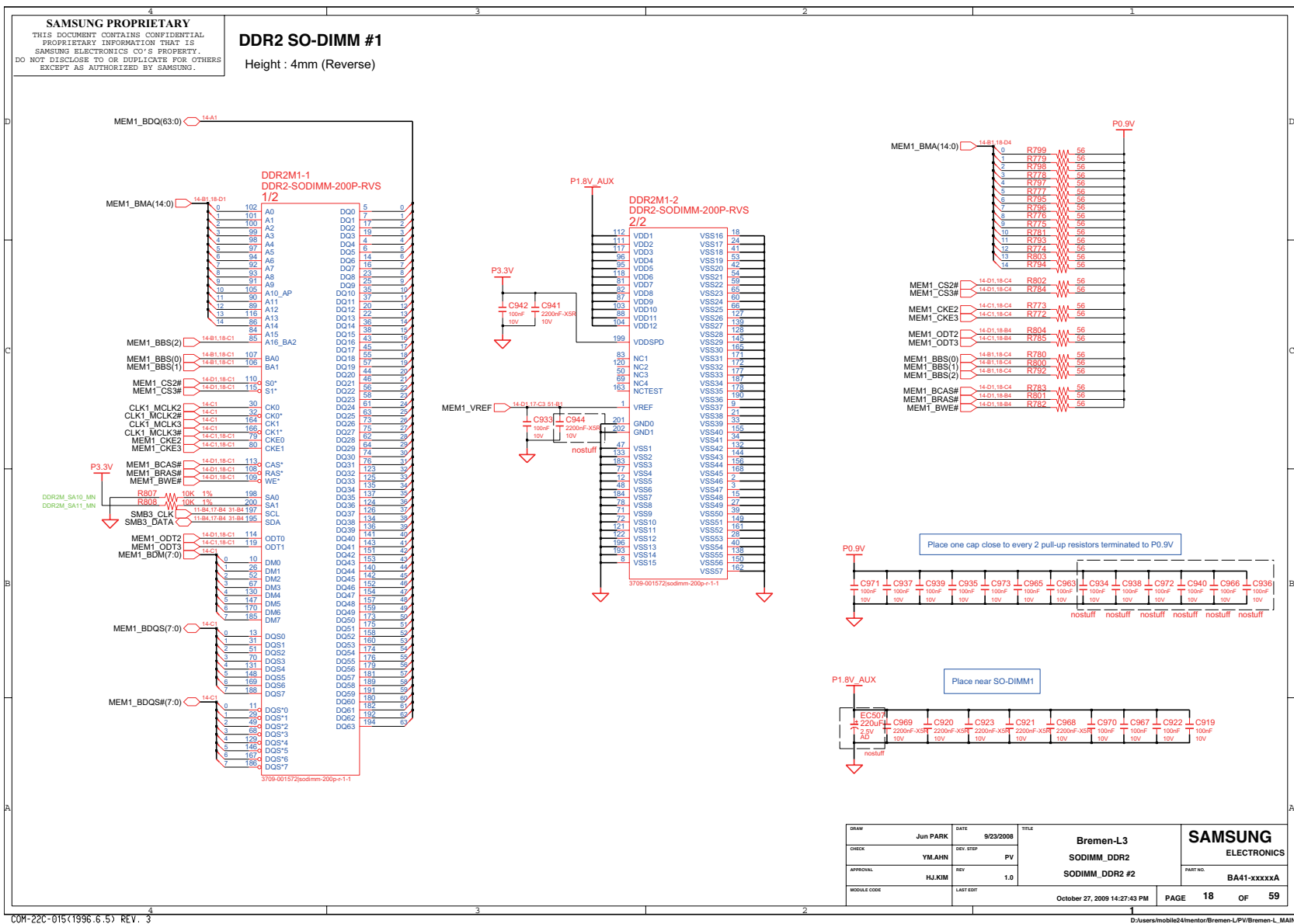
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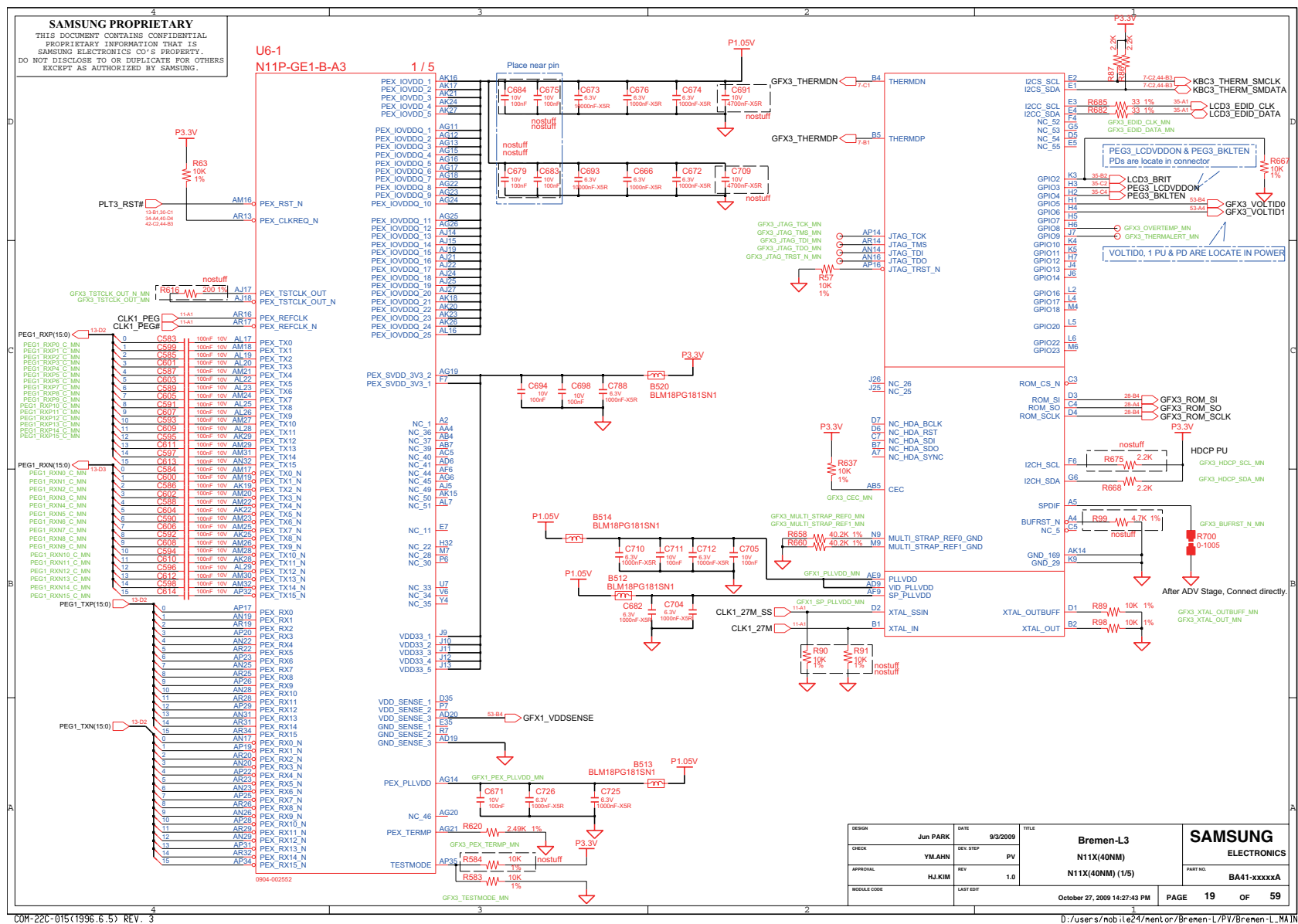
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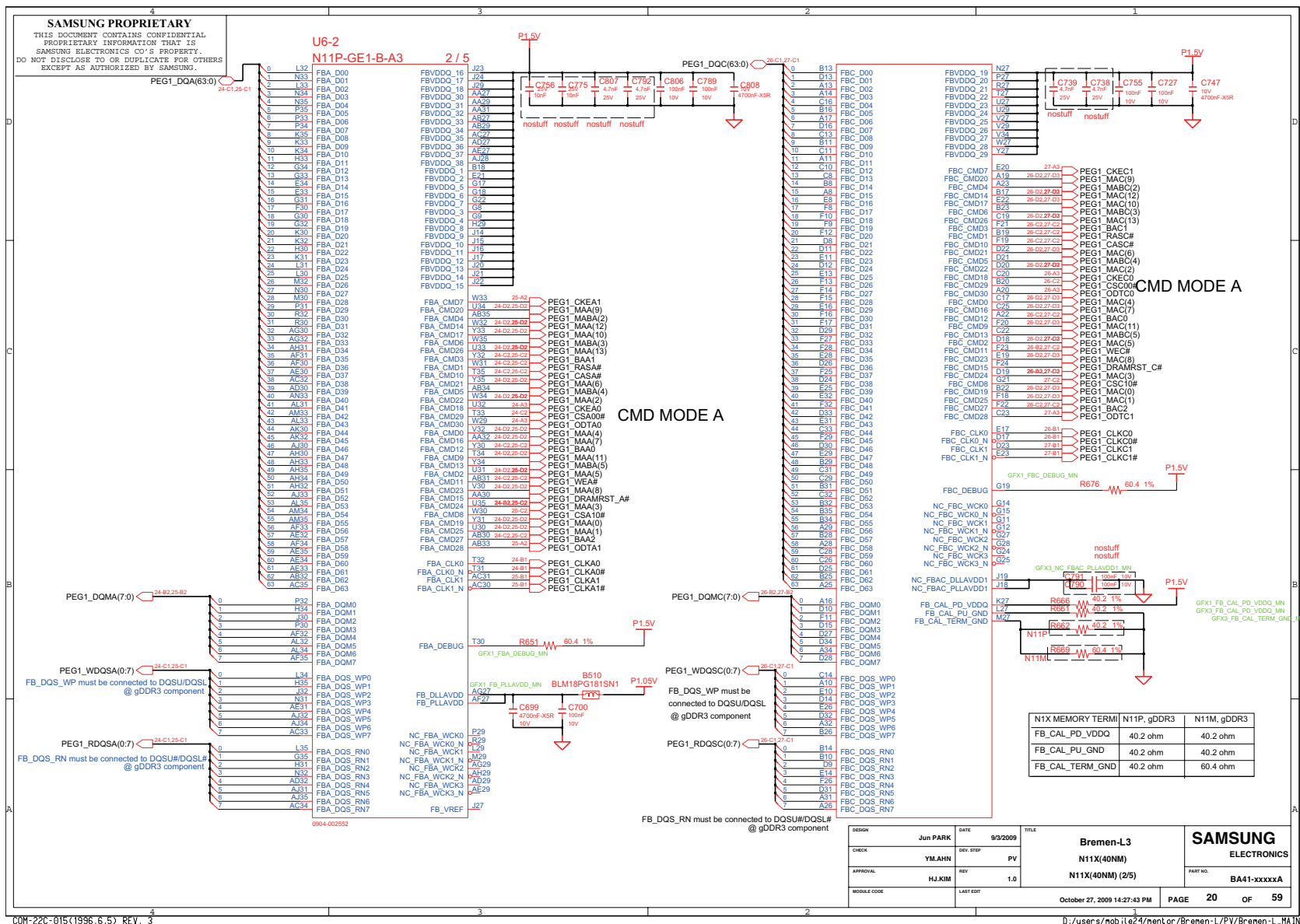


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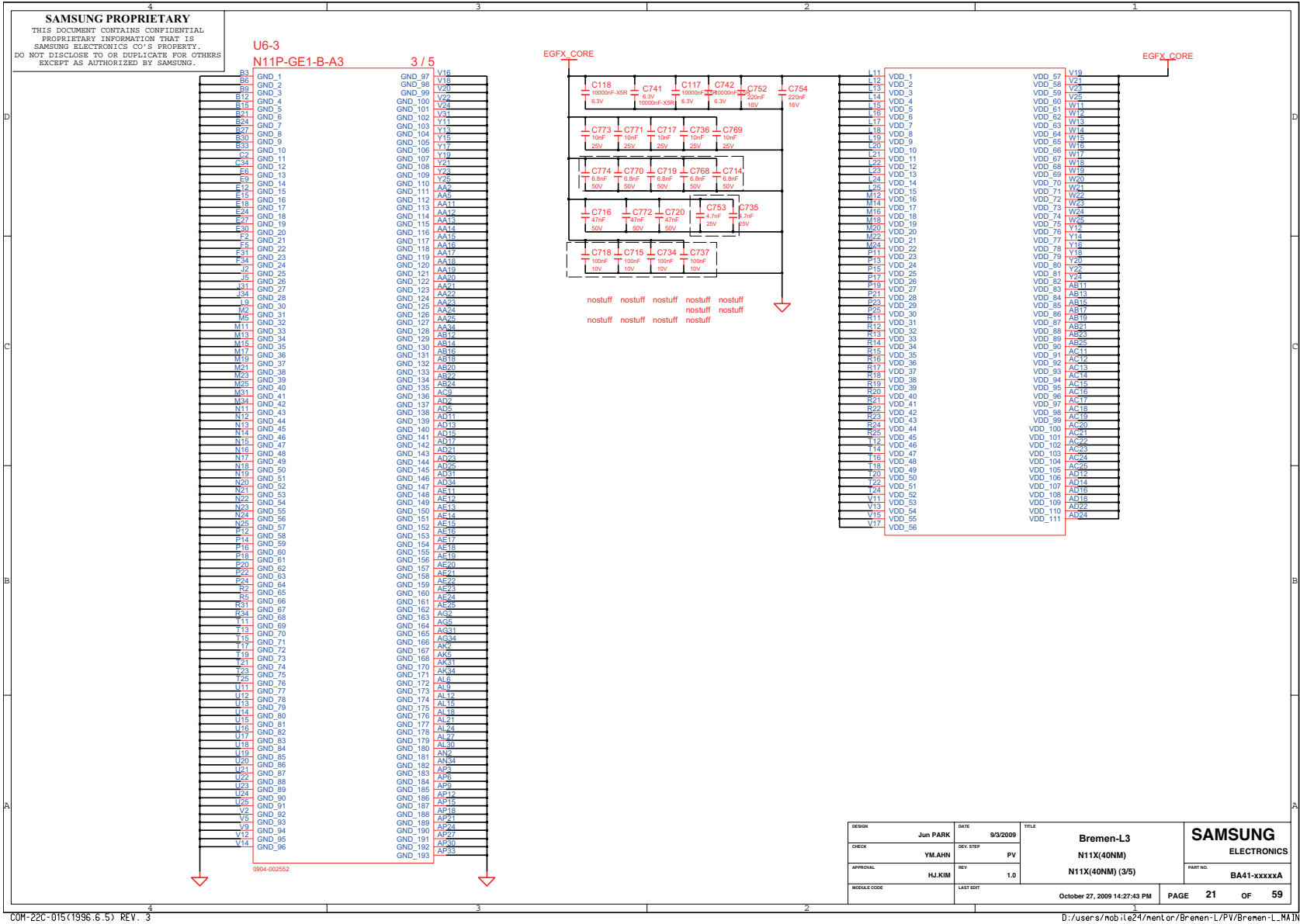


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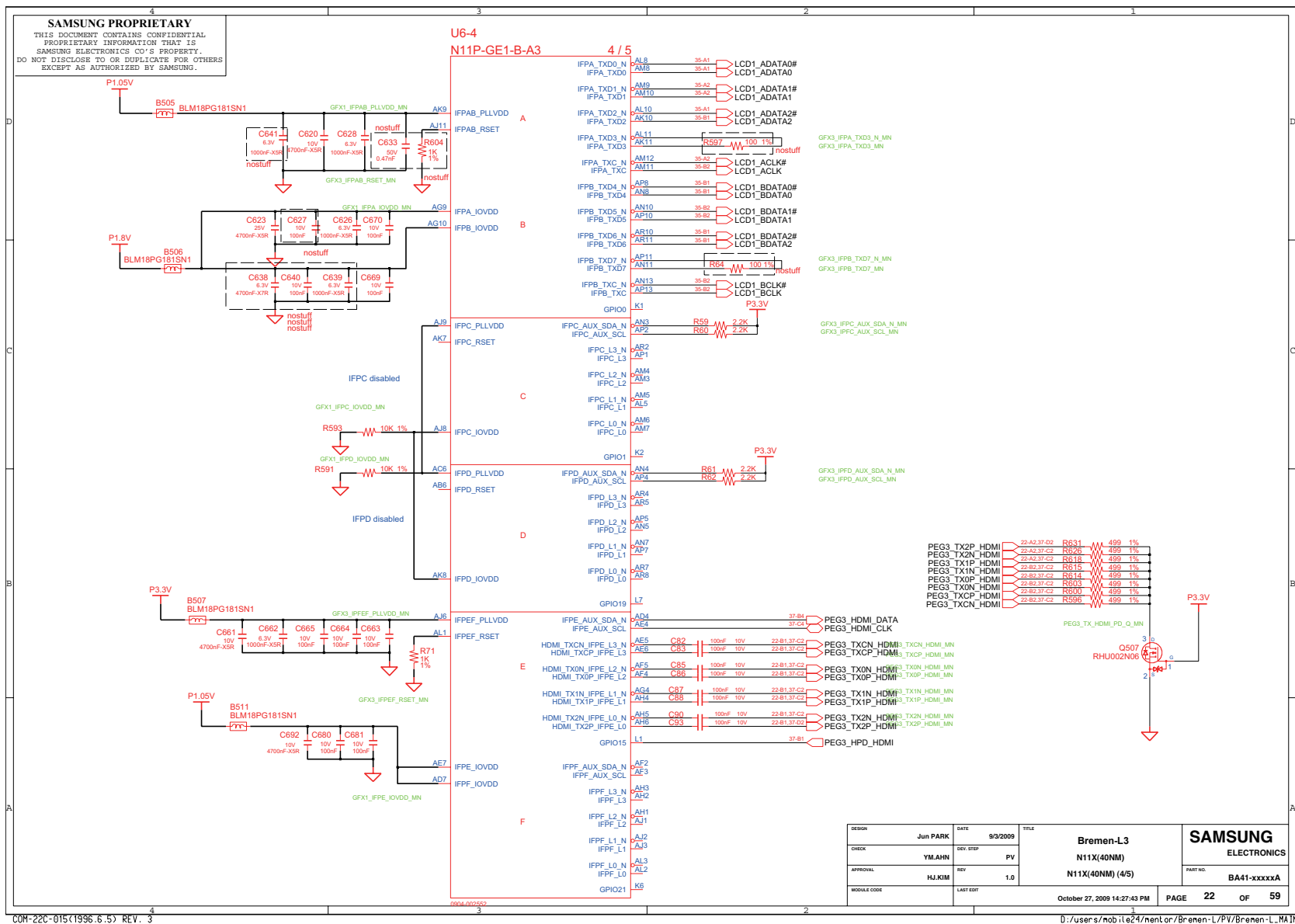
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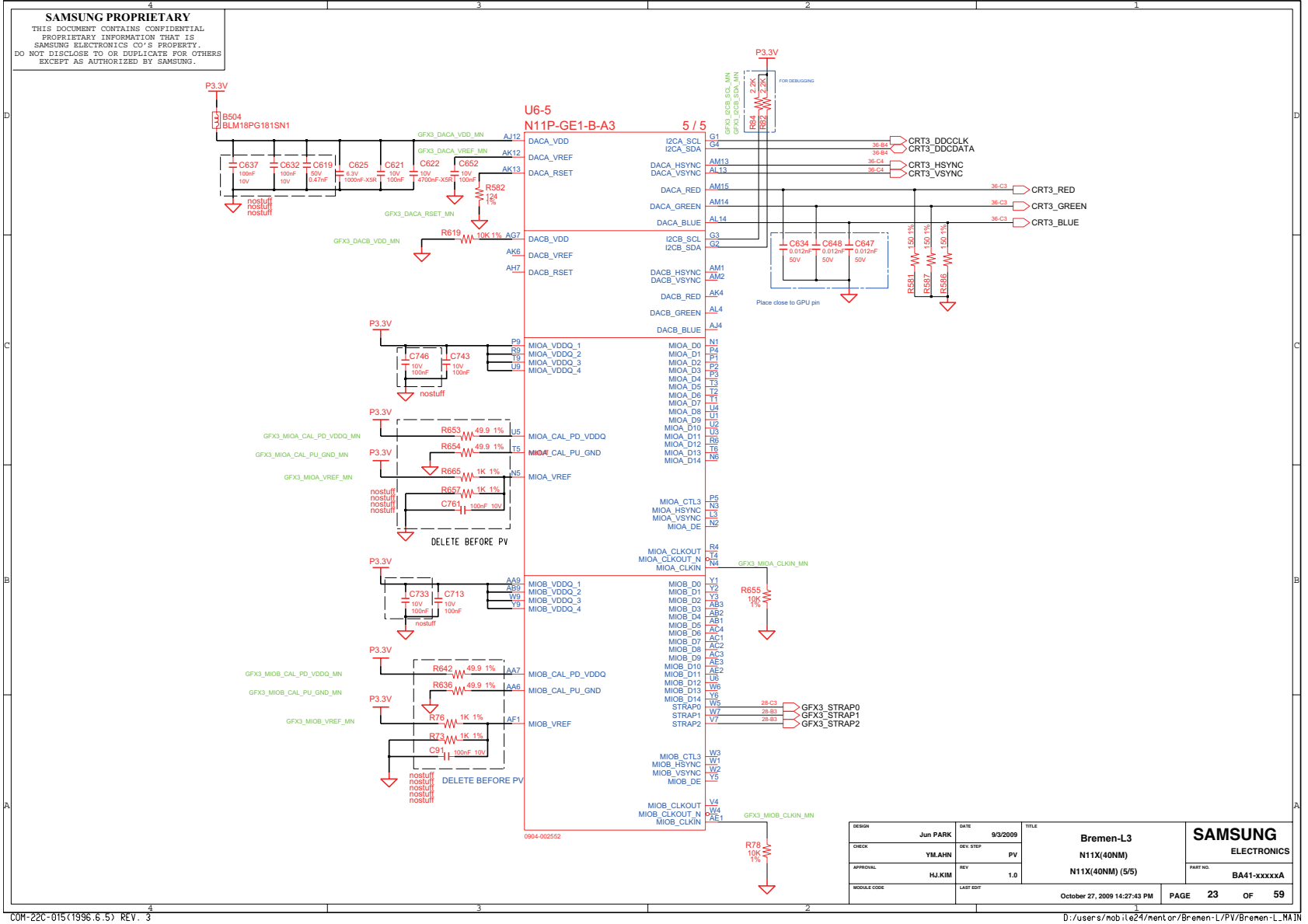
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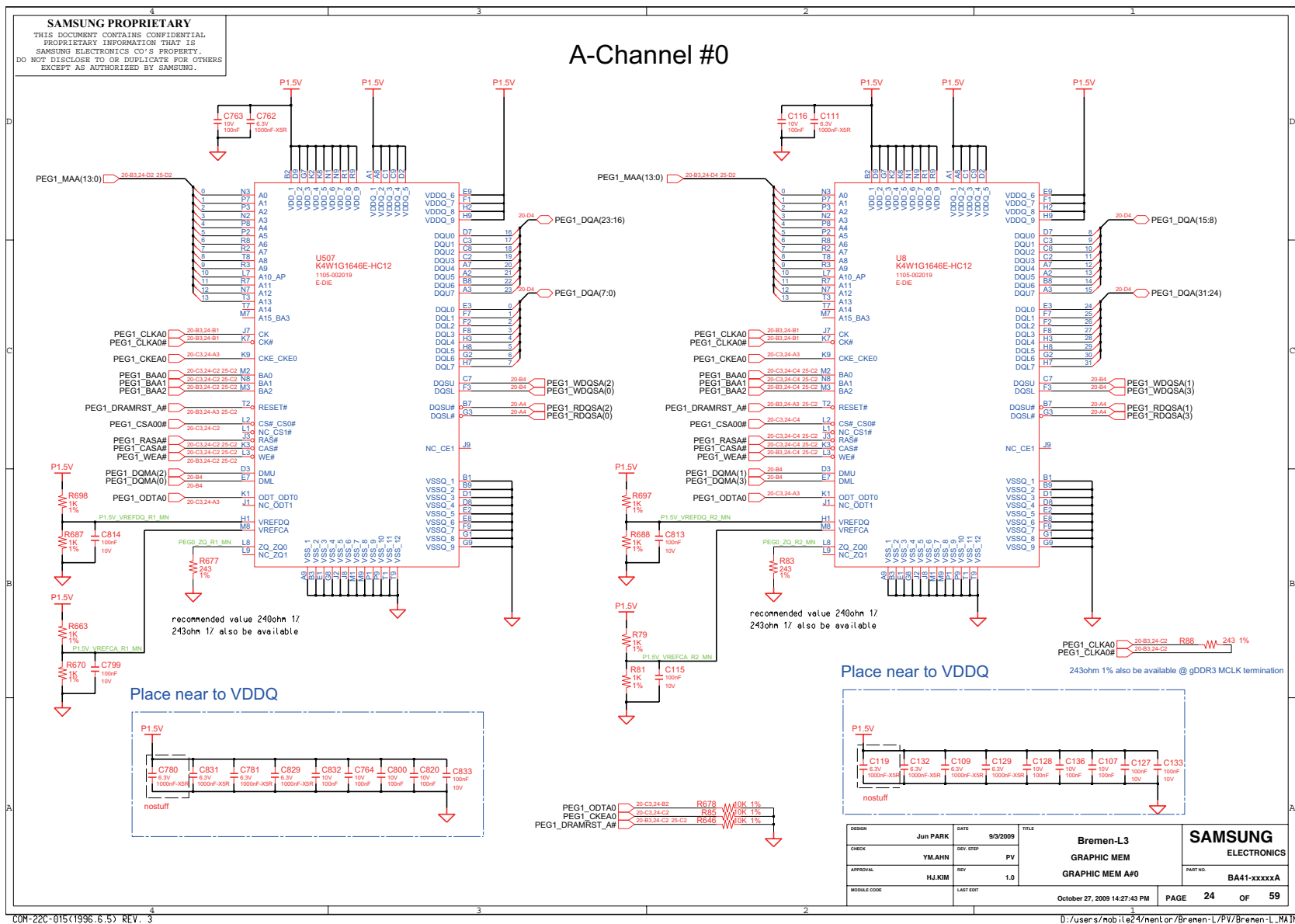
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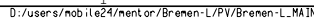
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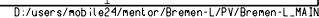
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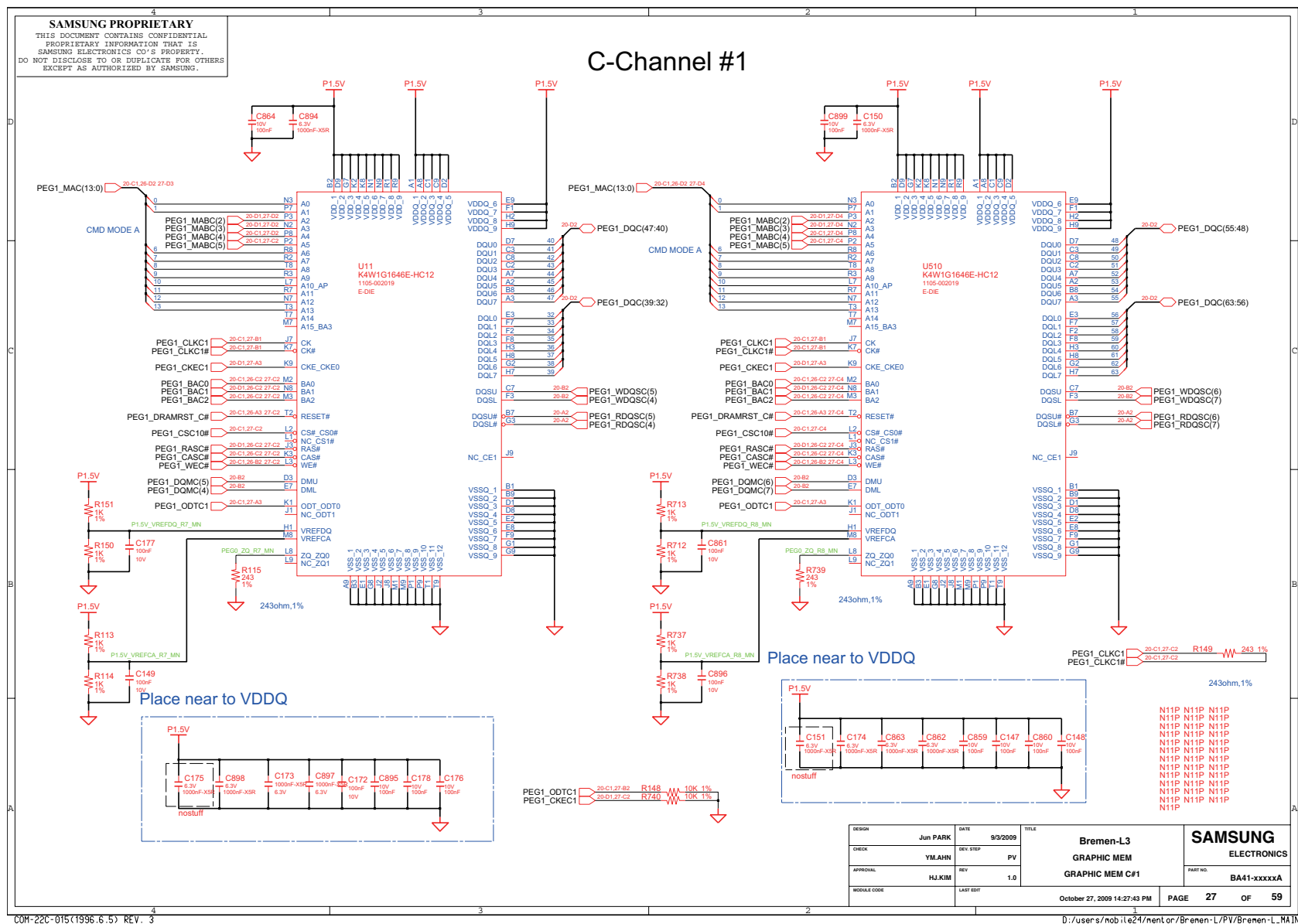
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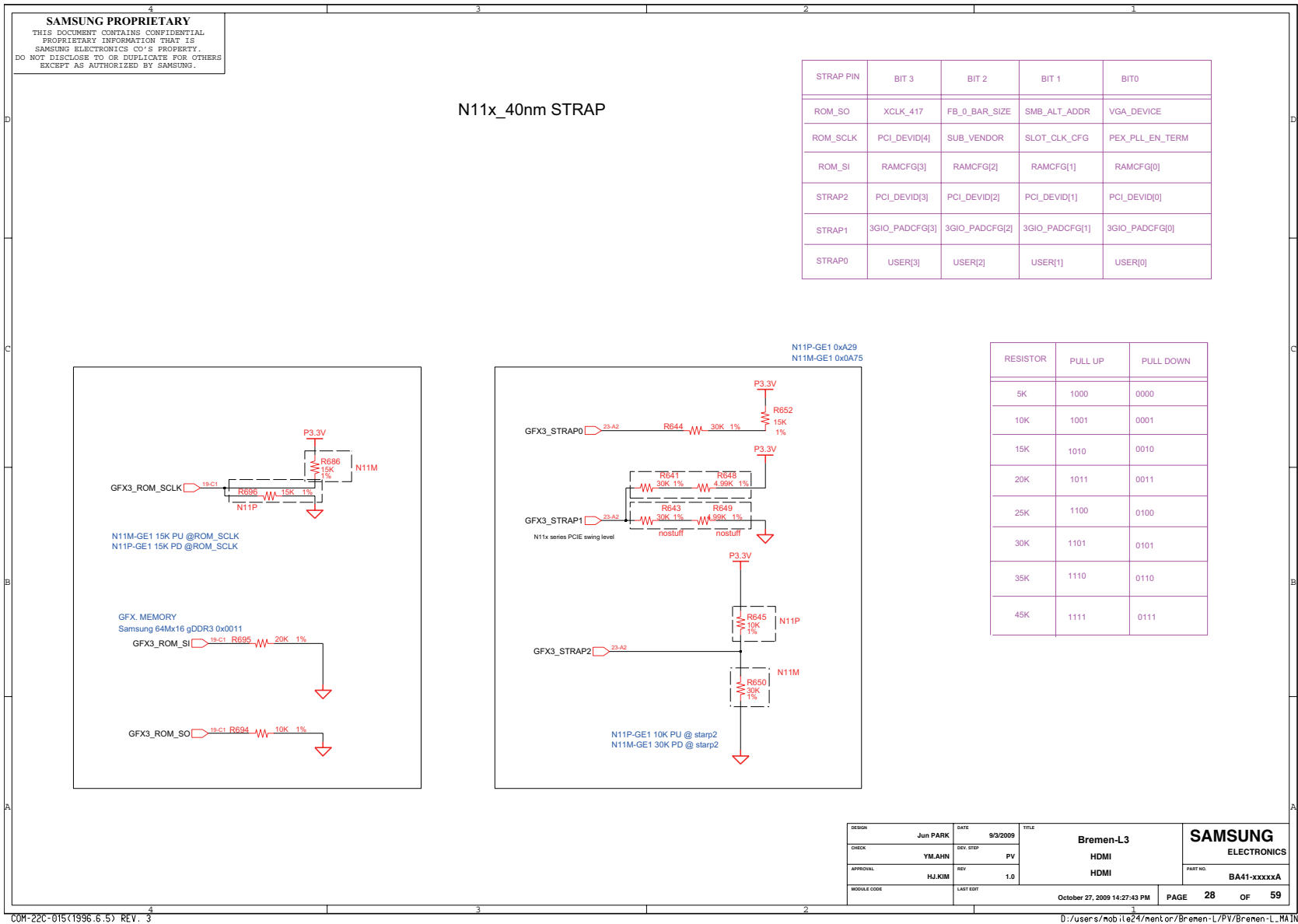
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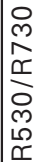


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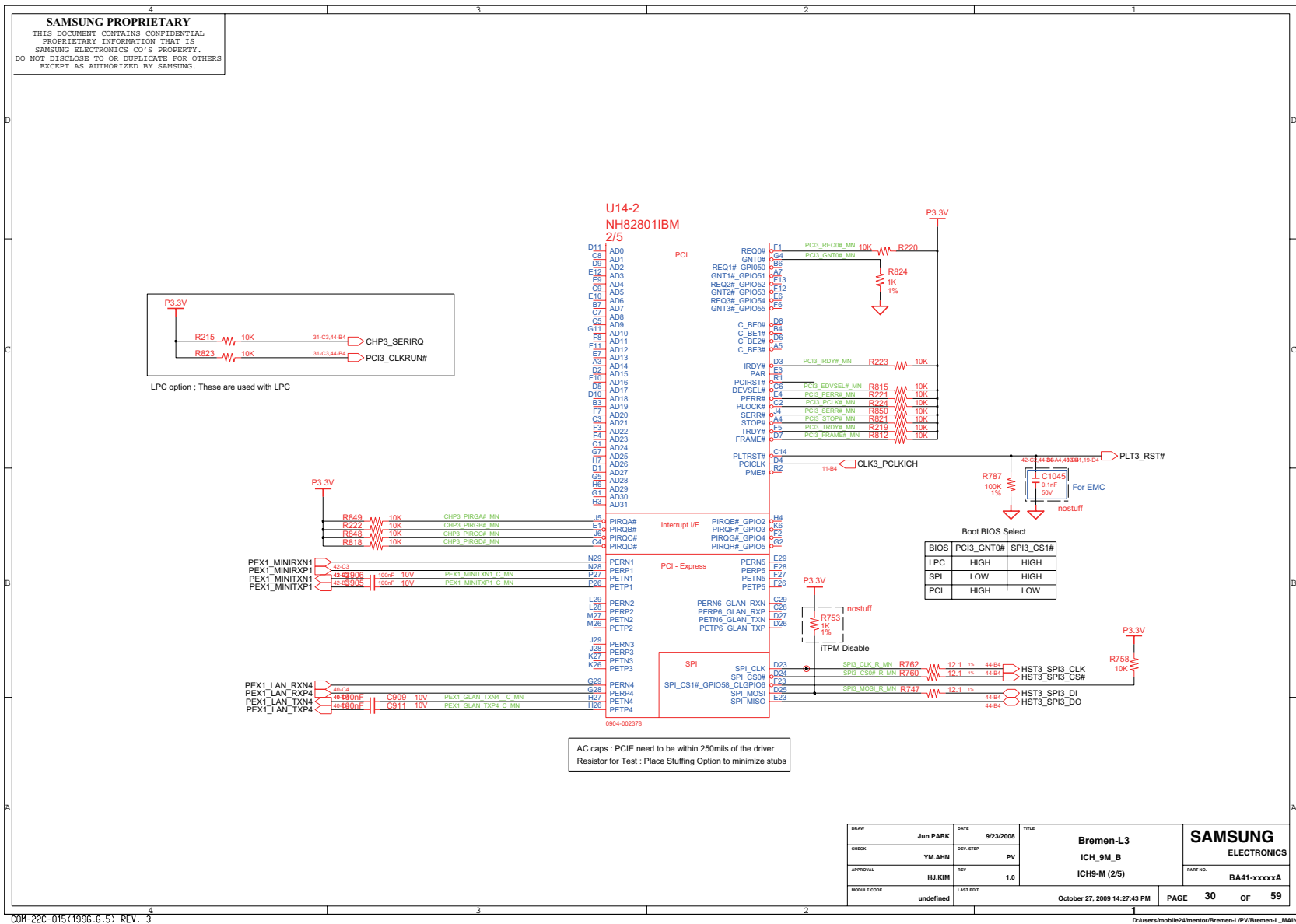


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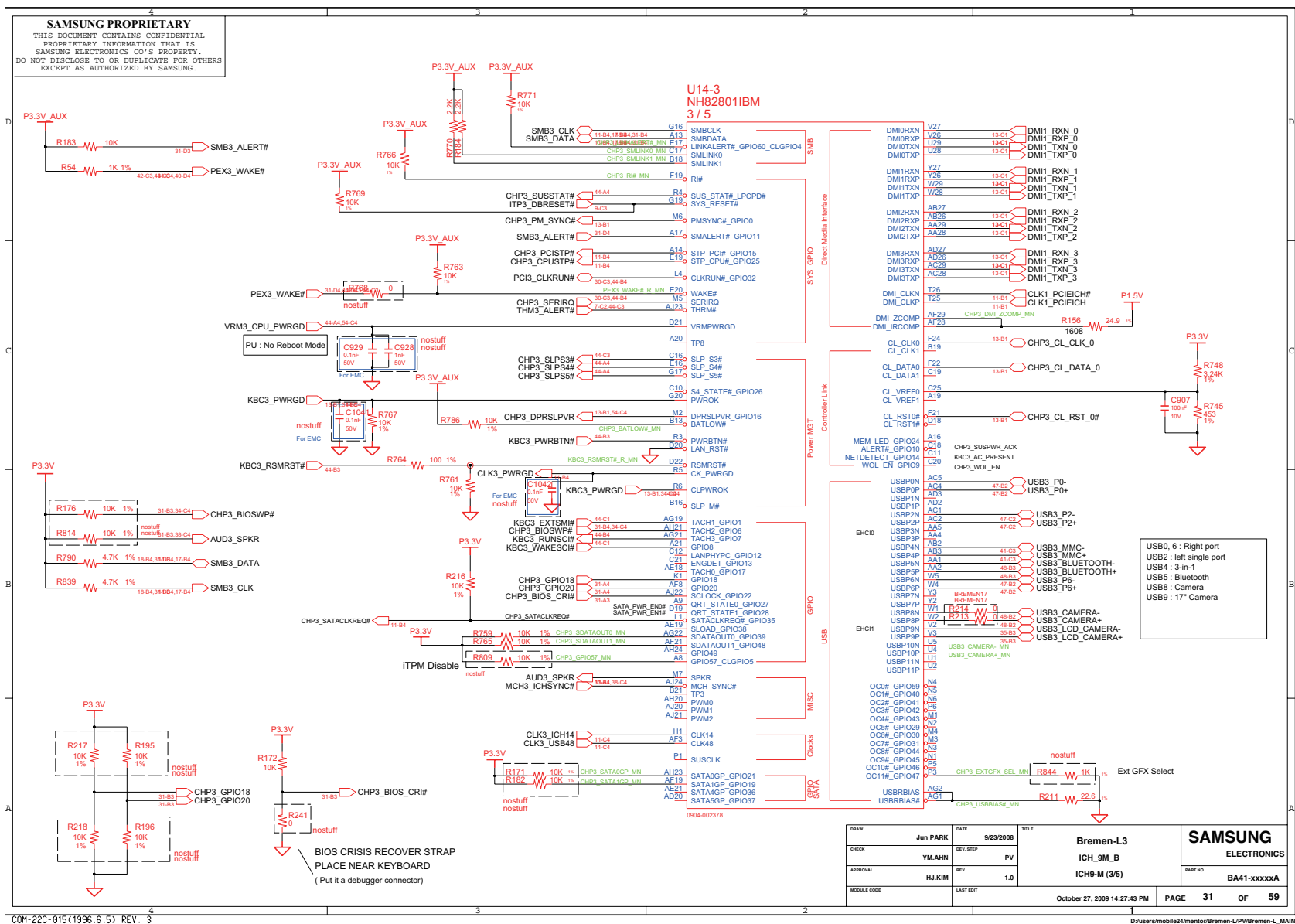




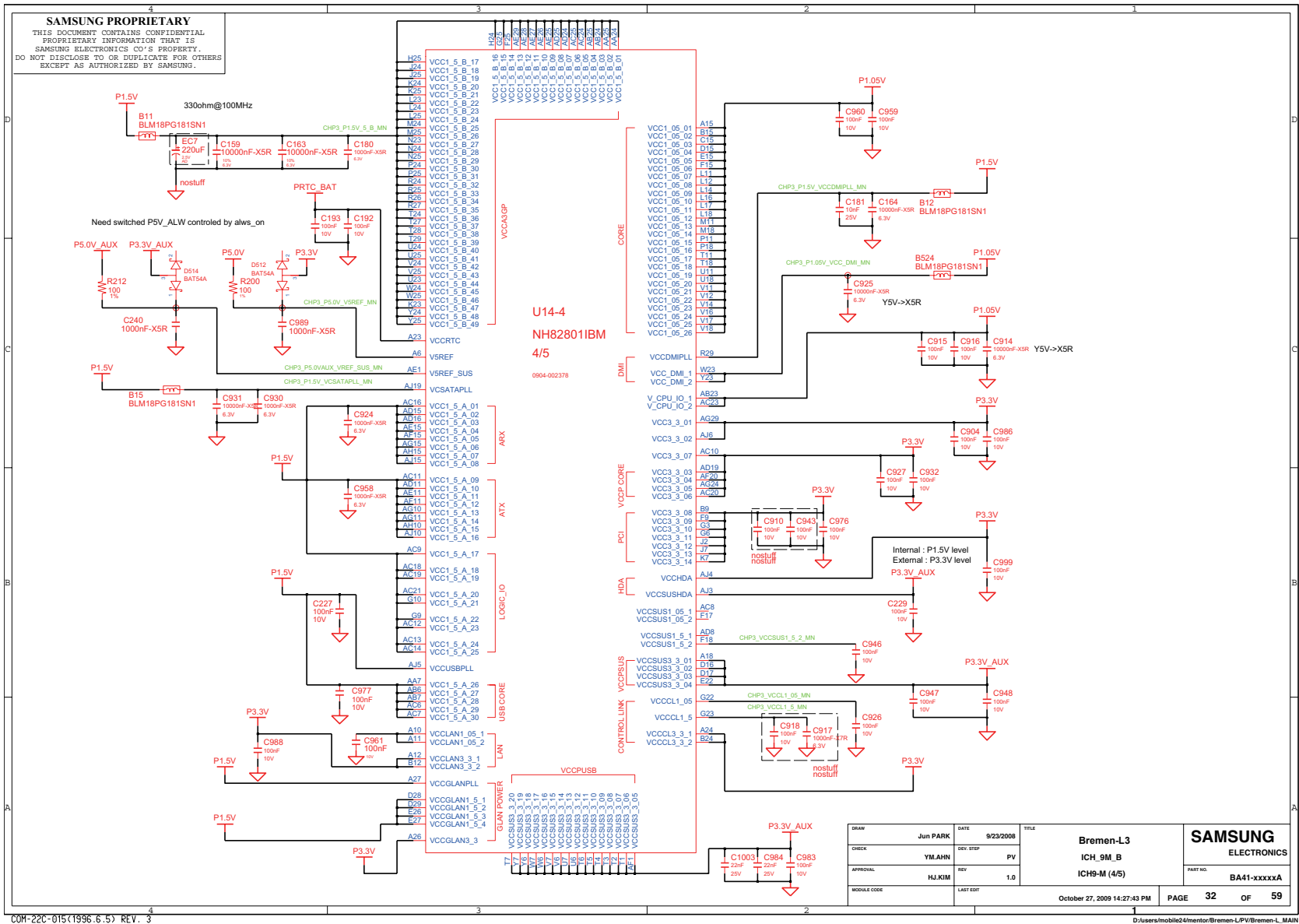
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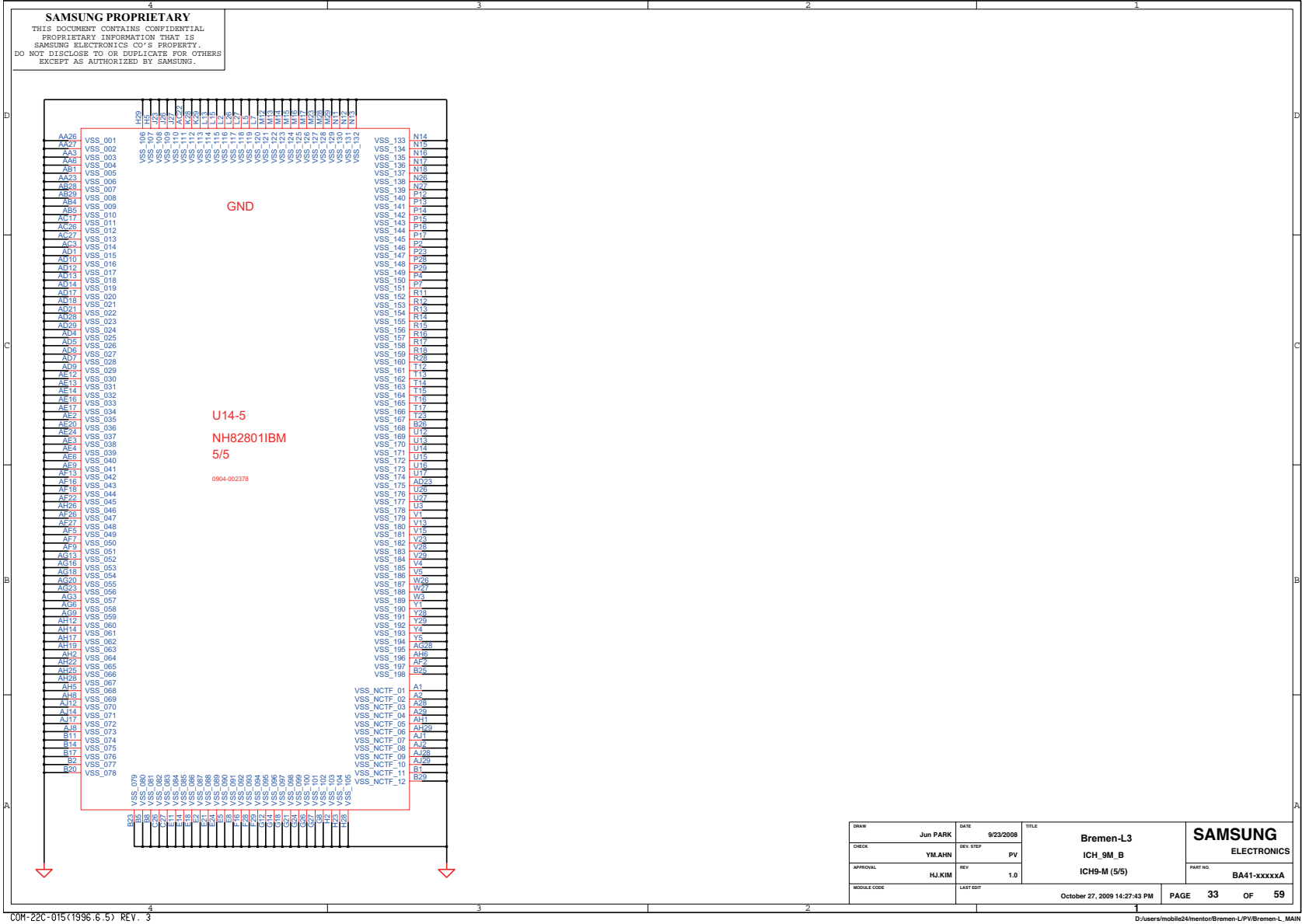
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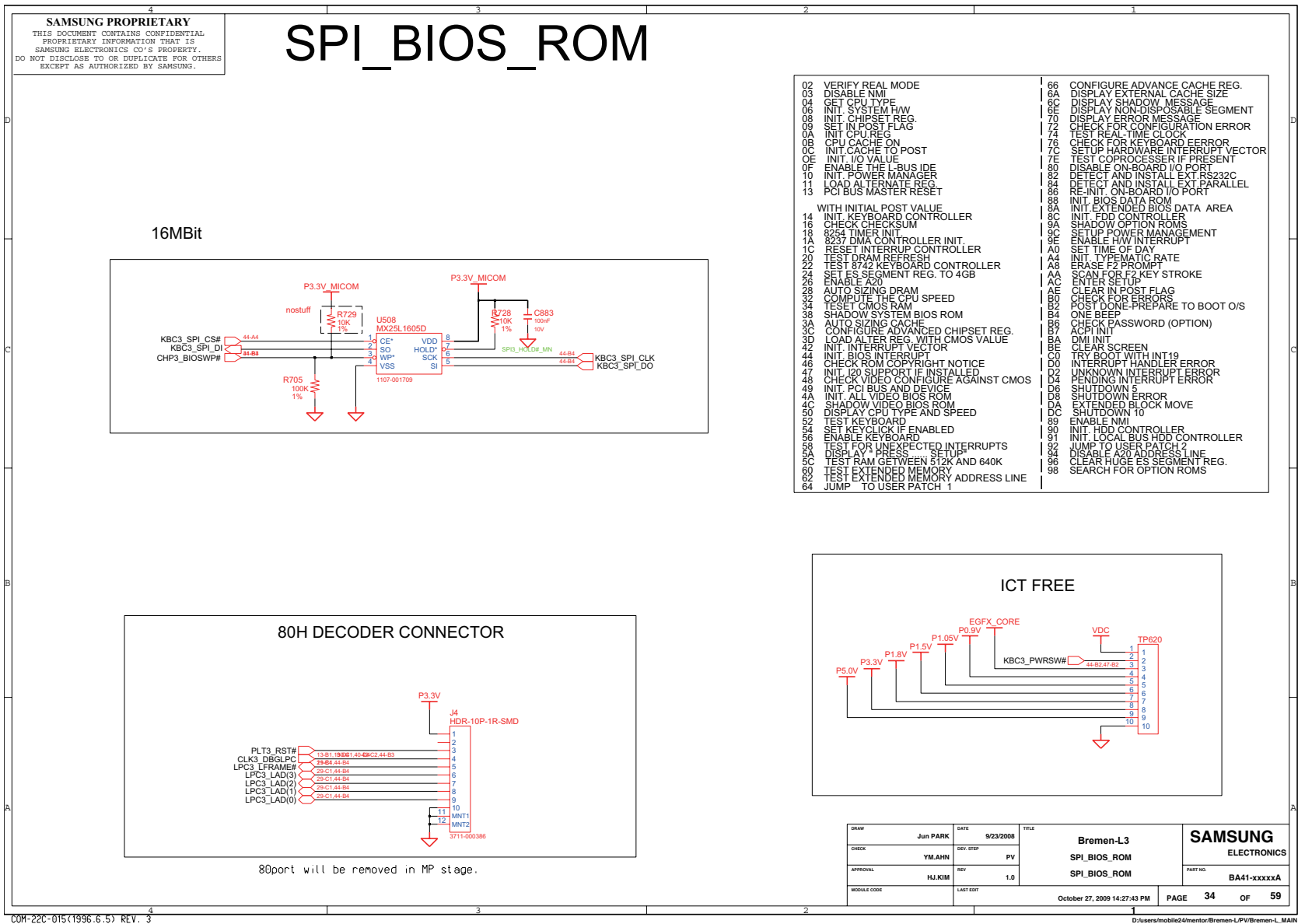
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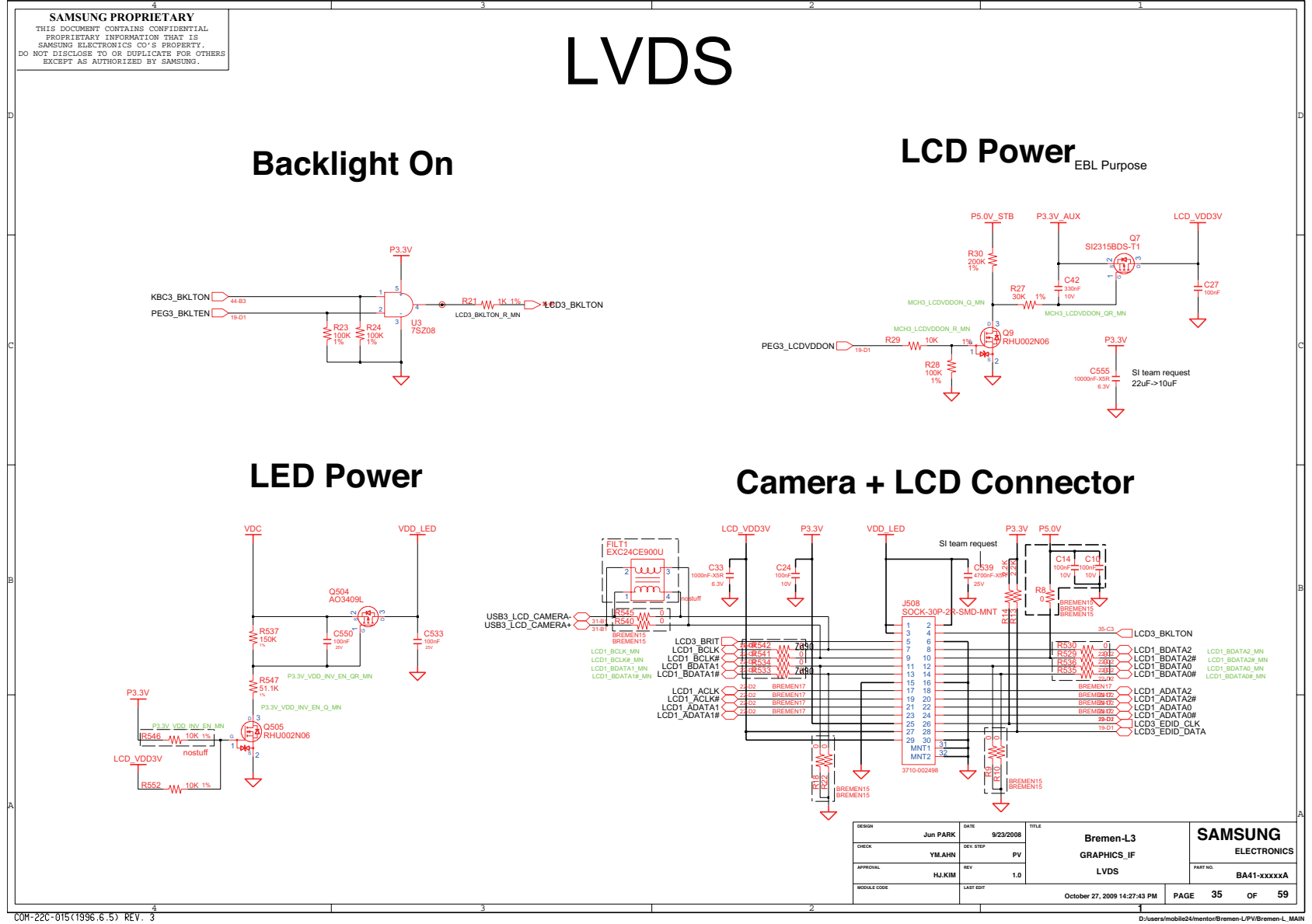
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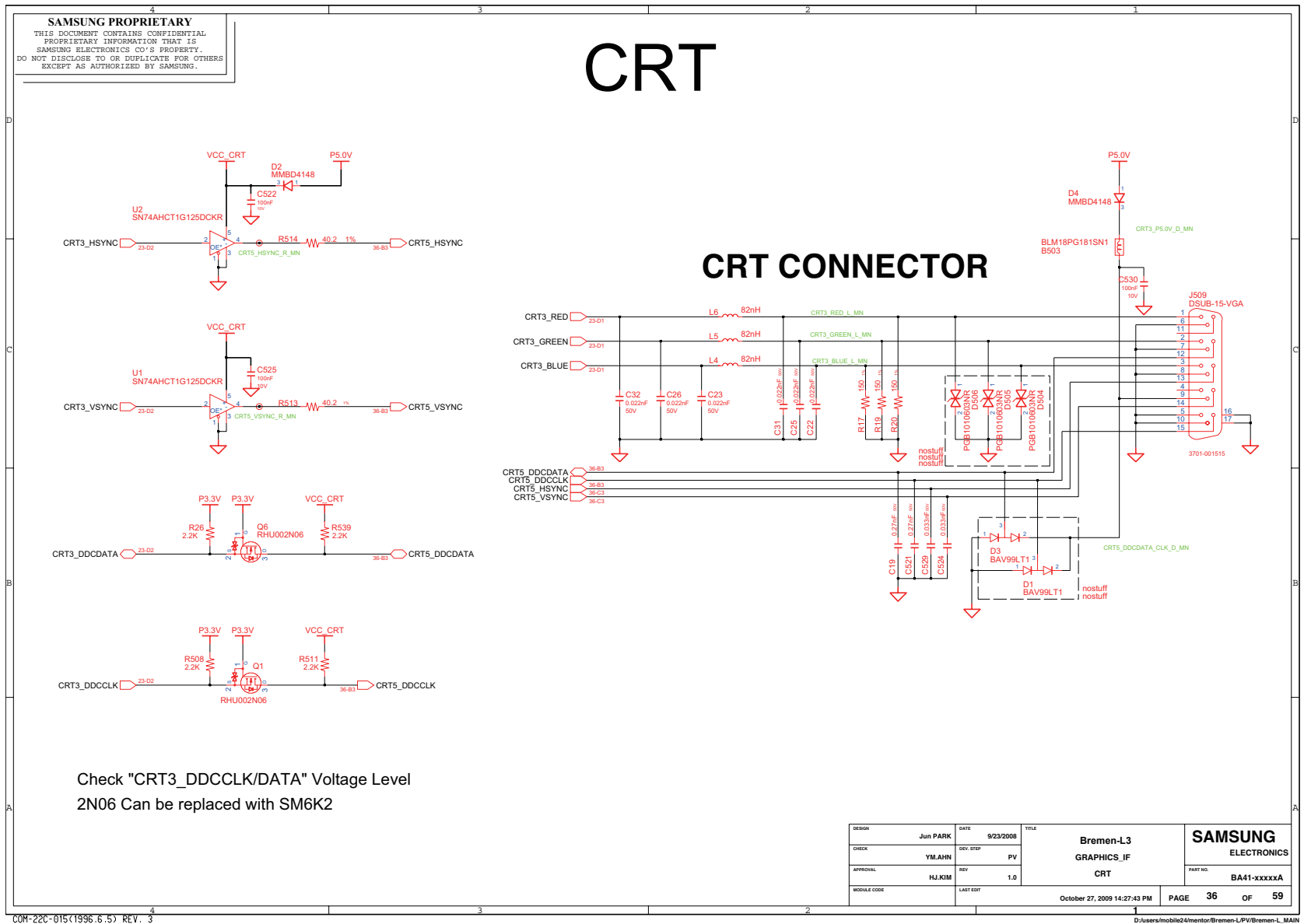
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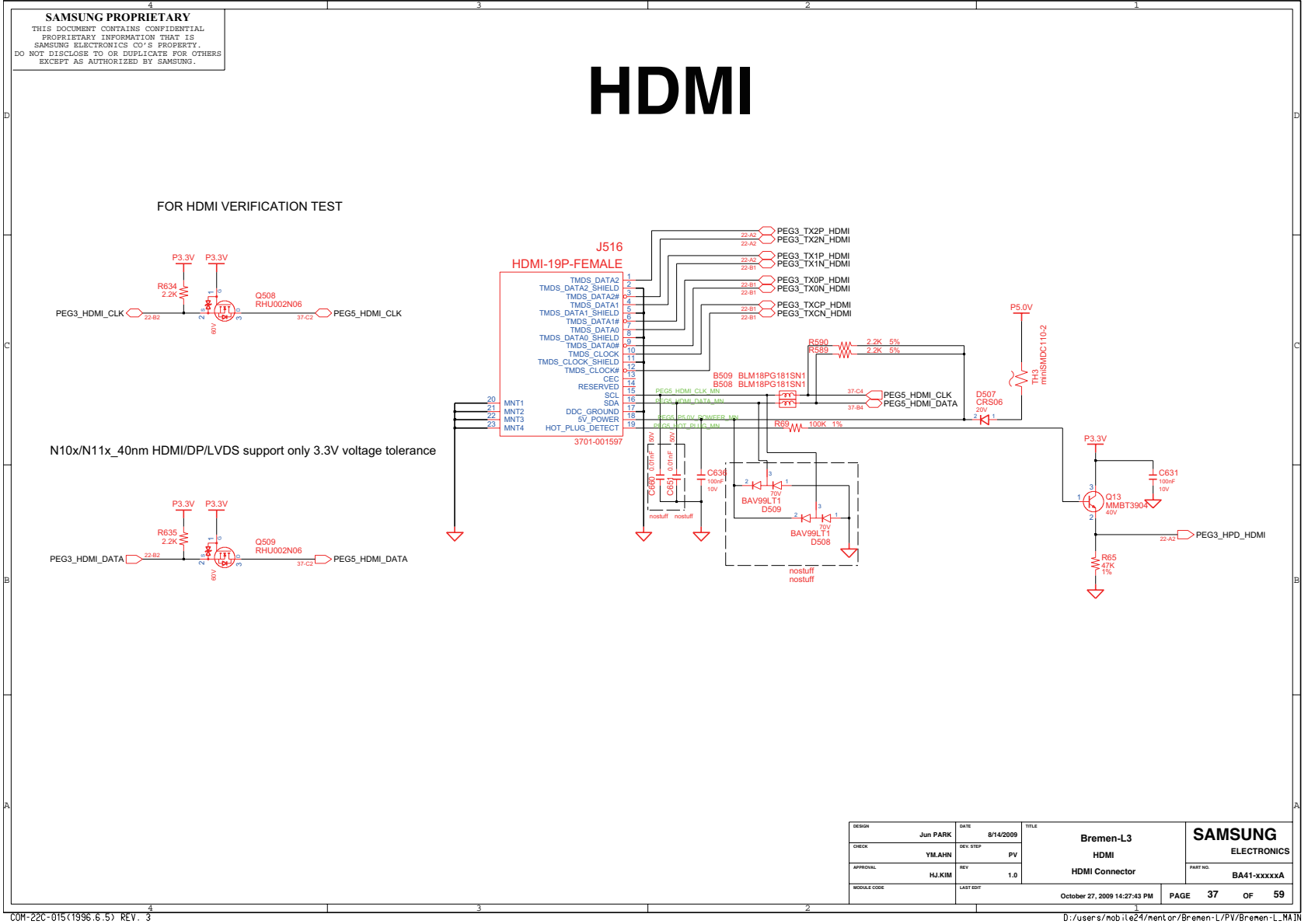
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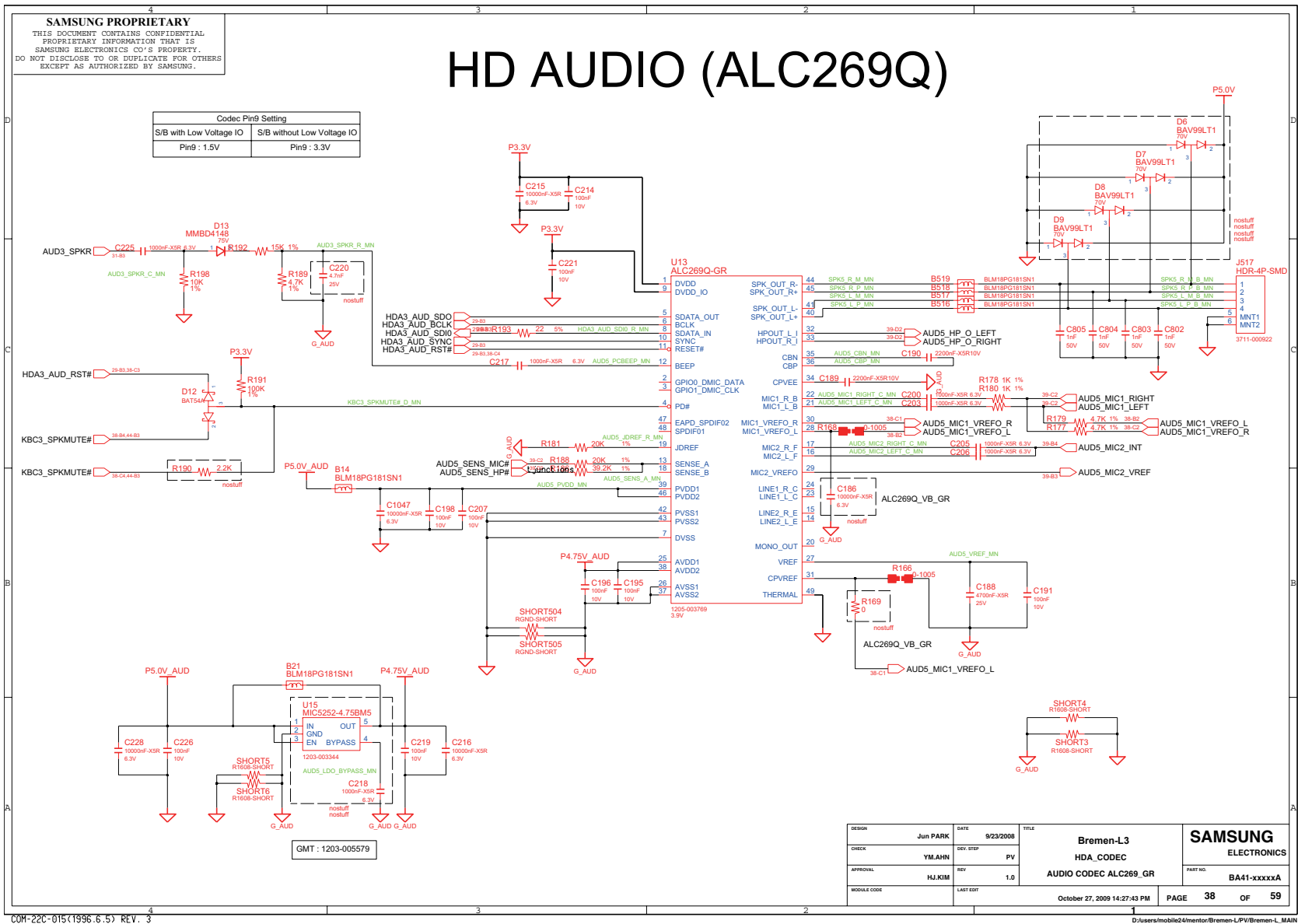
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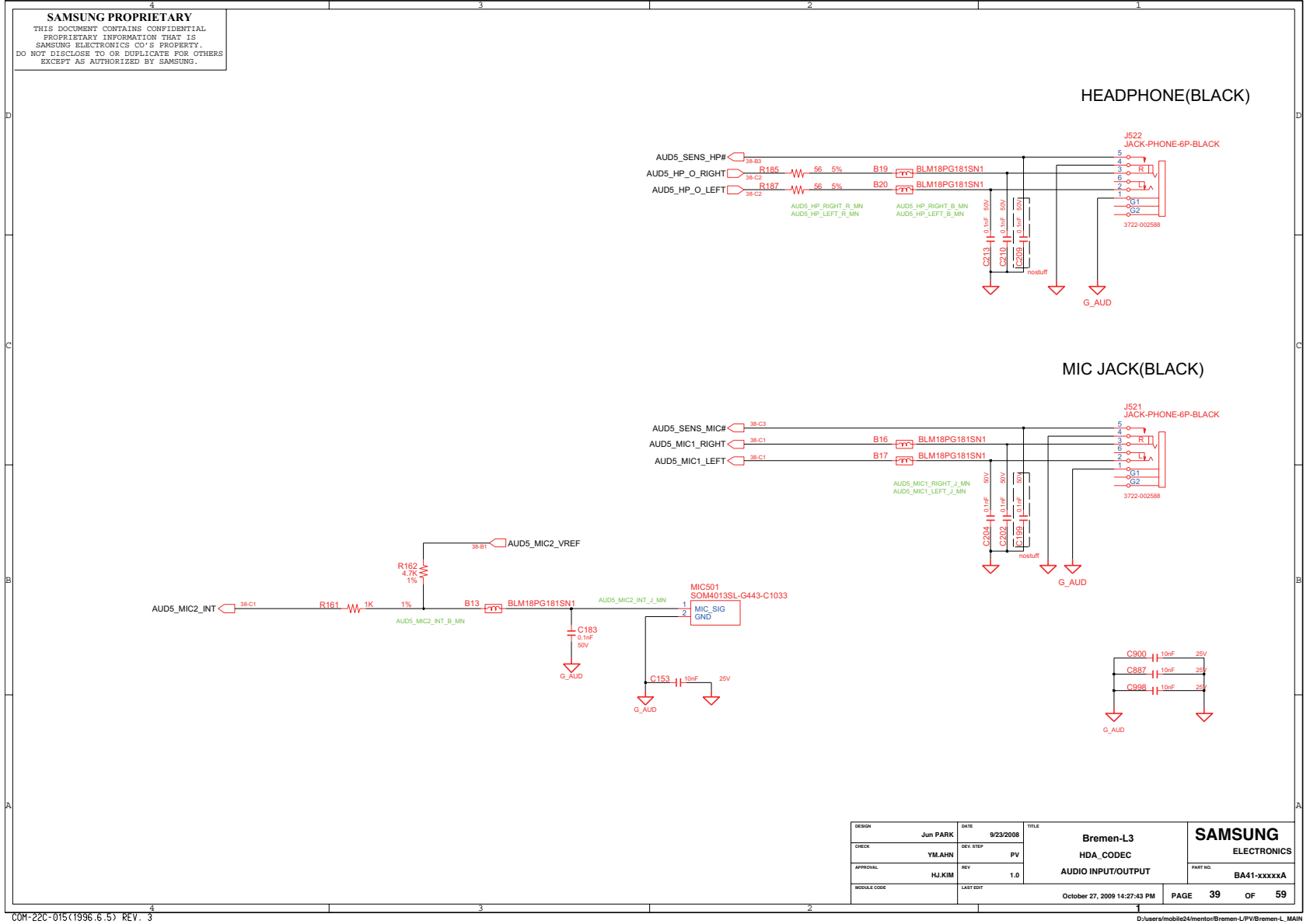
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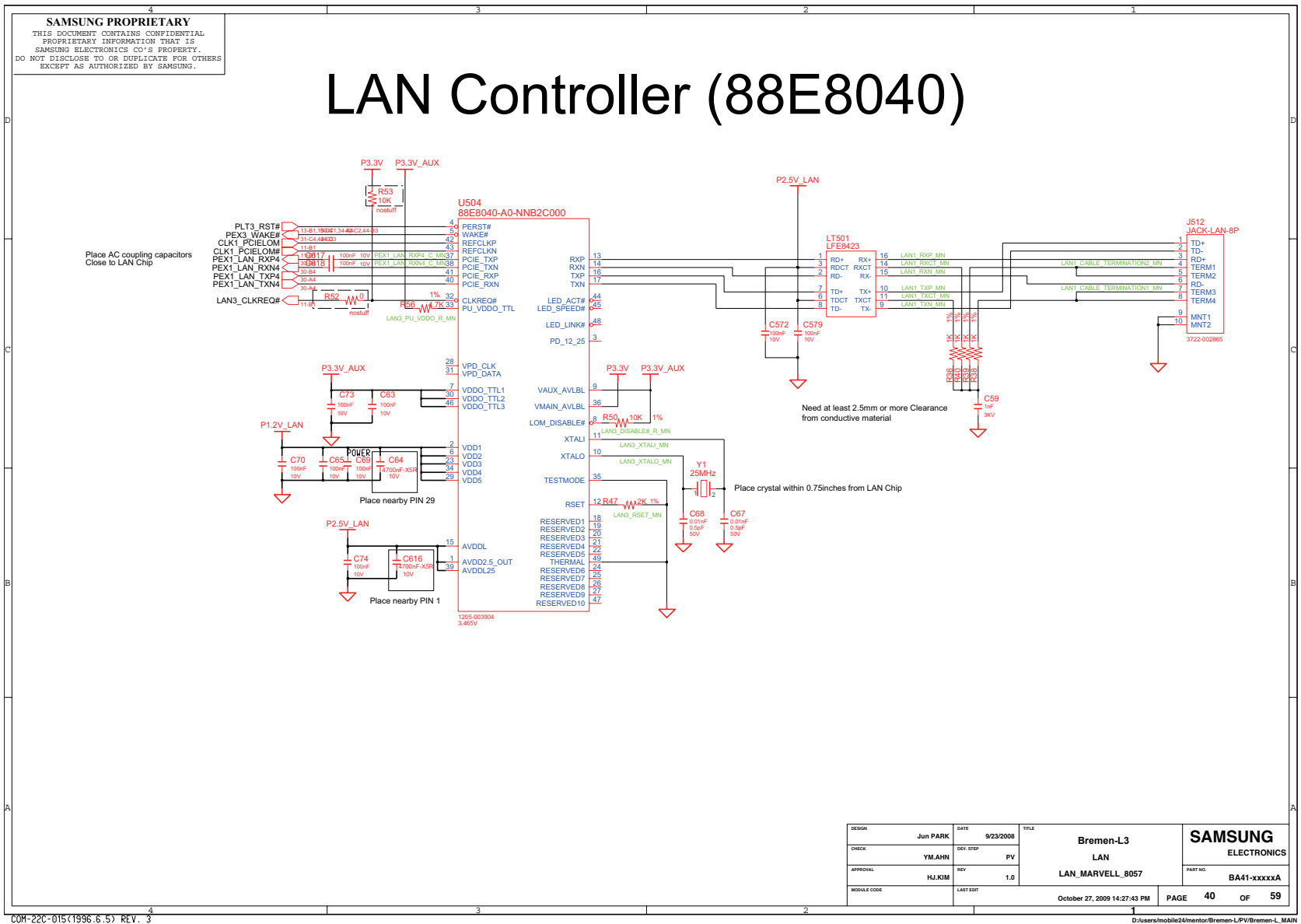
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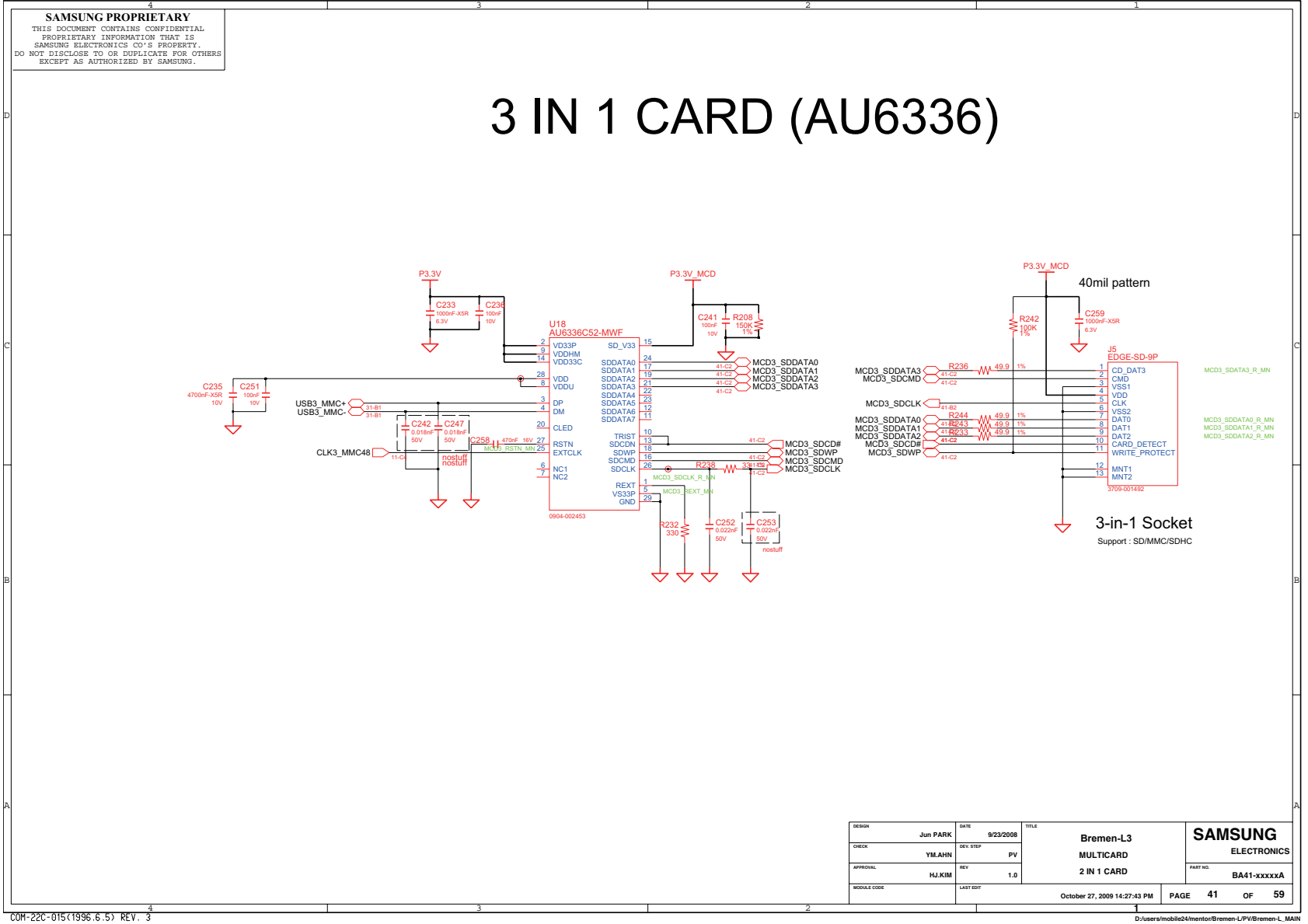
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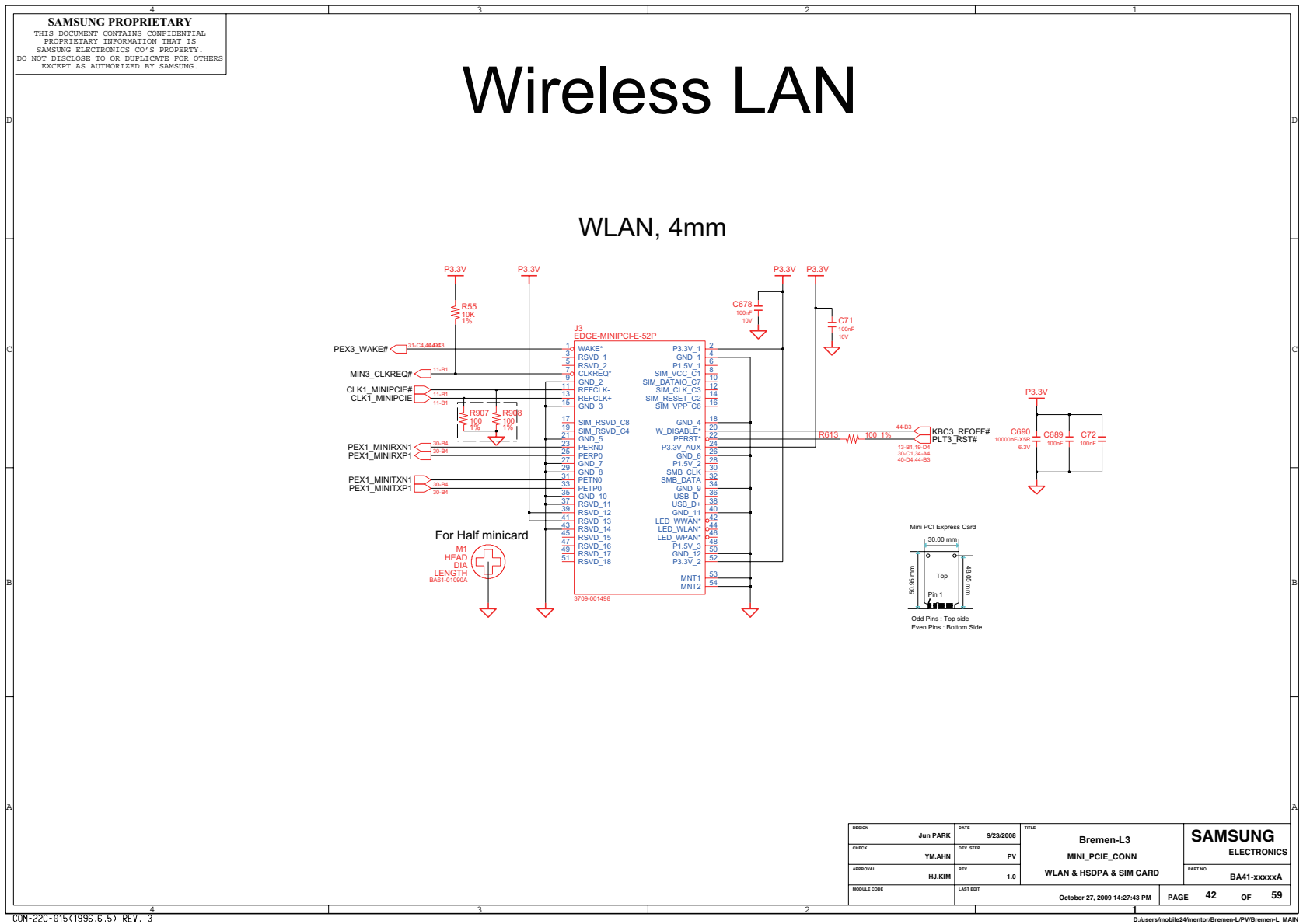
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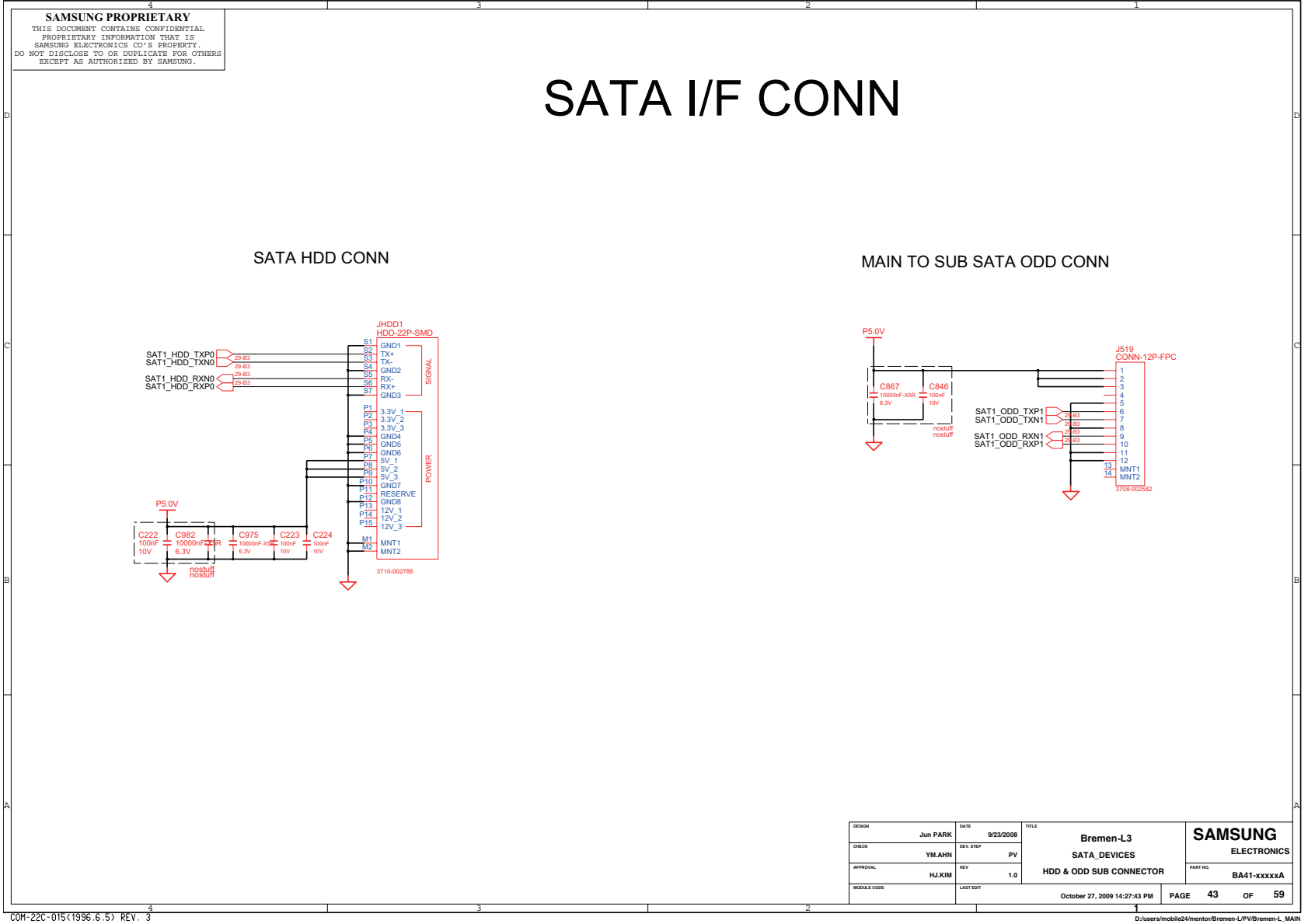
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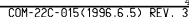
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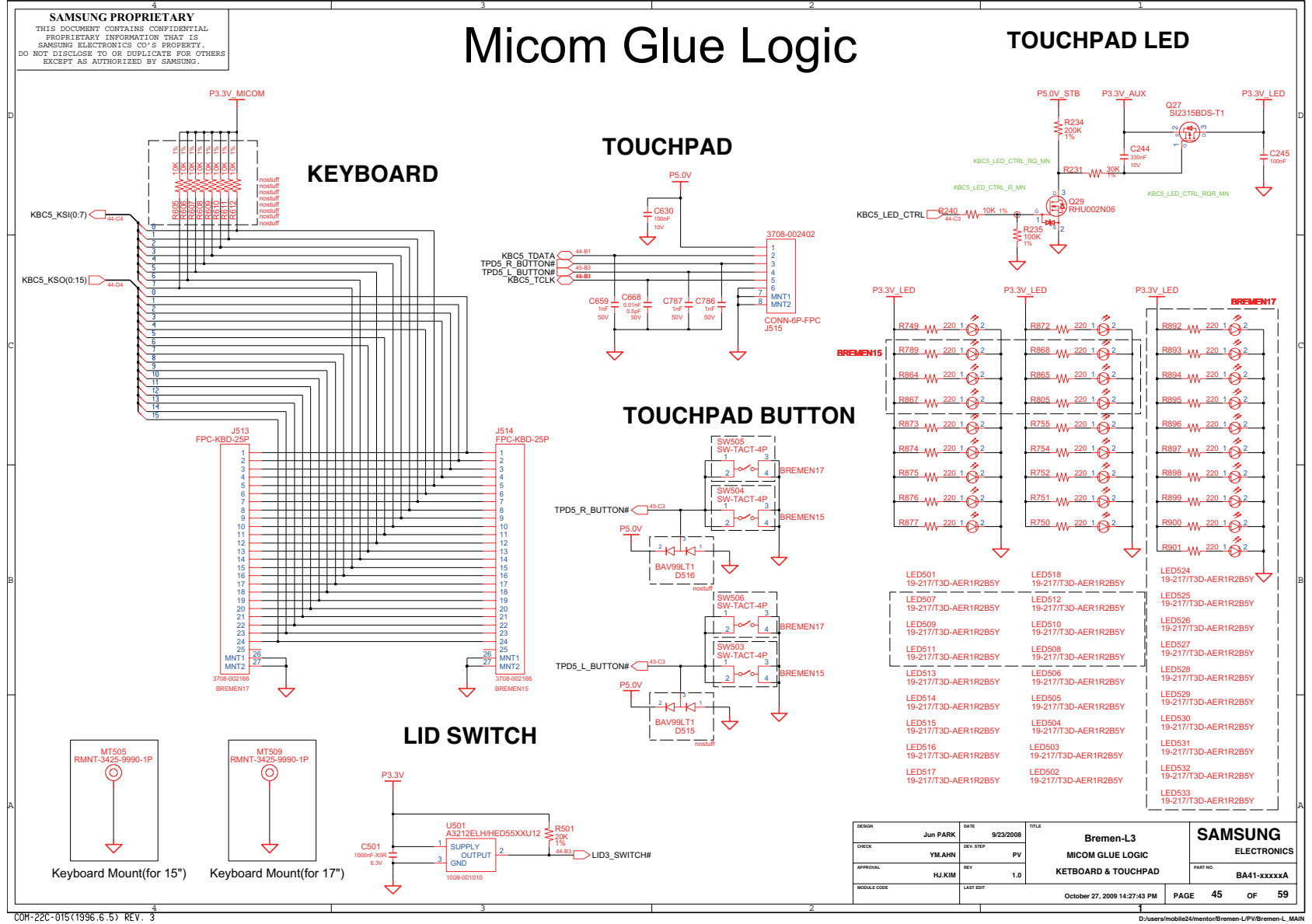
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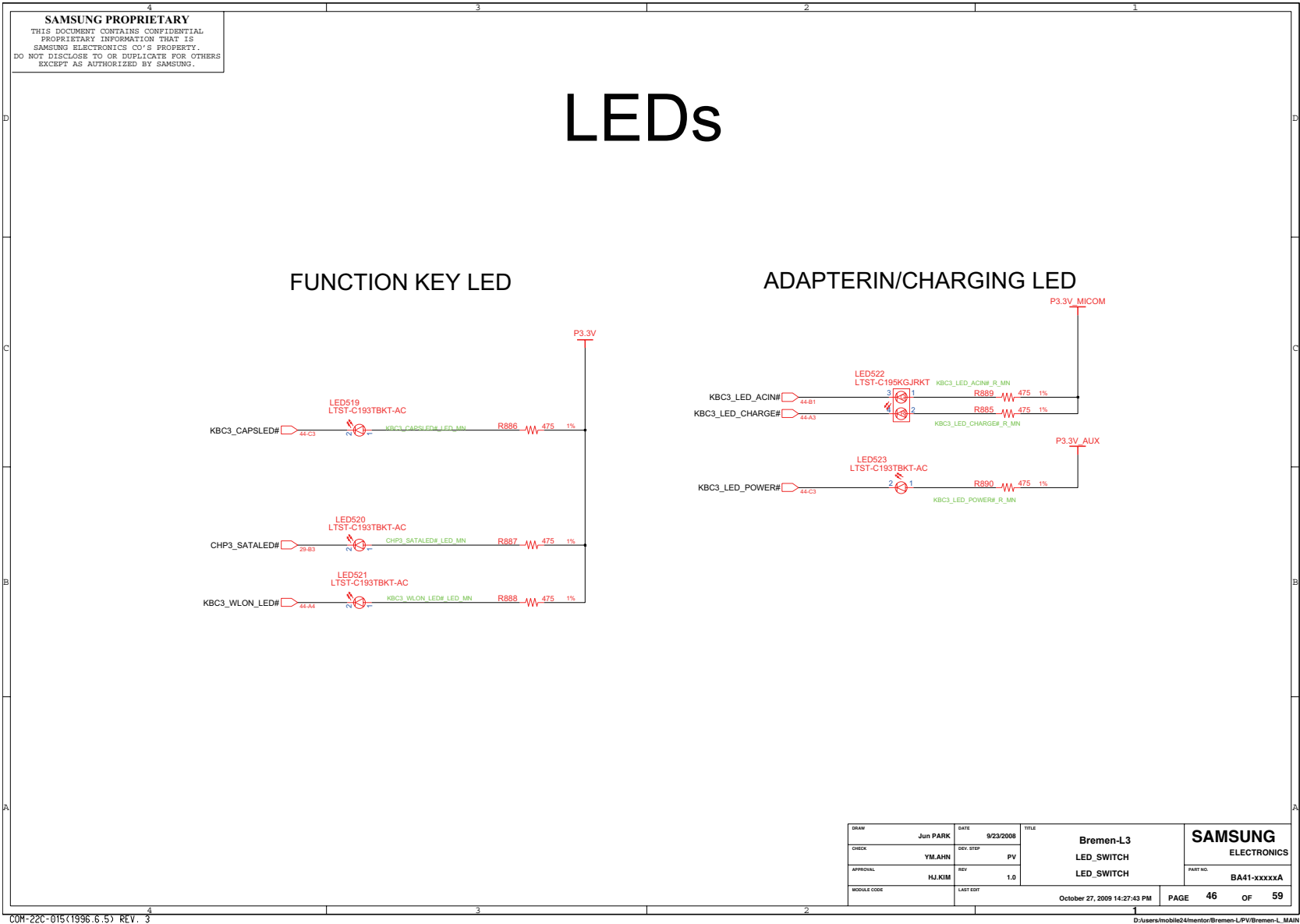
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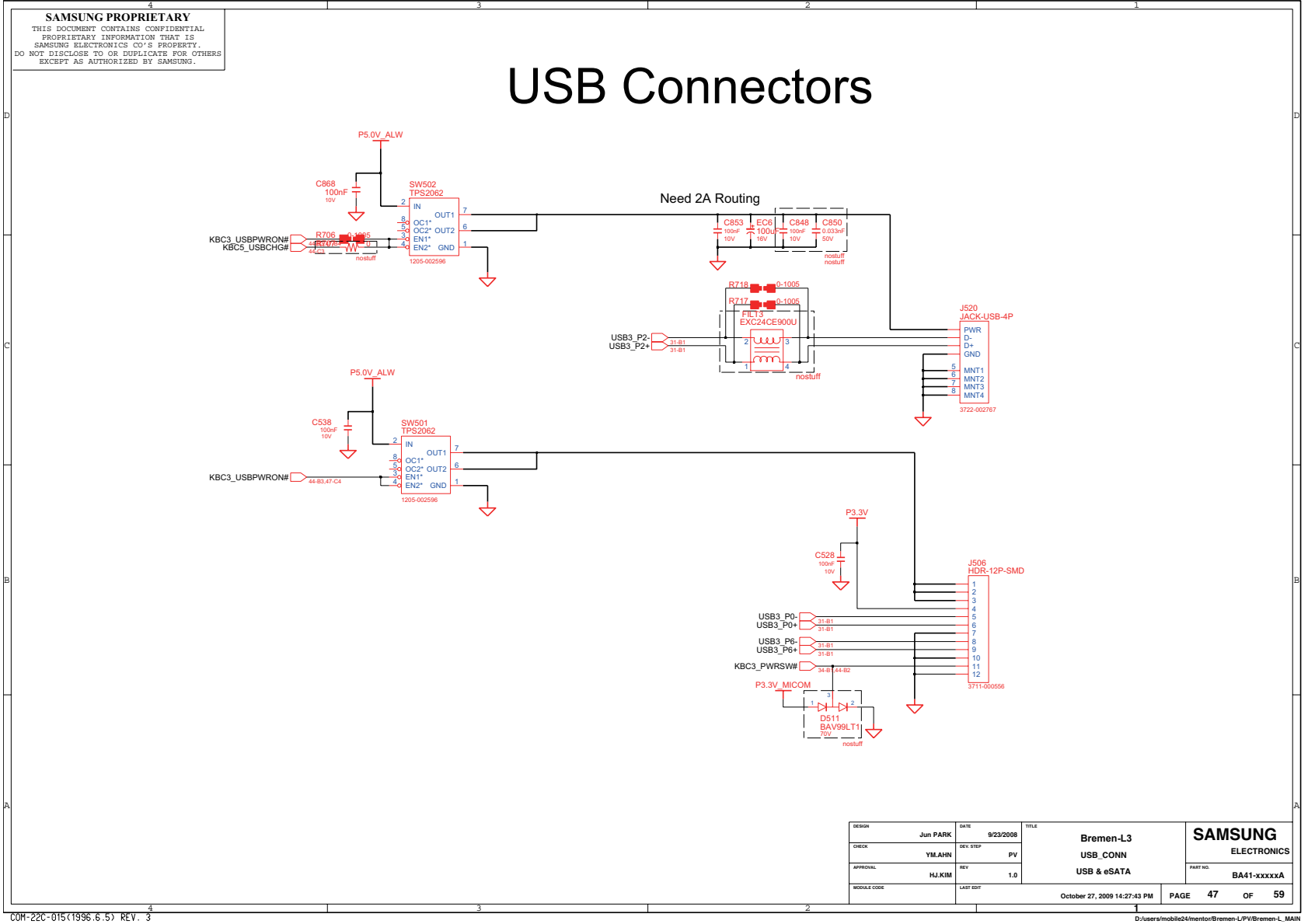
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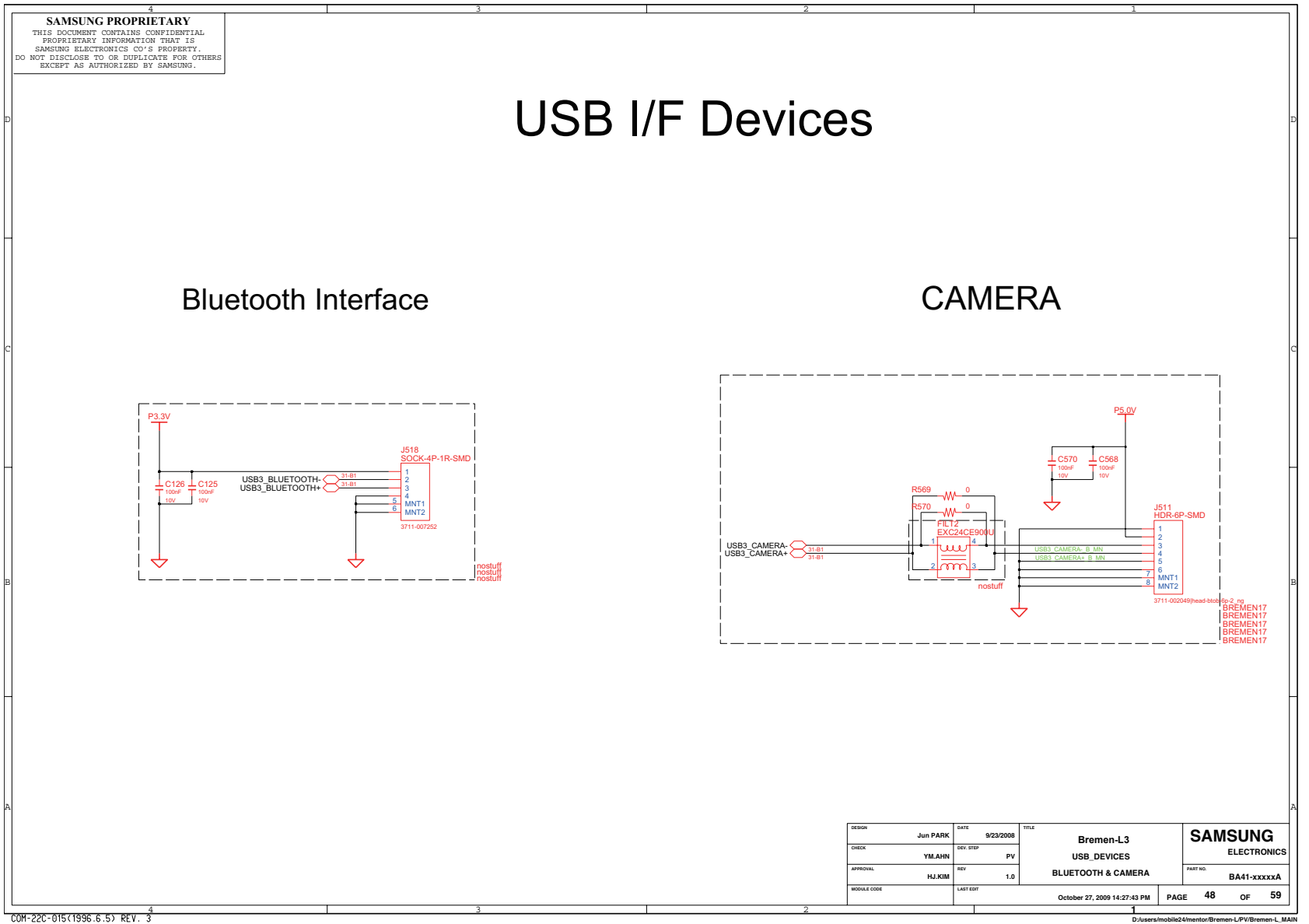
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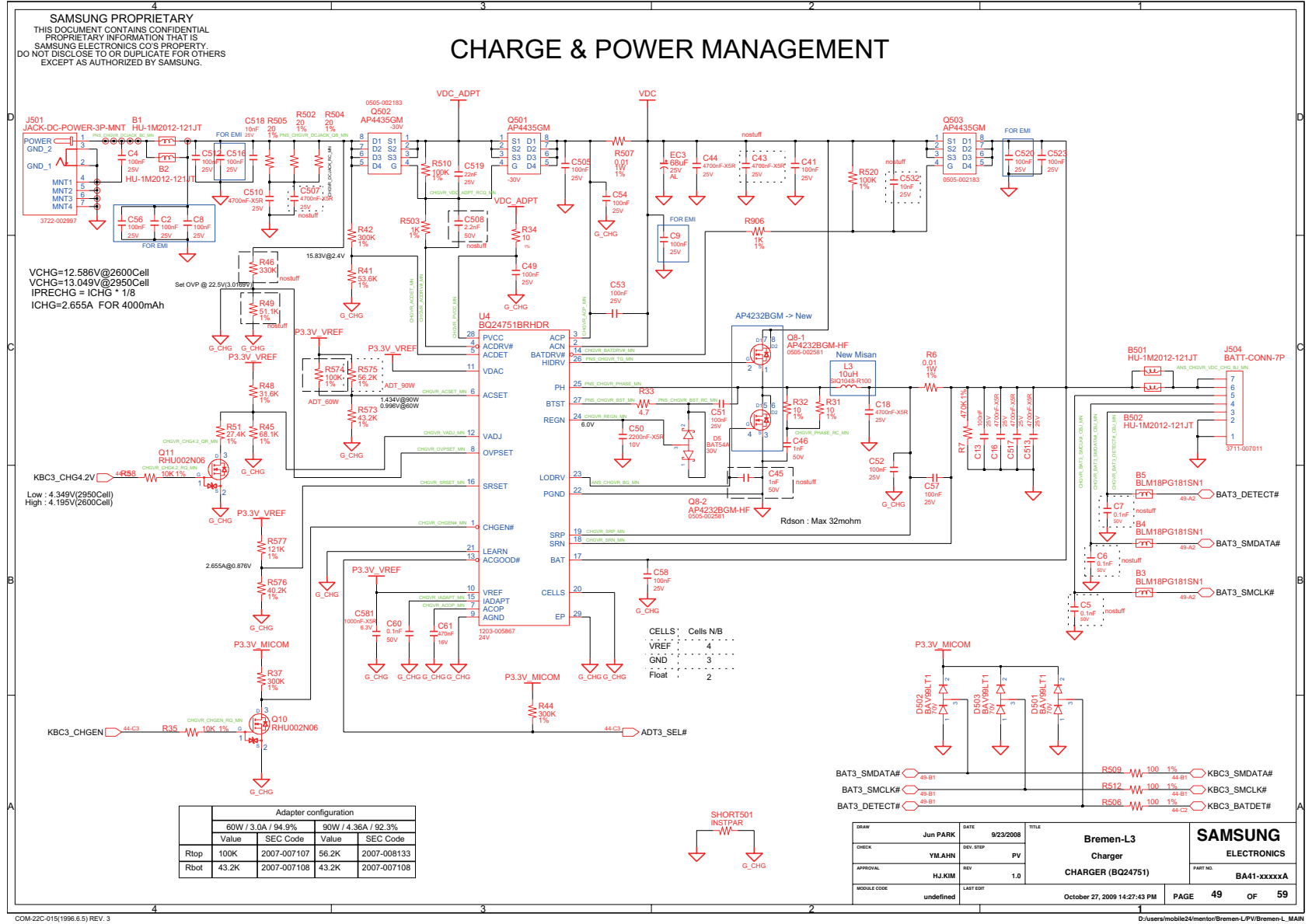
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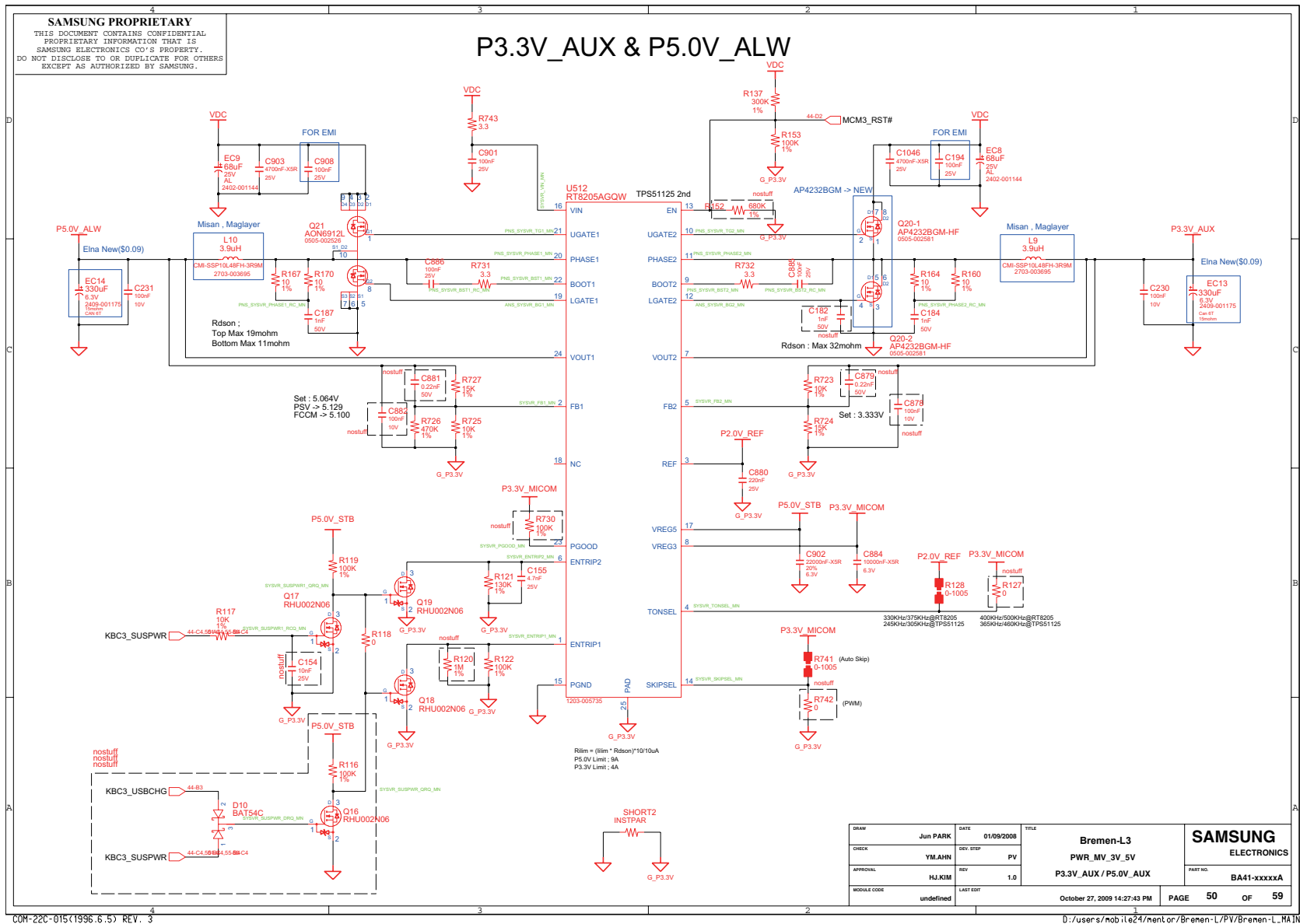
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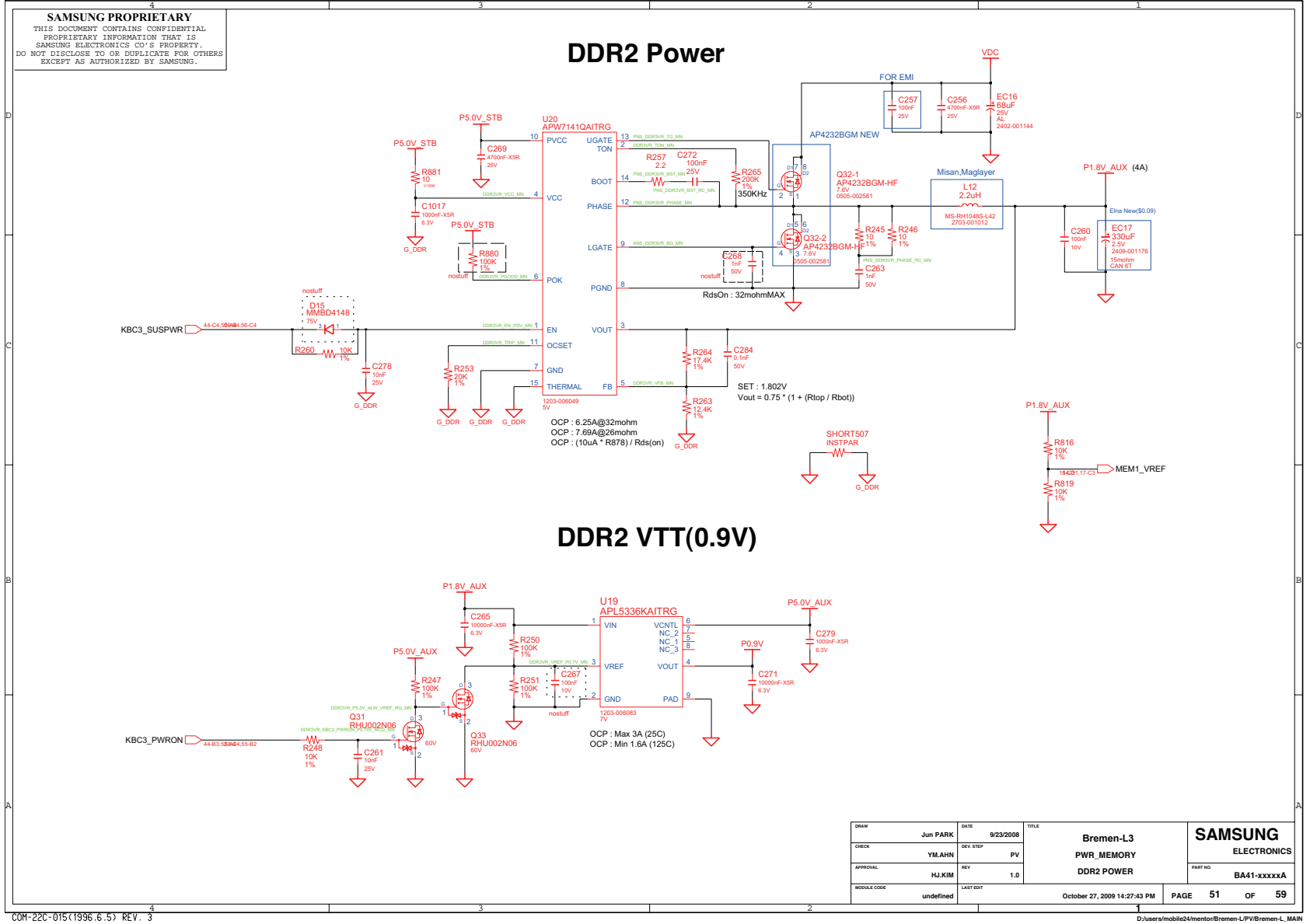
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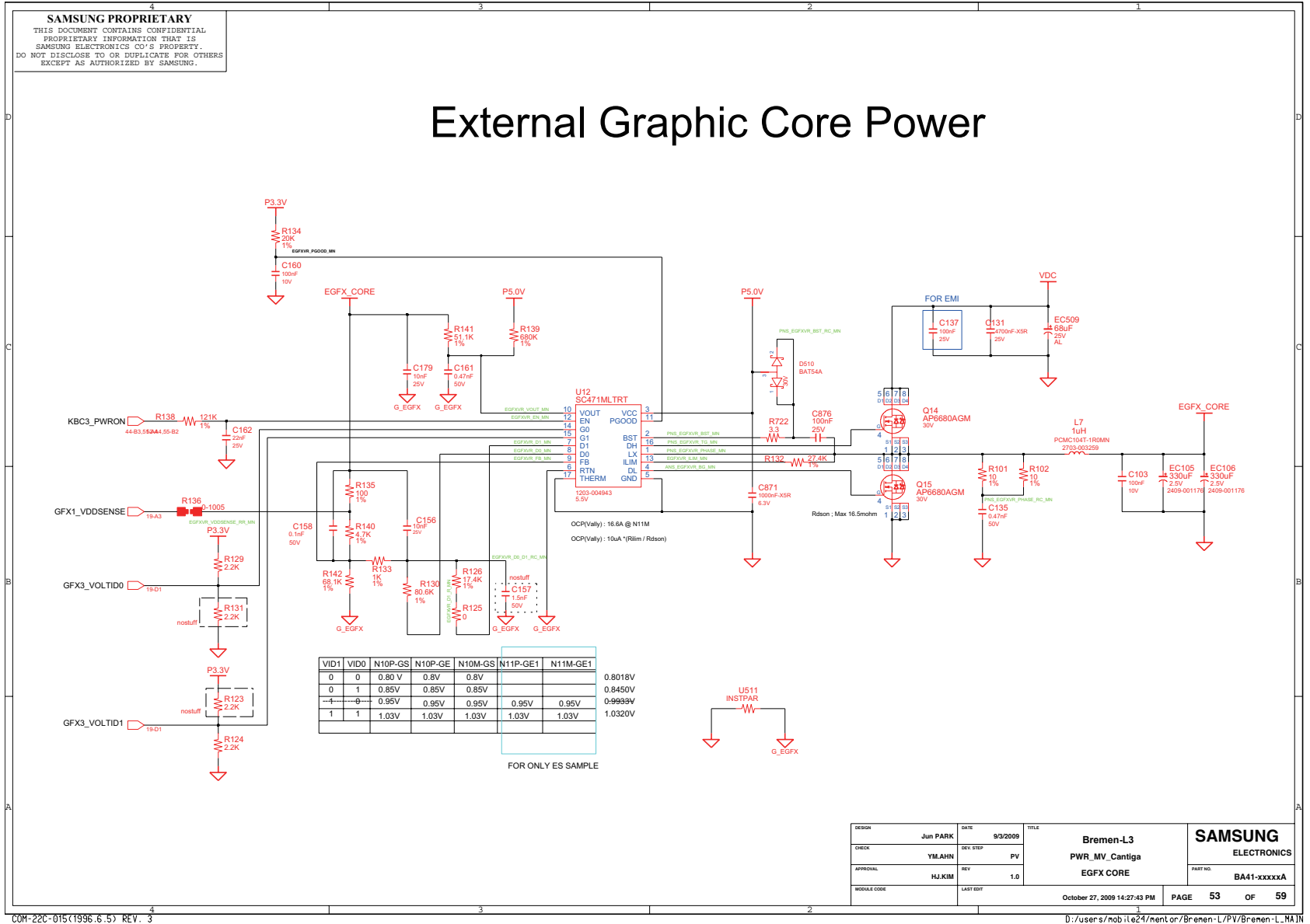


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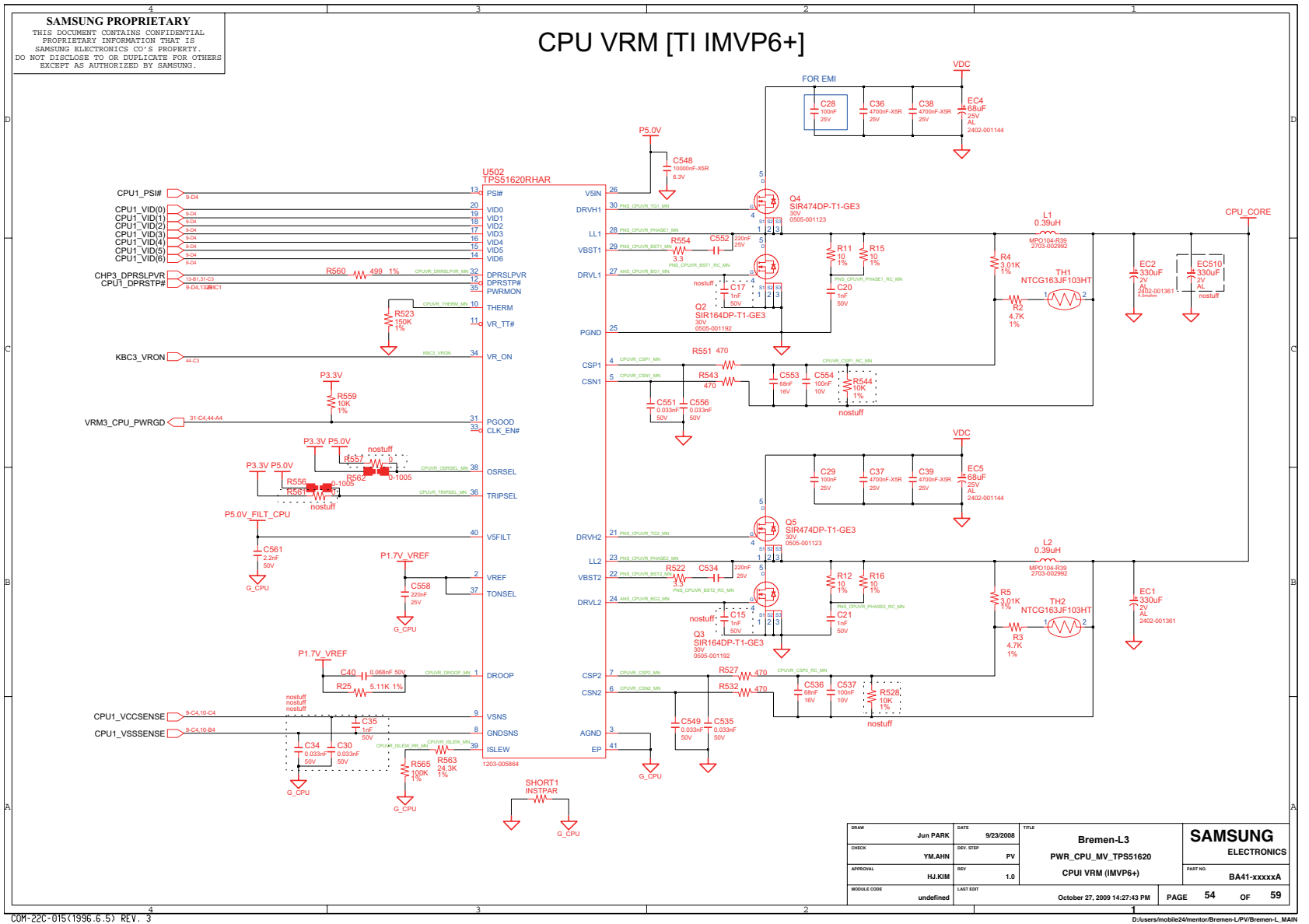




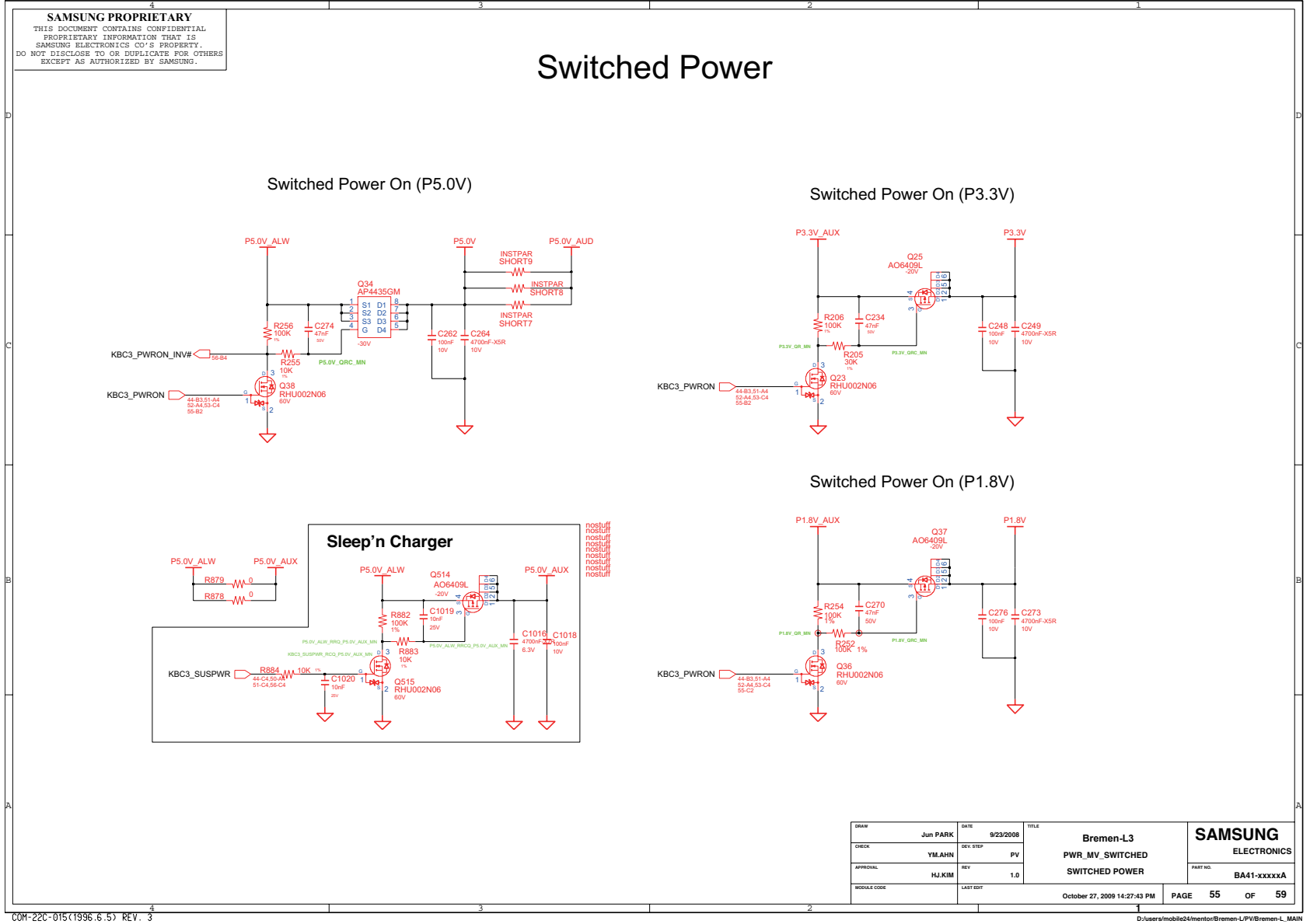
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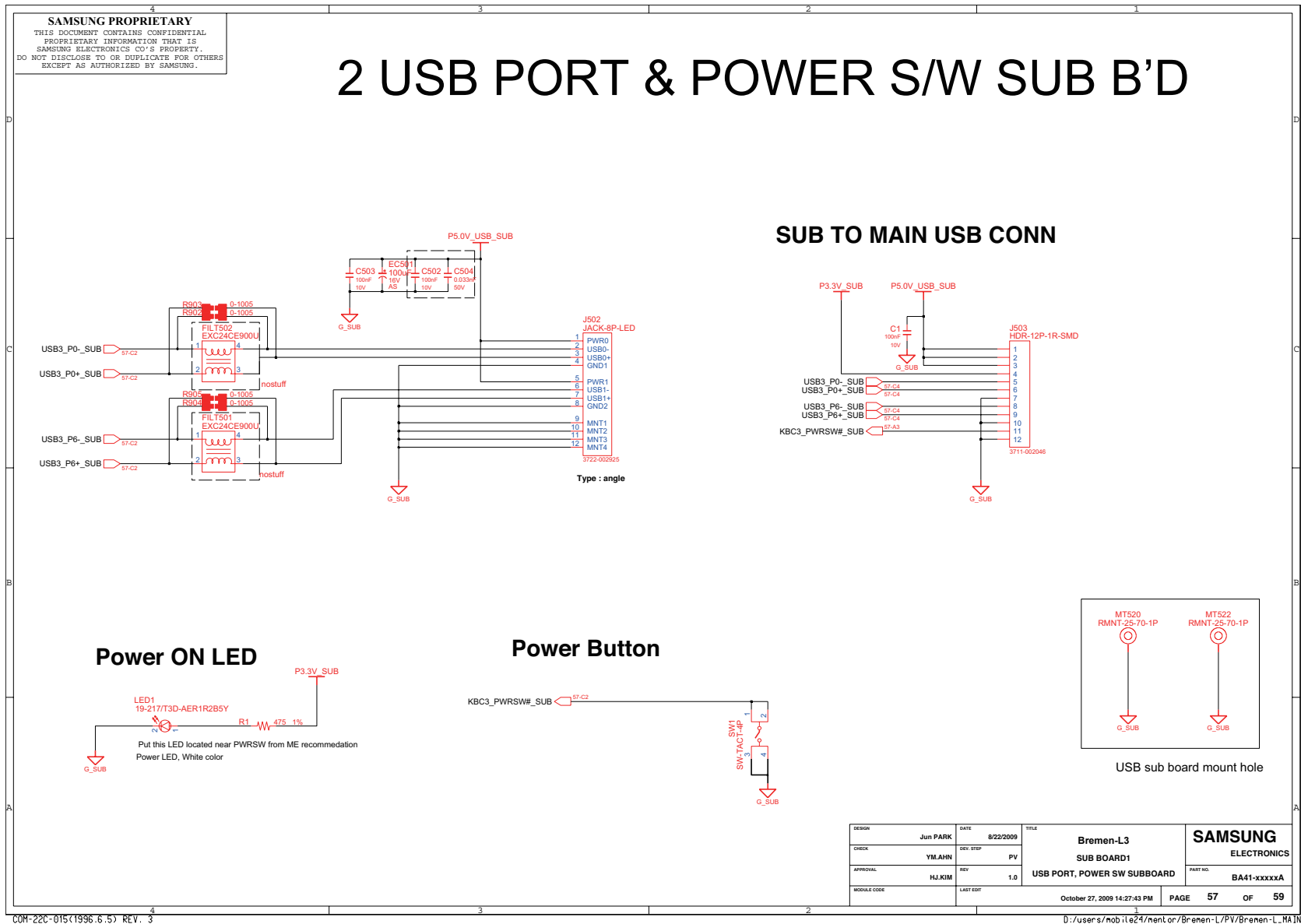


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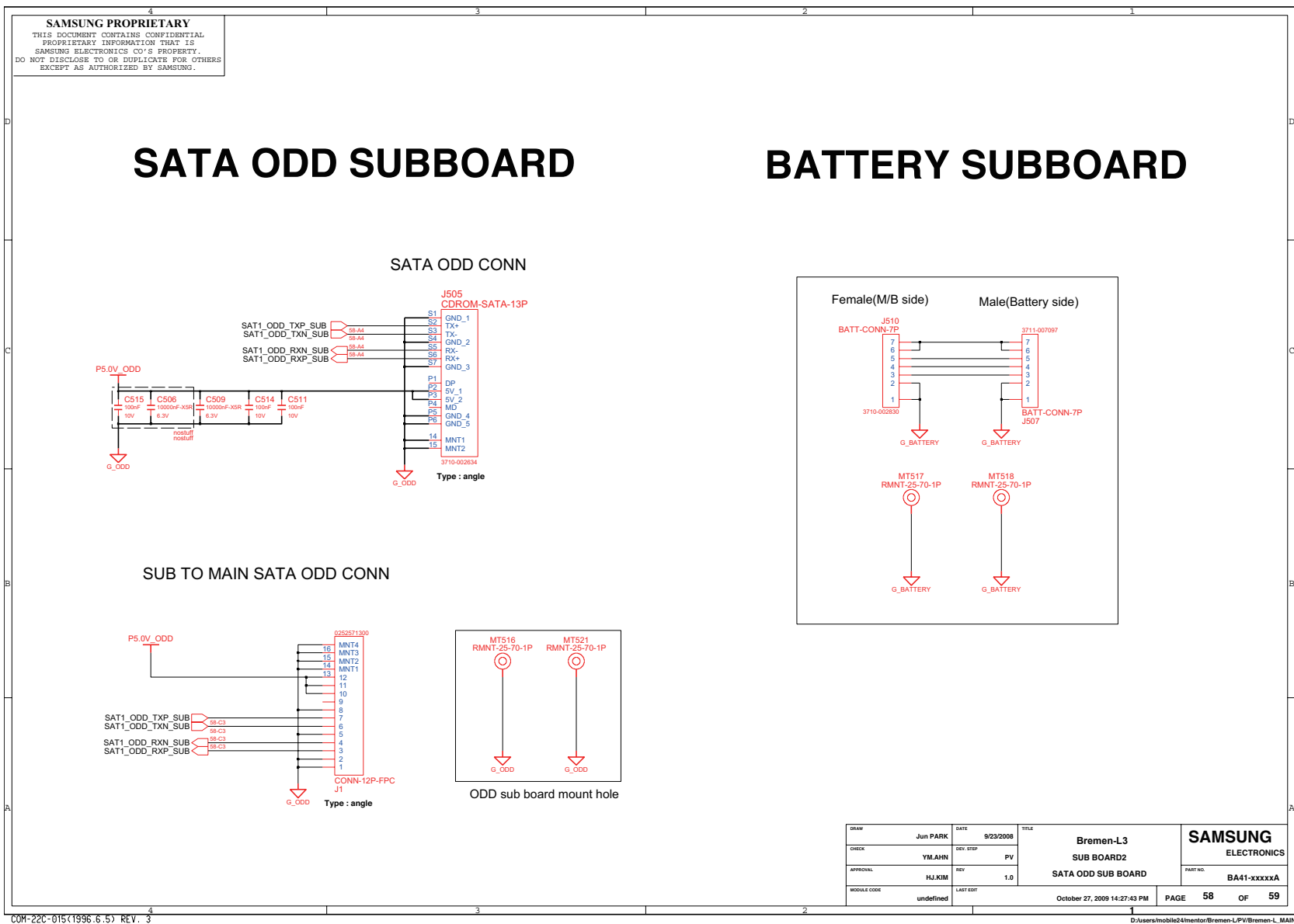




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