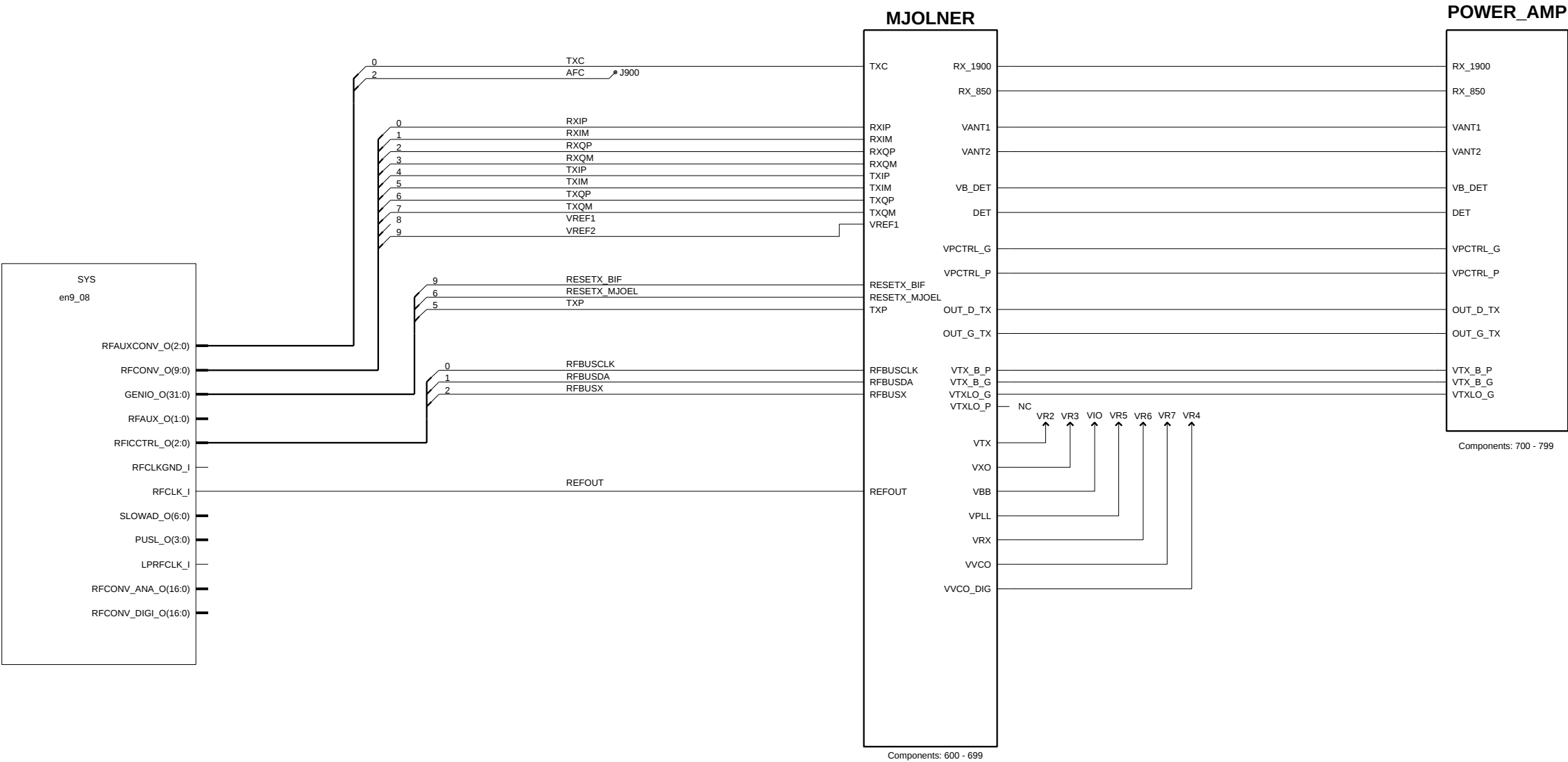
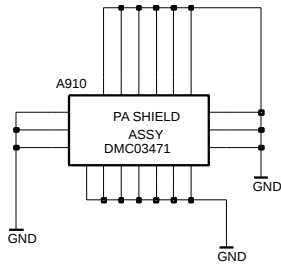
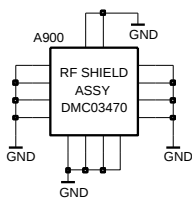
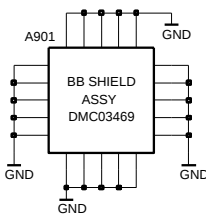
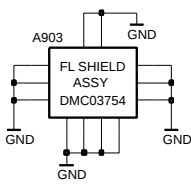


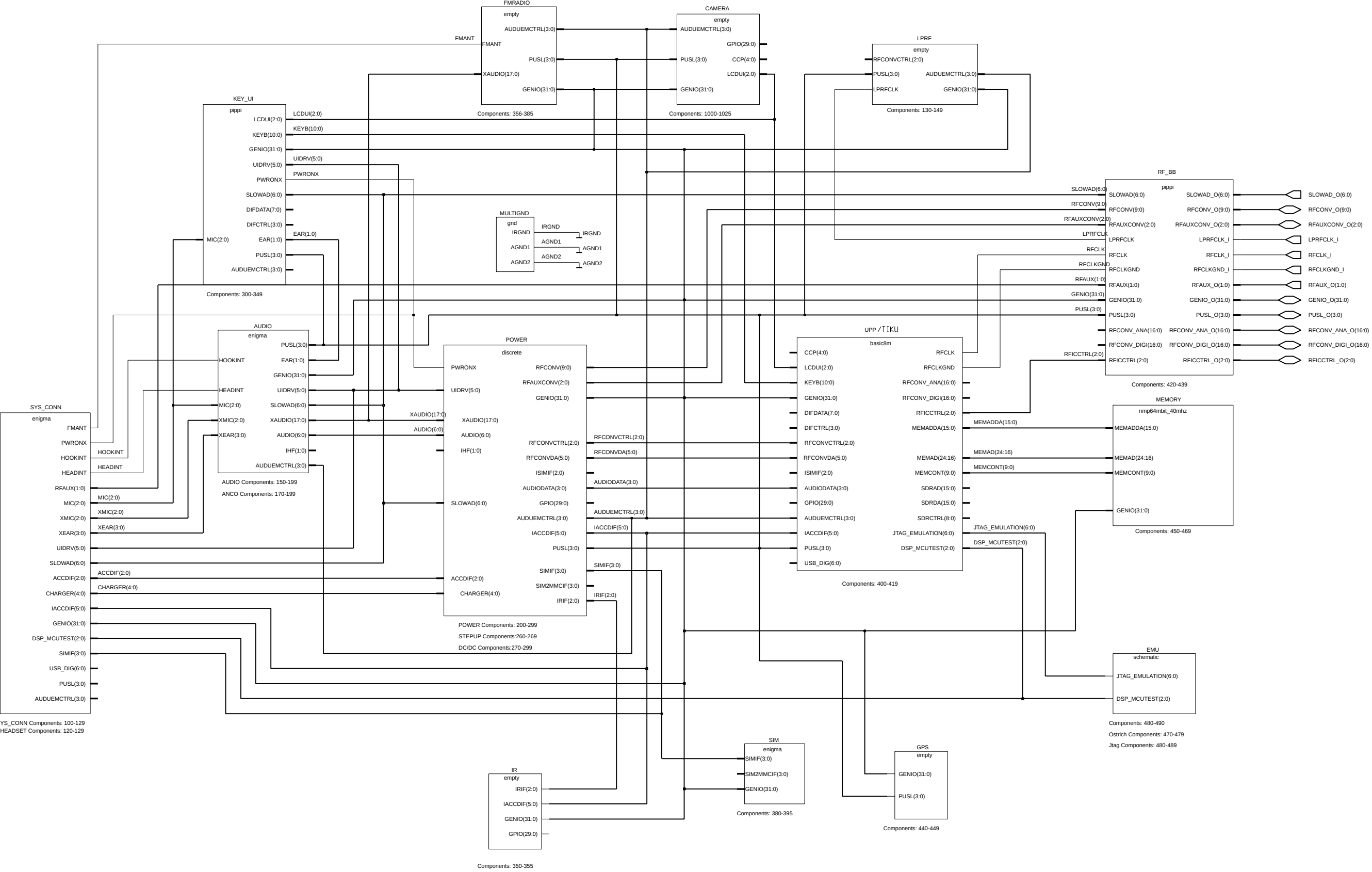
Top-level Schematic1



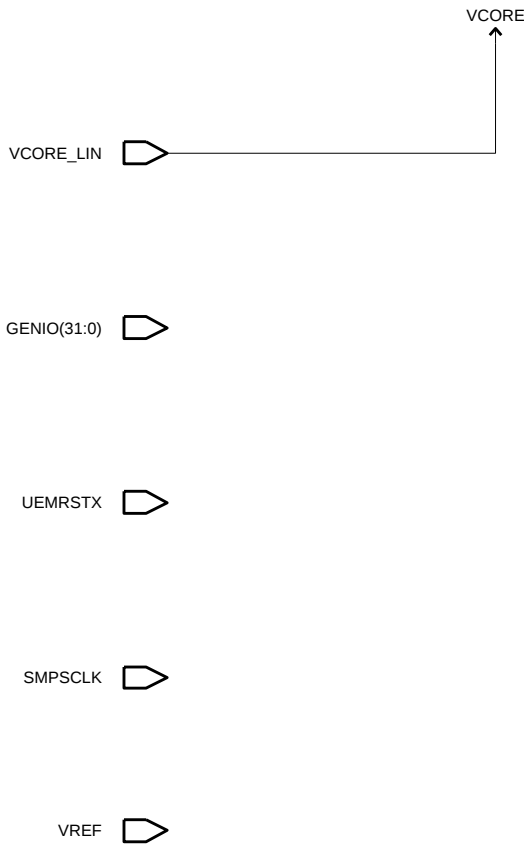
Shield Sheet



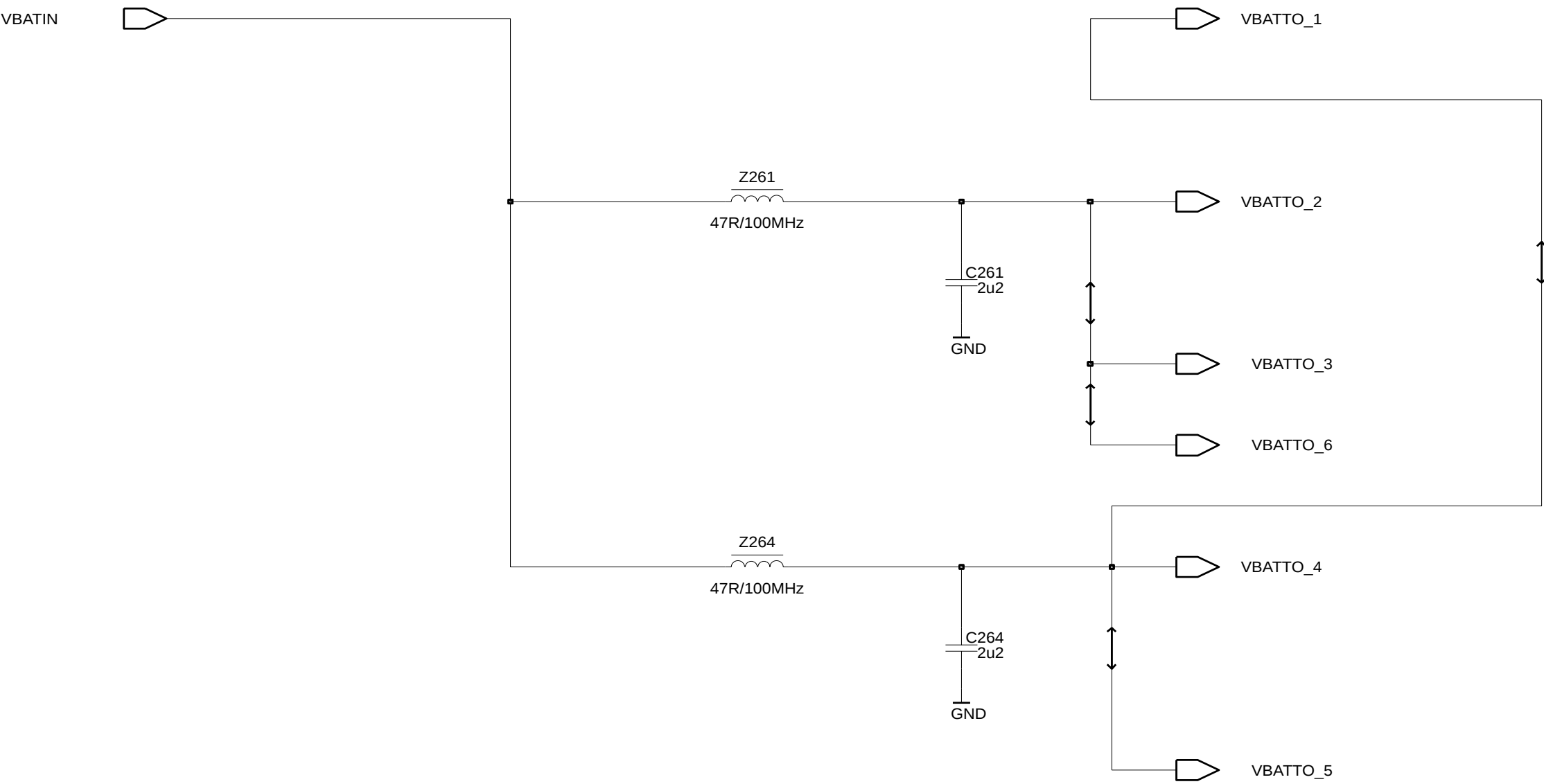
DCT4 Common Baseband



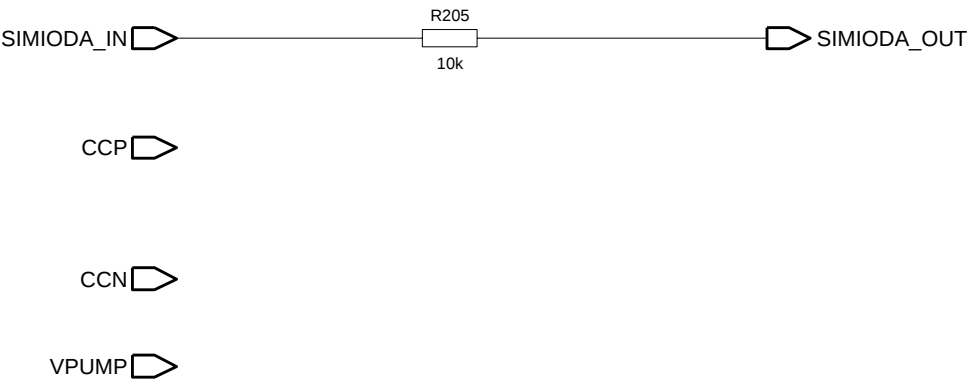
DC/DC Converter

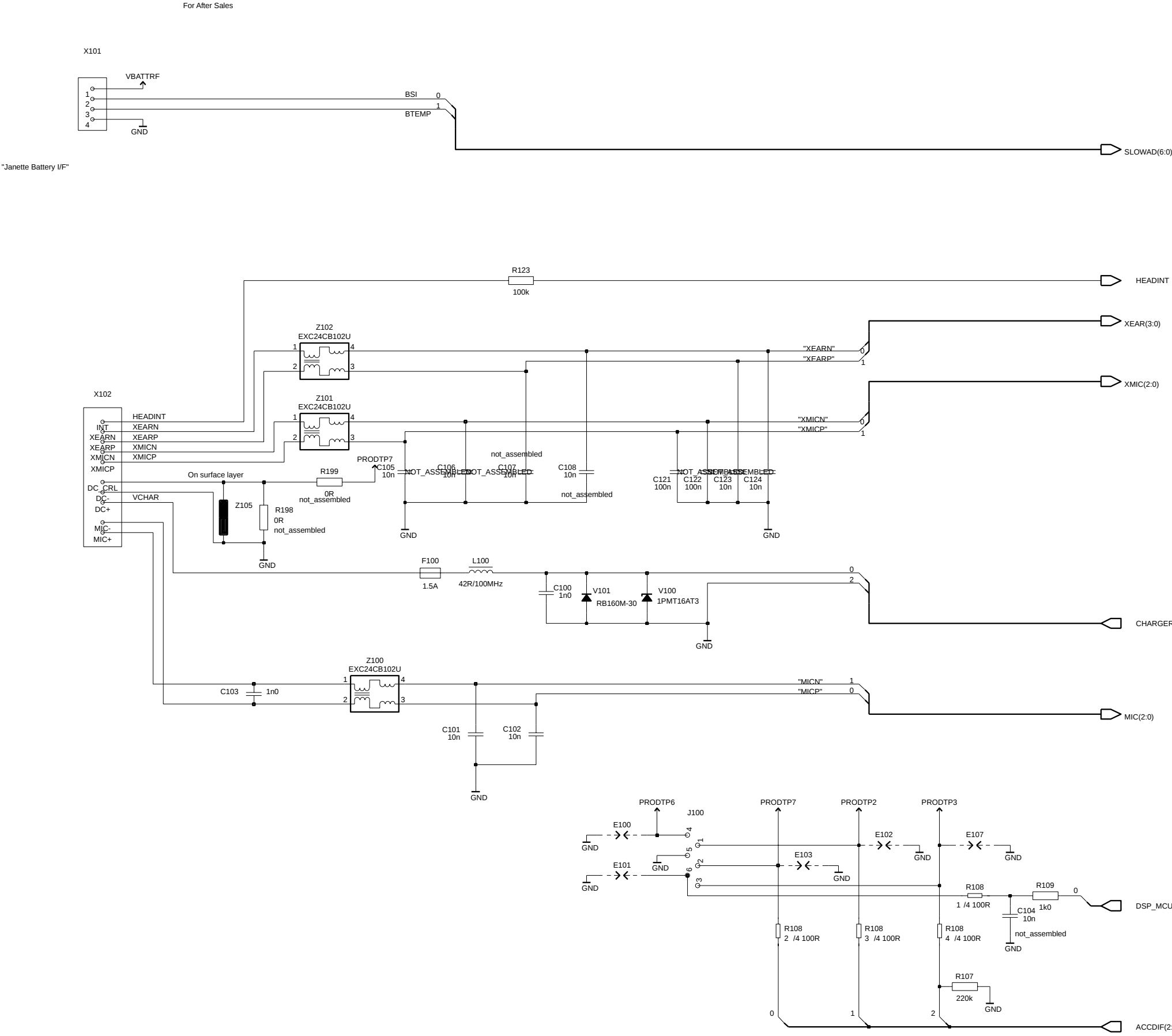


DCT4 Power Supply



DCT4 Common Baseband



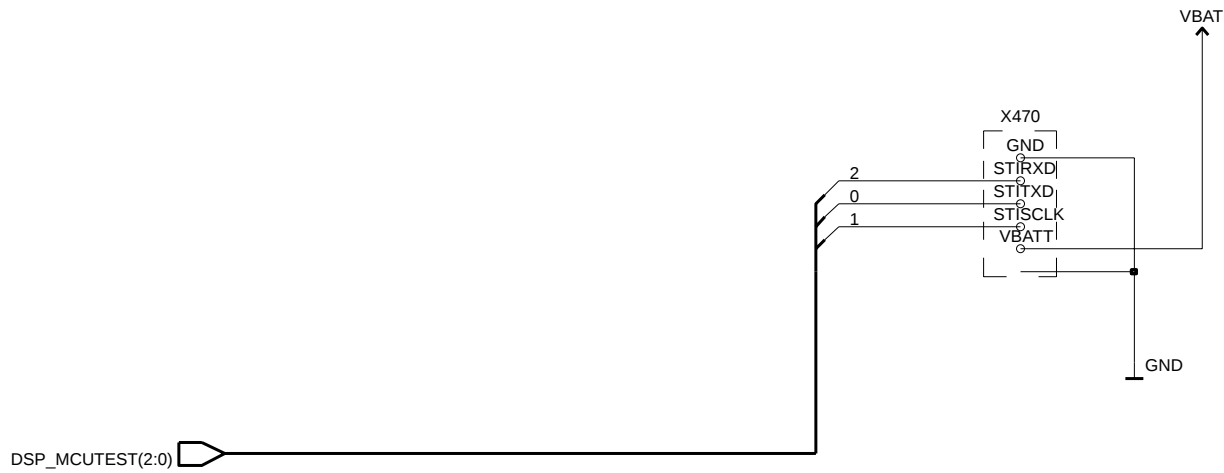


- UIDRV(5:0)
- IACCDIF(5:0)
- RFAUX(1:0)
- PWRONX
- SIMIF(3:0)
- HOOKINT
- GENIO(31:0)
- FMANT
- USB_DIG(6:0)
- PUSL(3:0)
- AUDUEMCTRL(3:0)

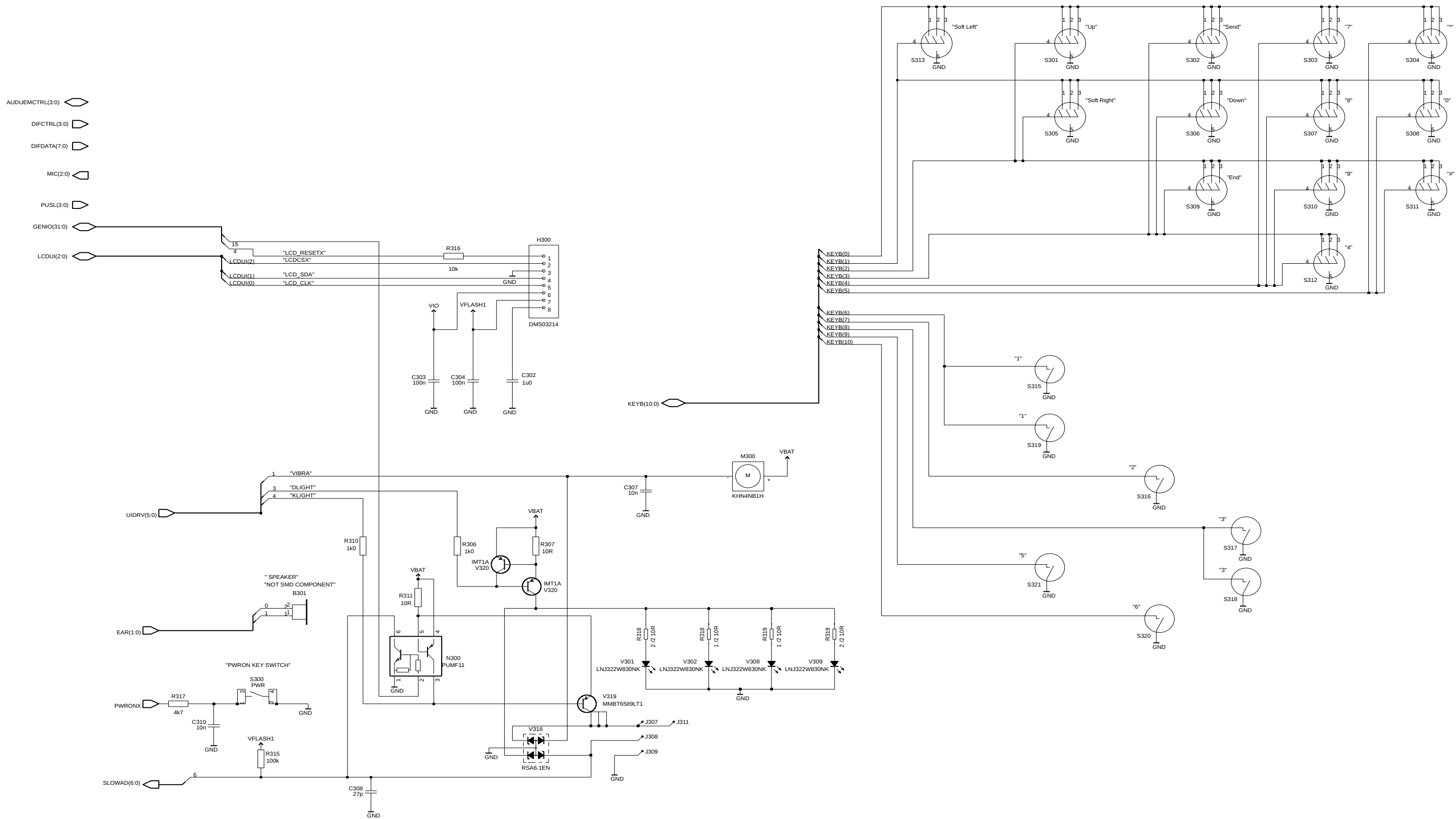
Test and Emulator Interface



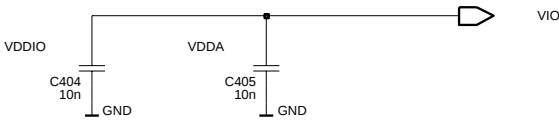
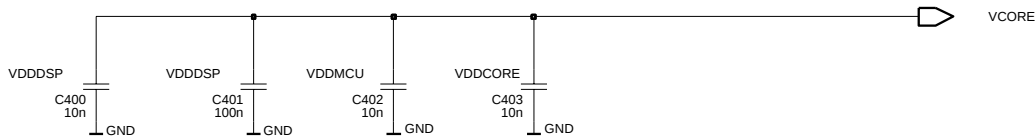
Connector-based Ostrich Test Interface



User Interface Clover Key Targets



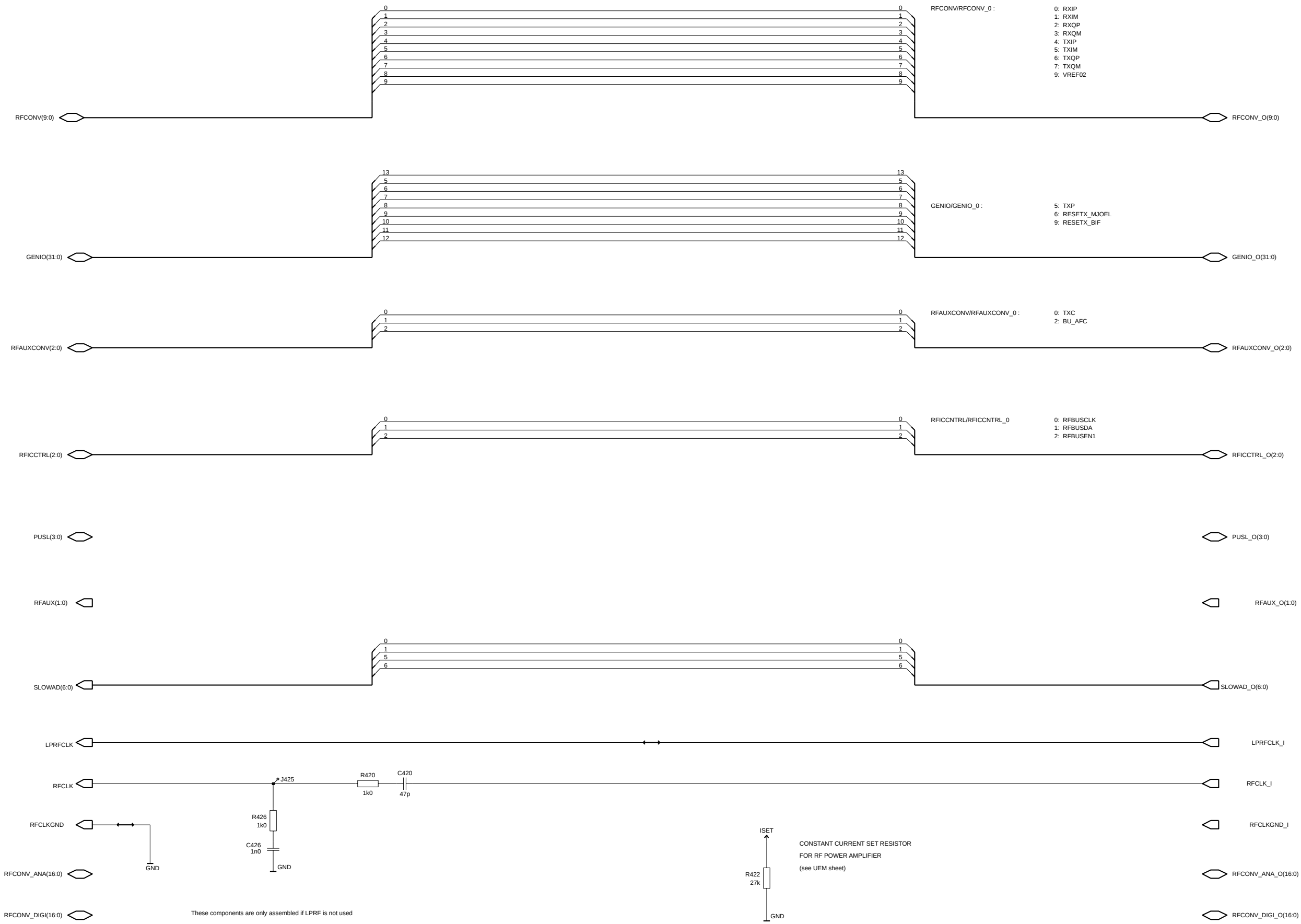
Discrete Decoupling Capacitors for UPP



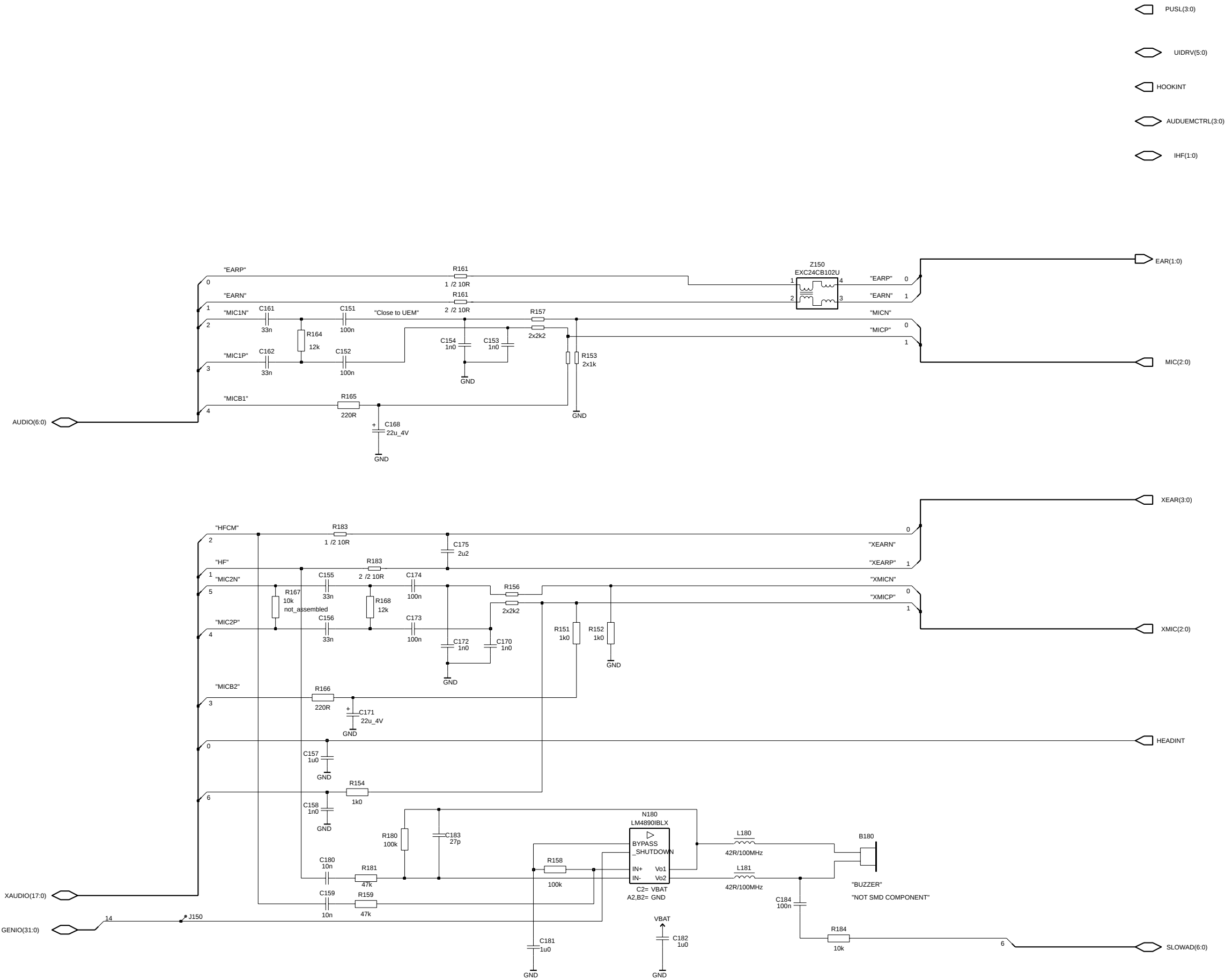
Discrete Capacitors for Memory Without VFlash



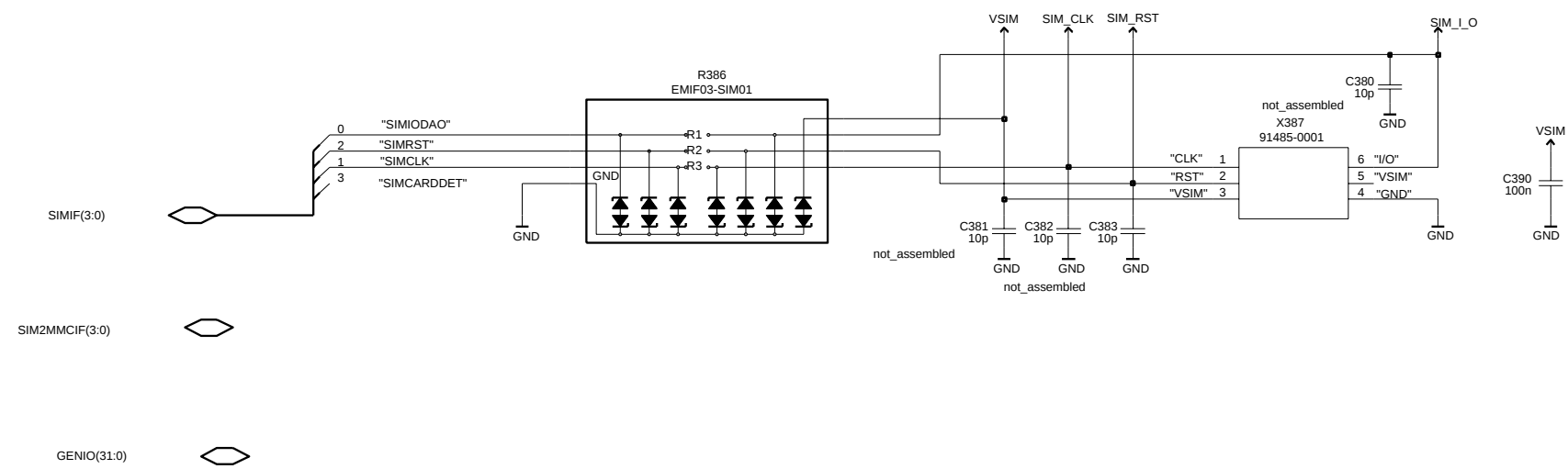
RF/BB Interface



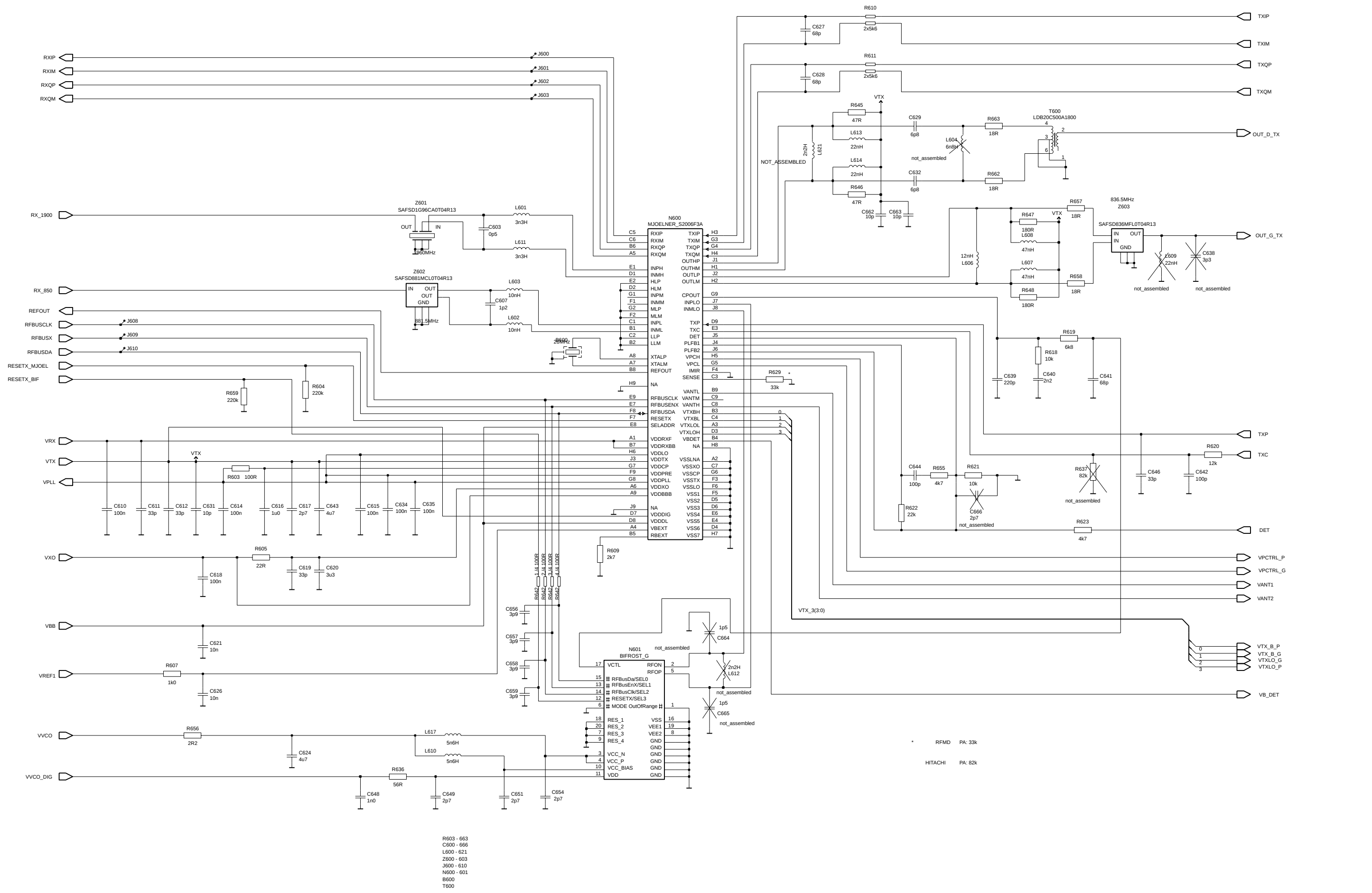
Audio Interface



SIM Interface



Mjoelner



Component Layout - Top

