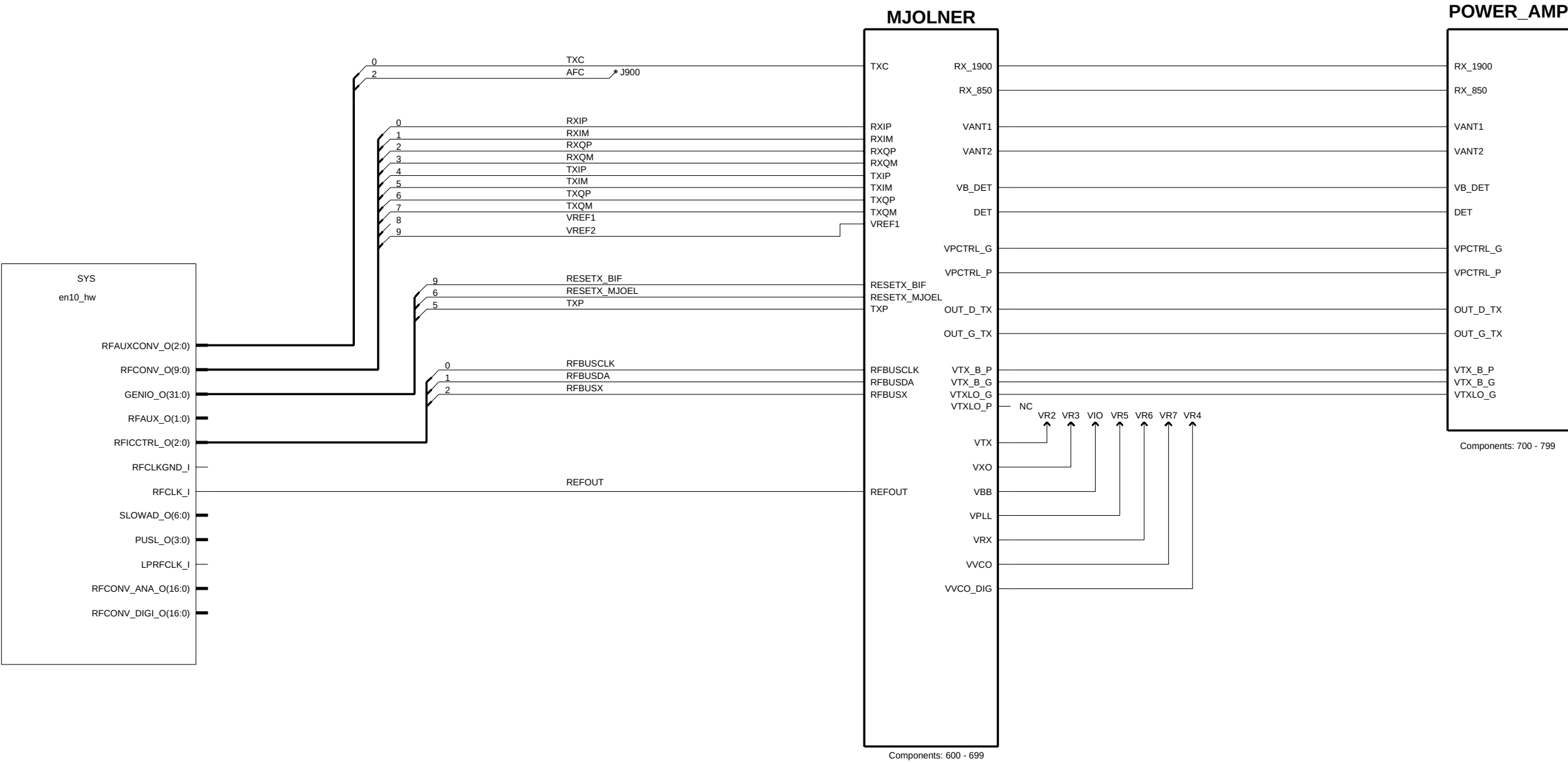
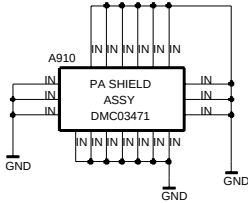
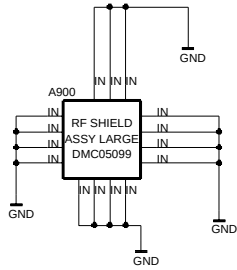
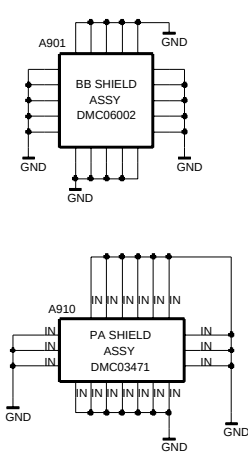
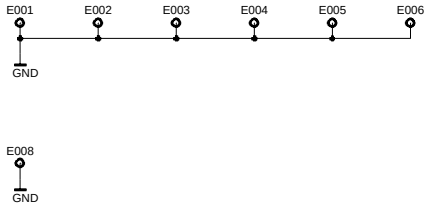


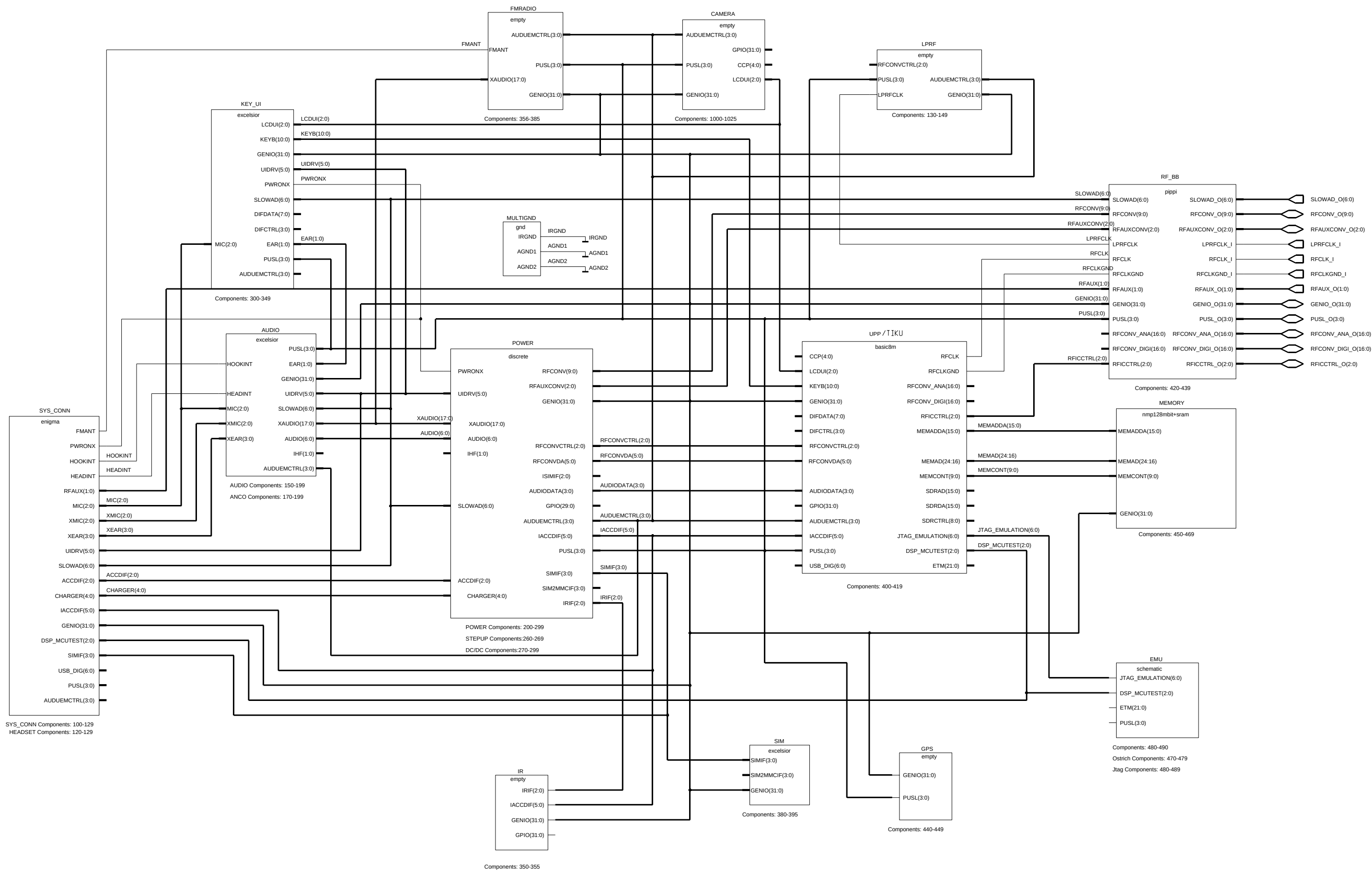
Top Level Schematic (FDK VCO)



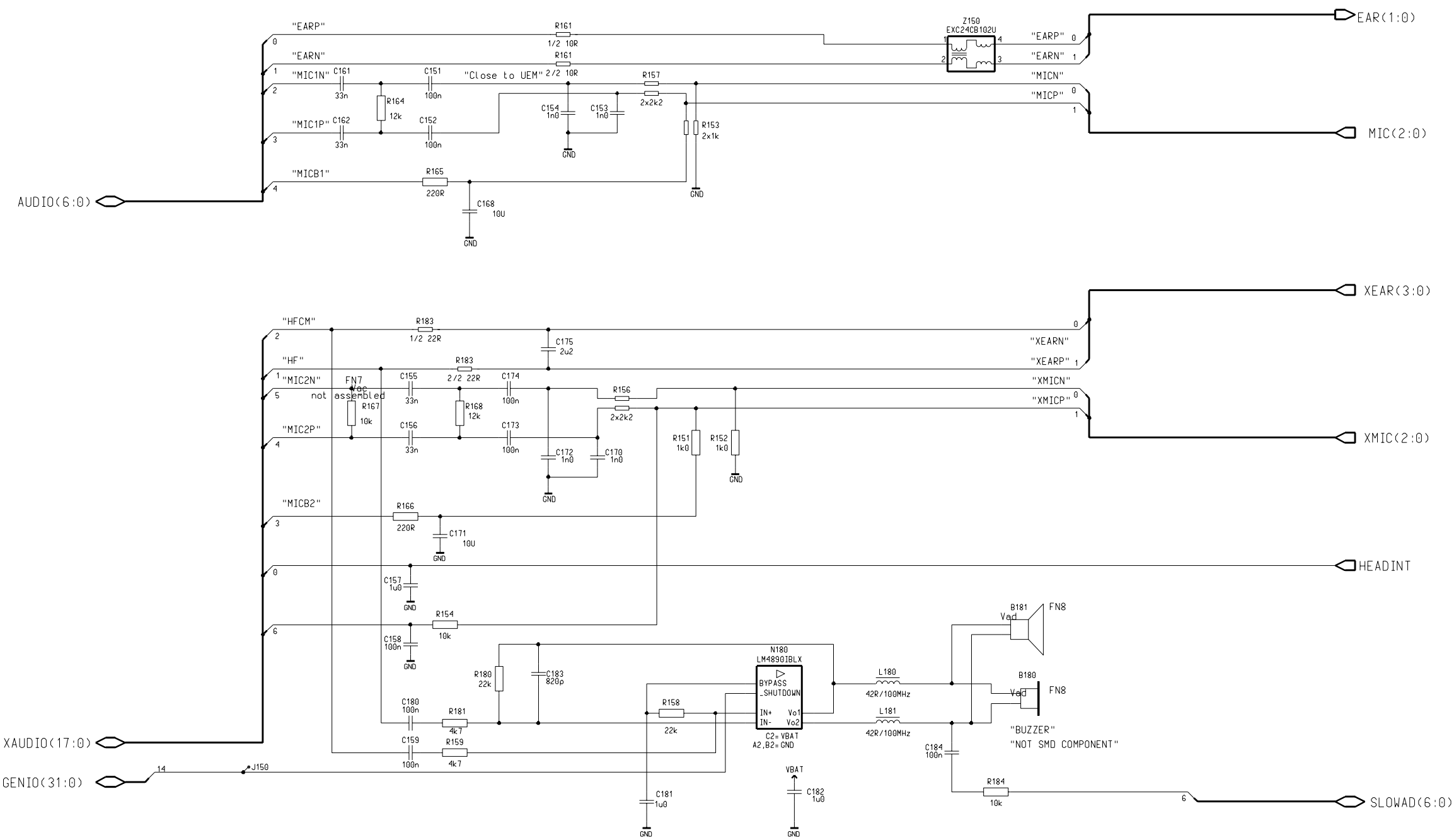
Shield Sheet for FDK VCO



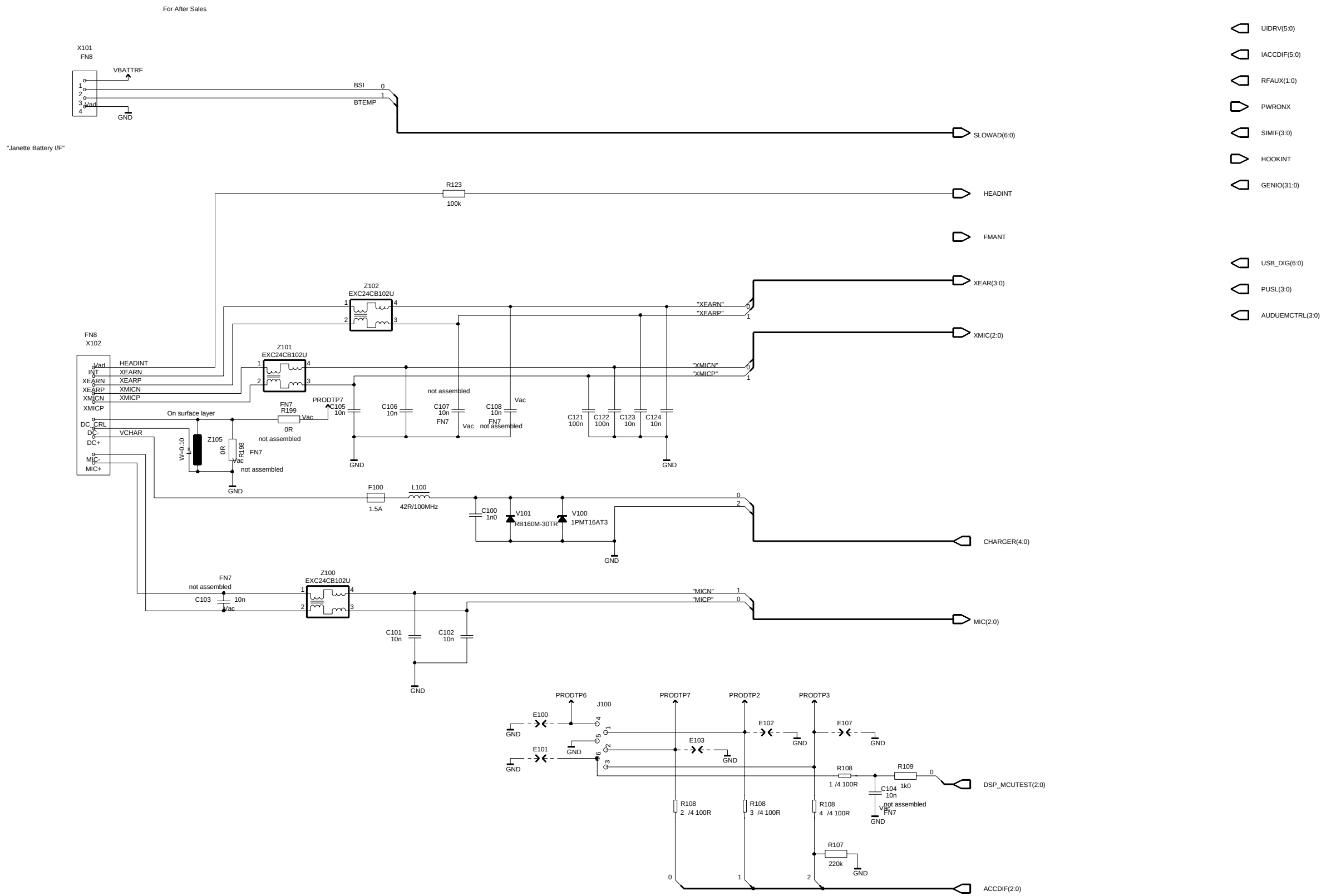
DCT4 Common Baseband Schematic (Top Level)



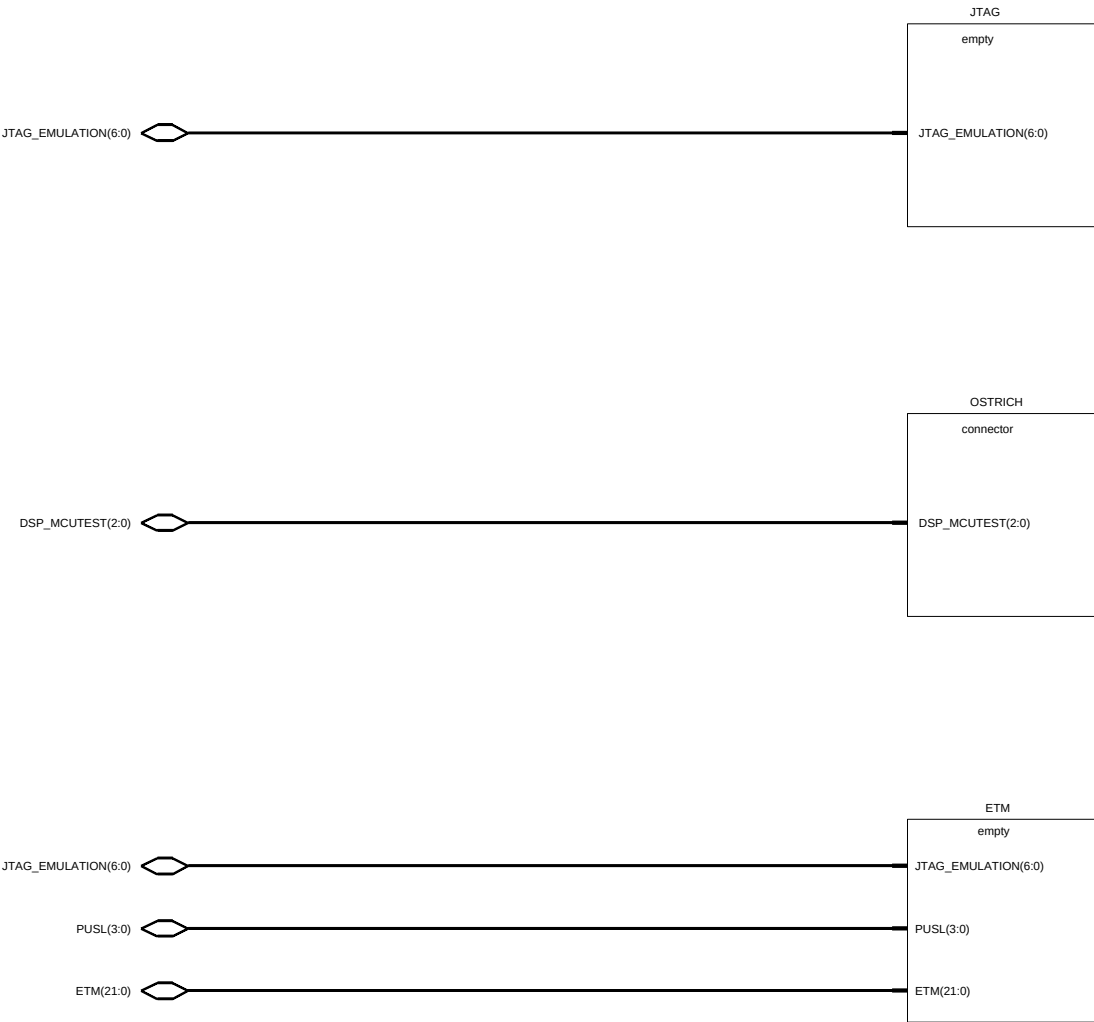
Audio Interface



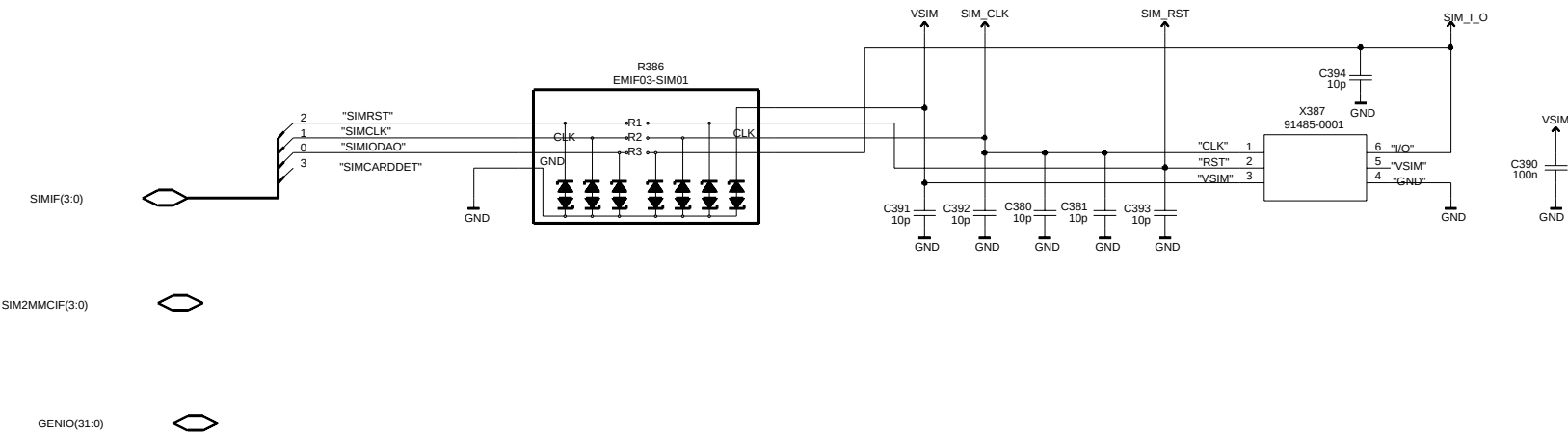
System Connector



Test and Emulator Interface



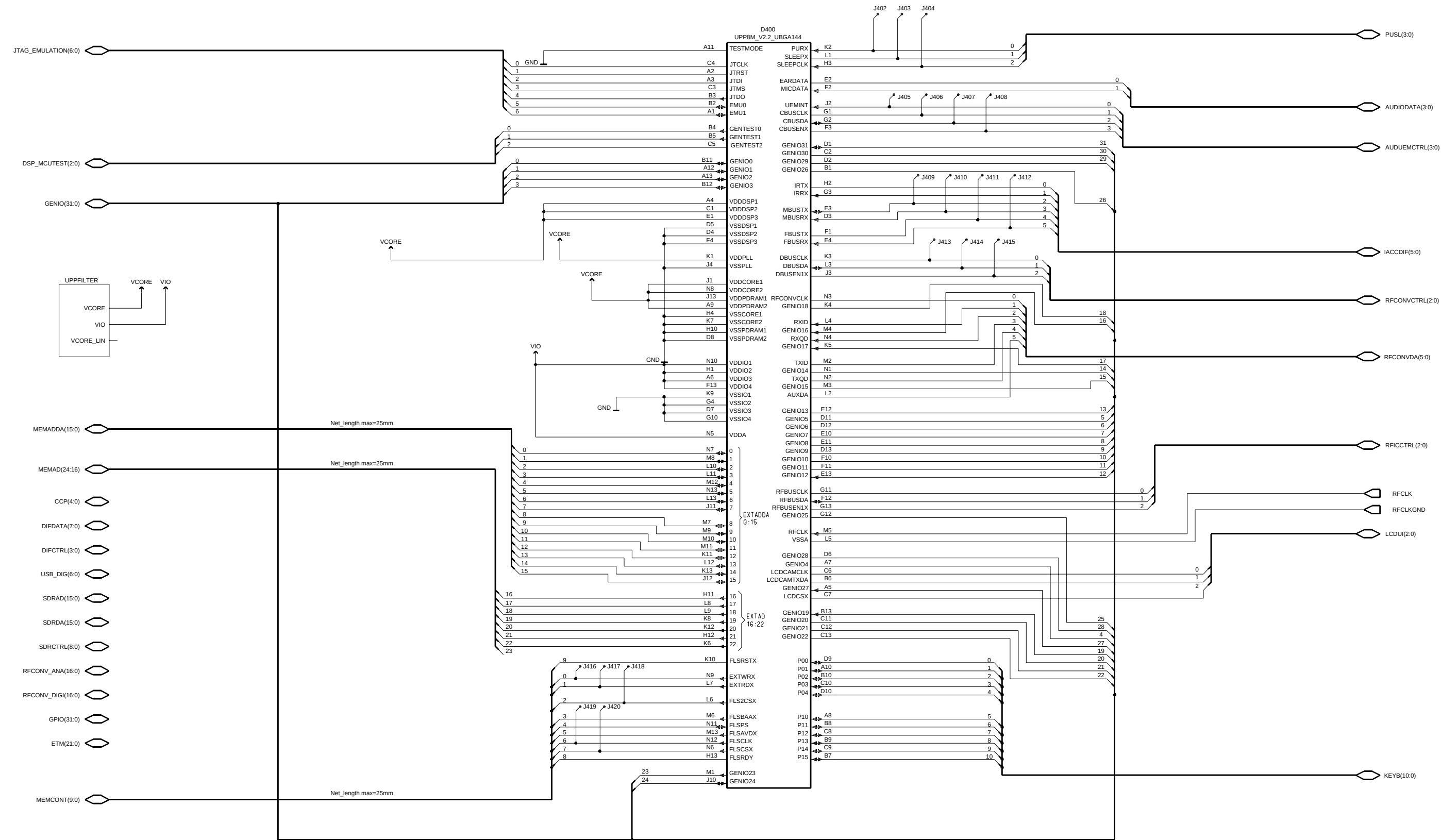
SIM Reader



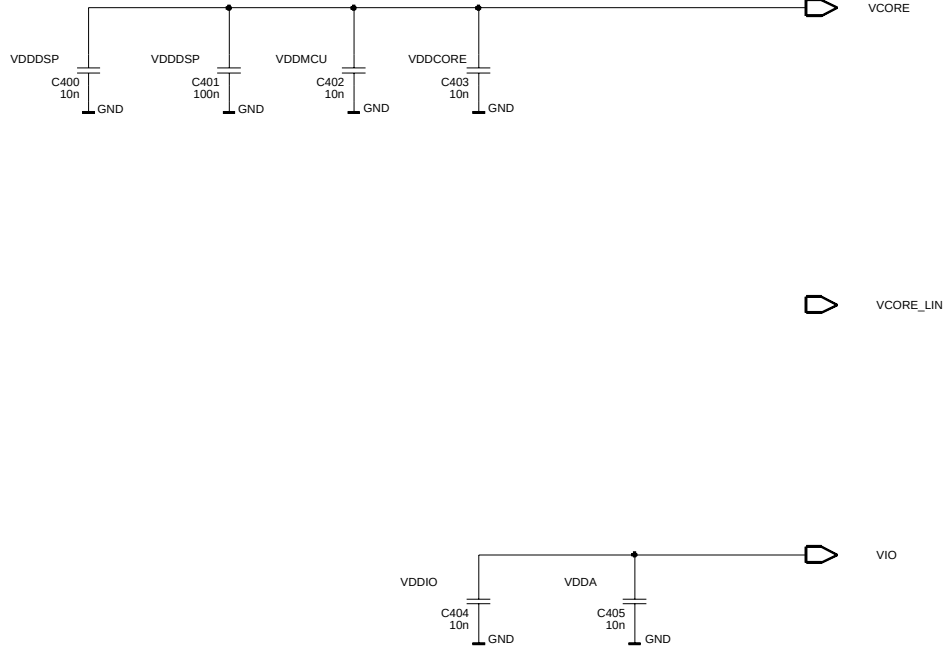
The schematic diagram illustrates the hardware for the Enigma V300, a custom electronic device. The components are interconnected to form a functional system. The BOM table provides a list of the components used, including their values and quantities.

Component	Value	Quantity
R301	10k	1
R302	1u0	1
R303	100n	1
R304	100n	1
R305	1k0	1
R306	1k0	1
R307	10n	1
R308	10R	1
R309	10R	1
R310	1k0	1
R311	10R	1
R312	100R	1
R313	100R	1
R314	100R	1
R315	100k	1
R316	10k	1
R317	4k7	1
C301	1u0	1
C302	1u0	1
C303	100n	1
C304	100n	1
C305	1u0	1
C306	1u0	1
C307	10n	1
C308	27p	1
C309	1u0	1
C310	10n	1
C311	1u0	1
C312	1u0	1
C313	1u0	1
C314	1u0	1
C315	1u0	1
C316	1u0	1
C317	1u0	1
C318	1u0	1
C319	1u0	1
C320	1u0	1
C321	1u0	1
C322	1u0	1
C323	1u0	1
C324	1u0	1
C325	1u0	1
C326	1u0	1
C327	1u0	1
C328	1u0	1
C329	1u0	1
C330	1u0	1
C331	1u0	1
C332	1u0	1
C333	1u0	1
C334	1u0	1
C335	1u0	1
C336	1u0	1
C337	1u0	1
C338	1u0	1
C339	1u0	1
C340	1u0	1
C341	1u0	1
C342	1u0	1
C343	1u0	1
C344	1u0	1
C345	1u0	1
C346	1u0	1
C347	1u0	1
C348	1u0	1
C349	1u0	1
C350	1u0	1
C351	1u0	1
C352	1u0	1
C353	1u0	1
C354	1u0	1
C355	1u0	1
C356	1u0	1
C357	1u0	1
C358	1u0	1
C359	1u0	1
C360	1u0	1
C361	1u0	1
C362	1u0	1
C363	1u0	1
C364	1u0	1
C365	1u0	1
C366	1u0	1
C367	1u0	1
C368	1u0	1
C369	1u0	1
C370	1u0	1
C371	1u0	1
C372	1u0	1
C373	1u0	1
C374	1u0	1
C375	1u0	1
C376	1u0	1
C377	1u0	1
C378	1u0	1
C379	1u0	1
C380	1u0	1
C381	1u0	1
C382	1u0	1
C383	1u0	1
C384	1u0	1
C385	1u0	1
C386	1u0	1
C387	1u0	1
C388	1u0	1
C389	1u0	1
C390	1u0	1
C391	1u0	1
C392	1u0	1
C393	1u0	1
C394	1u0	1
C395	1u0	1
C396	1u0	1
C397	1u0	1
C398	1u0	1
C399	1u0	1
C400	1u0	1
C401	1u0	1
C402	1u0	1
C403	1u0	1</

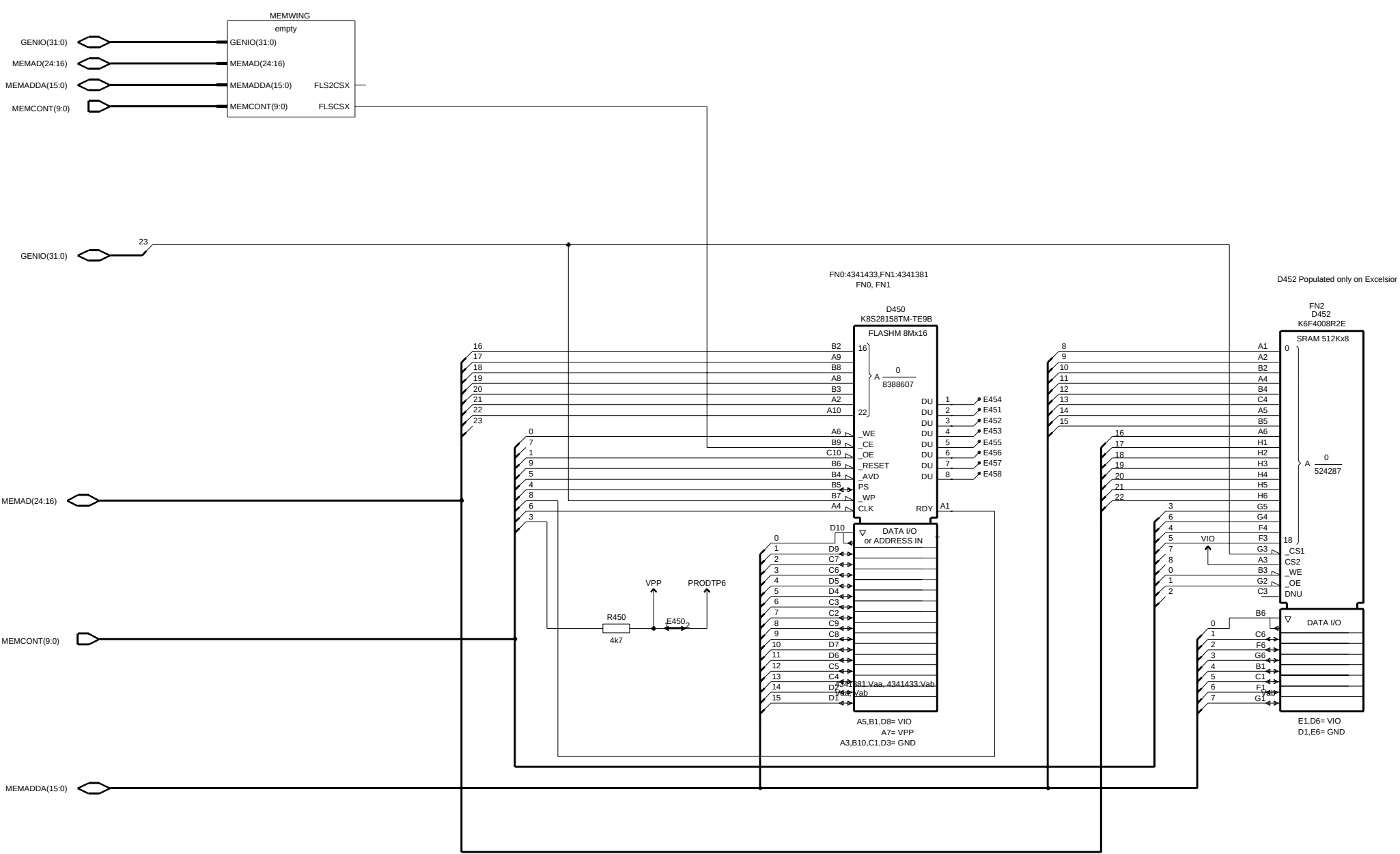
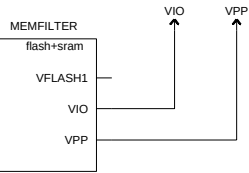
UPP 8M Implementation



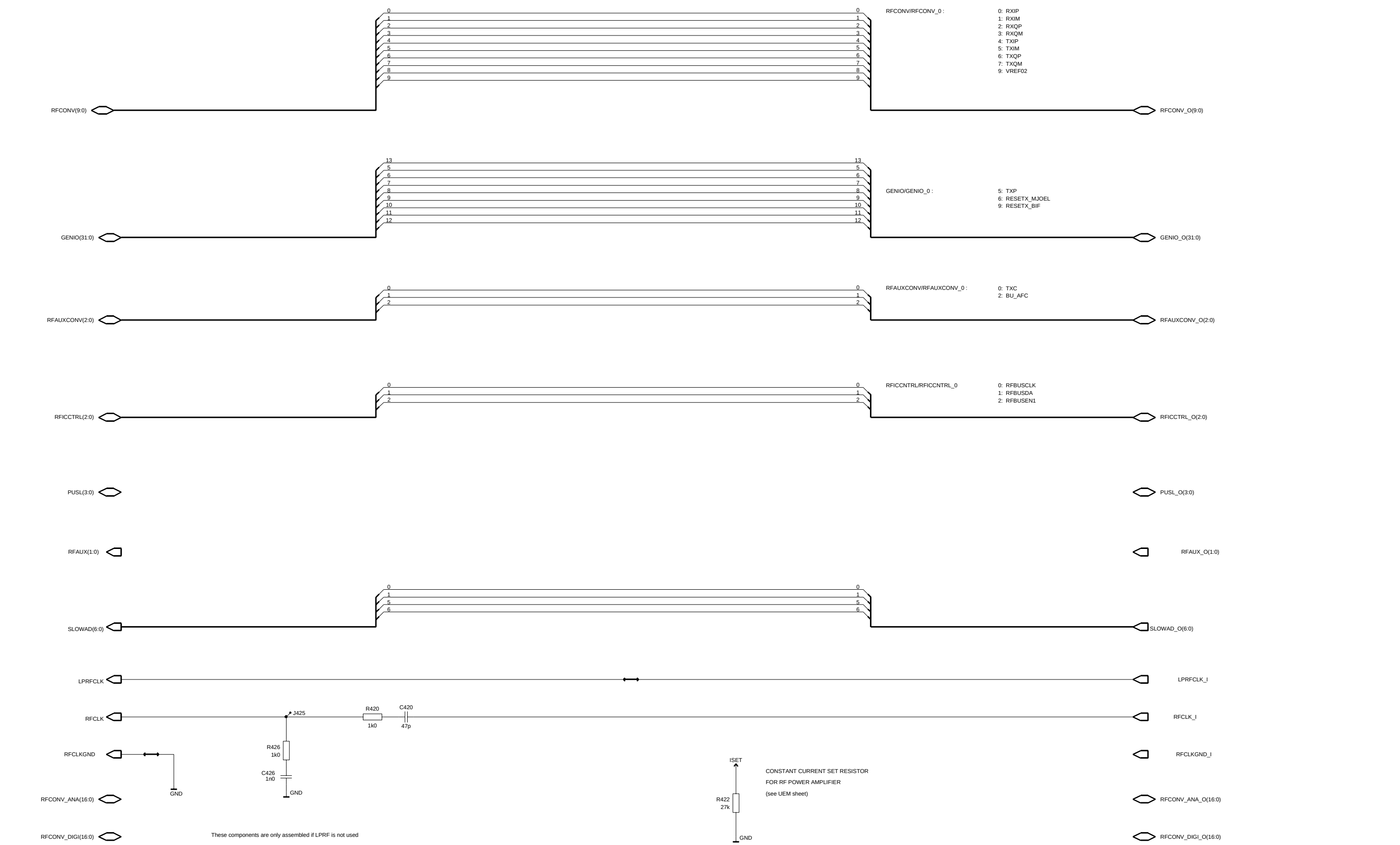
Discrete decoupling capacitors for UPP



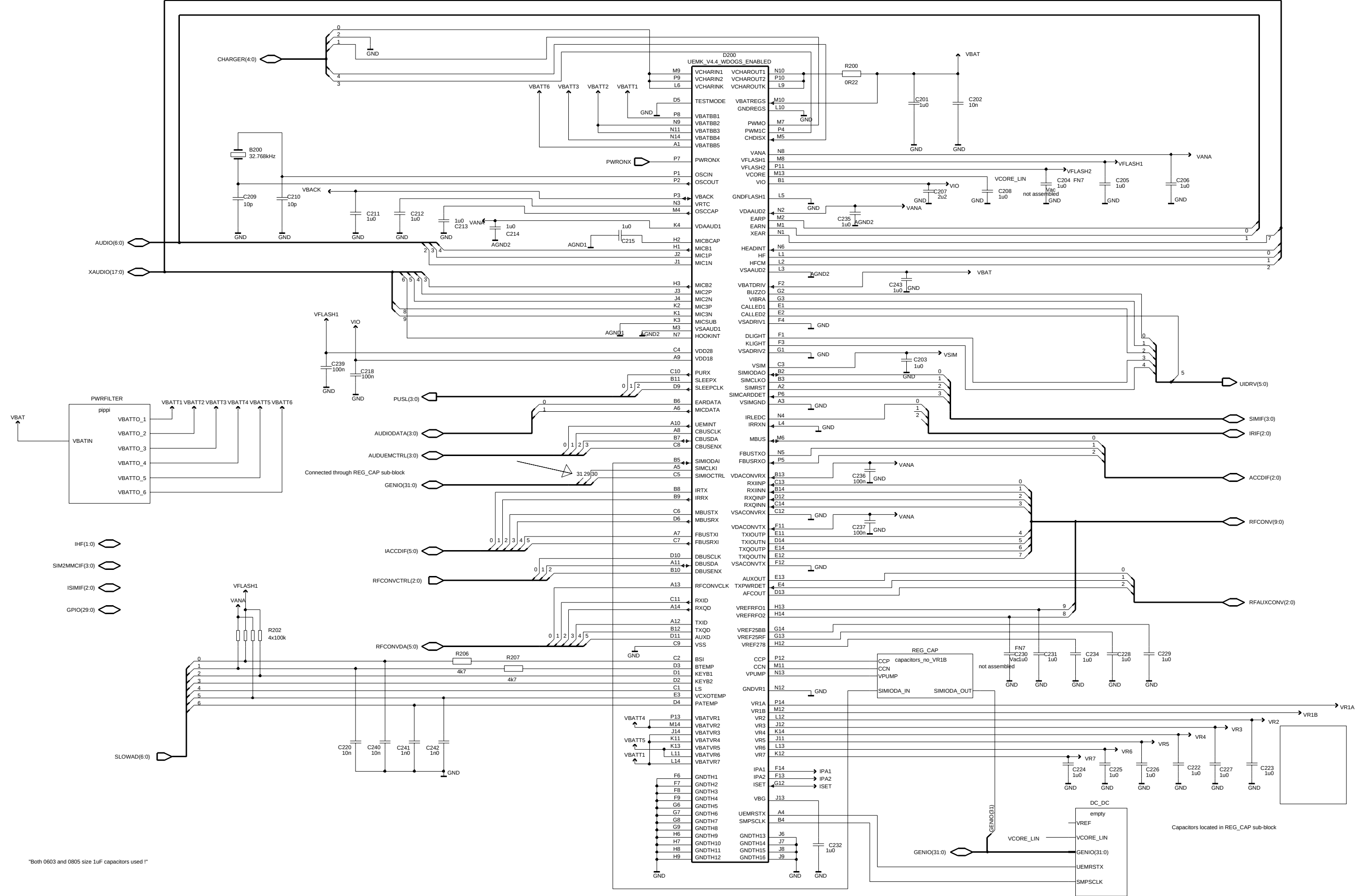
Intel 128 Mbit + 4 Mbit SRAM



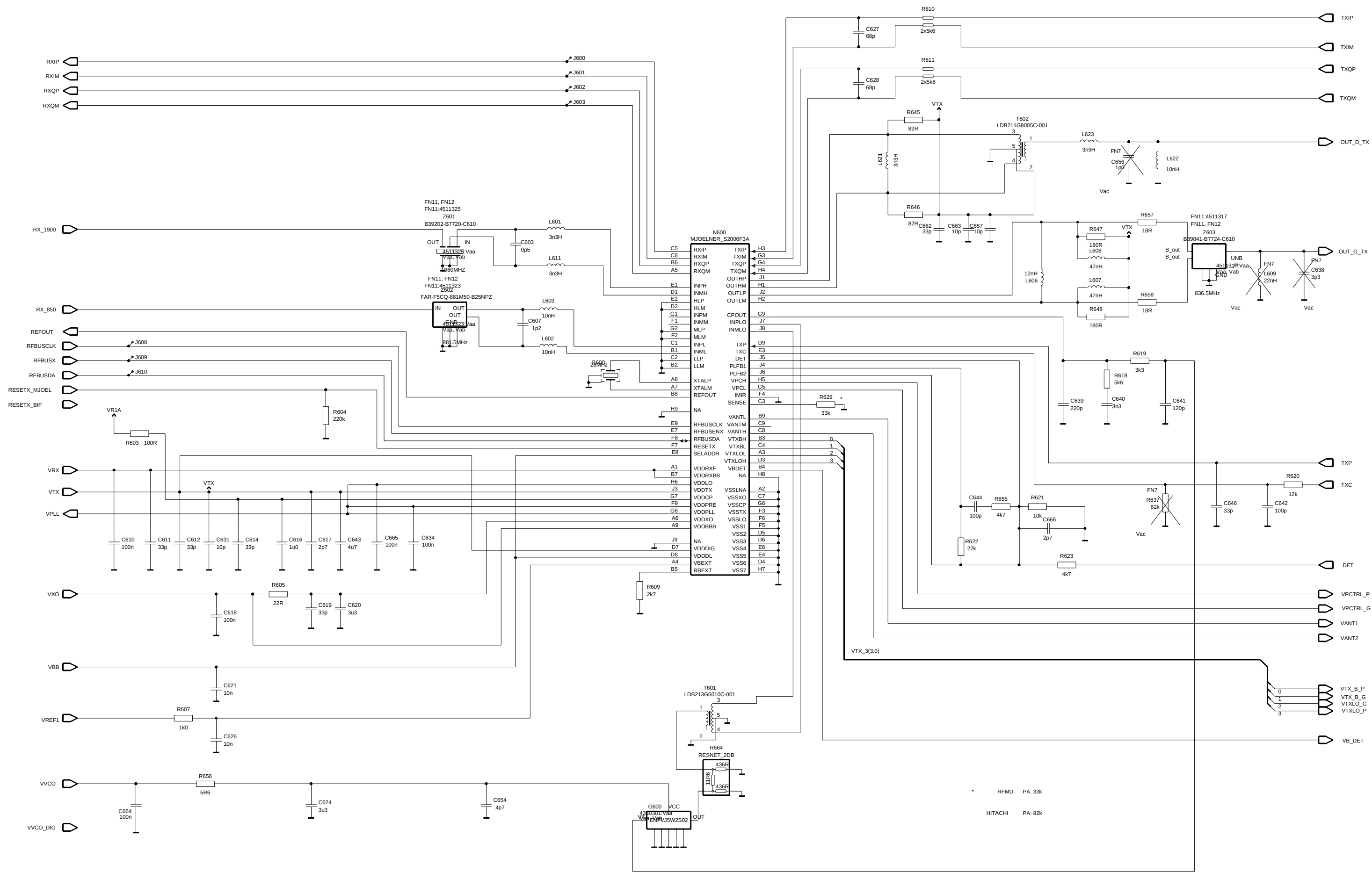
RF/BB Clark Sheet



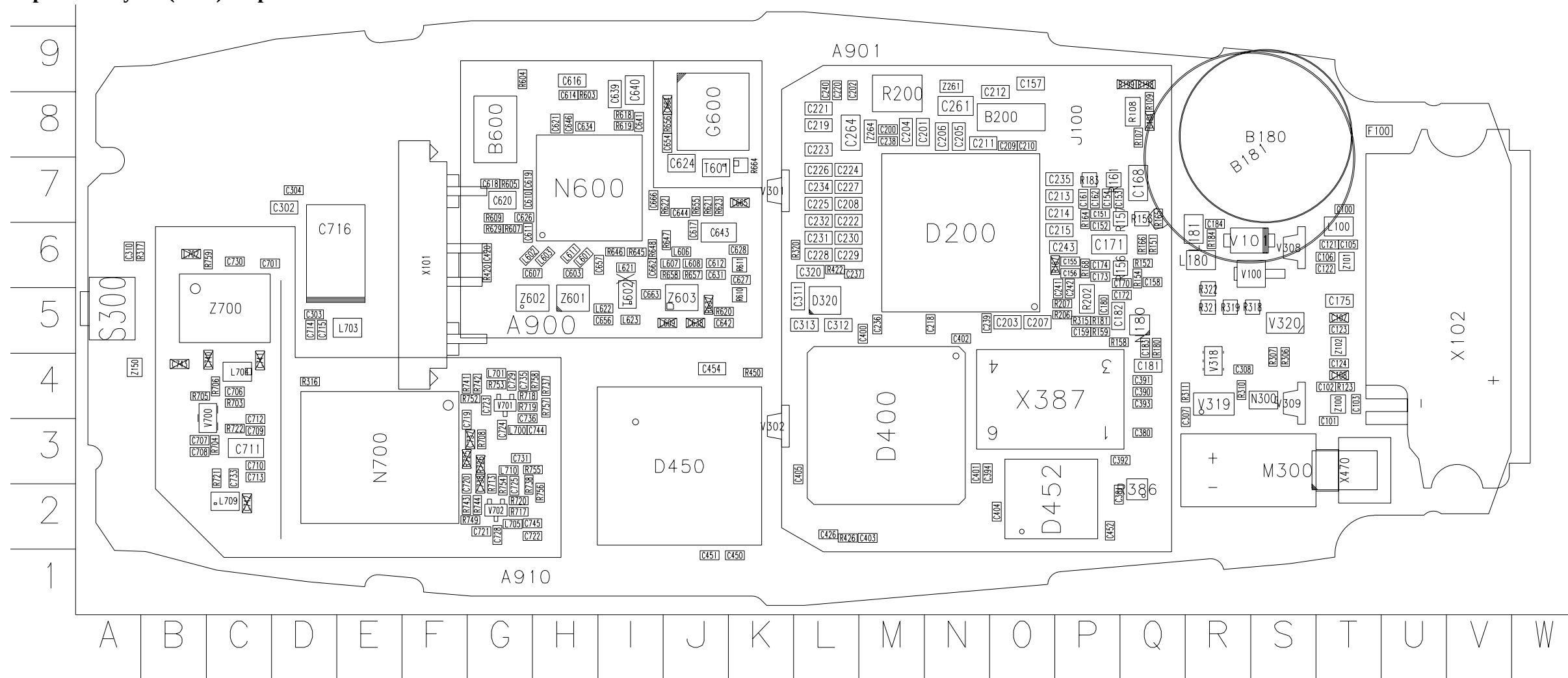
Discrete Power Management



Mjoelner



Component Layout (3595)- Top



Component Layout (3595) - Bottom

