

Compal Confidential

KIU10-Tiger M/B Schematics Document

2009-01-13

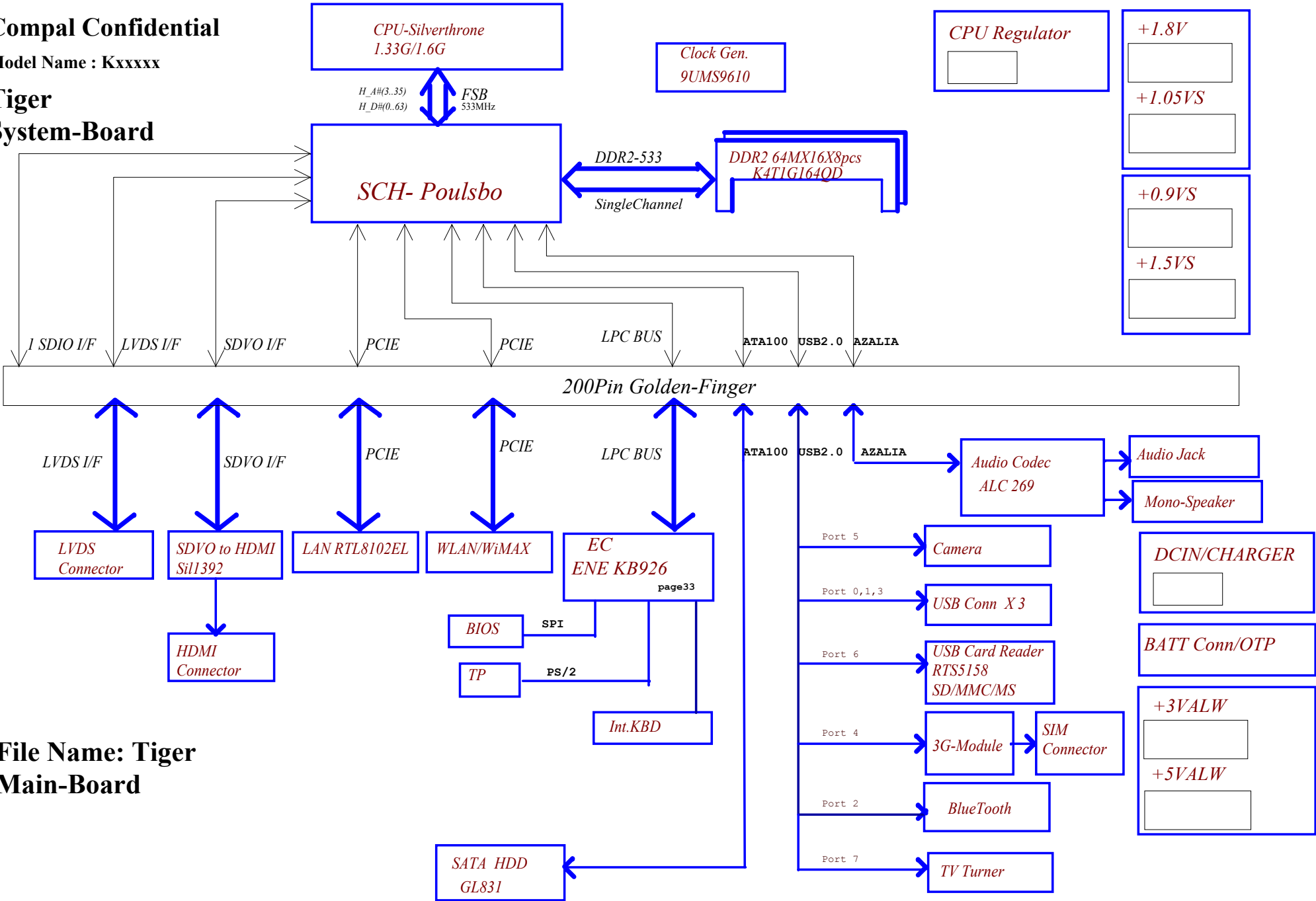
REV:1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date		Deciphered Date		Title	Cover Sheet
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				Date:	Rev 1.0
				Sheet 1 of 22	

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Model Name : Kxxxxx

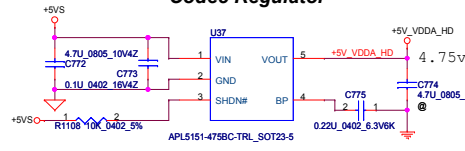
Tiger
System-Board



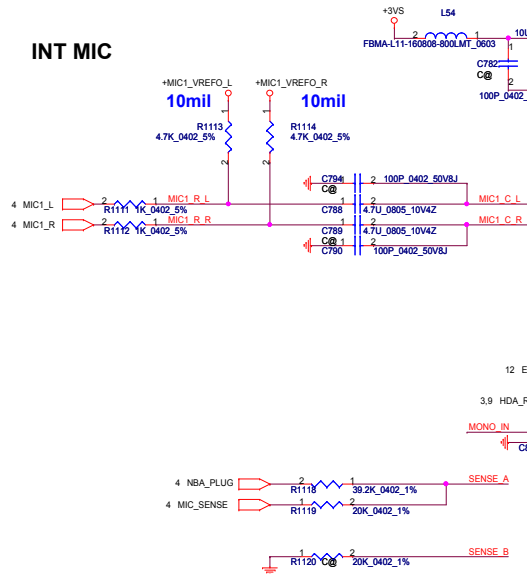
File Name: Tiger
Main-Board

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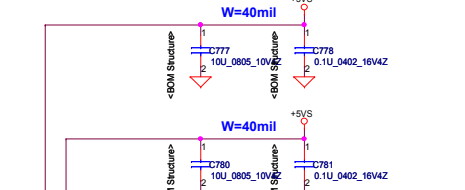
Codec Regulator



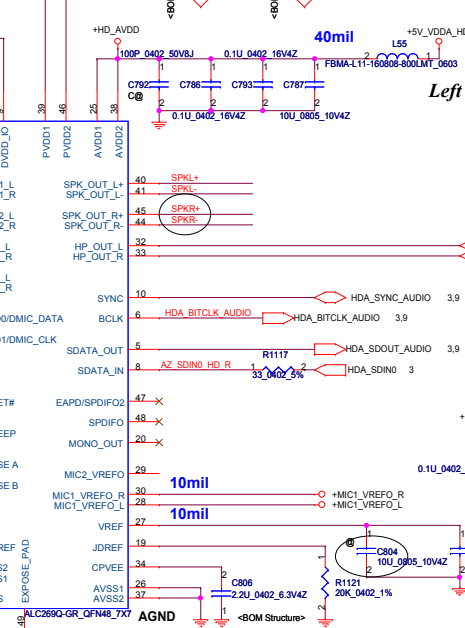
INT MIC



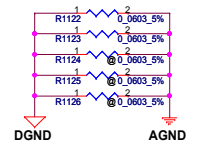
Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 32, 33)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
SENSE B	5.1K	PORT-D (PIN 48)
	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 20)
	5.1K	PORT-H (PIN 47)



Left Main Speaker Connector

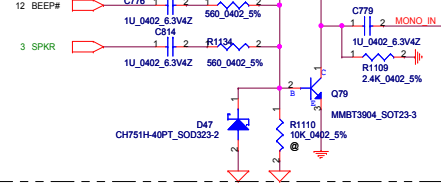


DGND To AGND Bypass

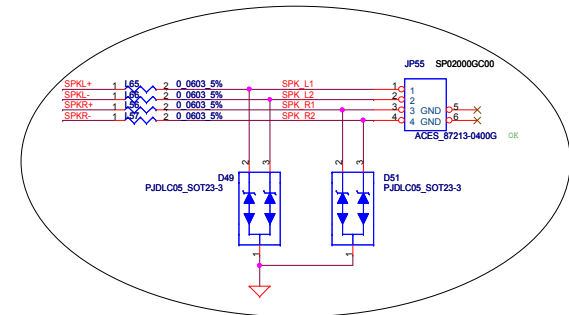


Beep Circuit

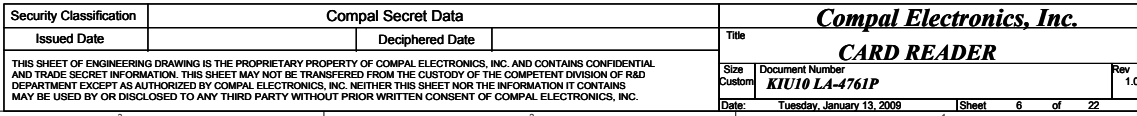
EC Beep



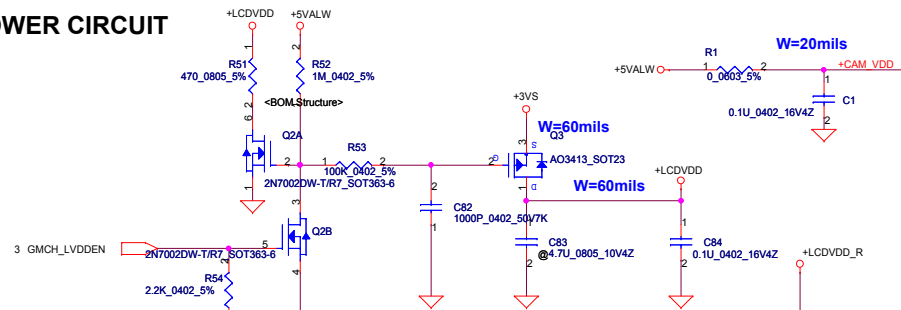
Speaker Conn.



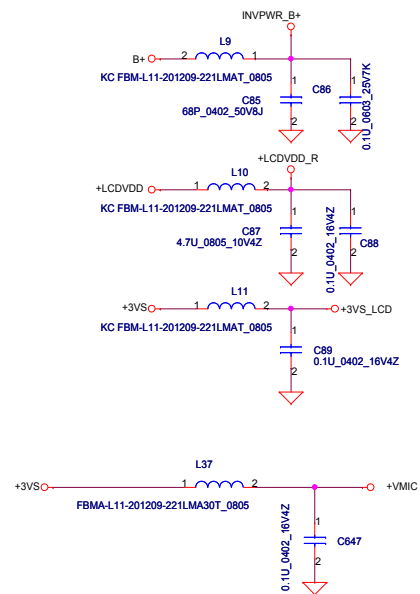
Compal Electronics, Inc.



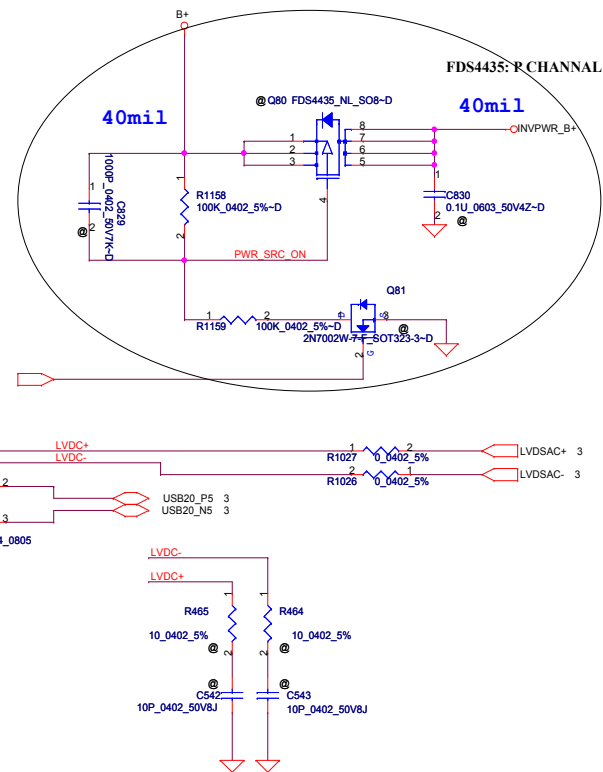
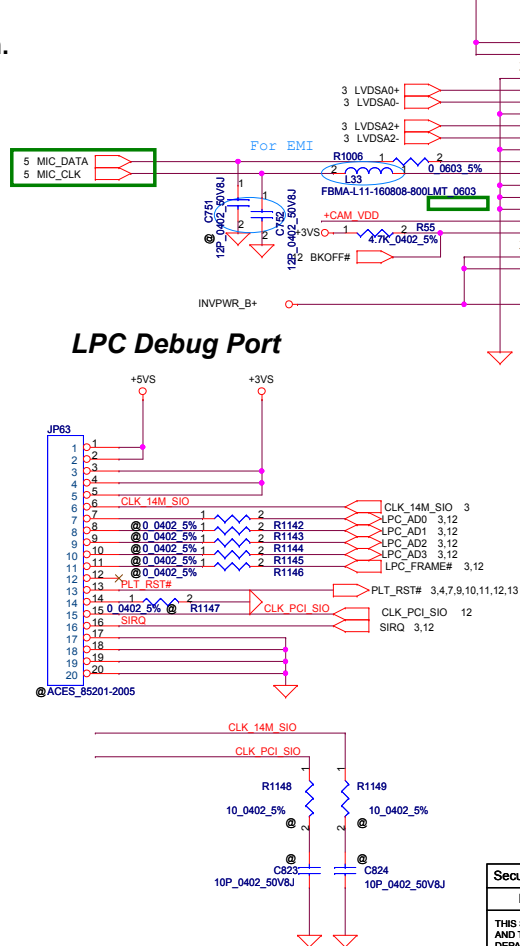
LCD POWER CIRCUIT



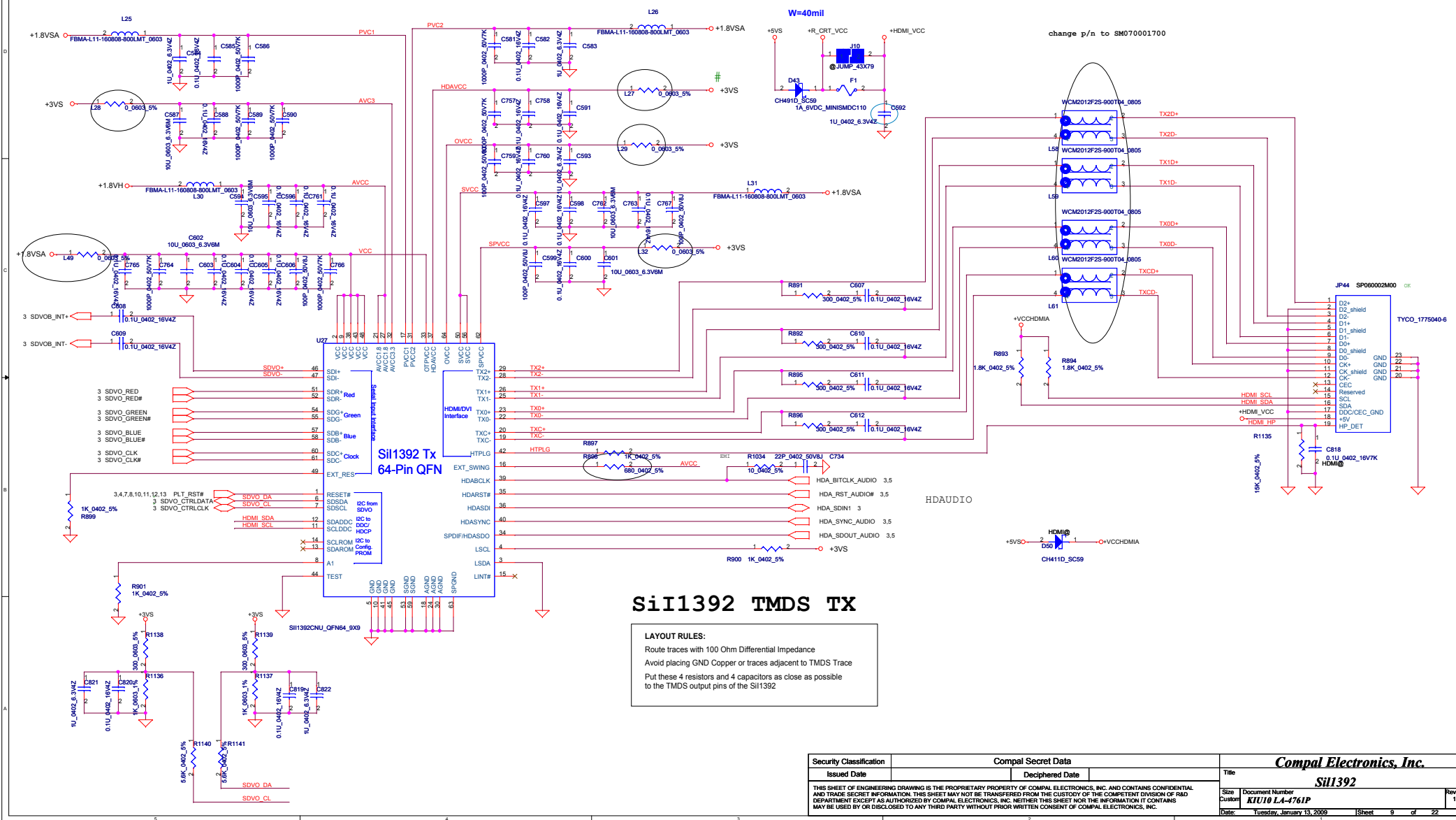
LCD/PANEL BD. Conn.

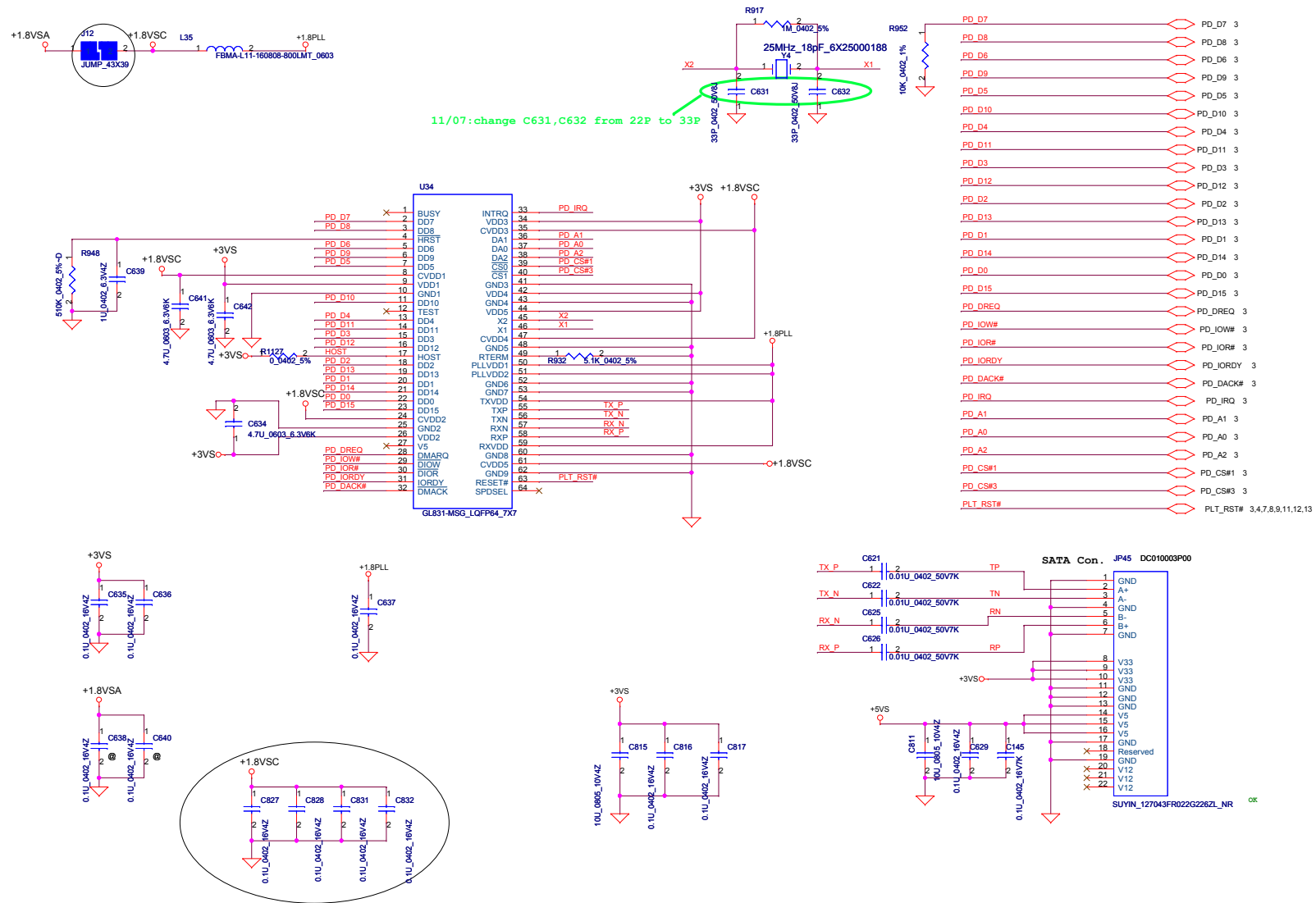


LPC Debug Port



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				Custom	
				Date	22

Compal Electronics, Inc.

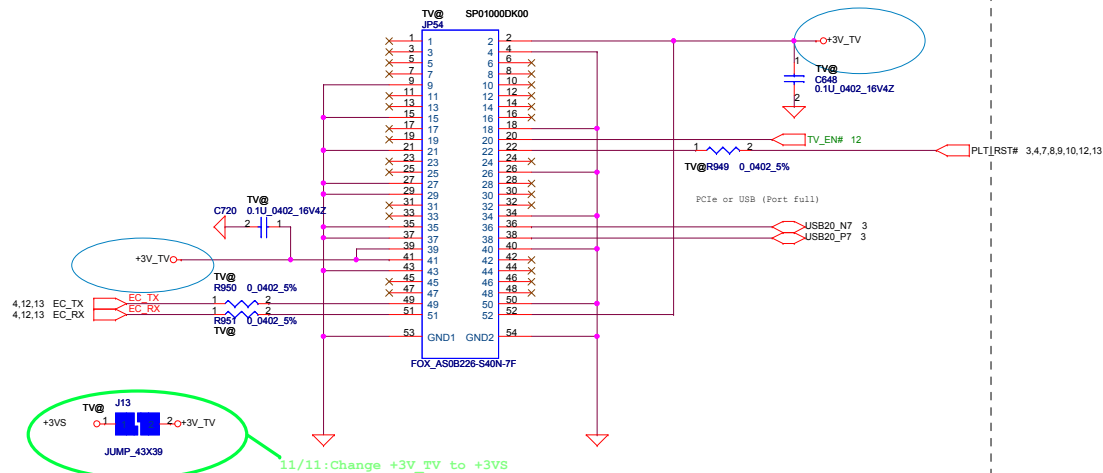
PATA/SATA

Document Number

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TV Turner

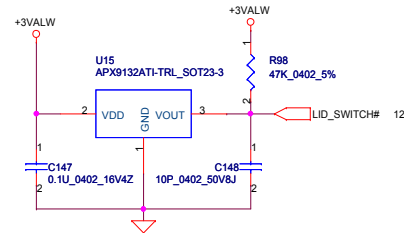


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				1.0
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MB Power On/Off SW

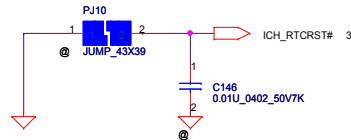
Lid SW

Change to hall senser



RTC Reset SW

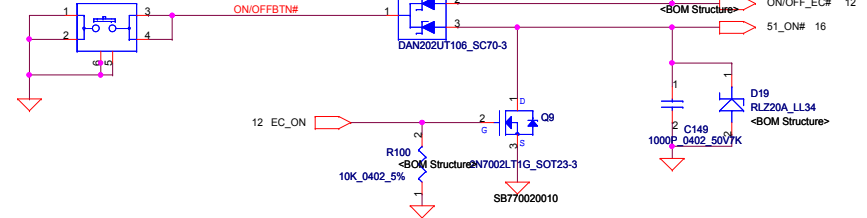
(Place under door)



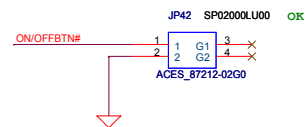
PUT ON TOP SIDE

TJG-533-V-T/R_6P

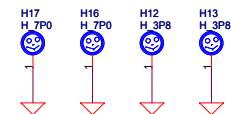
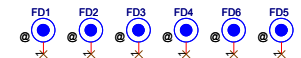
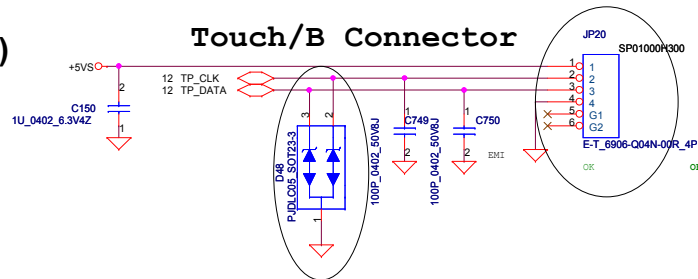
@SW2



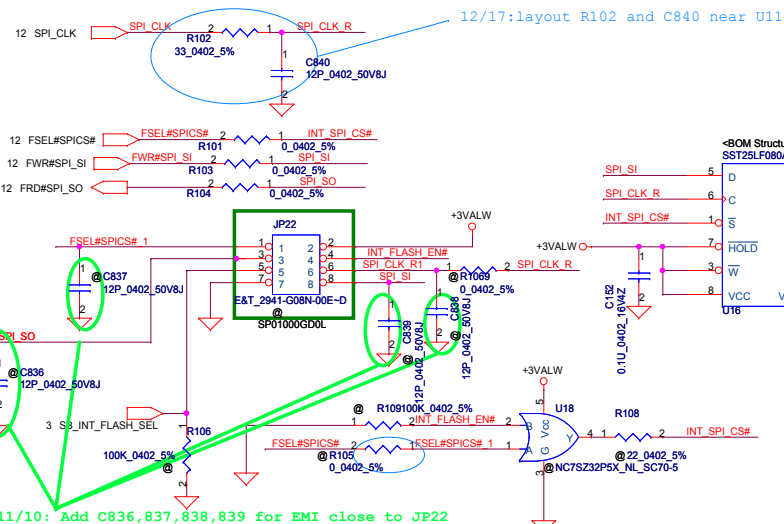
POWER/B CONN.(to DB3)



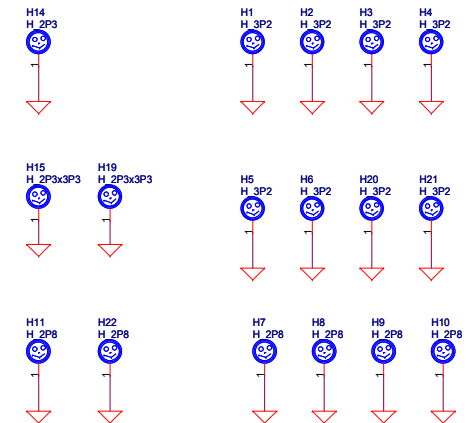
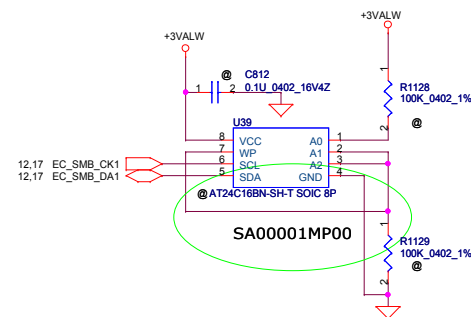
Touch/B Connector



System SPI Flash ROM (8Mb)

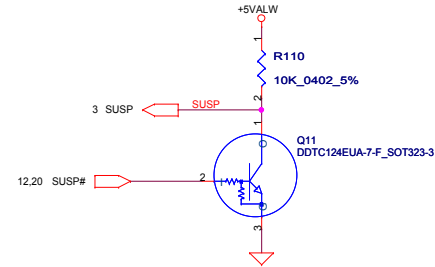
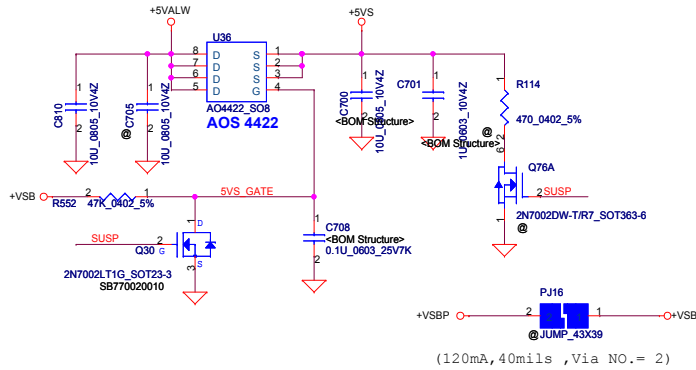


EC EEPROM

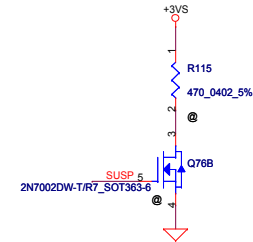
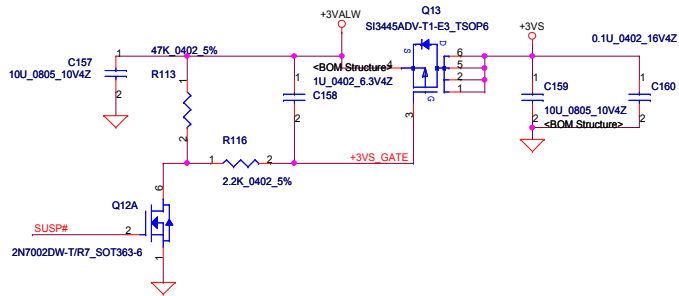


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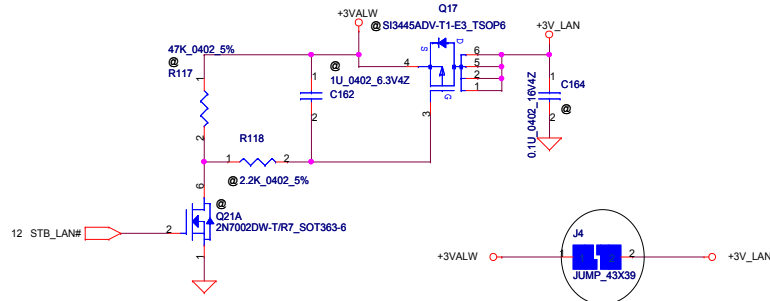
+5VALW TO +5VS



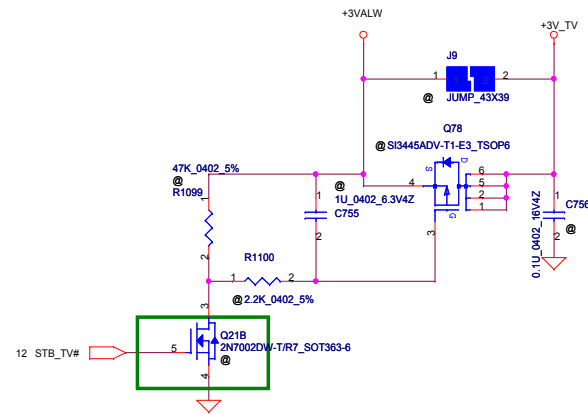
+3VALW to +3VS Transfer



+3VALW to +3V_LAN



+3VALW to +3V_TV



PRODUCT SUMMARY

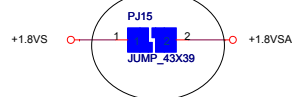
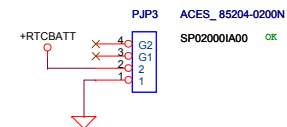
Vgs	rDS(on)	ID (A)
-8	0.042 @ VGS = .4.5 V	+~5.6
	0.060 @ VGS = .2.5 V	+~4.7
	0.080 @ VGS = .1.8 V	+~2.9

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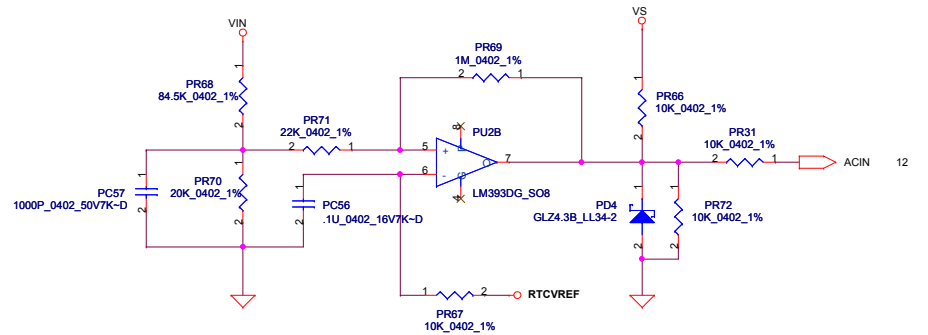
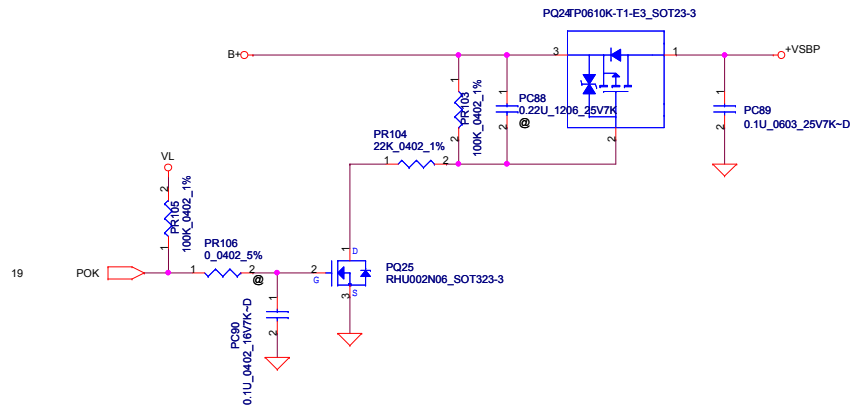
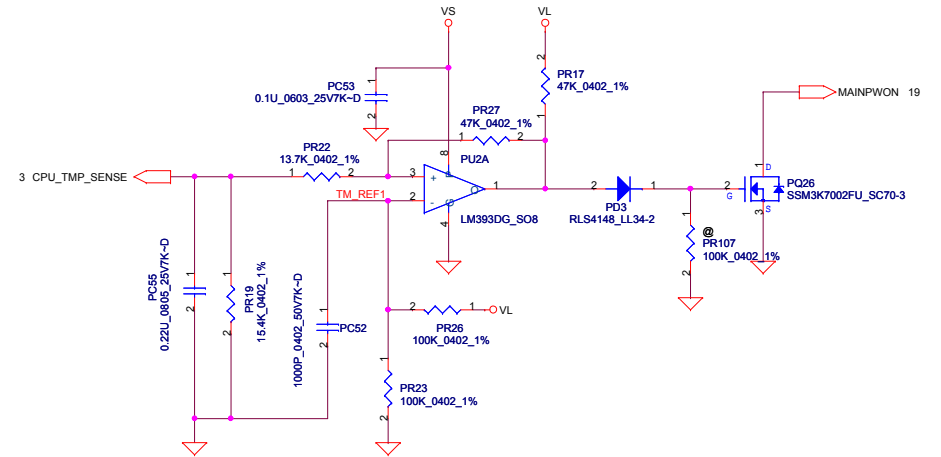
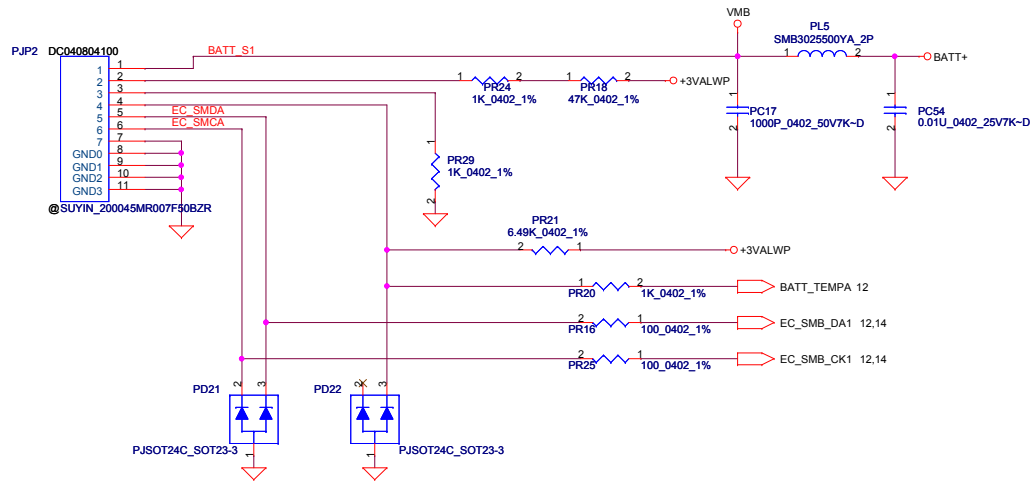
DC/DC Interface

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					KIU10 LA-4761P	0.3
				Date:	Tuesday, Jan. 3, 2009	Sheet 16 of 22

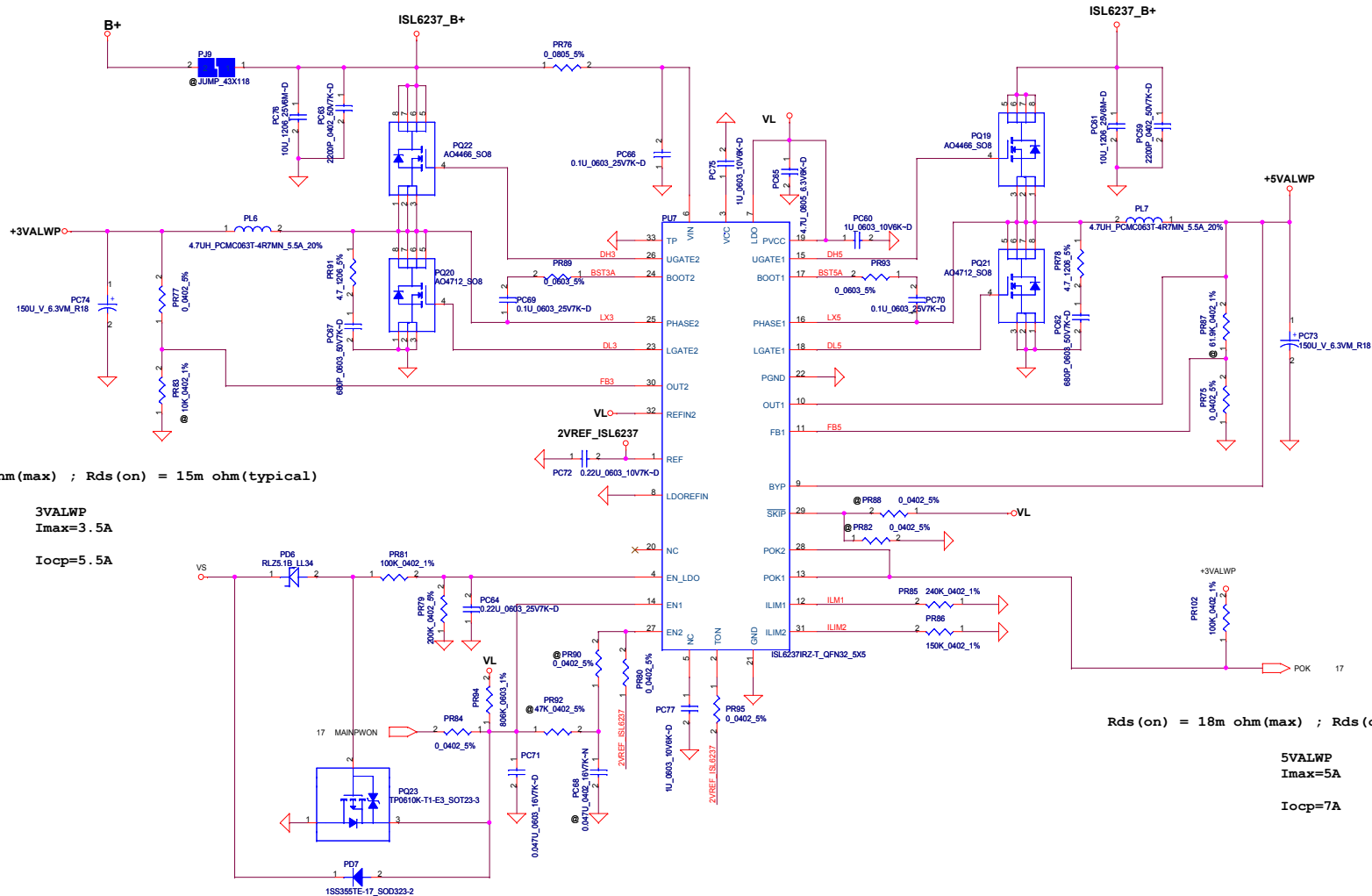
PH1 under CPU botten side :
 CPU thermal protection at 92 degree C
 Recovery at 56 degree C



Vin Detector

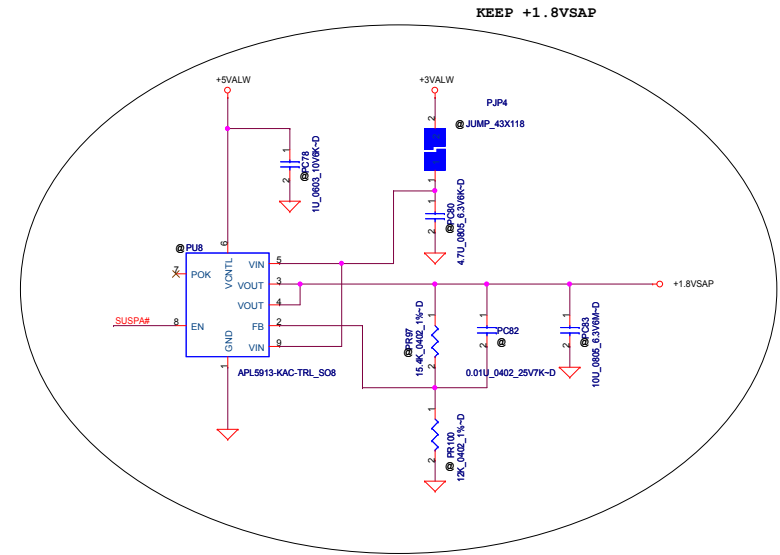
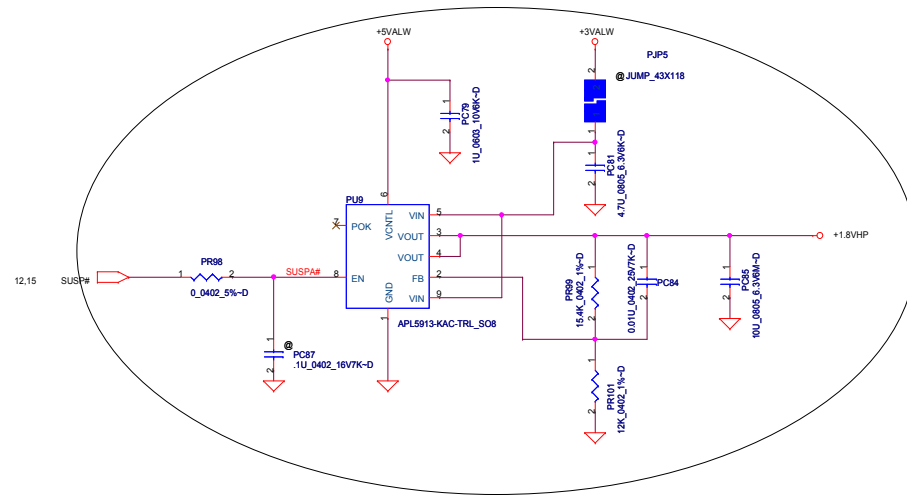
High 18.384 17.901 17.430
 Low 17.728 17.257 16.976

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$R_{ds(on)} = 18m\ ohm(max) ; R_{ds(on)} = 15m\ ohm(typical)$

$R_{ds(on)} = 18m\ ohm(max) ; R_{ds(on)} = 15m\ ohm(typical)$



	5		4		3		2		1
KIU10 LA-4761P SCHMATIC CHANGE LIST									
REVISION CHANGE : 0.1 TO 0.2									
NO	DATE	PAGE	MODIFICATION LIST	PURPOSE					
1	0907	03	del C729,C730,C727,C728	MODIFY HDMI CLK.					
2	0907	03	J5,J6,J11 del bom structure	default					
3	0907	06	RT5158E's pin11 and Pin9 should be isolate,define pin23,pin26	vendor suggestion					
4	0907	05	modify R1113,R1114,ADD L65 L66 D51,	vendor suggestion					
5	0918	03	add LPC debug port	vendor require					
6	0918	15	modify Q30 P/N as same Q8	for same P/N					
7	0918	12	del R458,C539,R1008,add R1150,R1151,R1152	Correct BOM					
8	0918	06	add bom structure @ R1032,C731	Correct BOM					
9	0918	14	add bom structure @ U39,R1128,R1129	Correct BOM					
10	0918	18	adjust PR52,PR53,PC43	Power team require					
11	0918	20	add bom structure @ page 20	Power team require					
12	0918	06	R1030,R1031 change to 0 ohm,R460,R466 change to 33 ohm,R454,R438 change to 0603 package,	correct schematic					
13	0918	06	Add one 0.1uF(C825)and 1uF(C826,0603) on JP59 pin 10 for +USB_VCCC	correct schematic					
14	0918	06	change p/n to SM070001700	correct schematic					
15	0918	15	del J4 bom structure	correct schematic					
16	0918	16	del PJ15 bom structure	correct schematic					
17	0919	06	u25 short pin8 and pin11/33 together	correct schematic					
18	0919	13	u12 pin3 connector to pin 4	vendor require					
19	0922	05	add bom structure @ C804	noise issue when s3 resume					
20	0922	13	CHANGE C134,C135,C138 FROM +USB_VCCA TO +USB_VCCC	FOR PORT 3 USB VOLTAGE					
21	0923	16	merge +1.8VSA ,+1.8VH	correct schematic					
22	0924	10	add +1.8vsc Voltage	correct schematic					
23	0924	05	SET MIC2 BOM @	me require					
24	0924	14	change JP20 pin defin	correct schematic					
25	0924	08	add LED invert switch circuit	correct schematic					
26	0924	03	Add R1161 on JP3 pin105 for LPC_FRAME#	correct schematic					
27	0924	09	Change 0ohm location to L*	correct schematic					
28	0924	09	C809 move to page by 3	correct schematic					
29	0924	05	Del MIC1	correct schematic					
30	0924	12	add R1150 for CLK_PCI_LPC	correct schematic					
31	1107	04	change R573 from 120 ohm to 470 ohm	LED is too light					
32	1107	06	change C511, C515 from 6P to 22P	correct schematic					
33	1107	07	change C59, C60 from 27P to 33P	correct schematic					
34	1107	10	change C631, C632 from 22P to 33P	correct schematic					
35	1107	12	change C126, C127 from 15P to 27P	correct schematic					
36	1108	03	change R1130 from 0 ohm to 22 ohm	correct schematic					
37	1108	12	Remove R1033 and C733	correct schematic					
38	1109	07	Add Orange LED	correct schematic					
39	1109	12	Add pull high resistor to KS01 and KS02	Correct schematic					
40	1110	03	Add C834,C835 (12pF) for EMI	For EMI					
41	1110	14	Add C836,C837,C838,C839,C840 (12pF) for EMI	For EMI					
42	1110	12	Key board signal reverse	key board signal reverse					
43	1110	12	ADD R94,R96 pull high resistor to +5VS	Correct schematic					
44	1111	11	Add J13 to connect +3VS to +3V_TV	TV turner issue					
45	1111	11	JP19 signal reverse	Correct schematic					
46	1113	14	Remove R1067,R1068,R1069	Correct schematic					
47	1113	08	Add C751,C752 for EMI	For EMI					
48	1202	03	Remove R1009,R1132,C833	For Audio can not work issue					
49	1202	03	Change R1152 from 10K ohm to 1.5M ohm	For Audio can not work issue					
50	1202	14	reverse T/P CONN signal	Correct schematic					
51	1209	04	change DA1 and DA2 to +5V_ALW	Correct schematic					
52	1209	04	Add and gate to PLT_RST# to 3G function	TV turner issue					
53	1211	07	Add Bead for EMI on +3V_LAN to LAN connect	For EMI					
54	1212	09	change C592 from 0.1U to 1U	Correct schematic					
55	1212	11	change JP54 power from +3VS to +3V_TV	Correct schematic					
56	1215	04	change R573 from 470 to 820	LED too light issue					
57	1216	14	Add R105(0 Ohm) to cut off FBSEL#SPIC# to JP22&U18	Correct schematic					
58	1217	08	Change R1007(0Ohm) to L33(FBMA-L11-160808-8001MT)	For EMI					
59	1217	03	change U8 circuit design	For EMI					
60	0102	03	Remove U8 circuit design and Add R1153	Correct schematic					