

Compal Confidential

Schematic Document

Rev: 1.0

2009.09.10

Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Cover Sheet	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-5541P	1.0
Date:		Thursday, September 10, 2009		Sheet	1 of 43

Model Name : NAT20



Security Classification		Compal Secret Data		Compal Electronics, Inc.			
Issued Date		2009/04/01	Deciphered Date	2010/12/31	Title	Block Diagram	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE DEPARTMENT EXCIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					Doc. No.	Document Number	Rev
						LA-5541P	1.0
					Date:	Thursday, September 10, 2009	Sheet 2 of 43

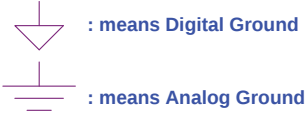
Board ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra	100K +/- 5%			
Board ID	Rb	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	18K +/- 5%	0.436 V	0.503 V	0.538 V
2	33K +/- 5%	0.712 V	0.819 V	0.875 V
3	56K +/- 5%	1.036 V	1.185 V	1.264 V
4	100K +/- 5%	1.453 V	1.650 V	1.759 V
5	200K +/- 5%	1.935 V	2.200 V	2.341 V
6	NC	2.500 V	3.300 V	3.300 V
7				

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	0.4
4	
5	
6	
7	

Symbol Note :



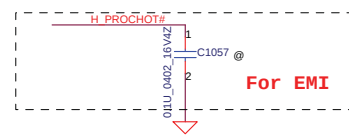
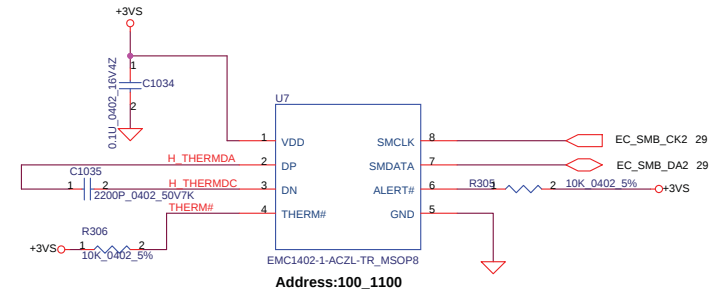
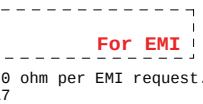
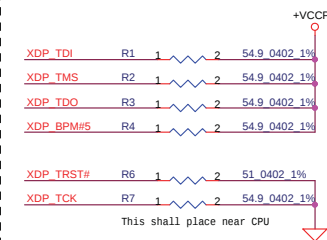
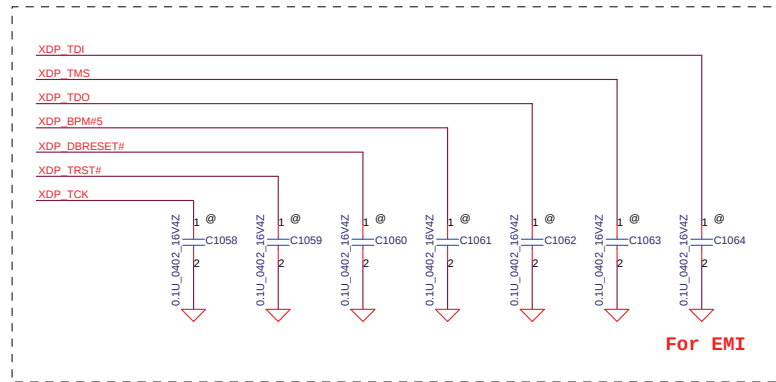
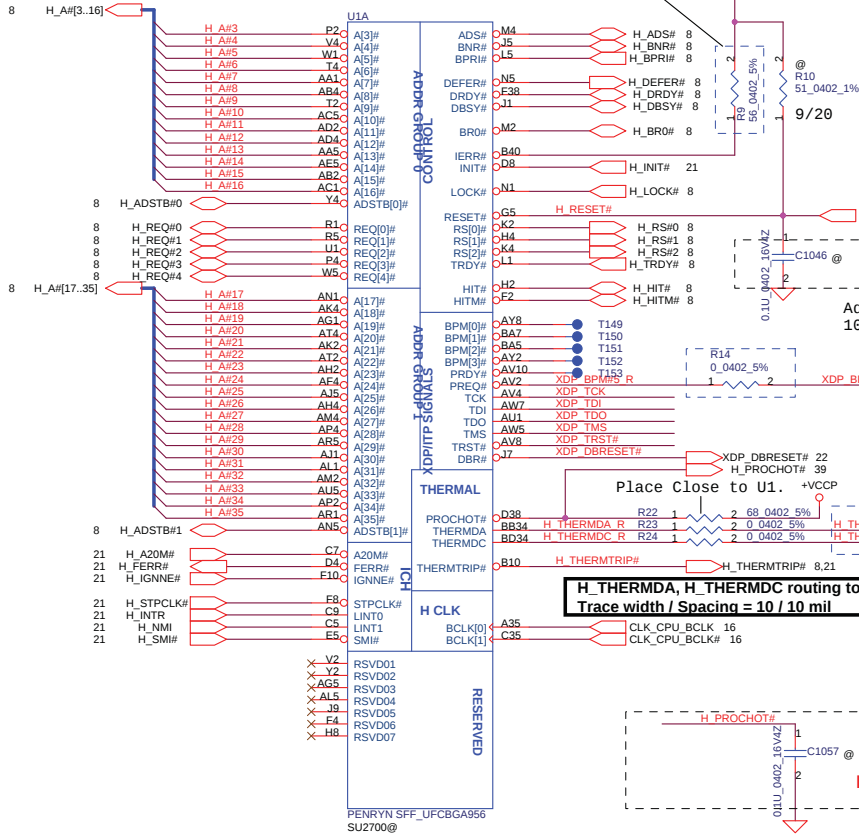
SMBUS Control Table

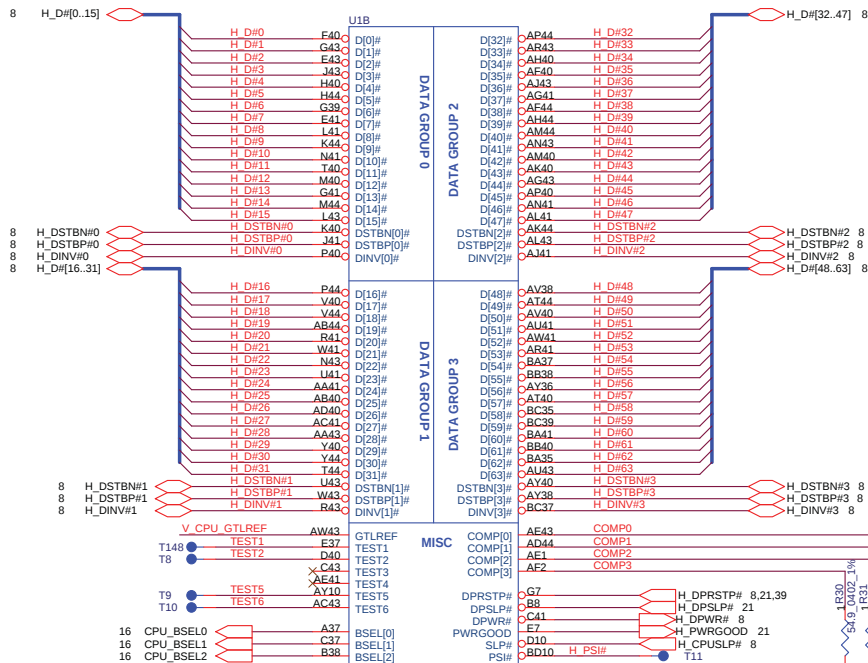
	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM
EC_SMB_CK1 EC_SMB_DA1	KB926	X	V	X	X	X
EC_SMB_CK2 EC_SMB_DA2	KB926	X	X	X	V	X
ICH_SMBCLK ICH_SMBDATA	ICH9	X	X	X	X	V
MCP_SMB_CLK MCP_SMB_DATA	ICH9	X	X	X	X	X

MINI CARD 1 RESERVED +3VALW TO PULL HIGH

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	10100000
DDR SO-DIMM 1	A4	10100100
CLOCK GENERATOR (EXT.)	D2	11010010



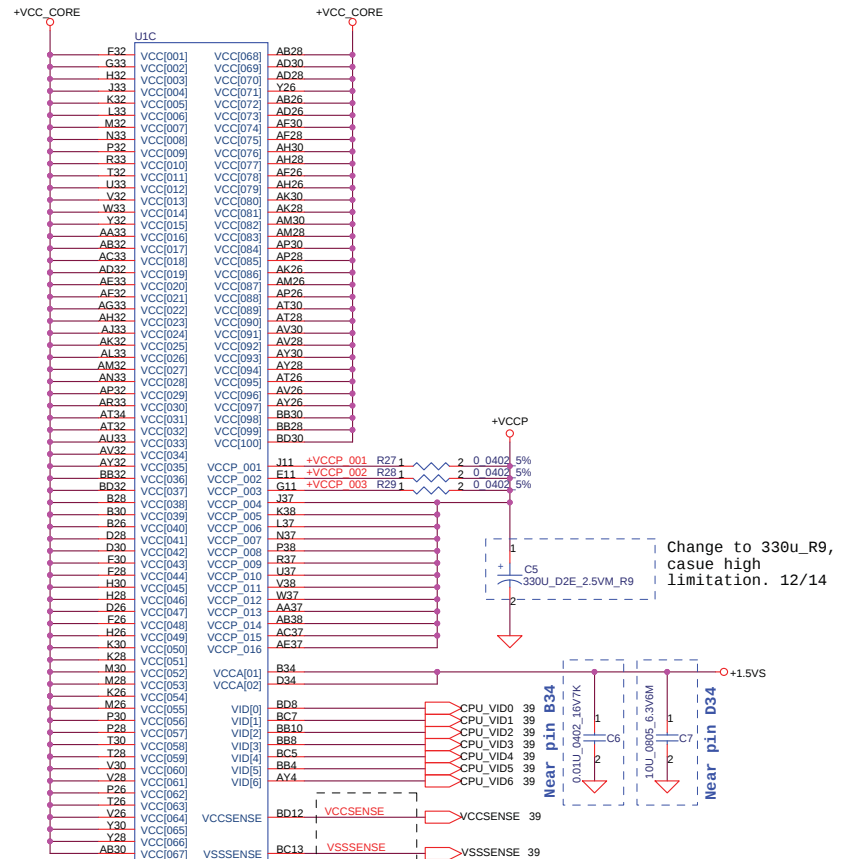
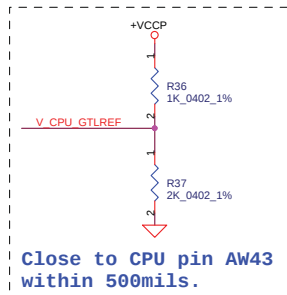


layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

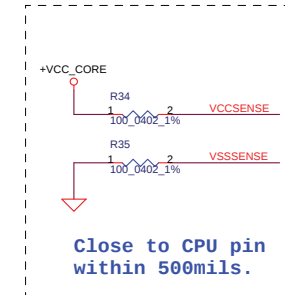
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
166	0	1	1
200	0	1	0
266	0	0	0

Cause CPU core power change to 1 phase, and not need support the pin, leave it as TP. 10/02

Resistor placed within 0.5" of CPU pin.Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.



Length match within 25 mils. The trace width/space/other is 20/7/25.



Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Document Number	LA-5541P
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date	Thursday, September 10, 2009
				Sheet	5 of 43

Compal Electronics, Inc.

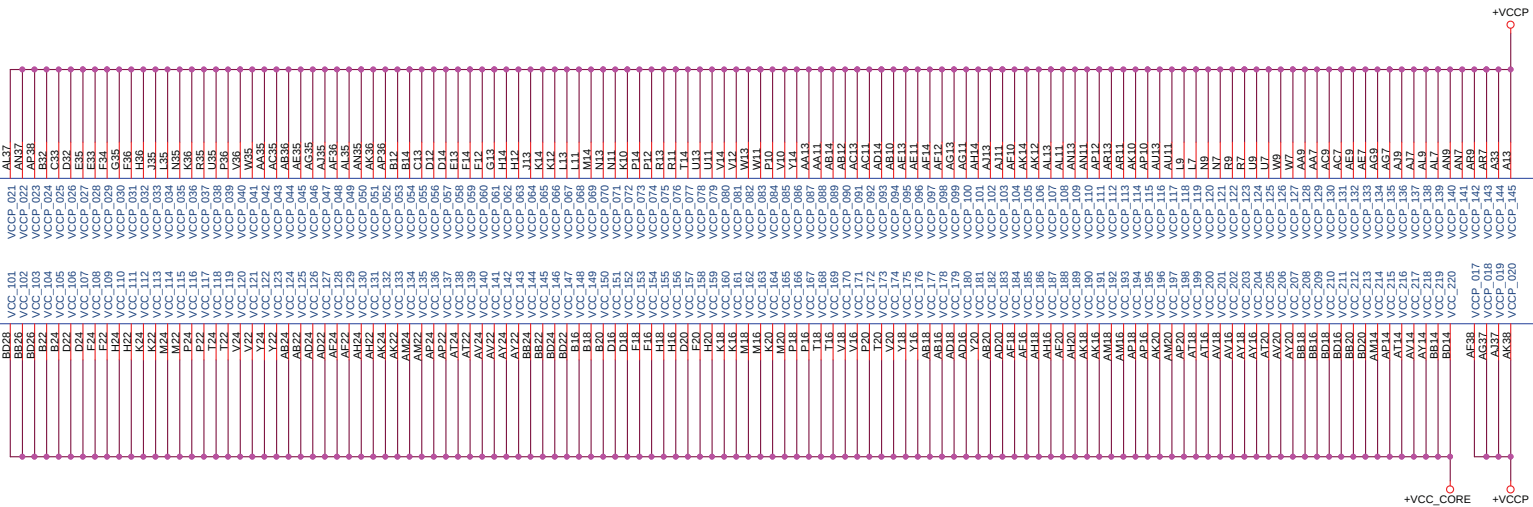
Penryn(2/4)-AGTL/PWR

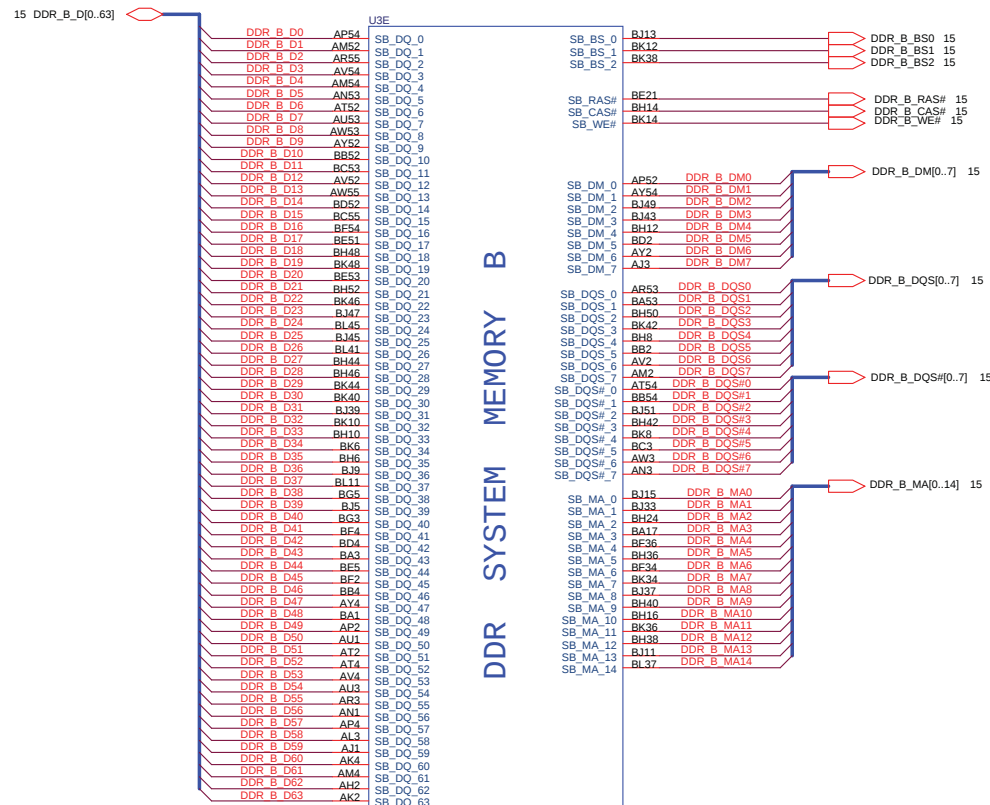
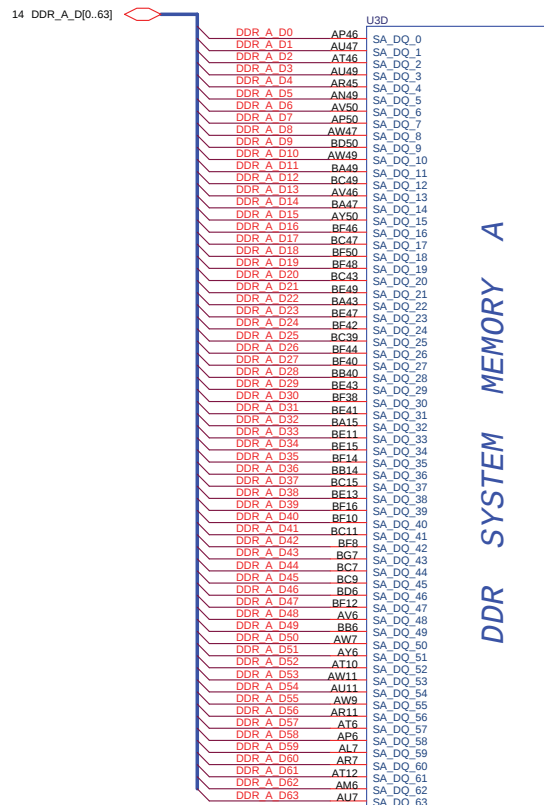
Document Number

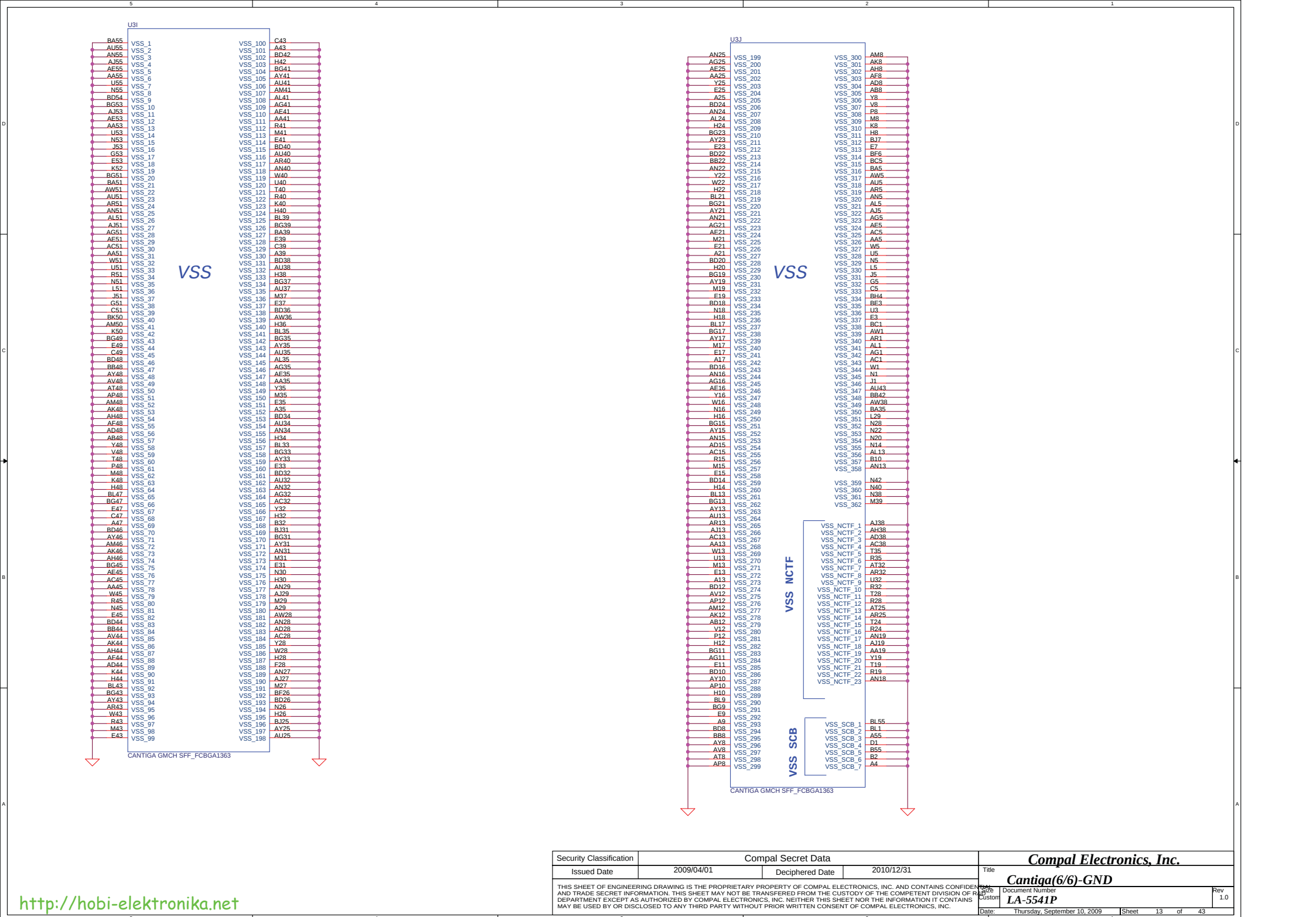
LA-5541P

Rev 1.0

Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D TO ANY OTHER DIVISION OR DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev 1.0	Document Number
Date: Thursday, September 10, 2009				Sheet 6	of 43





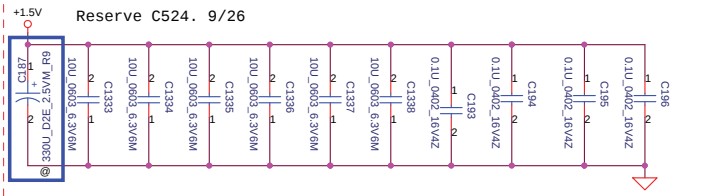


Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Document Number	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET IS NOT TO BE REPRODUCED OR TRANSMITTED IN ANY FORM OR BY ANY MEANS, WITHOUT THE WRITTEN PERMISSION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev	1.0
Date: Thursday, September 10, 2009				Sheet	13 of 43

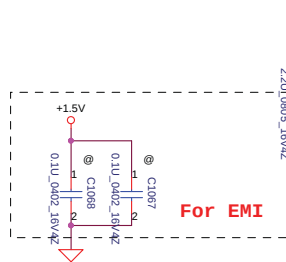
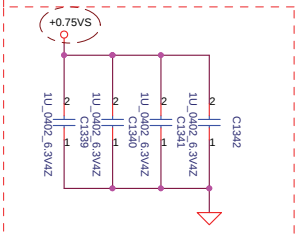


9 DDR_B_DQS#[0..7]
9 DDR_B_D[0..63]
9 DDR_B_DM[0..7]
9 DDR_B_DQS[0..7]
9 DDR_B_MA[0..14]

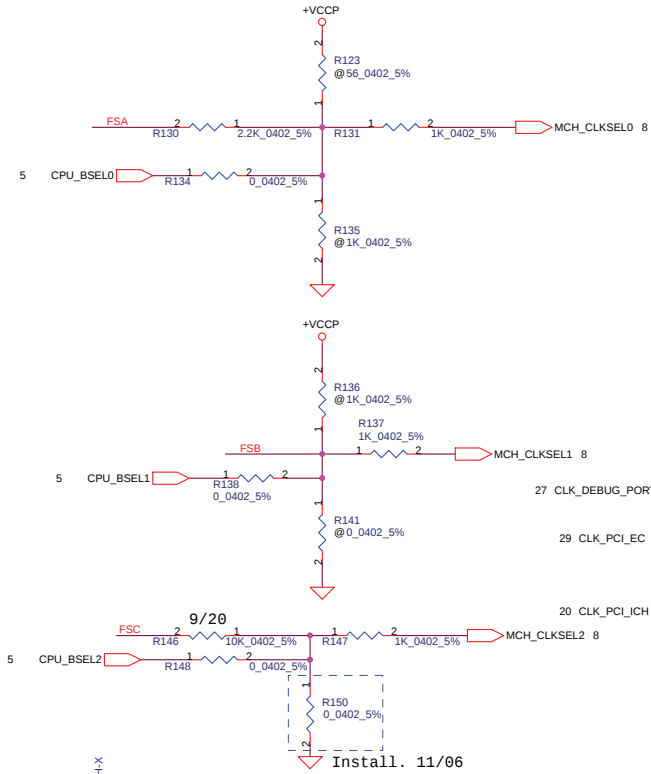
Layout Note:
Place near JP4



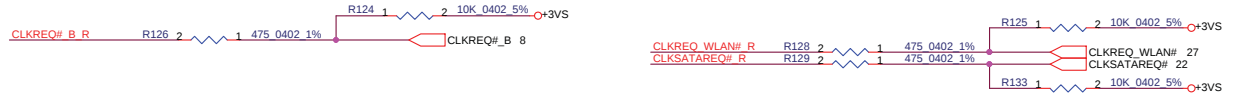
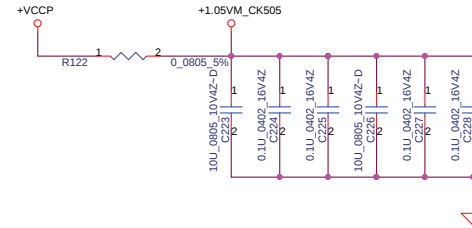
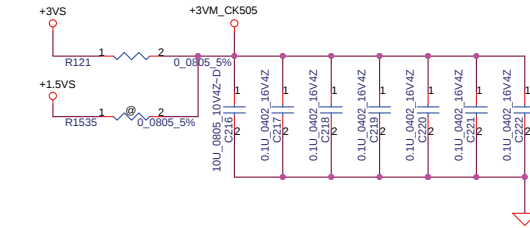
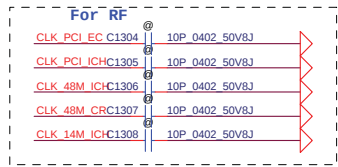
Layout Note:
Place near JP4.203,204



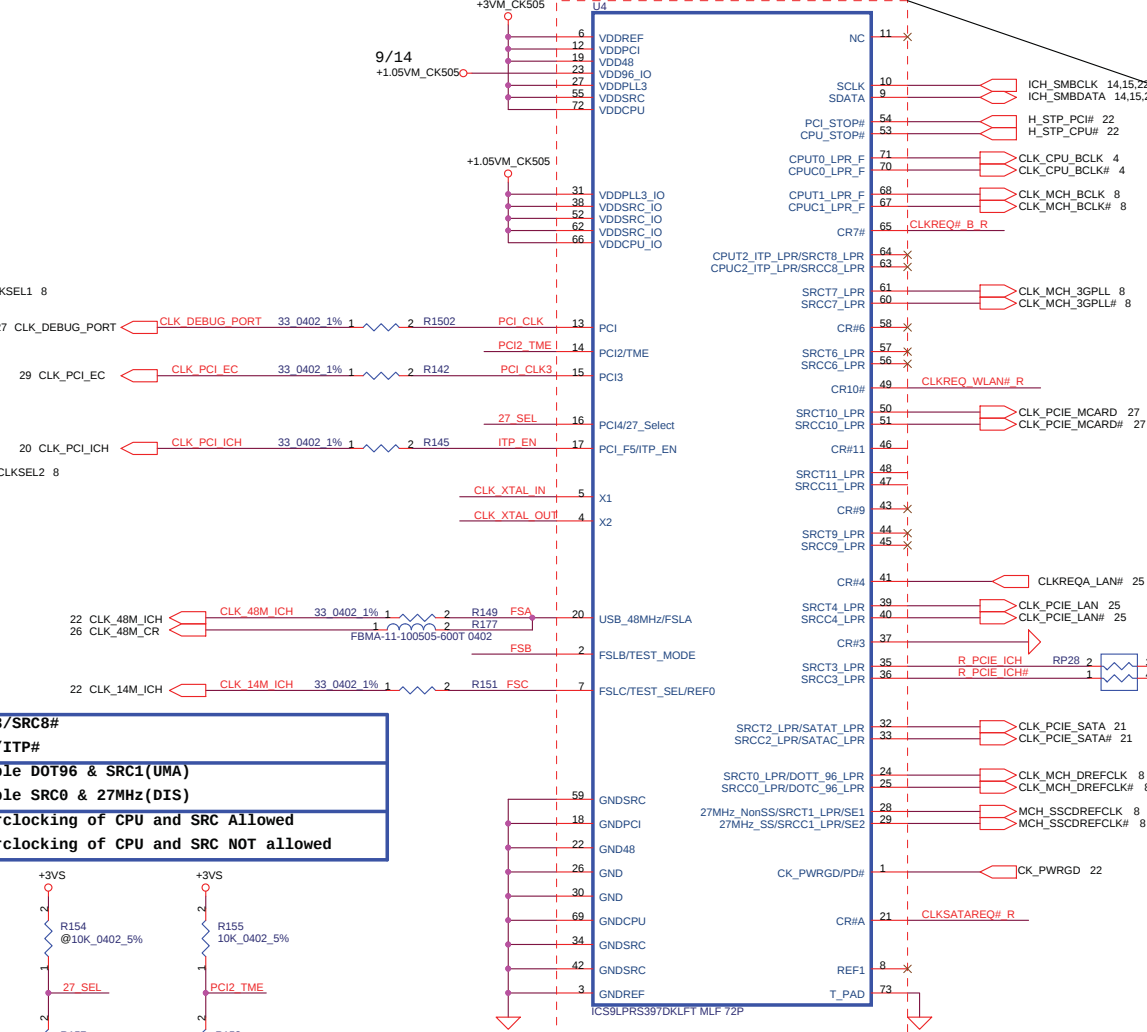
FSLC CLKSEL2	FSLB CLKSEL1	FSLA CLKSEL0	CPU MHZ	FSB MHZ	SRC MHZ	PCI MHZ
0	0	0	266	1066	100	33.3
0	1	0	200	800	100	33.3
0	1	1	166	667	100	33.3



ITP_EN	0 = SRC8/SRC8# *1 = ITP/ITP#
27_SEL	*0 = Enable DOT96 & SRC1(UMA) 1 = Enable SRC0 & 27MHZ(DIS)
PCI2_TME	0 = Overclocking of CPU and SRC Allowed *1 = Overclocking of CPU and SRC NOT allowed

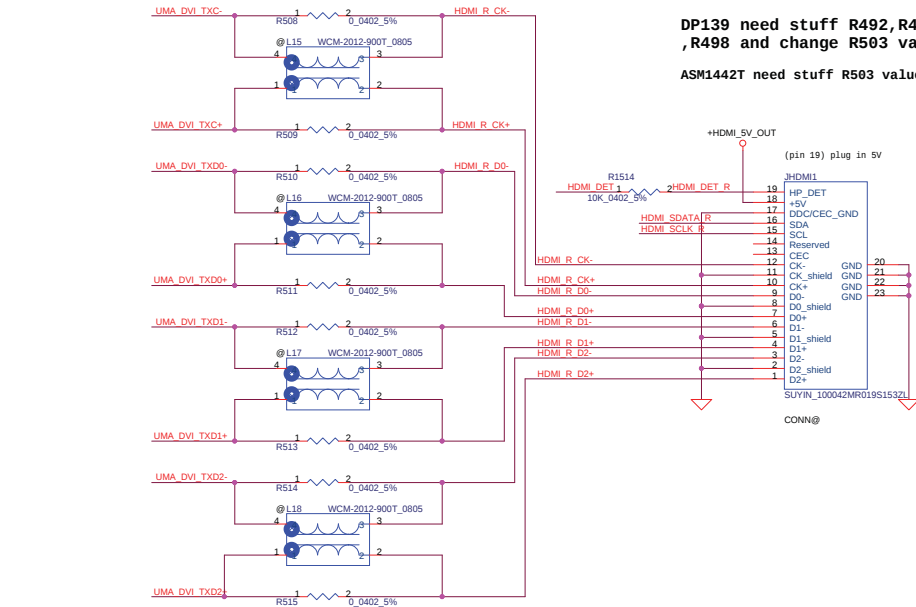


SA000020H10 for Compal
SA00001YJ20 for HP



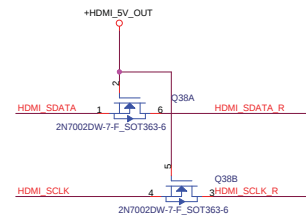
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2009/04/01				Title			
Deciphered Date				2010/12/31				Clock Generator CK505			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number				Rev			
				LA-5541P				1.0			
				Date: Thursday, September 10, 2009				Sheet 16 of 43			

Intel Cantiga TMD5 Pin Definition	
TMD5_B_CLK	PEG_TXP_3
TMD5_B_CLK#	PEG_TXN_3
TMD5_B_DATA0	PEG_TXP_2
TMD5_B_DATA0#	PEG_TXN_2
TMD5_B_DATA1	PEG_TXP_1
TMD5_B_DATA1#	PEG_TXN_1
TMD5_B_DATA2	PEG_TXP_0
TMD5_B_DATA2#	PEG_TXN_0
TMD5_B_HPD#	PEG_RXP_3



DP139 need stuff R492,R494,
,R498 and change R503 value to 3.9K

ASM1442T need stuff R503 value 3.6K

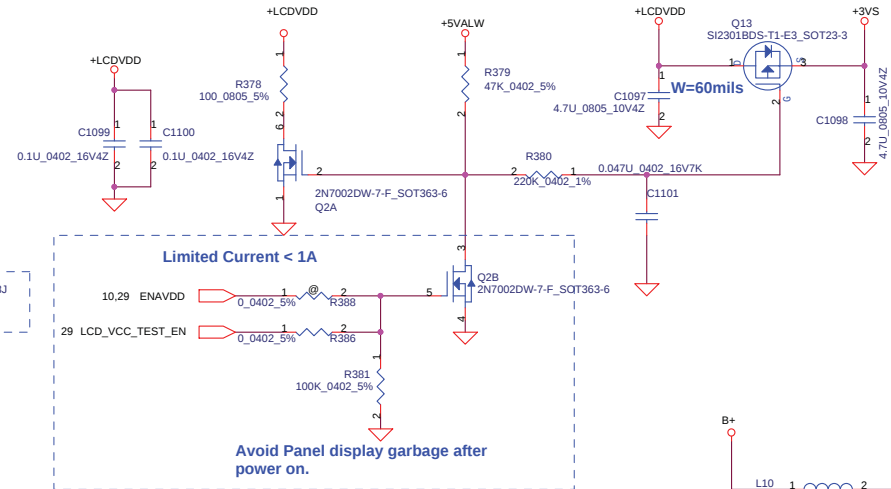
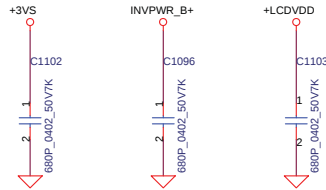
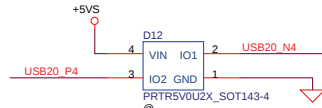
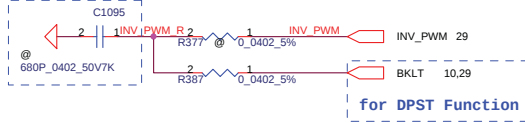


Q38 Please close to JHDMI1

Pin 10	Pin 4	Pin 3	Swing	Pre-amp	Slew-rate	Default
0	0	0	450	0	0	
0	0	1	420	0	0	
0	1	0	450	0	0	
0	1	1	460	0	-4dB	*
1	0	0	340	0	0	
1	0	1	480	2dB	0	
1	1	0	480	2dB	0	
1	1	1	420	0	0	

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				LA-5541P
				Rev 1.0
				Date: Thursday, September 10, 2009 Sheet 17 of 43

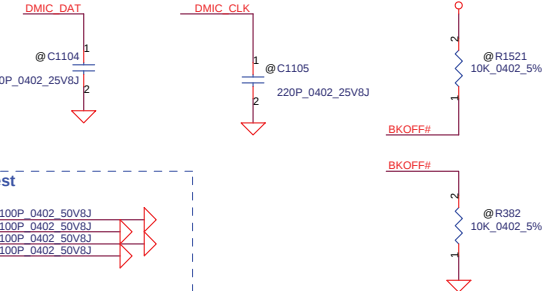
EMI request.



Limited Current < 1A

Avoid Panel display garbage after power on.

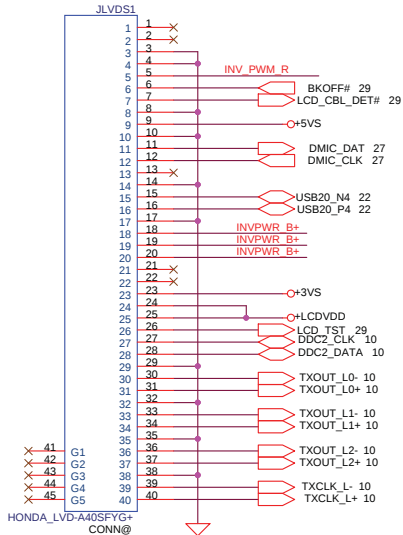
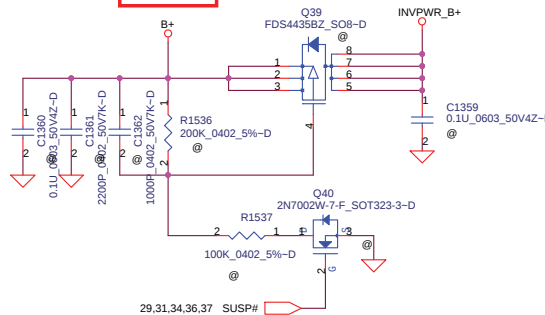
Must close JLVDS1pin 27, 28.



EMI request

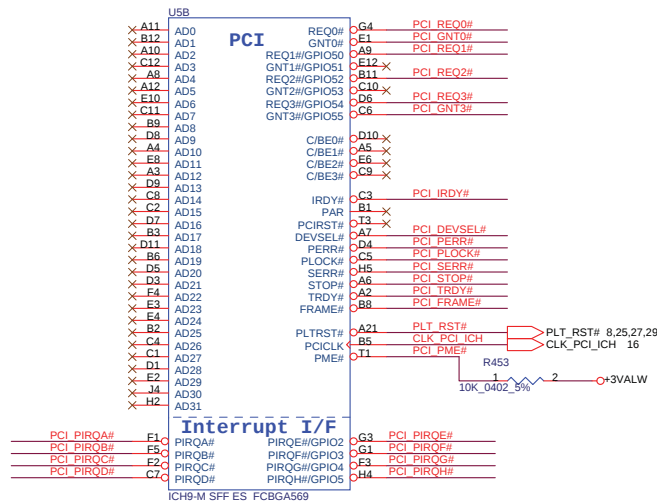
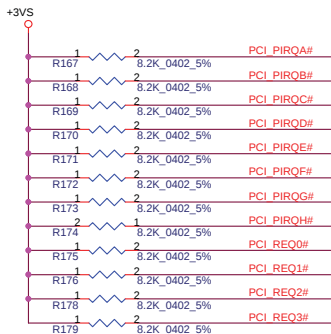
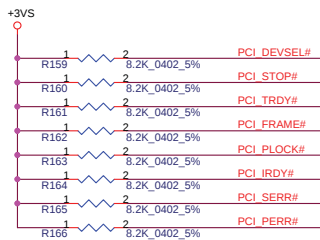
TXCLK L+	@C1106	2	100P_0402_50V8J
TXCLK L-	@C1107	2	100P_0402_50V8J
DDC2 CLK	@C1108	2	100P_0402_50V8J
DDC2 DATA	@C1109	2	100P_0402_50V8J

W=60mils FDS4435: P Channel MOS W=60mils

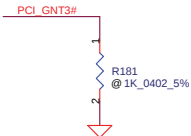


LVDS CONN & USB Camera + Dig Mic

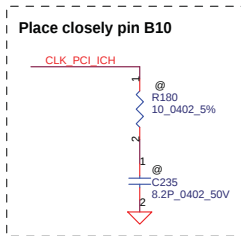
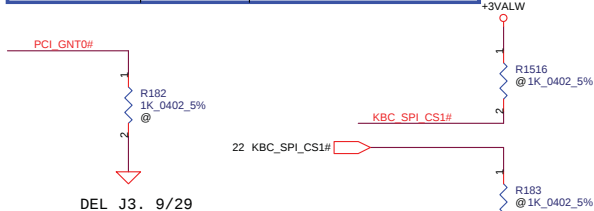
Security Classification	Compal Secret Data			Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	LCD/Camera/DMIC Connector	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-5541P	1.0
				Date: Thursday, September 10, 2009	Sheet 19 of 43



A16 swap override Strap	
PCI_GNT3#	Low= A16 swap override Enble High= Default *



Boot BIOS Strap		
PCI_GNT0#	SPI_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *



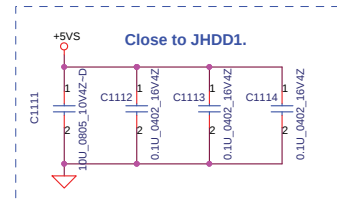
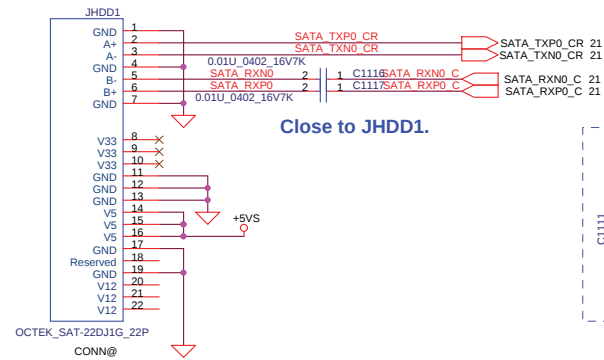
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	ICH9M(1/4)-PCI/INT
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	LA-5541P
				Date	Thursday, September 10, 2009
				Sheet	20 of 43



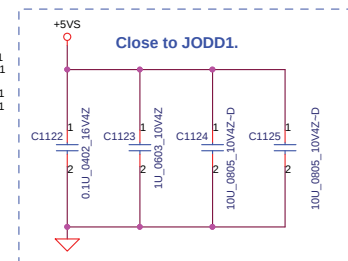
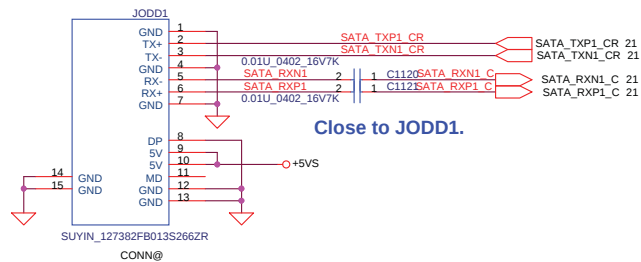




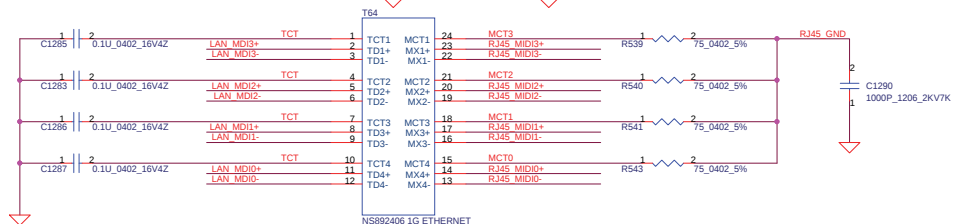
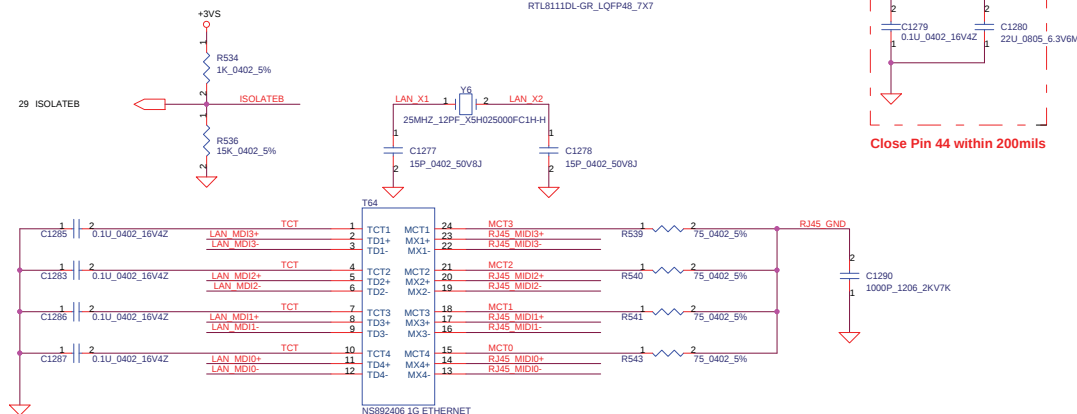
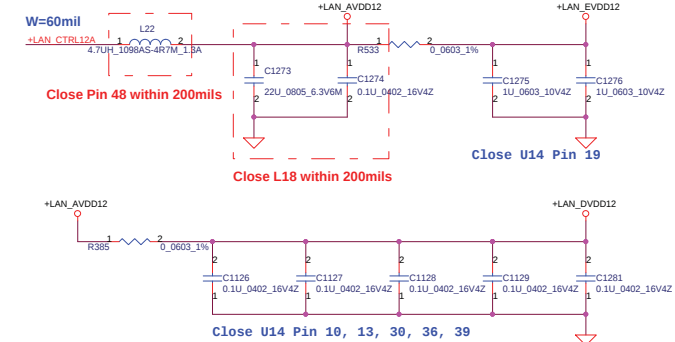
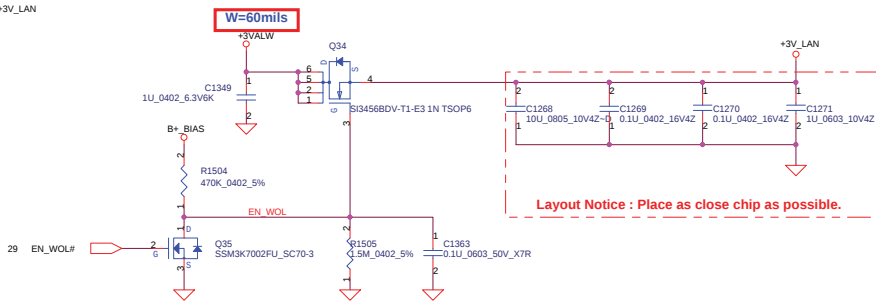
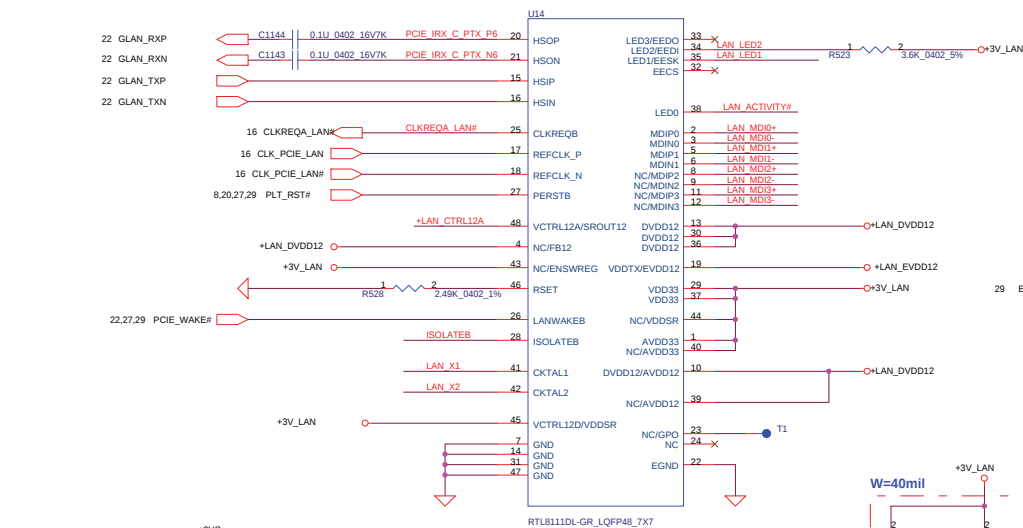
HDD Connector



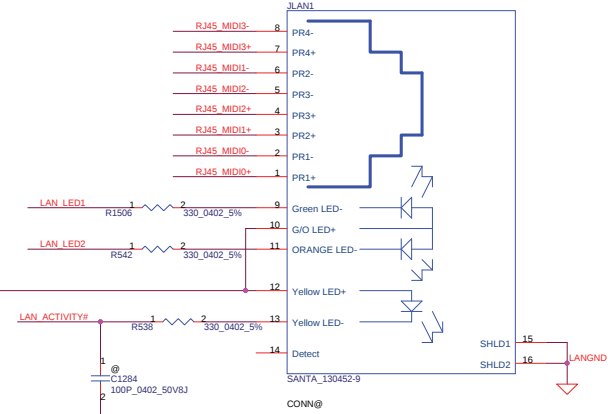
DVD-ROM Connector



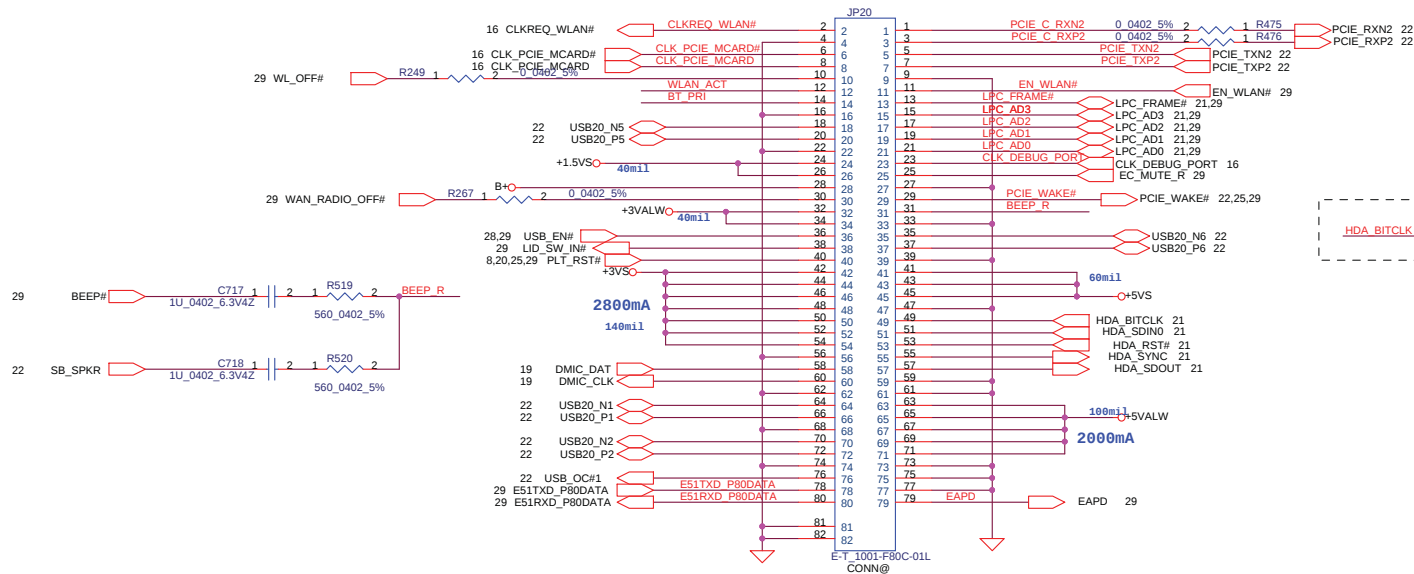
Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	HDD/ODD Connector	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-5541P	1.0
				Date: Thursday, September 10, 2009	Sheet 24 of 43



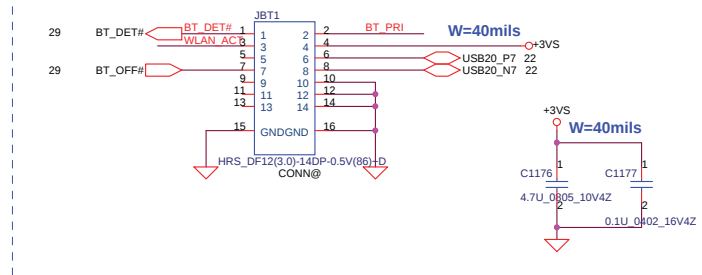
LED1, LED0, LED3, LED2
LEDCFG = 5F 06 ; for NAT20 8111DL LED setting



BTB Conn.

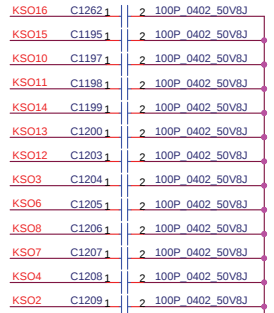


Blue Tooth Conn.

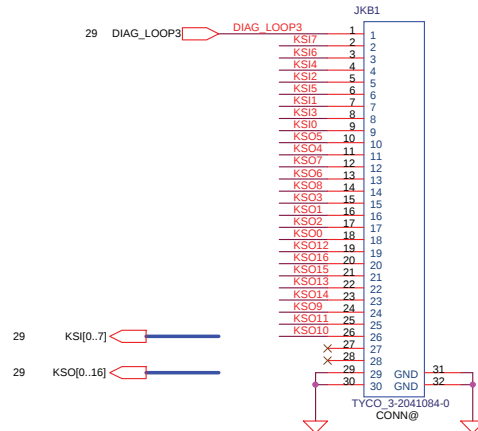


Security Classification		Compal Secret Data		Title	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	BTB/Blue Tooth Connector	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				LA-5541P	1.0
Date: Thursday, September 10, 2009				Sheet	27 of 43

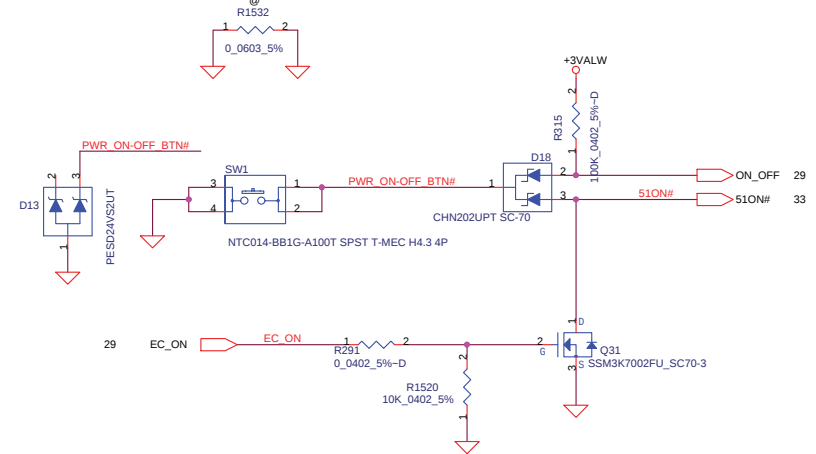
For EMI



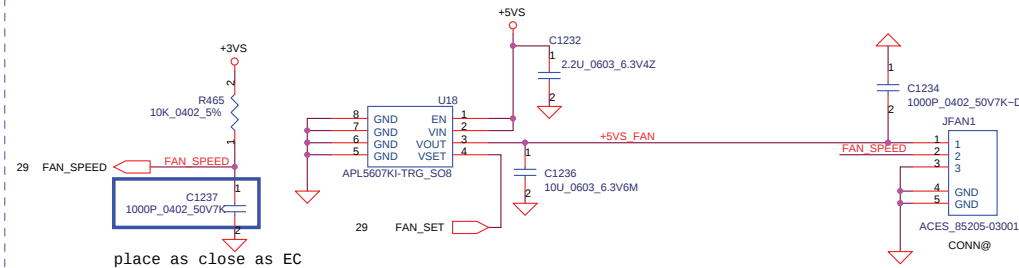
INT KBD Conn.



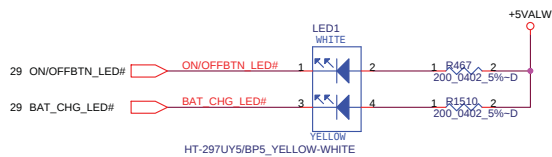
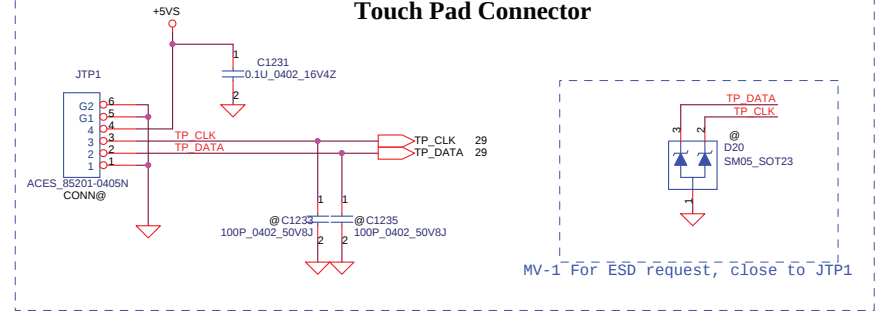
Power Button



Fan Control Circuit

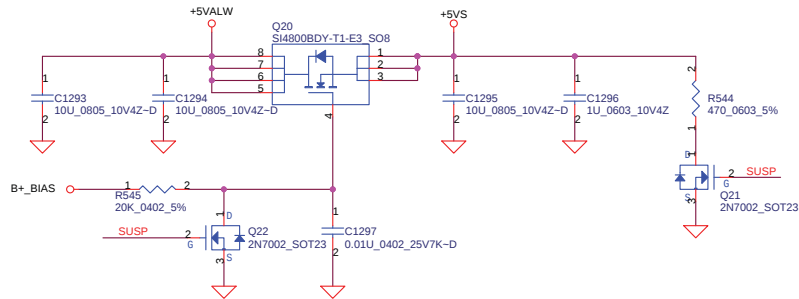


Touch Pad Connector

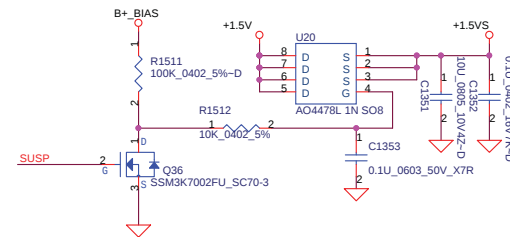


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				Deciphered Date				Title			
2009/04/01				2010/12/31				KB/BTN/FAN/TP			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Date: Thursday, September 10, 2009				Sheet 30 of 43			
Document Number				Rev				1.0			
LA-5541P											

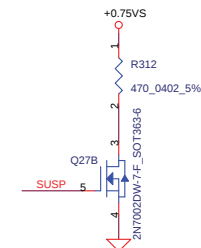
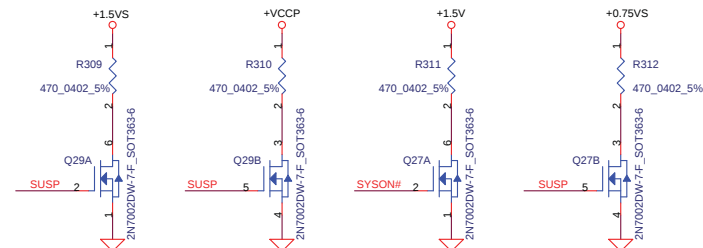
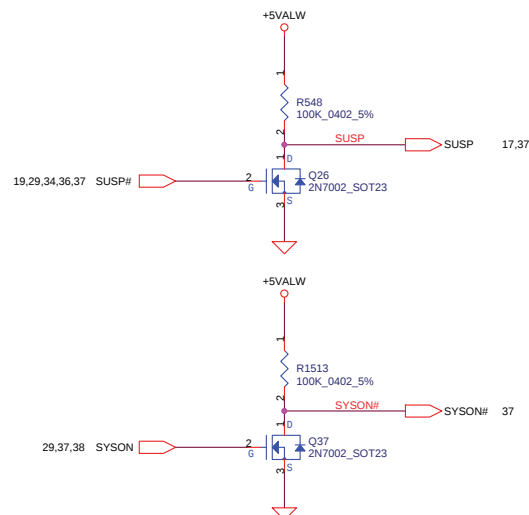
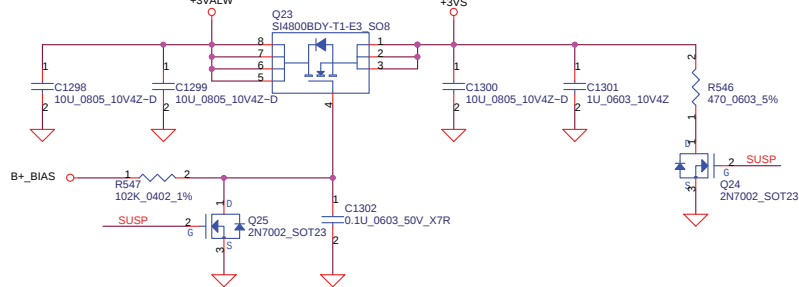
+5VALW to +5VS



+1.5V To +1.5VS



+3VALW to +3VS



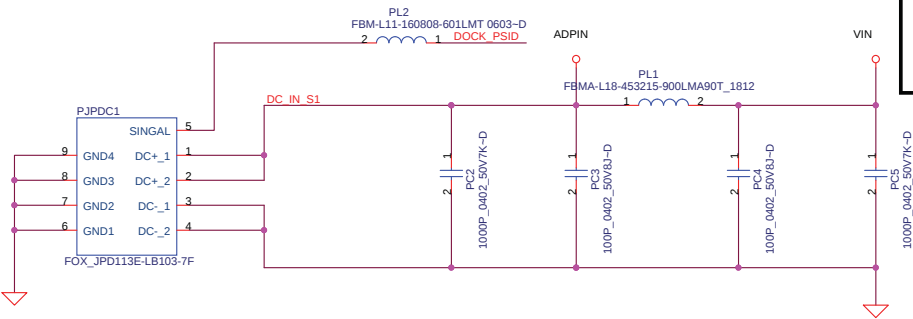
Security Classification		Compal Secret Data				Compal Electronics, Inc.			
Issued Date		2009/04/01		Deciphered Date		2010/12/31		Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						DC/DC Interface			
						Document Number		Rev	
						LA-5541P		1.0	
						Date:		Thursday, September 10, 2009	

Compal Electronics, Inc.

DC/DC Interface

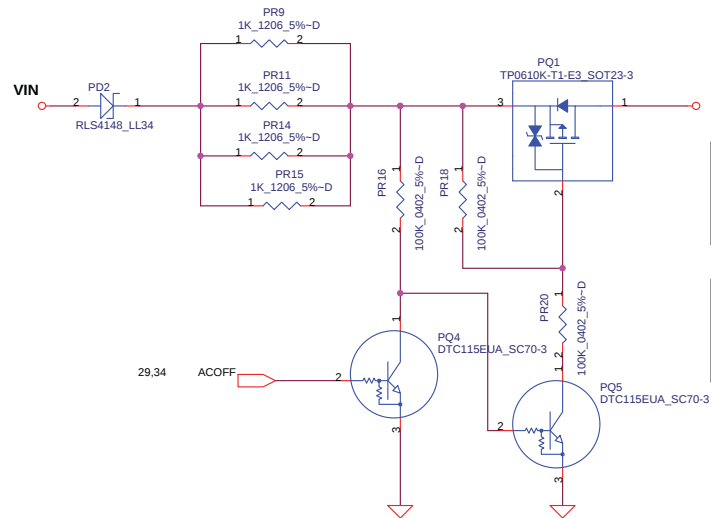
LA-5541P

Rev 1.0



Vin Detector

	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813

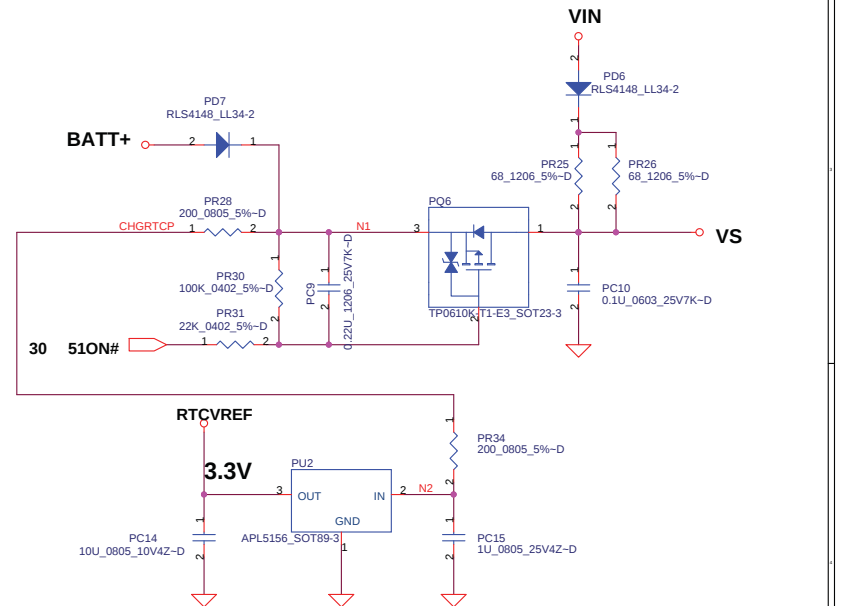
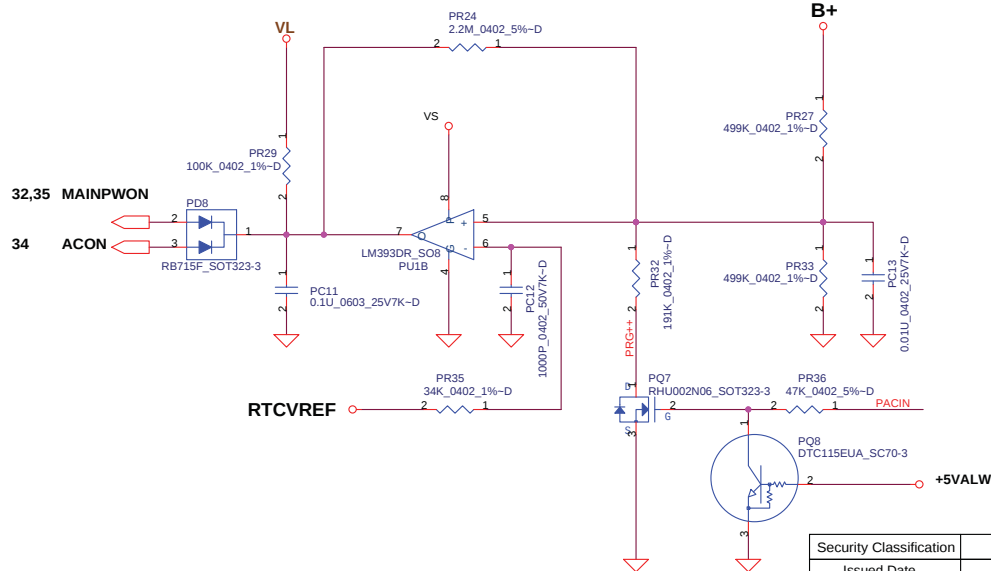
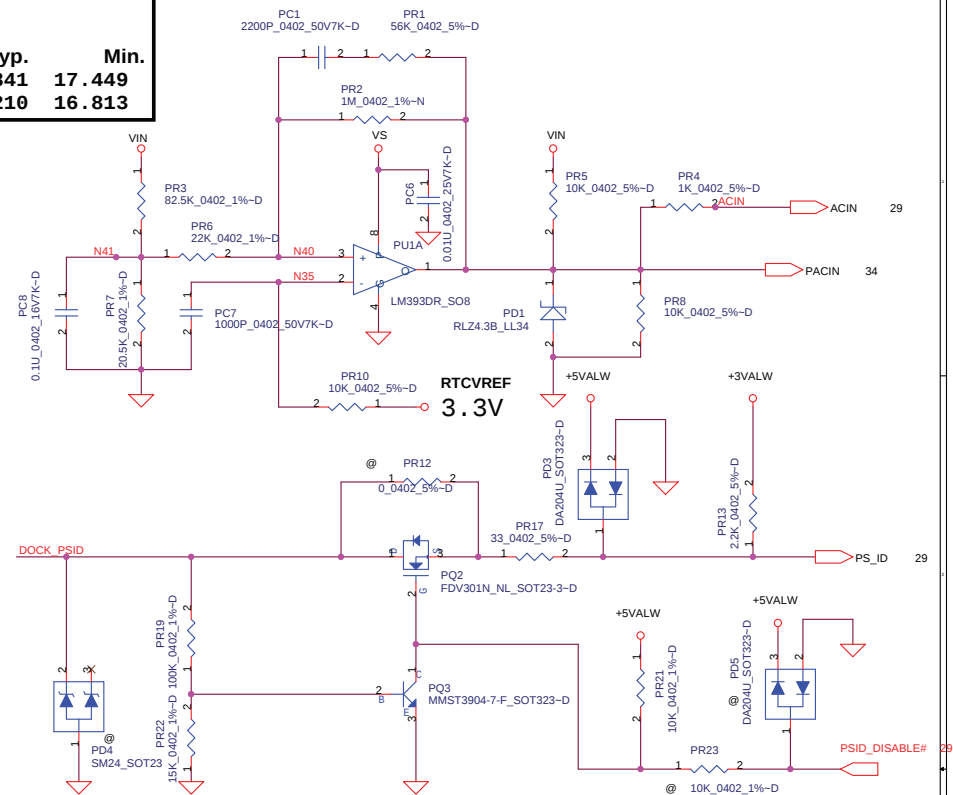


B+
ACIN

Precharge detector			
	Min.	typ.	Max
H-->L	14.589V	14.84V	15.243V
L-->H	15.562V	15.97V	16.388V

BATT ONLY

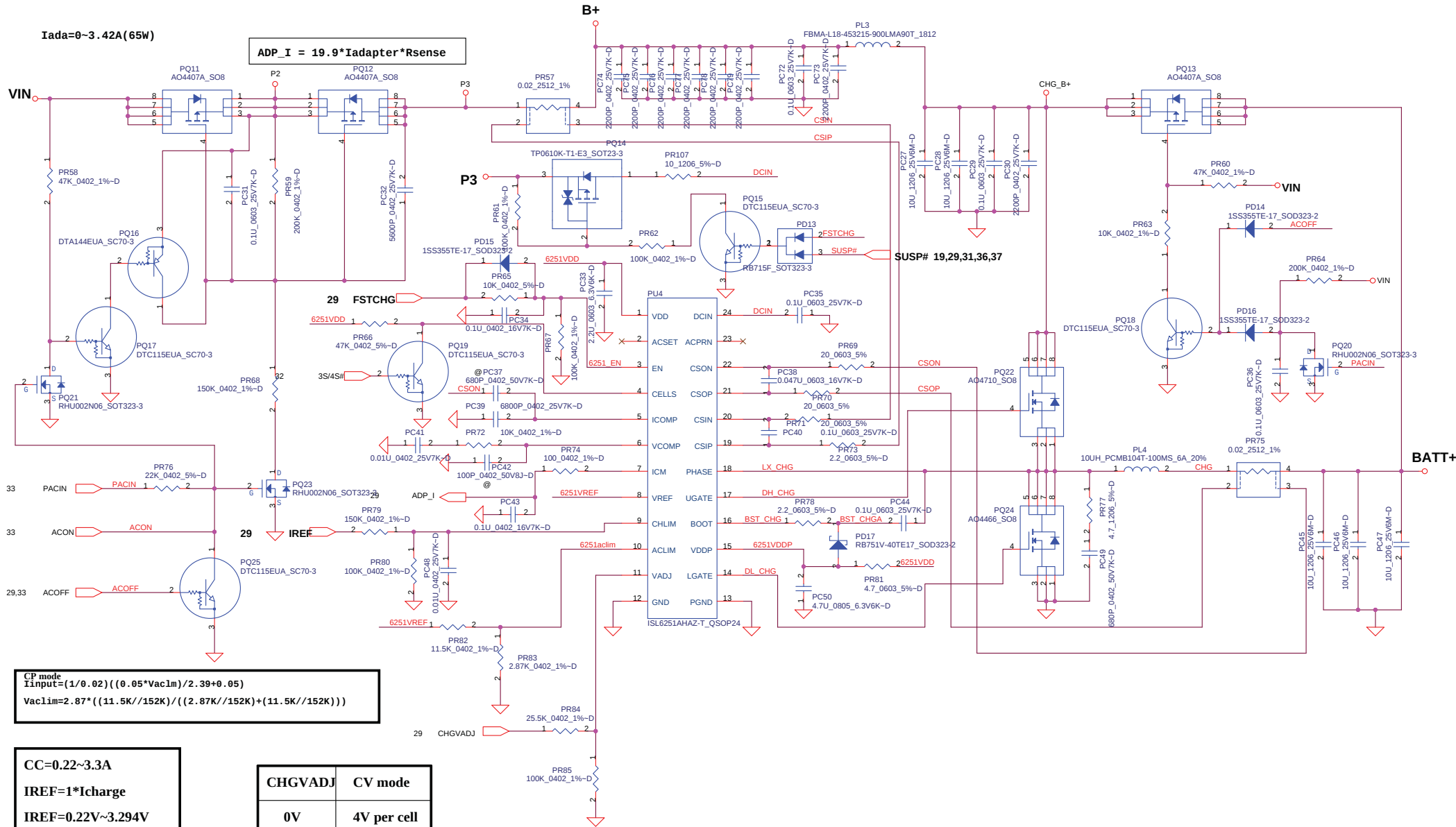
Precharge detector			
	Min.	typ.	Max
H-->L	6.138V	6.214V	6.359V
L-->H	7.196V	7.349V	7.505V



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2009/2/6	Deciphered Date	2010/12/31	Title	DCIN & DETECTOR	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev	1.0	
Date:	Thursday, September 10, 2009	Sheet	33	of	43	

I_{ada}=0~3.42A(65W)

$$ADP_I = 19.9 \cdot I_{\text{adapter}} \cdot R_{\text{sense}}$$

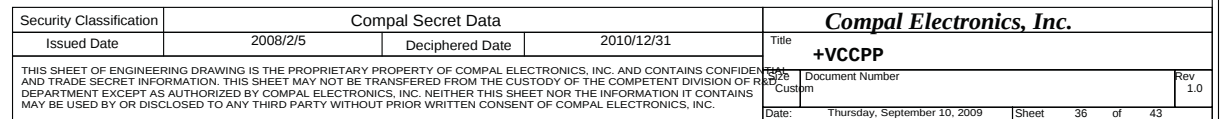


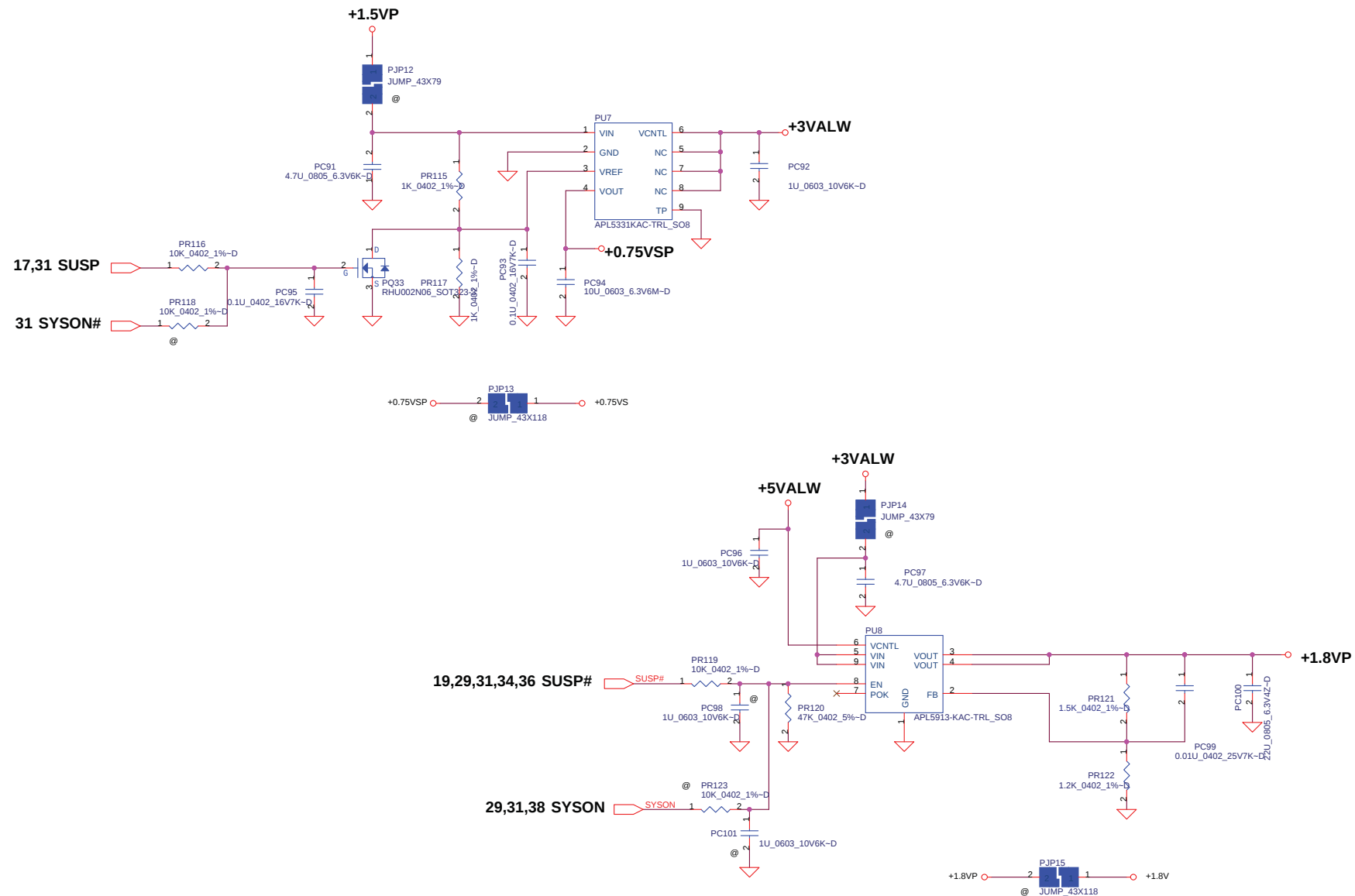
CP mode
 $I_{\text{input}} = (1/0.02) \cdot ((0.05 \cdot V_{\text{ac1m}}) / 2.39 + 0.05)$
 $V_{\text{ac1m}} = 2.87 \cdot ((11.5K / 152K) / ((2.87K / 152K) + (11.5K / 152K)))$

CC=0.22~3.3A
 IREF=1*I_{charge}
 IREF=0.22V~3.294V

CHGVADJ	CV mode
0V	4V per cell
1.882V	4.2V per cell
3.294V	4.45V per cell

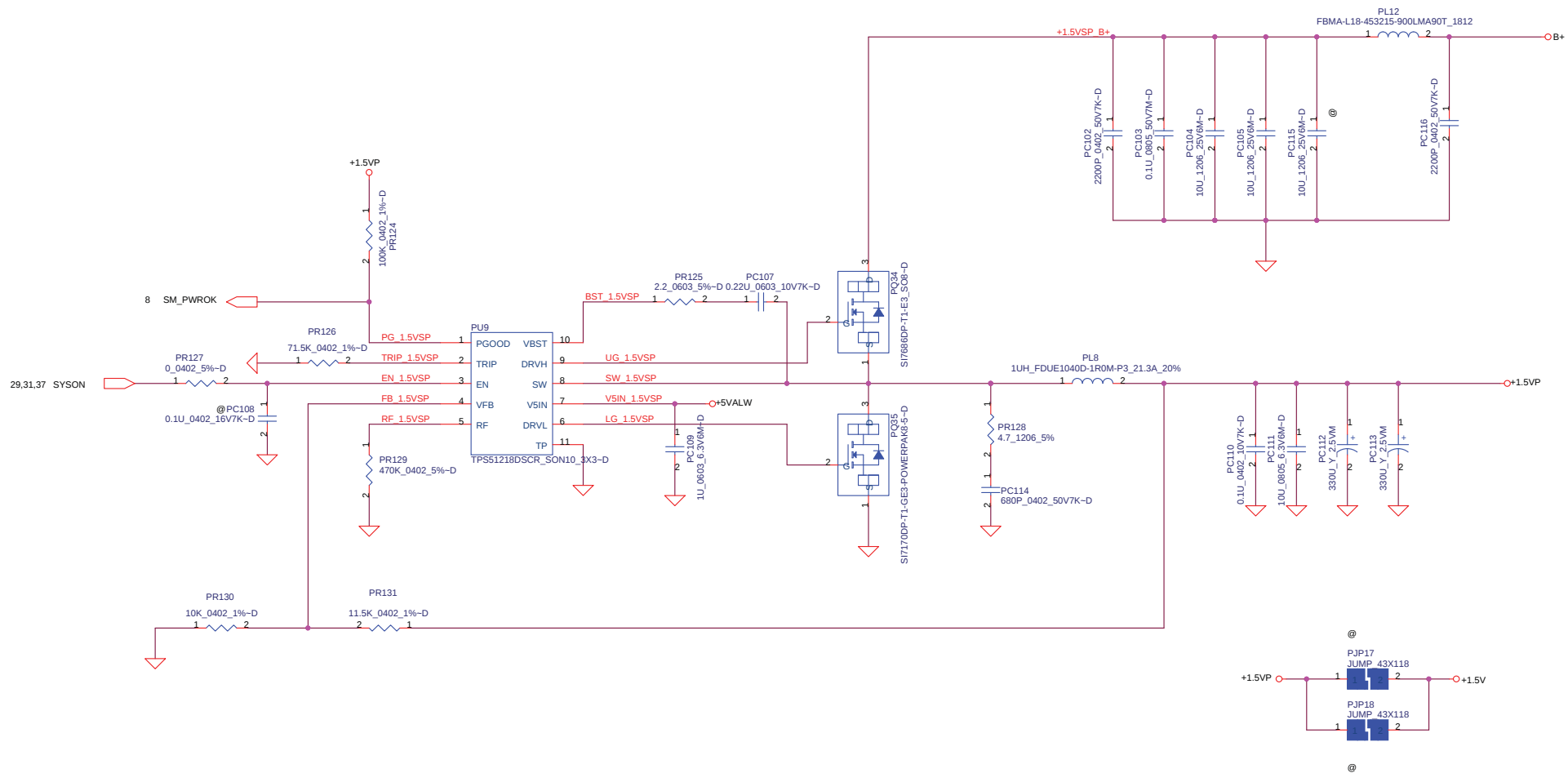
F_{SW}=290KHZ





Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2008/2/5	Deciphered Date	2010/12/31	Title	+1.8VP / +0.75VP
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc. Number	Rev 1.0
				Date:	Thursday, September 10, 2009
				Sheet	37 of 43

Fsw=300KHZ



Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2008/2/5	Deciphered Date	2010/12/31	Title	+1.5VSP	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Doc. Number	Rev	
				Custom	1.0	
				Date:	Thursday, September 10, 2009	Sheet 38 of 43

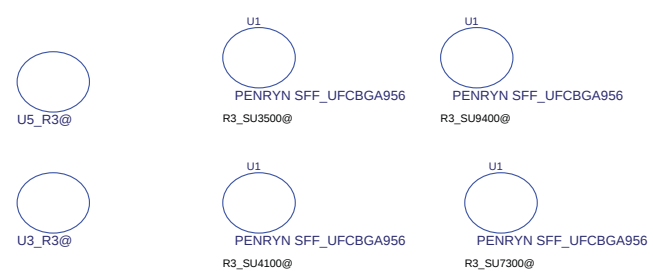
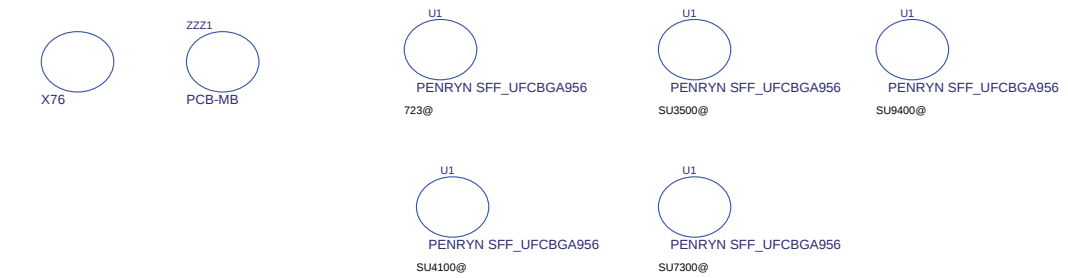
<http://hobi-elektronika.net>

Compal Electronics, Inc.			
Title			
PWR-PIR			
DESIGN OF PRODUCT			Rev
CONTAINING			1.0
LA-5541P			
Date:	Thursday, September 10, 2009	Sheet	40 of 43

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	21, 23		2009/06/05	Compal	HDMI NO SOUND	ADD R319, R320, R323, R321 DEL R322, R324, R92	0.2
2	29, 30		2009/06/05	Compal	Battery LED can't work	ADD NET BAT_CHG_LED#	0.2
3	19		2009/06/05	Compal		Change JLVDS connect	0.2
4	24		2009/06/05	Compal		Change JODD1 connect	0.2
5	17		2009/06/05	Compal		Change Level shift IC to ASMEDIA	0.2
6	16		2009/06/18	Compal		reserve R1535 to using LOW POWER CLK GEN RTM890-397	0.3
7	26		2009/06/24	Compal		DEL Y5 C1244, C1246	0.3
8	19		2009/07/08	Compal		reserve Q39,Q40,R1536, R1537, C1359, C1360, C1361, C1362	0.3
9	30		2009/07/08	Compal		Del C313 D14	0.3
10	29,30		2009/07/08	Compal		Add EAPD trace	0.3
11	25		2009/07/23	Compal		Add C1363	1.0
12	30		2009/07/23	Compal		Change Keyboard footprint	1.0
13							
14							
15							
16							
17							
18							
19							
20							
21							
22							
23							
24							
25							
26							
27							
28							
29							
30							
31							
32							

Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	EE PIR-1	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev	Document Number	Rev
				1.0	LA-5541P	1.0
				Date:	Thursday, September 10, 2009	Sheet 41 of 43

5 4 3 2 1



Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	BOM structure
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev 1.0
				Date: Thursday, September 10, 2009	Sheet 42 of 43

5 4 3 2 1

