

COMPAL CONFIDENTIAL

MODEL NAME : NAW20
PCB NO : LS-5671P
Revision : 0.2

NAW20 CULV 14

Compal Confidential

Schematic Document

Cantiga GS + ICH9M-SFF

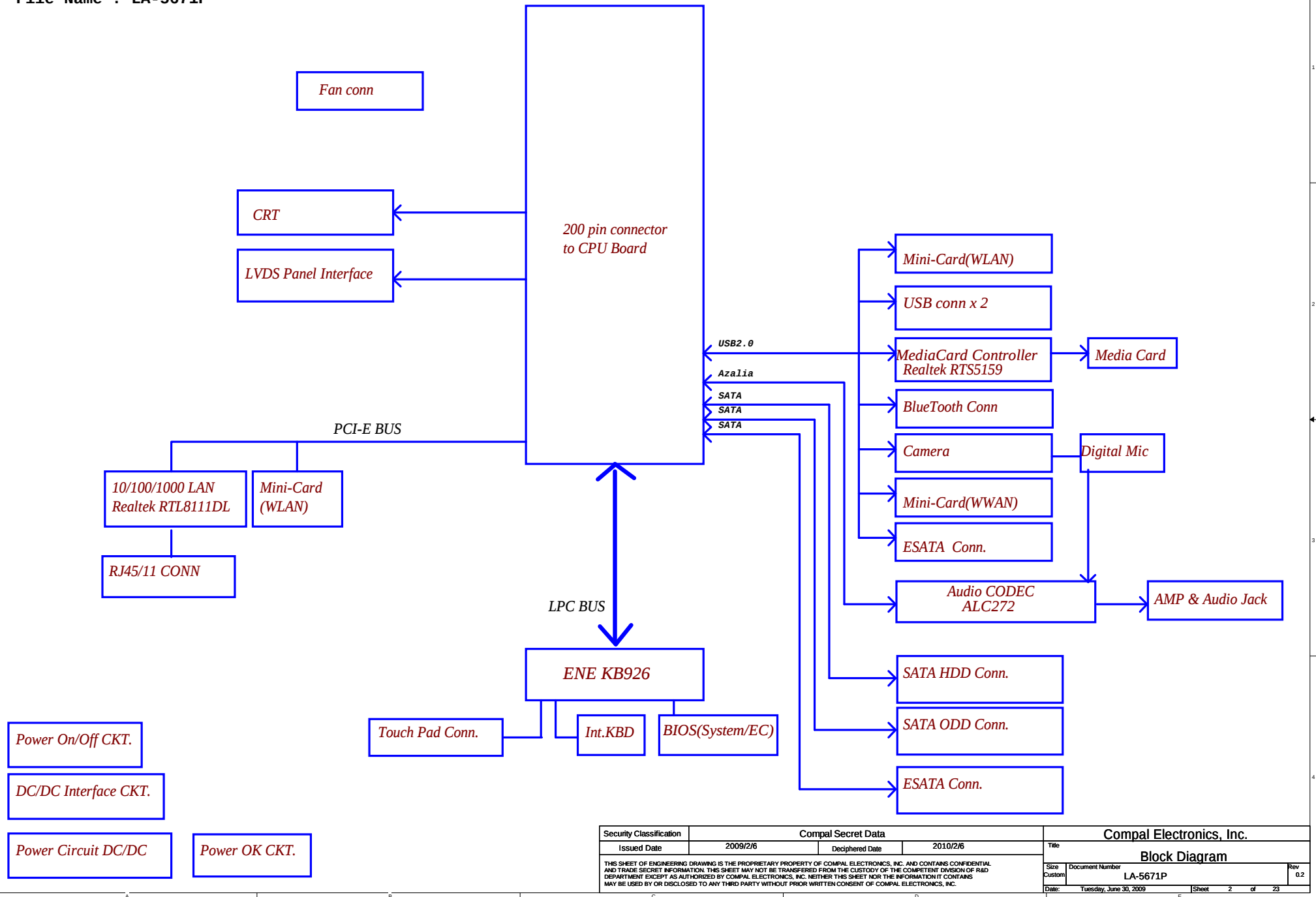
2009 /6/25 Rev:0.2

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Issued Date		2009/2/6	Deciphered Date	Title	
			2010/2/6	Cover Page	
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				Custom	LA-5671P
				Date: Wednesday, July 01, 2009	Rev 0.2
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Compal confidential

File Name : LA-5671P

CULV 14

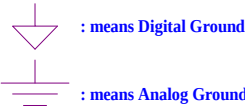


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				Size	Document Number	Rev
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Voltage Rails 0 MEANS ON X MEANS OFF

power plane State	+B	+5VALW +3VALW	+1.8V	+5VS +3VS +1.5VS +0.9V +VCCP +CPU_CORE +VGA_CORE +2.5VS +1.8VS +1.2VS +0.9VGA
S0	0	0	0	0
S1	0	0	0	0
S3	0	0	0	X
S5 S4/AC	0	0	X	X
S5 S4/ Battery only	0	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

Symbol Note :



@ : means just reserve , no build
DEBUG@ : means just reserve for debug.

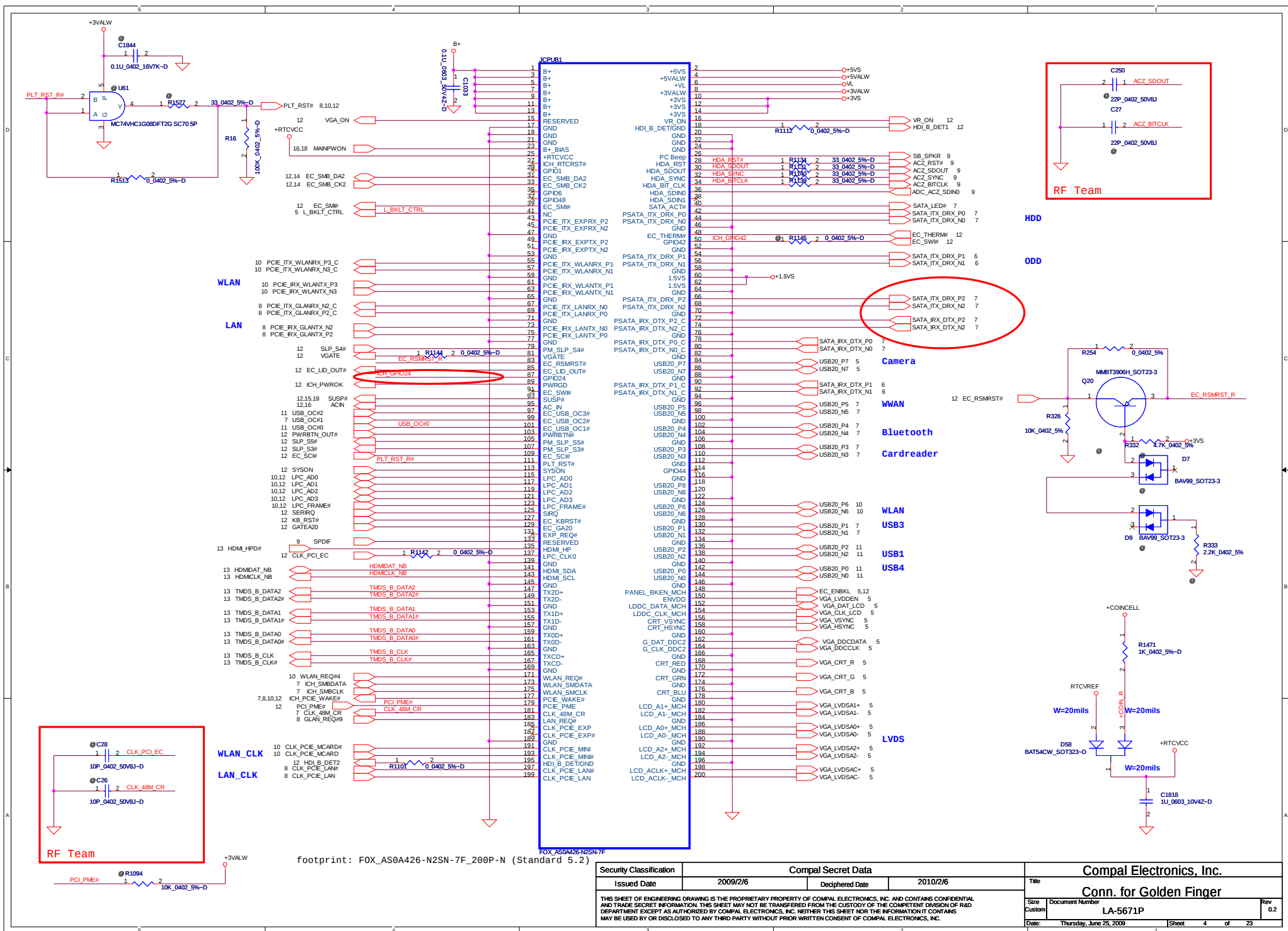
SMBUS Control Table

	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM	CLK CHIP	MINI CARD	LCD
SMB_EC_CLK1 SMB_EC_DA1	KB926	X	V	V	X	X	X	X	X
SMB_EC_CLK2 SMB_EC_DA2	KB926	X	X	X	V	X	X	X	X
SMB_CLK_CLK1 SMB_CLK_DAT1	ICH9	X	X	X	X	V	V	V	X
LCD_CLK LCD_DAT	Cantiga	X	X	X	X	X	X	X	V

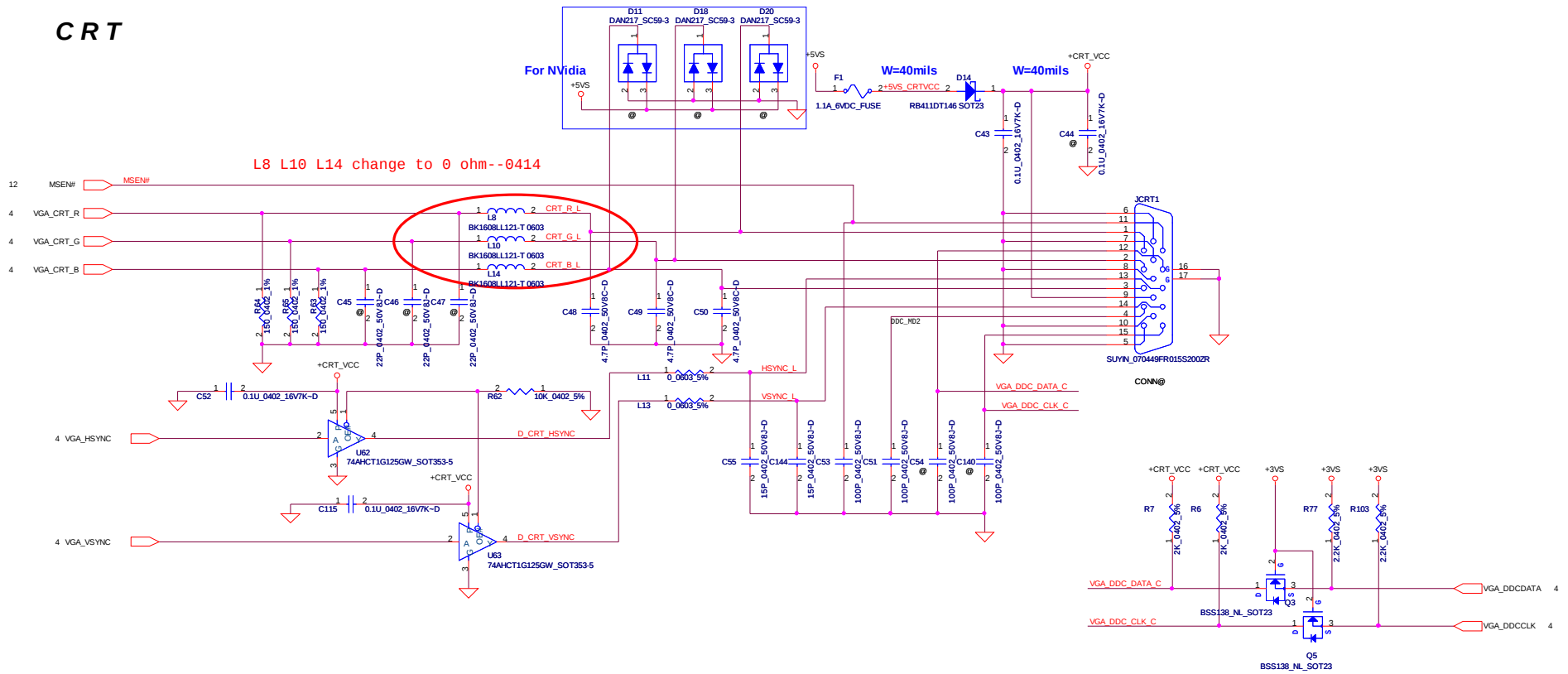
I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

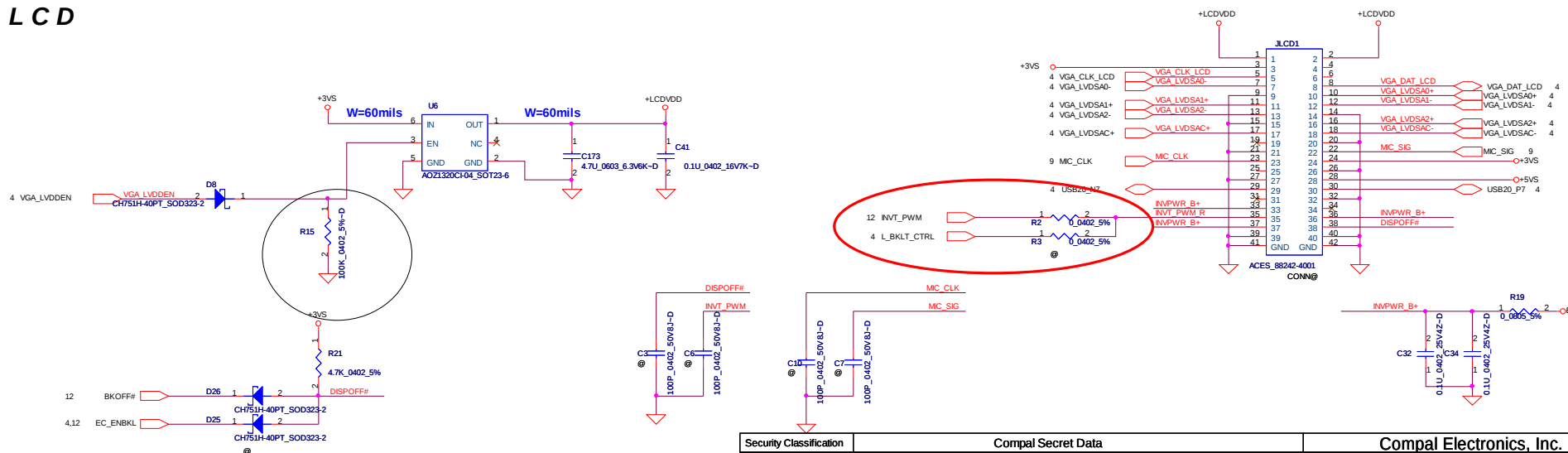
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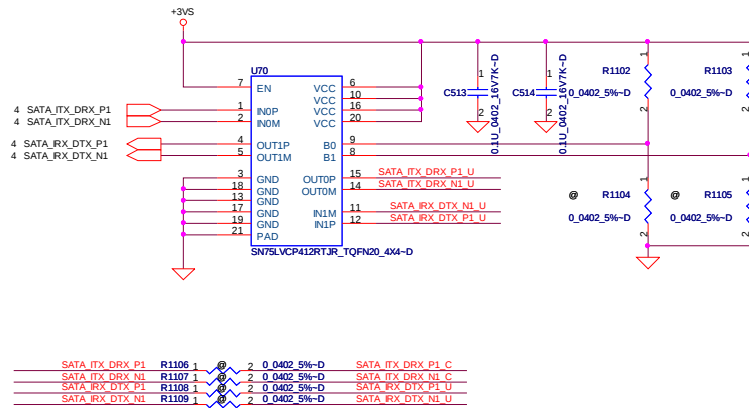
CRT



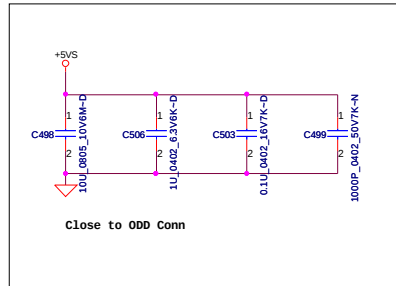
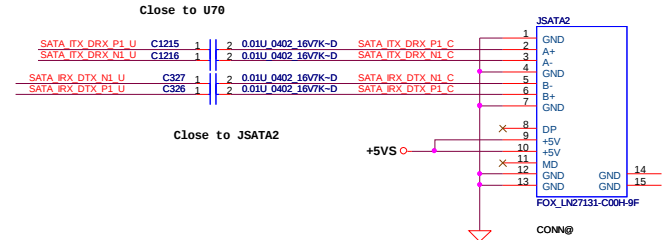
LCD



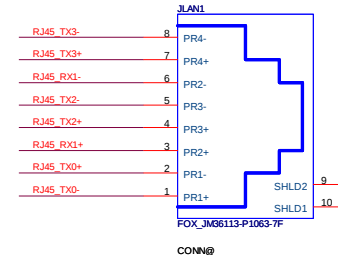
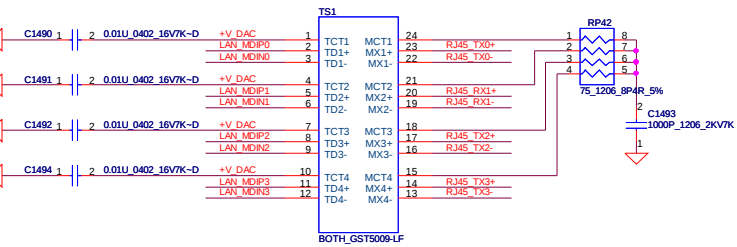
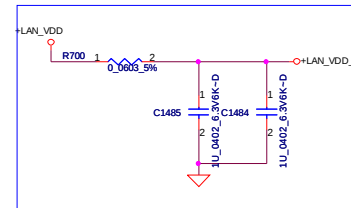
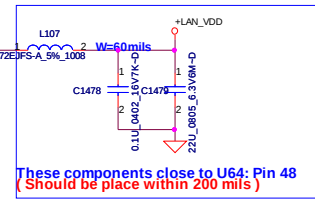
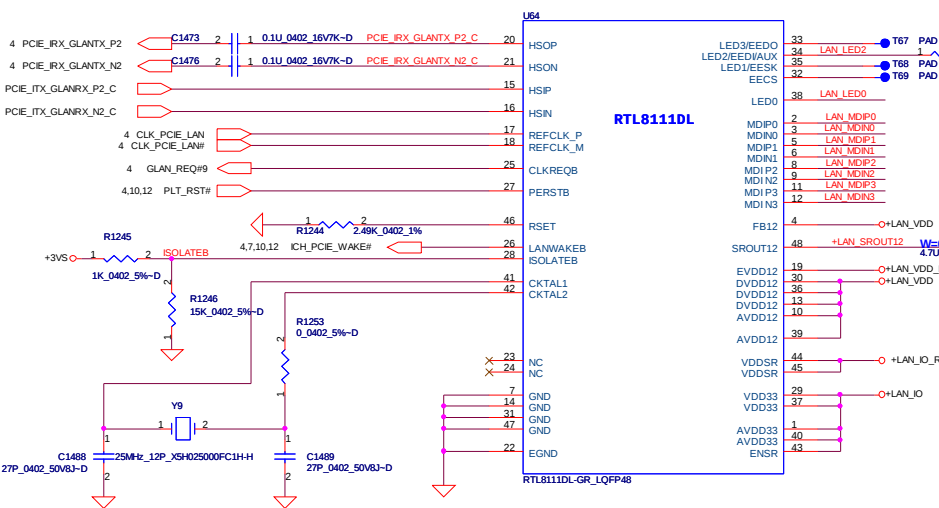
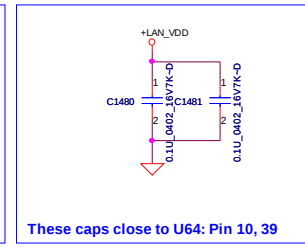
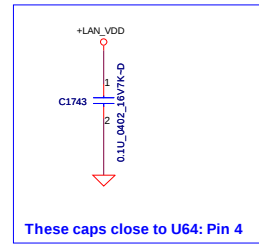
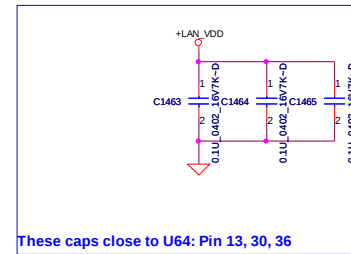
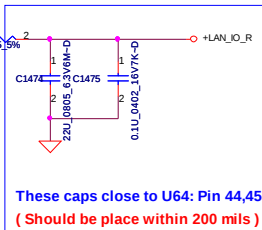
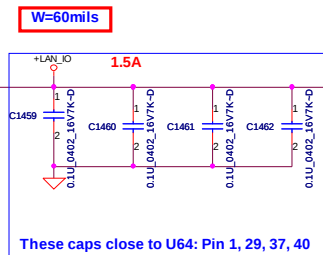
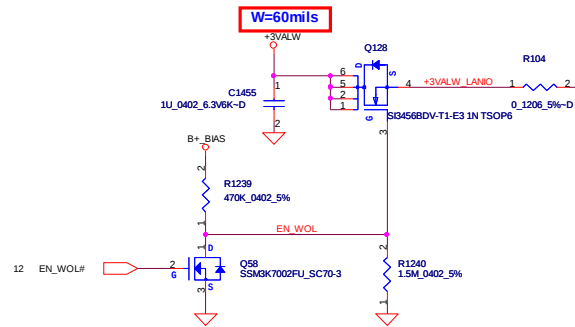
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Issued Date	2009/2/6	Deciphered Date	2010/2/6	Title CRT & LCD Conn. (W/ Camera)		
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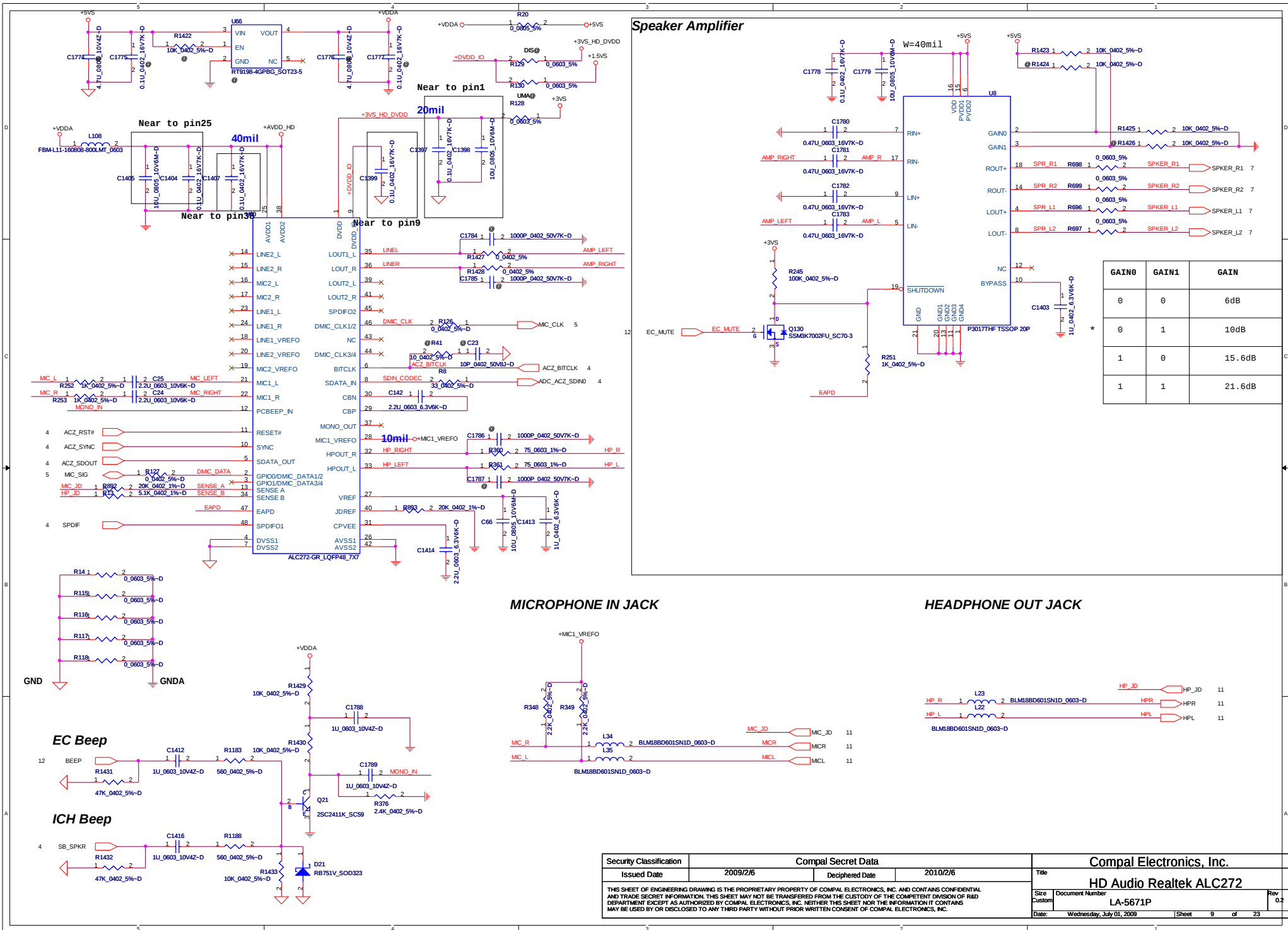
SATA ODD CONN



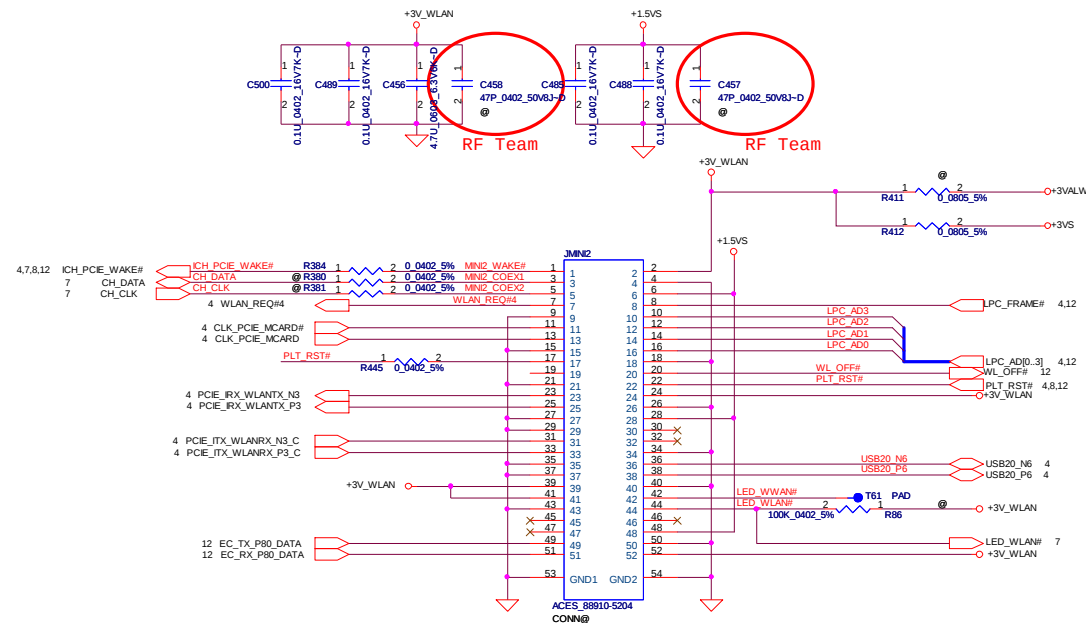
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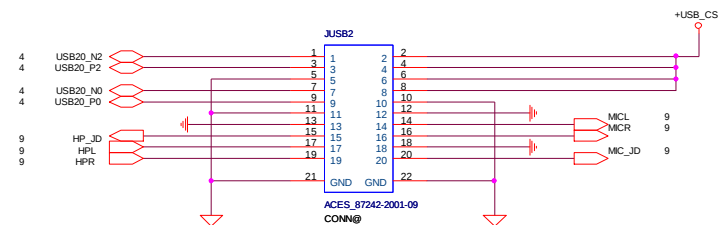
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Issued Date	2009/2/6	Deciphered Date	2010/2/6	Title	LAN RTL8111DL/ RJ45 Conn.	
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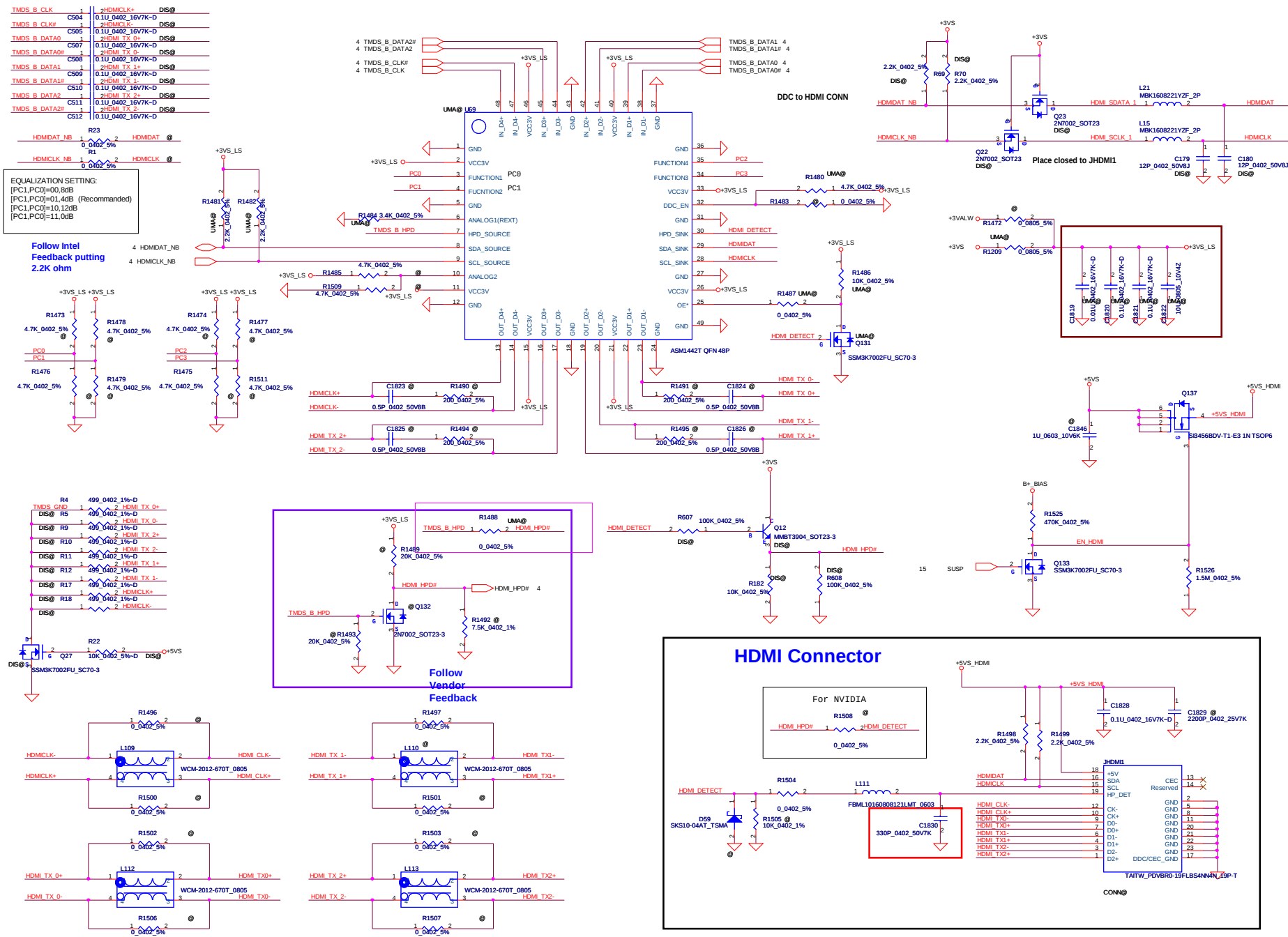
Mini-Express Card---WLAN



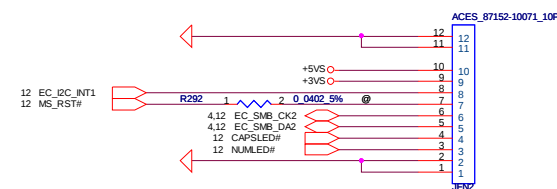
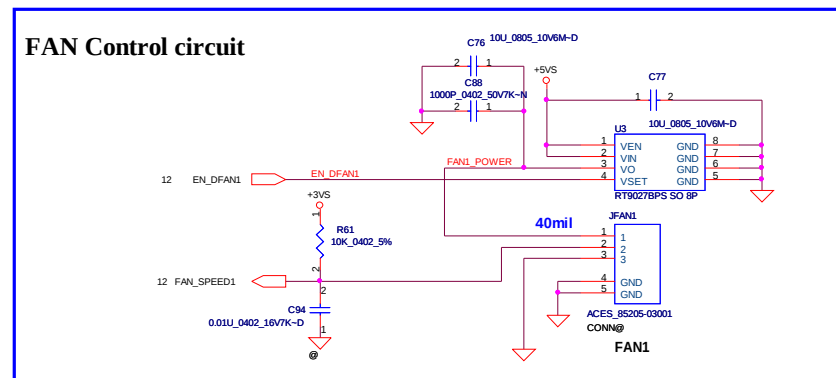
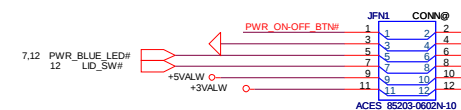
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						Custom		LA-5671P		0.2	
						Date:		Thursday, June 25, 2009		Sheet 10 of 23	



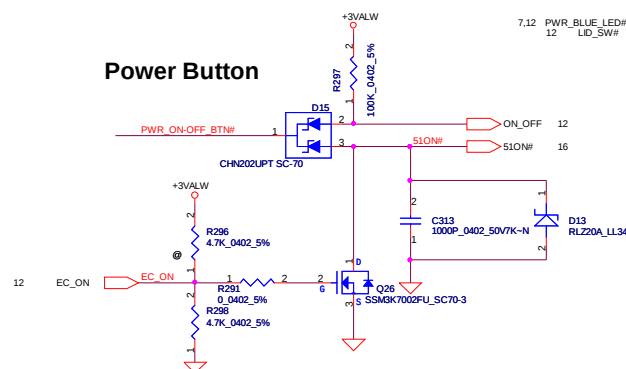
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Issued Date	2009/2/6	Deciphered Date	2010/2/6	Title	USB/ Bluetooth/ KB/ TP
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Issued Date	2007/08/28	Deciphered Date	2009/11/8
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Size		HDMI LS & Conn.	
Customer	LA-5671P	Rev	0.2
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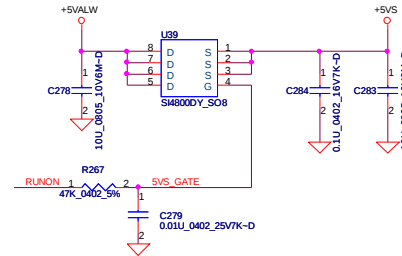
**Power/B CONN.**

Power Button

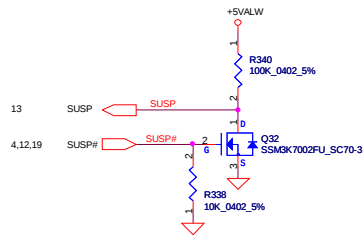
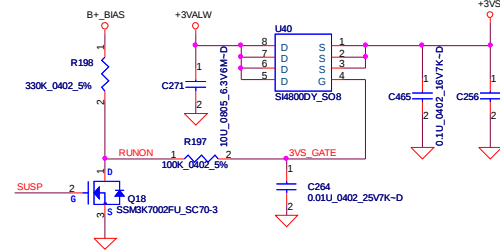


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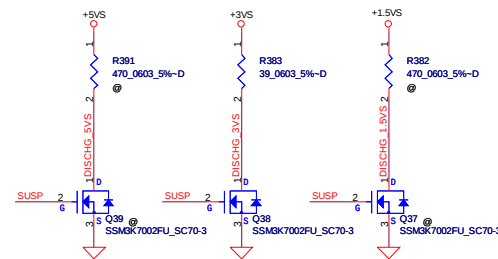
+5VALW to +5VS Transfer



+3VALW to +3VS Transfer



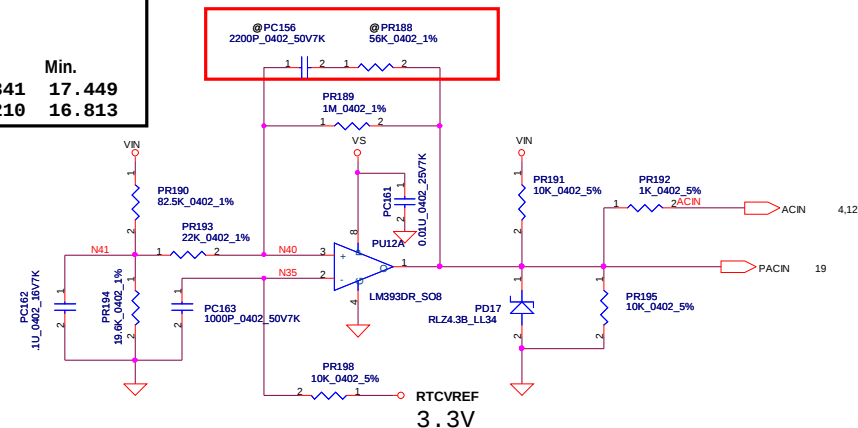
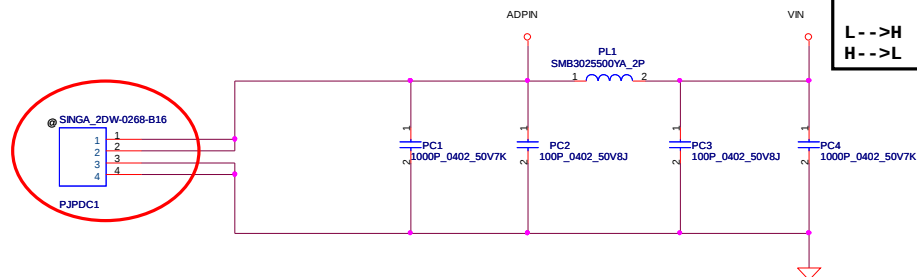
Discharge circuit



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Issued Date	2009/2/6	Deciphered Date	2010/2/6	Title	DC/DC Interface	
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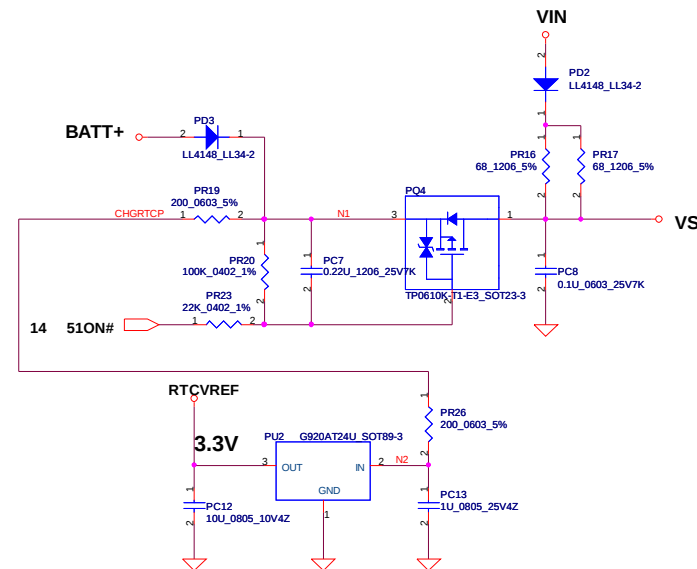
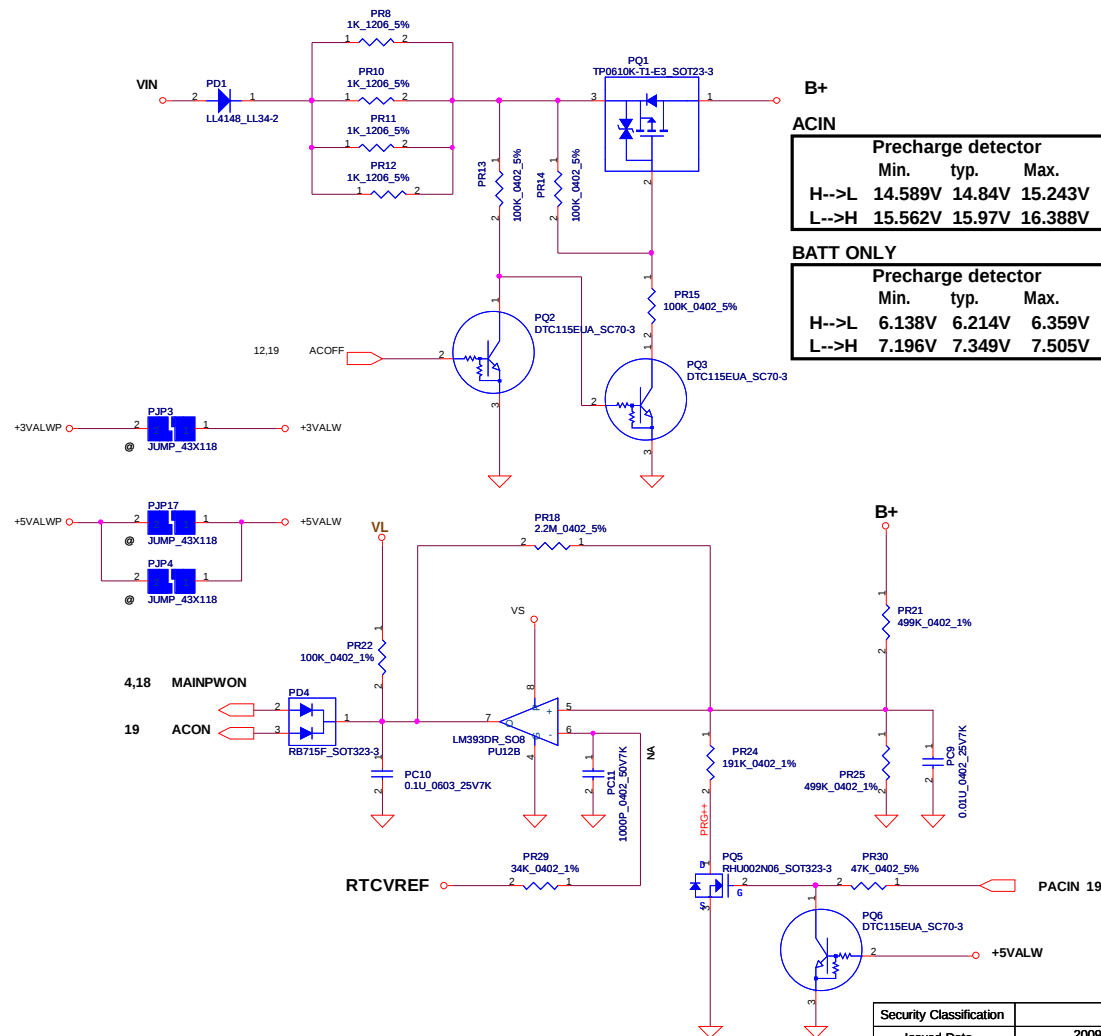
Vin Detector

	Max.	typ.	Min.
L-->H	18.234	17.841	17.449
H-->L	17.597	17.210	16.813

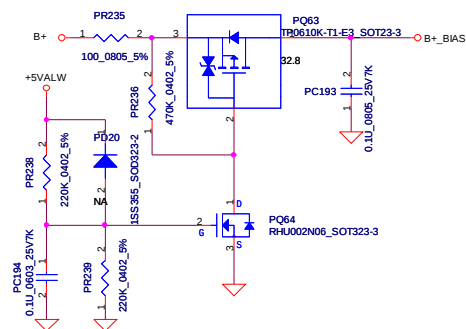


Precharge detector			
Min.	typ.	Max.	
H-->L	14.589V	14.84V	15.243V
L-->H	15.562V	15.97V	16.388V

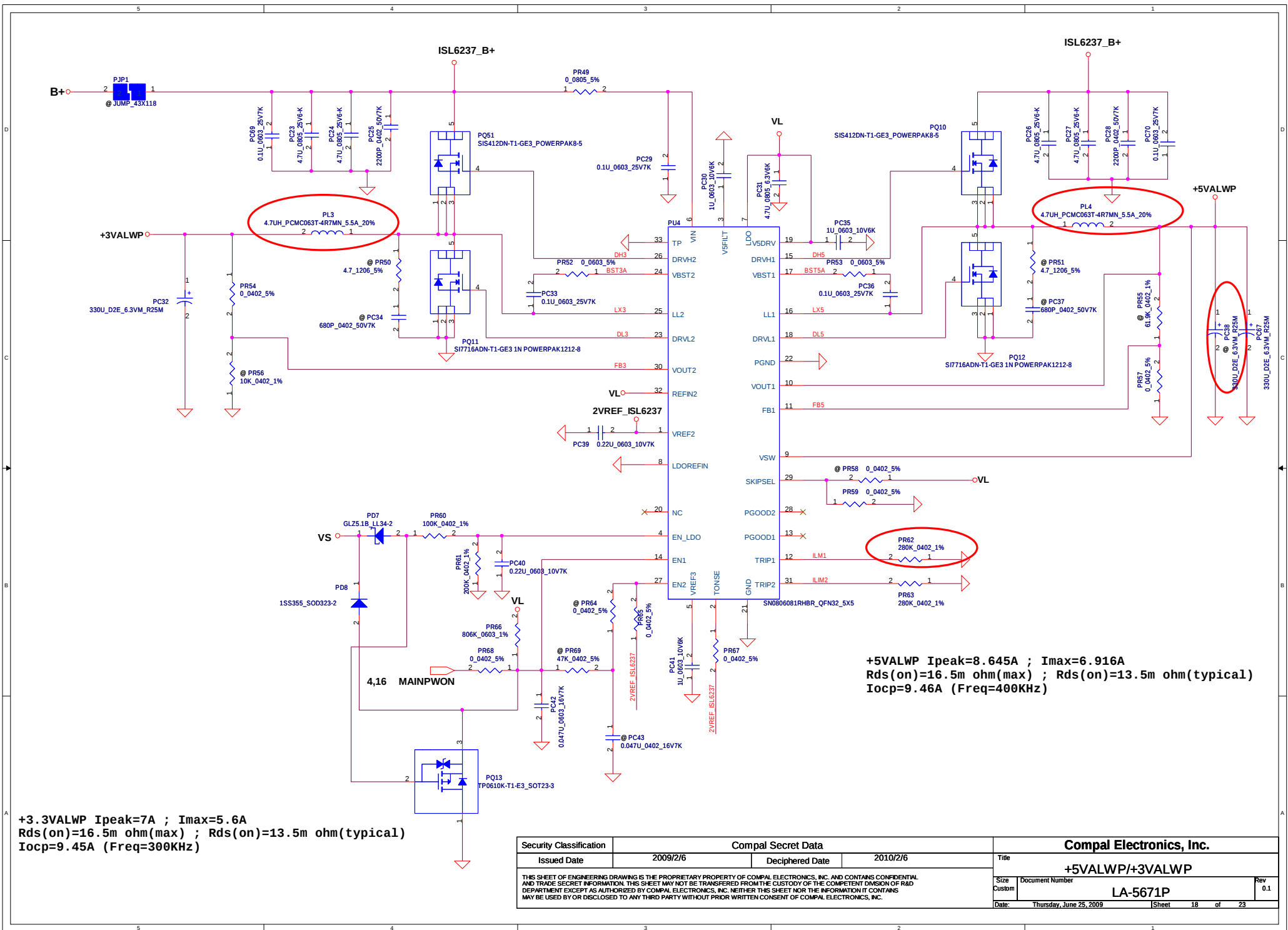
Precharge detector			
Min.	typ.	Max.	
H-->L	6.138V	6.214V	6.359V
L-->H	7.196V	7.349V	7.505V



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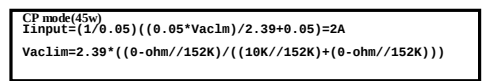


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Issued Date	2009/2/6	Deciphered Date	2010/2/6	Title BATTERY CONN			
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				+5VALWP/+3VALWP					
				Size		Document Number		Rev	
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				Date:		Thursday, June 25, 2009			
				Sheet		18 of 23			

$$CP(45W) = 85\% \cdot I_{ada} ; CP = 2A$$



CC=0.5~2.1A
IREF=1.548*Icharge
IREF=0.774V~3.25V

Version Change List (P. I. R. List)

Page 1

Item	Fixed Issue	Reason for change	Rev.	PG #	Modify List	Date	Phase
1		Move OTP circuit to CPU Board			Delete PU23、PD21、PQ65、PR354、PR355、PR356 PR357、PR358、PR359 Delete PC290、PC291、PC292	06/19	
2		Delete Battery OVP circuit for cost down			Delete PR330、PR332、PR353、PR337 Delete PC285、PC282	06/19	
3		Material shortage			Change PC193 from SE043104M80 to SE043104K80	06/19	
4		For common design			Add PR98-->10_0603_1%(SD014100A80) Change PR97 from 18.2k-ohm to 15.4k-ohm	06/19	
5		Change current sensing resisitor size			Change PR70、PR88 size from 2512 to 1206	06/19	
6		Cost down			Change PL5 from SH000005Z80 to SH000002L80	06/19	
7		Modify CP point for 45W adaptor			Change PR95 from 1.18k-ohm to 0-ohm	06/19	
8		Modify CC point			Change PR91 from 150k-ohm to 28.7k-ohm Change PR93 from 100k-ohm to 10k-ohm	06/19	
9							
10							
11							
12							
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14							
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16							

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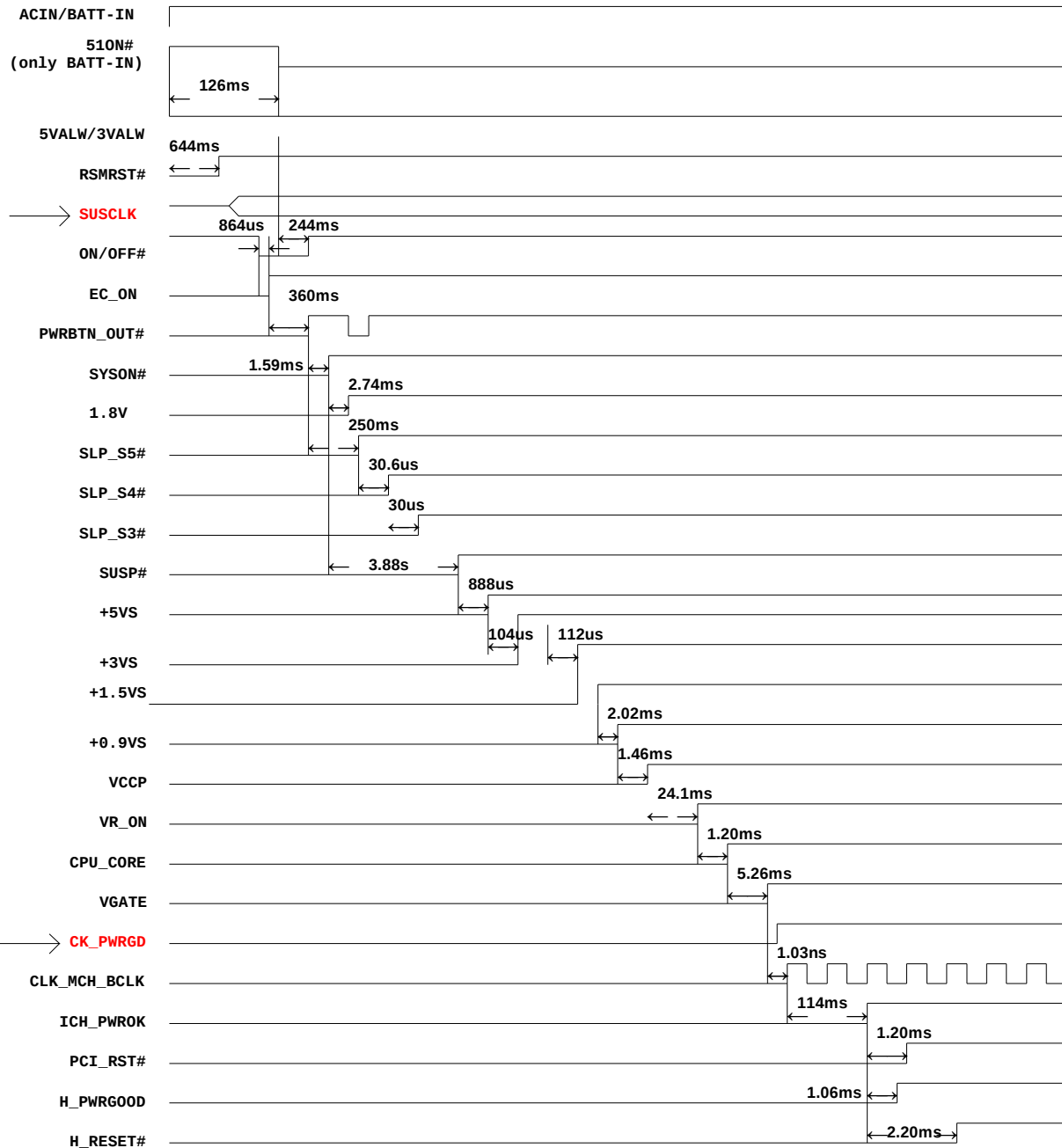
Compal Electronics, Inc.			
Title			
PWR-PIR			
Size	Document Number		Rev
	LA-5442P		0.1
Date:	Thursday, June 25, 2009		Sheet 20 of 23

Version change list (P.I.R. List)

Page 1 of 1 for HW

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List
1		ADD LID Function to Power Board	0.2		JFN1 Change 8pin to 12pin
2		Move OTP to CPU Board	0.2		JCPUB1 Pin 23 change MAINPWON
3		Change HDMI function	0.2		Change R69,R70 to HDMIDAT_NB,HDMICKL_NB
4		Change to IO2 Board	0.2		Del JSPK1,D12,D17
5		Change LID Function to Power Board	0.2		Modify JIOB1 Pin43
6		Change SPK to IO2 Board	0.2		Modify JIOB1 Pin21,23,25,27
7		ADD SATA Signal redriver	0.2		ADD U70,C513,C514,R1102-R1109
8		ADD WLAN LED Contral	0.2		ADD R446,R447
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