

Compal Confidential

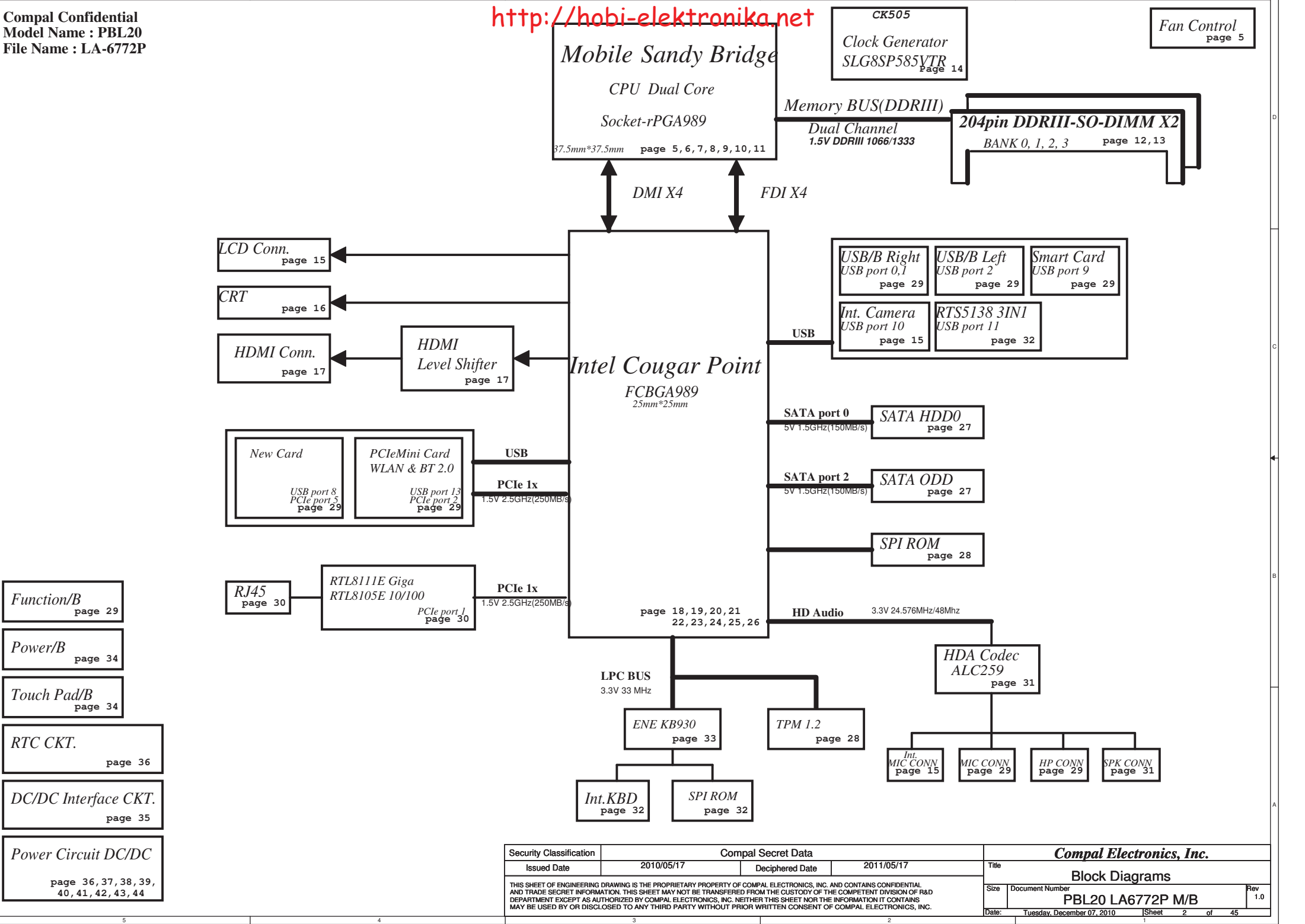
PBL20 Project

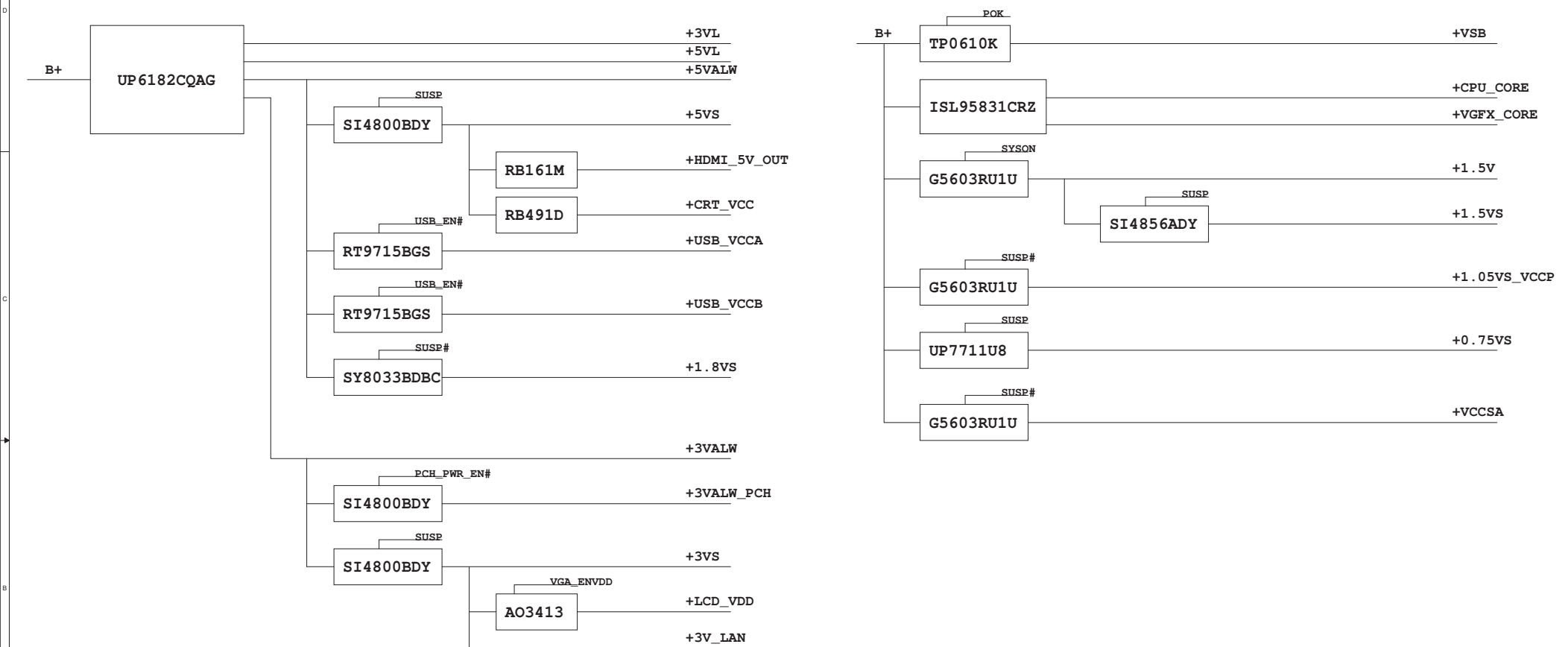
LA-6772P REV 1.0 Schematic

Intel Sandy Bridge/Cougar Point (UMA)

2010-12-07 Rev. 1.0

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2010/05/17	Deciphered Date	2011/05/17	Title	
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Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
BATT+	Battery power supply (12.6V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+VGA_CORE	Core voltage for GPU	ON	OFF	OFF
+VGFX_CORE	Core voltage for UMA graphic	ON	OFF	OFF
+0.75VS	+0.75VP to +0.75VS switched power rail for DDR terminator	ON	OFF	OFF
+1.0VSDGPU	+1.0VSPDGPU to +1.0VSDGPU switched power rail for GPU	ON	OFF	OFF
+1.05VS_VCCP	+1.05VS_VCCPP to +1.05VS_VCCP switched power rail for CPU	ON	OFF	OFF
+1.05VS_PCH	+1.05VS_VCCP to +1.05VS_PCH power for PCH	ON	OFF	OFF
+1.5V	+1.5VP to +1.5V power rail for DDRIII	ON	ON	OFF
+1.5VS	+1.5V to +1.5VS switched power rail	ON	OFF	OFF
+1.5VSDGPU	+1.5VS to +1.5VSDGPU switched power rail for GPU	ON	OFF	OFF
+1.8VS	(+5VALW or +3VALW) to 1.8V switched power rail to PCH & GPU	ON	OFF	OFF
+3VALW	+3VALW always on power rail	ON	ON	ON*
+3VALW_EC	+3VALW always to KBC	ON	ON	ON*
+3V_LAN	+3VALW to +3V_LAN power rail for LAN	ON	ON	ON*
+3VALW_PCH	+3VALW to +3VALW_PCH power rail for PCH (Short Jumper)	ON	ON	ON*
+3VS	+3VALW to +3VS power rail	ON	OFF	OFF
+5VALW	+5VALWP to +5VALW power rail	ON	ON	ON*
+5VALW_PCH	+5VALW to +5VALW_PCH power rail for PCH (Short resister)	ON	ON	ON*
+5VS	+5VALW to +5VS switched power rail	ON	OFF	OFF
+VSB	+VSBP to +VSB always on power rail for sequence control	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON
Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.				

STATE \ SIGNAL	SIGNAL							
	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

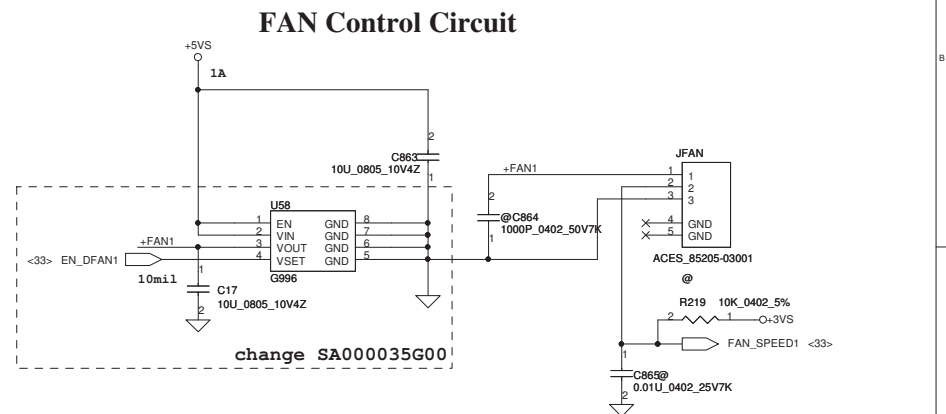
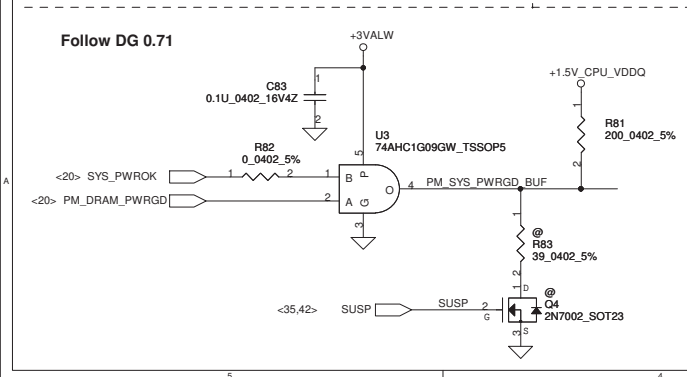
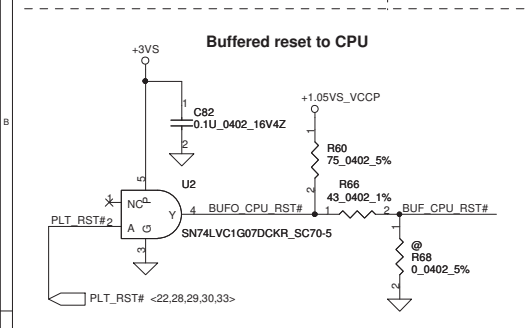
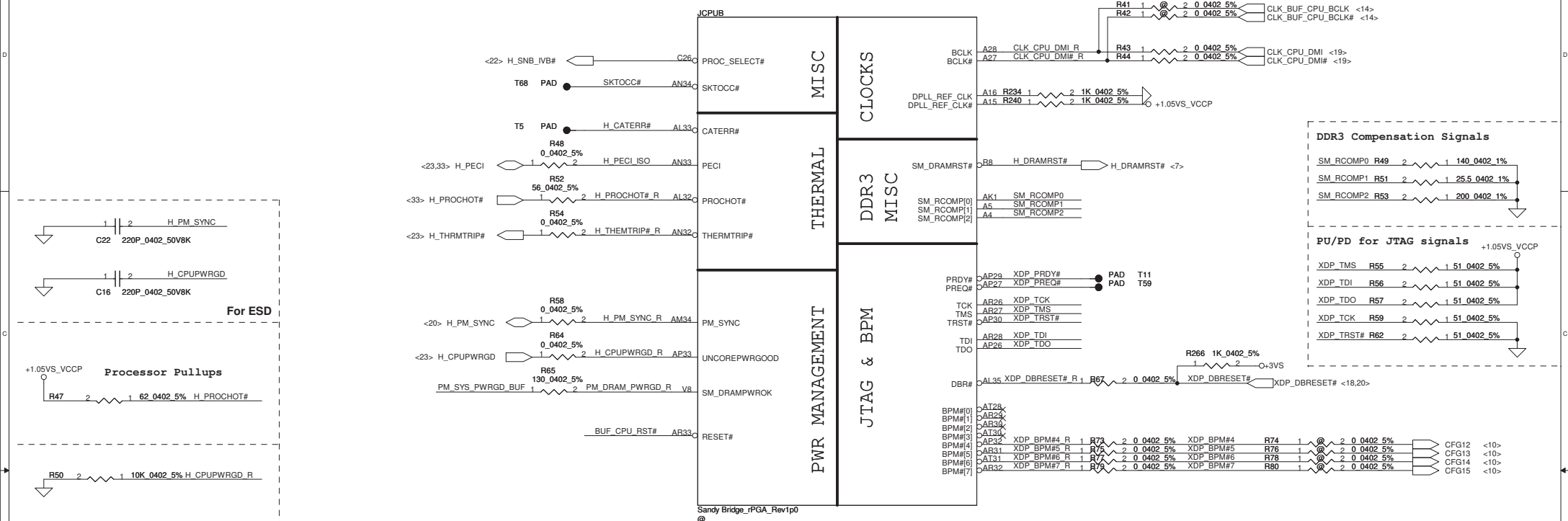
EC SM Bus1 address

EC SM Bus1 address		EC SM Bus2 address	
Device	Address	Device	Address
Smart Battery	0001 011X b		

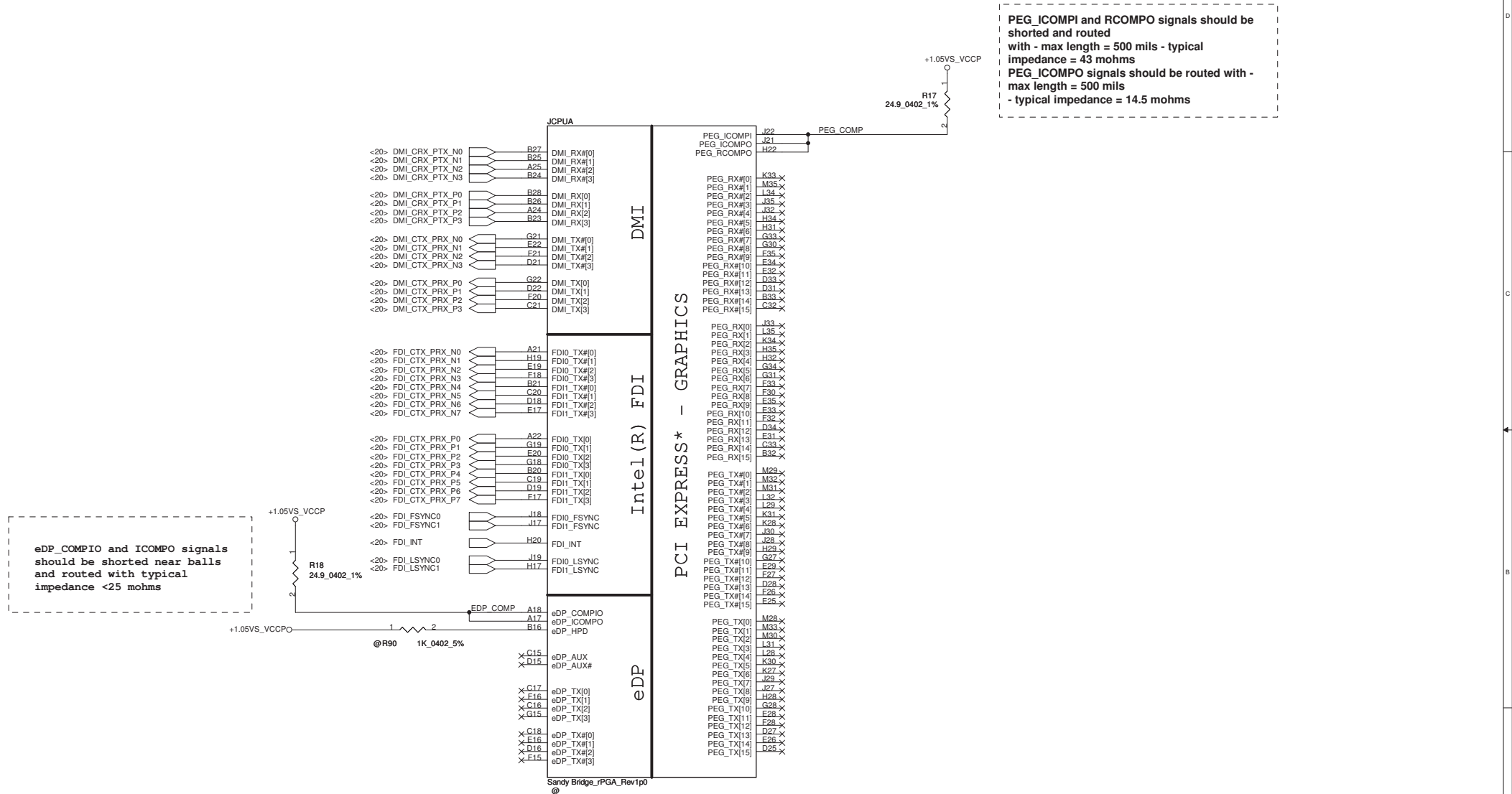
PCH SM Bus address

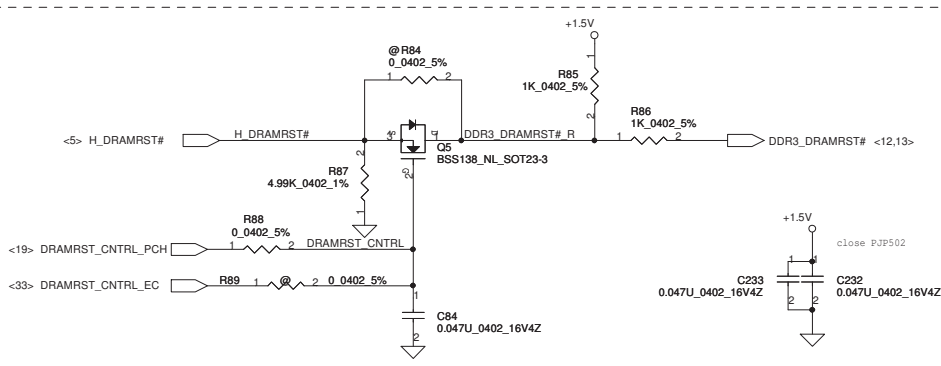
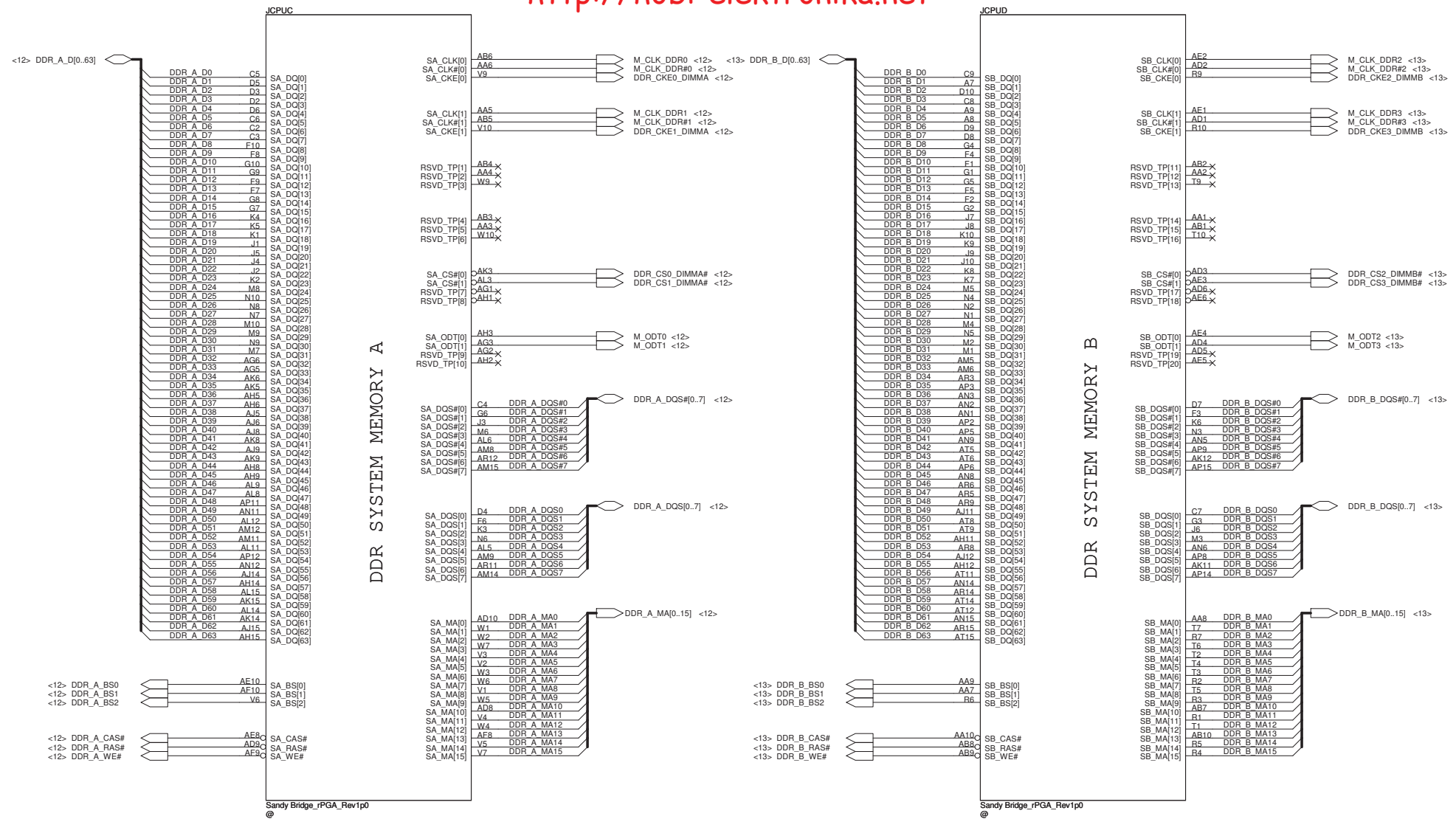
Device	Address
Clock Generator (9LVS3199AKLFT, RTM890N-631-VB-GRT)	1101 0010b
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

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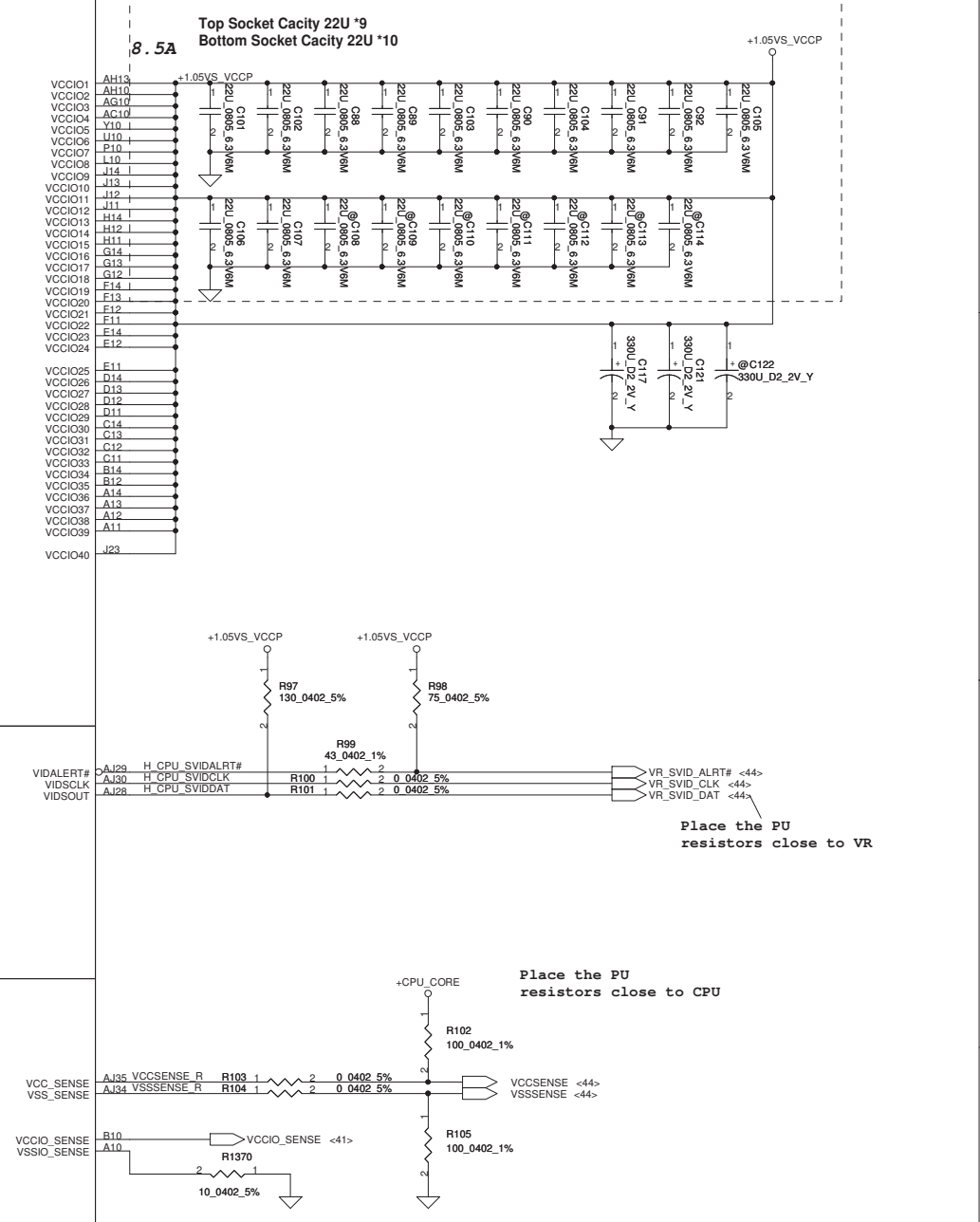
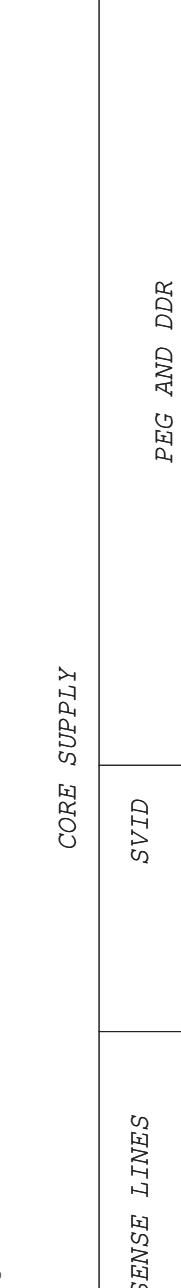
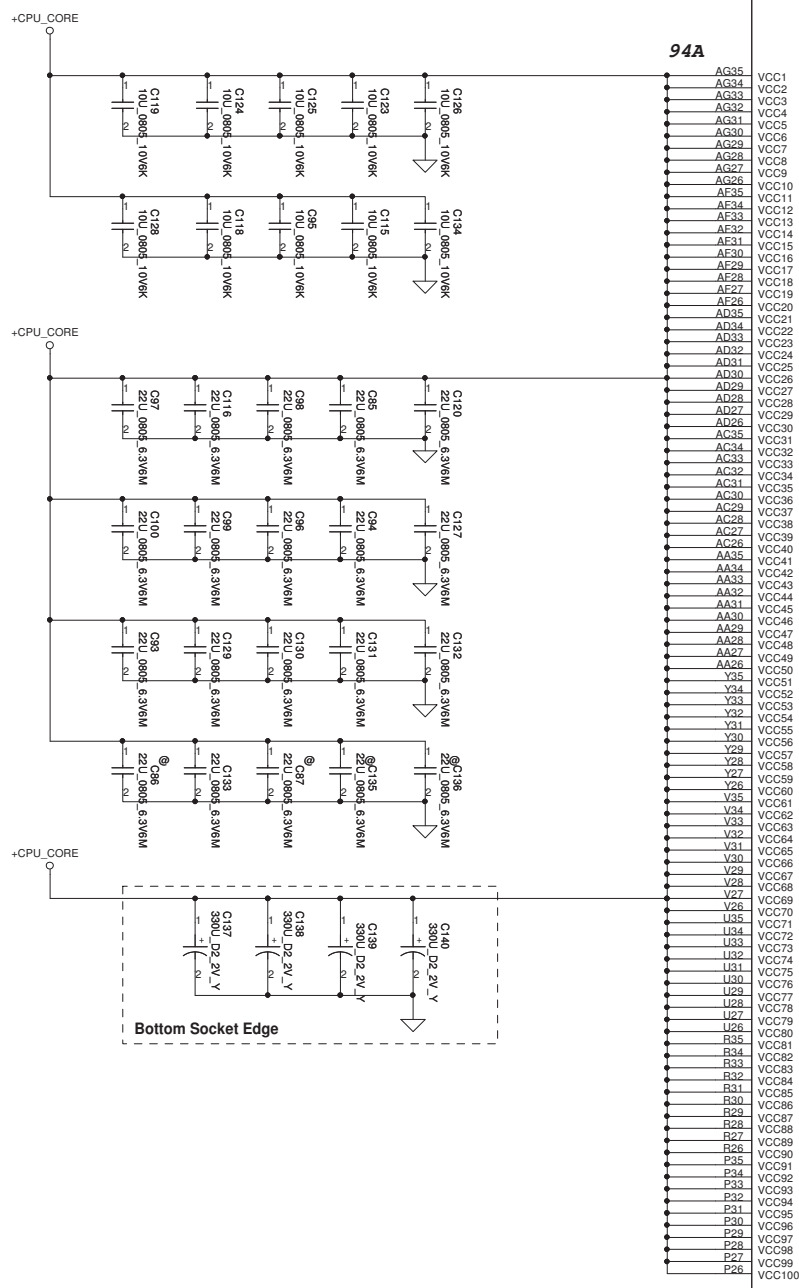


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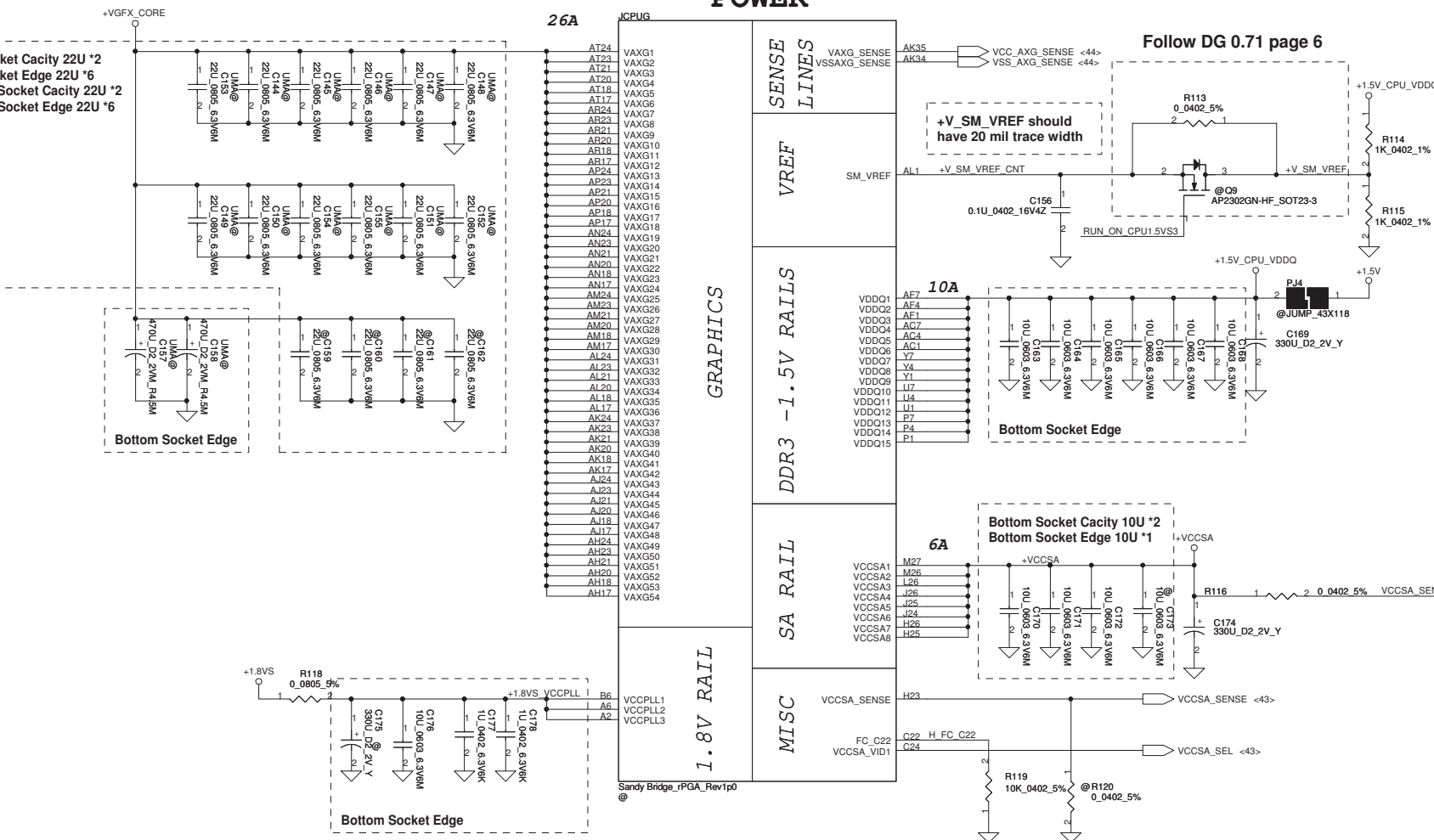


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Title		Compal Electronics, Inc.	
Size		PROCESSOR(3/7) DDRIII	
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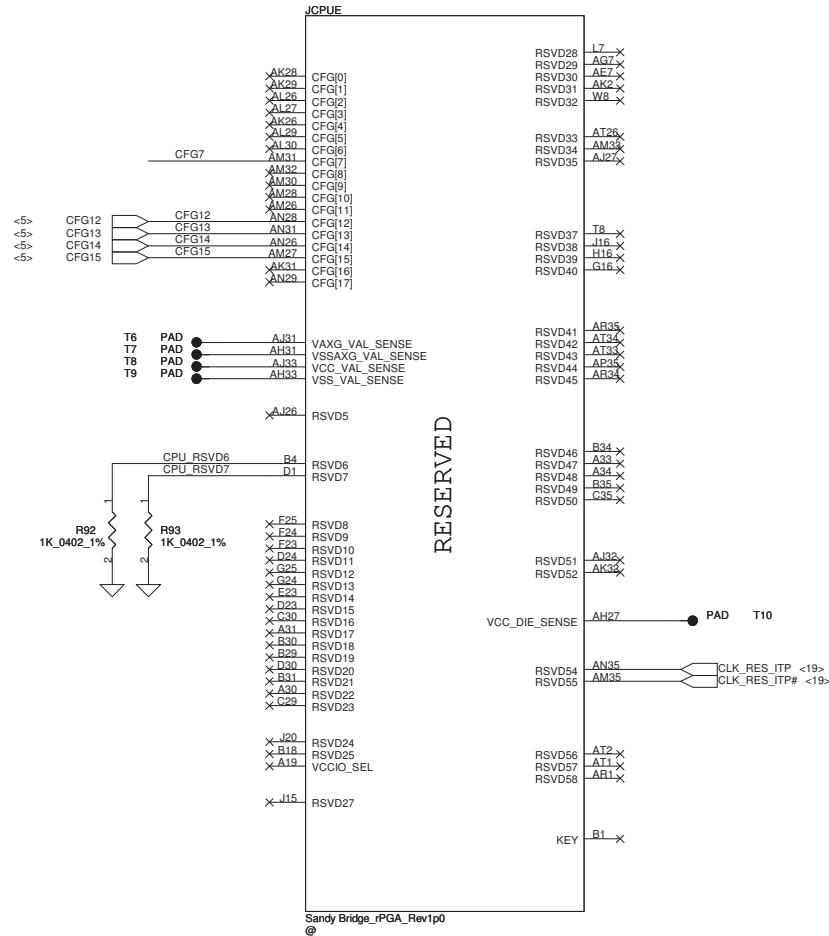


Sandy Bridge_rPGA Rev1p0

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				PROCESSOR(4/7) PWR,BYPASS				
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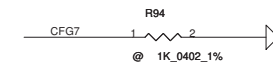
CFG Straps for Processor

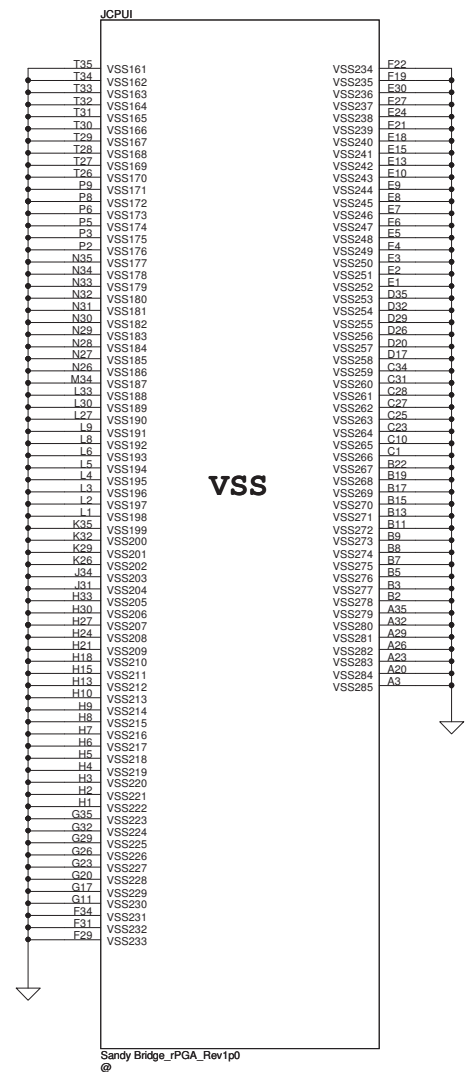
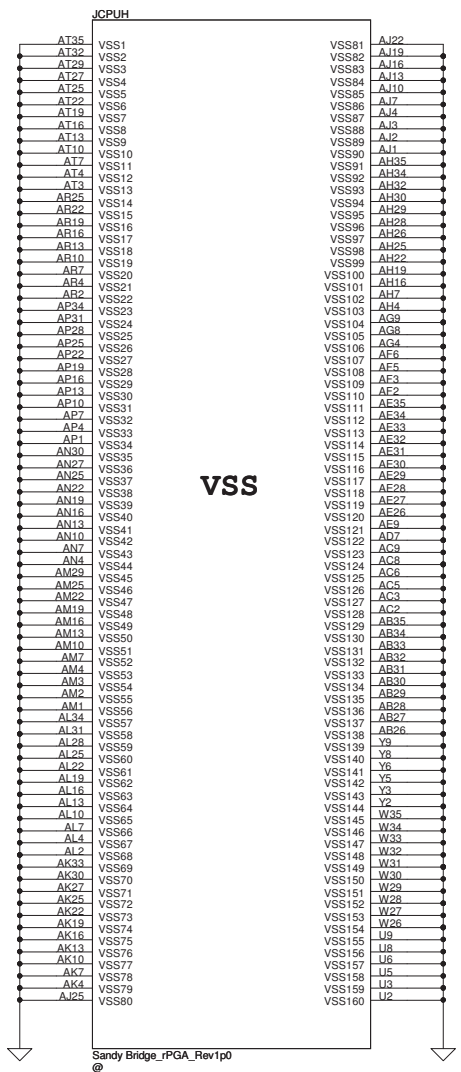
PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1: Normal Operation; Lane # definition matches socket pin map definition 0: Lane Reversed

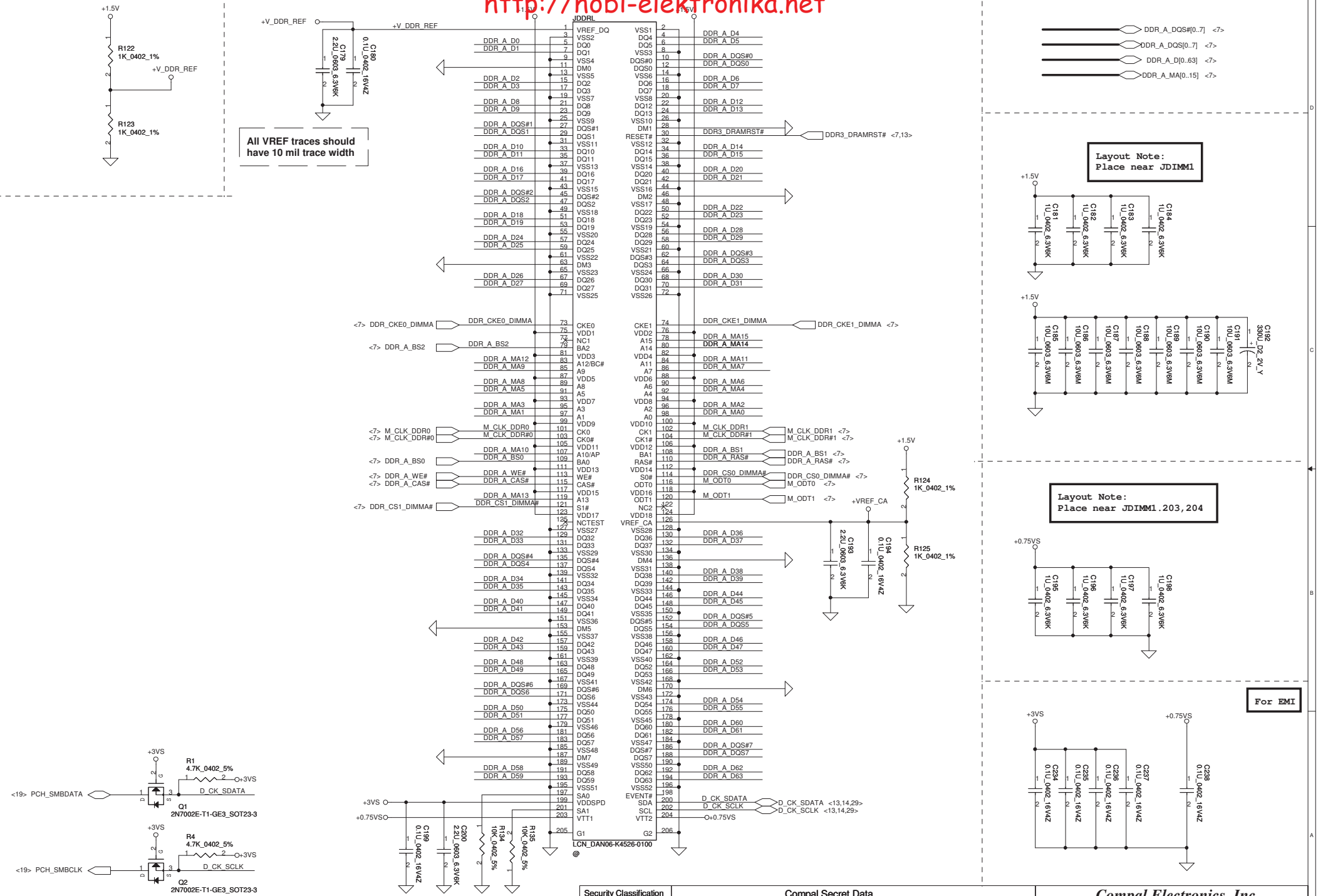
Display Port Presence Strap	
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port ★ 0 : Enabled; An external Display Port device is connected to the Embedded Display Port

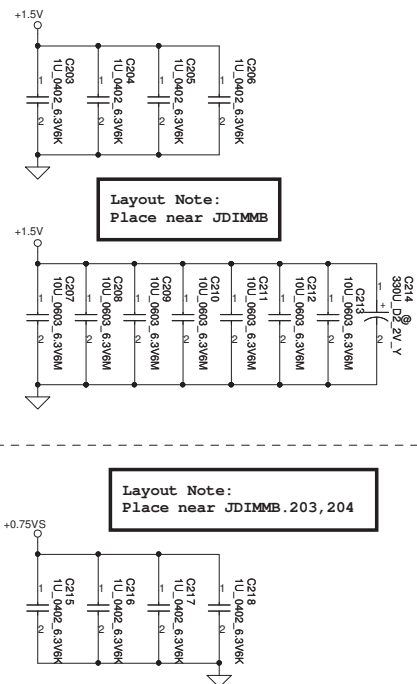
PCIe Port Bifurcation Straps	
CFG[6:5]	★11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8,x4,x4 - Device 1 functions 1 and 2 enabled

PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training



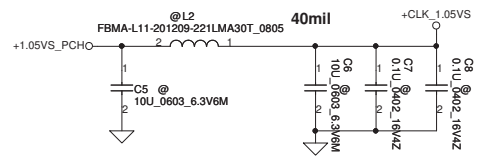
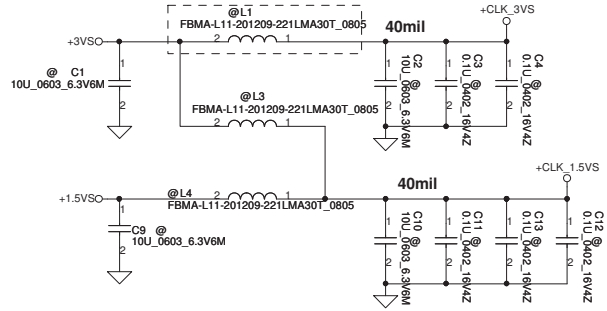




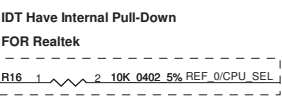
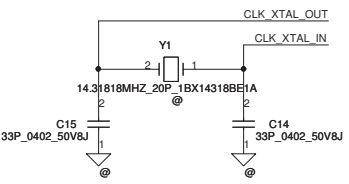
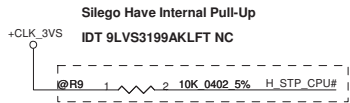


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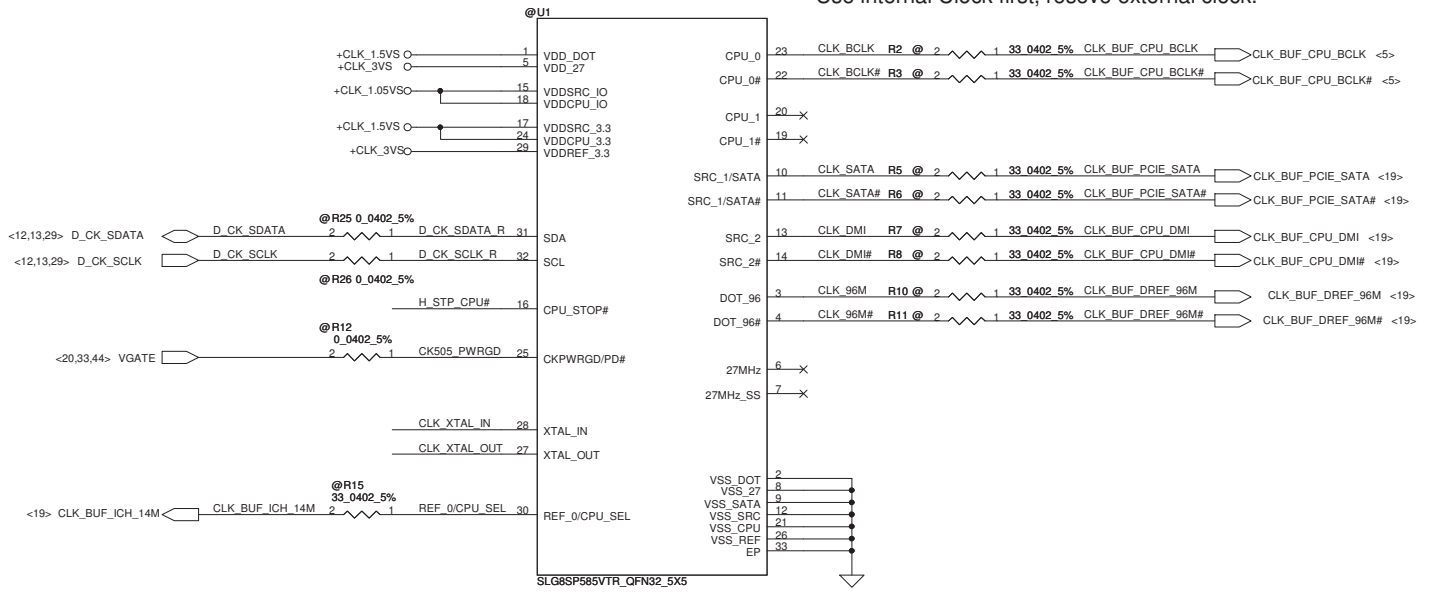
SM010014520 3000ma 220ohm@100mhz DCR 0.04



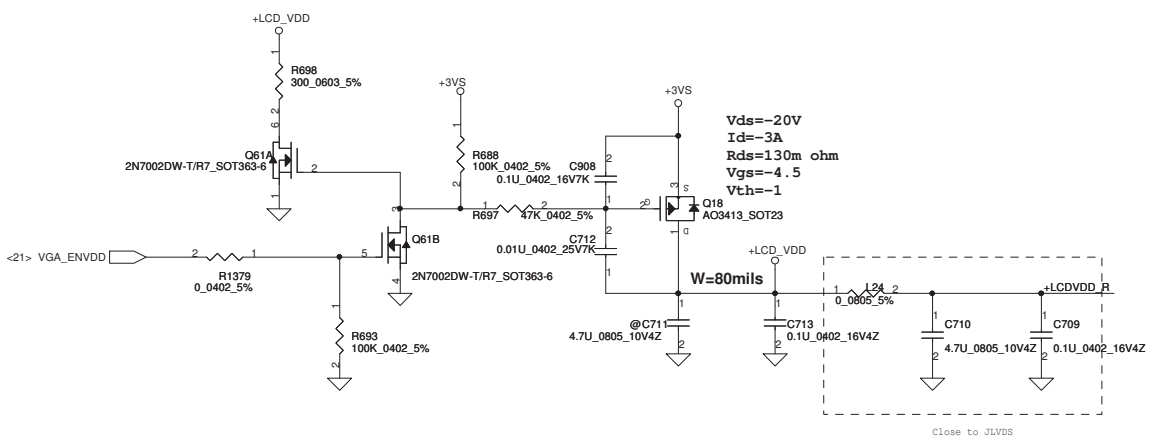
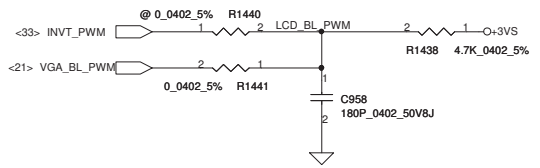
Use internal Clock first, reseve external clock.



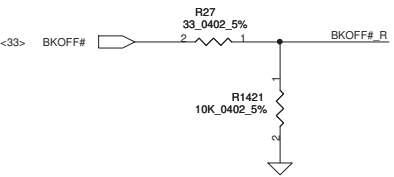
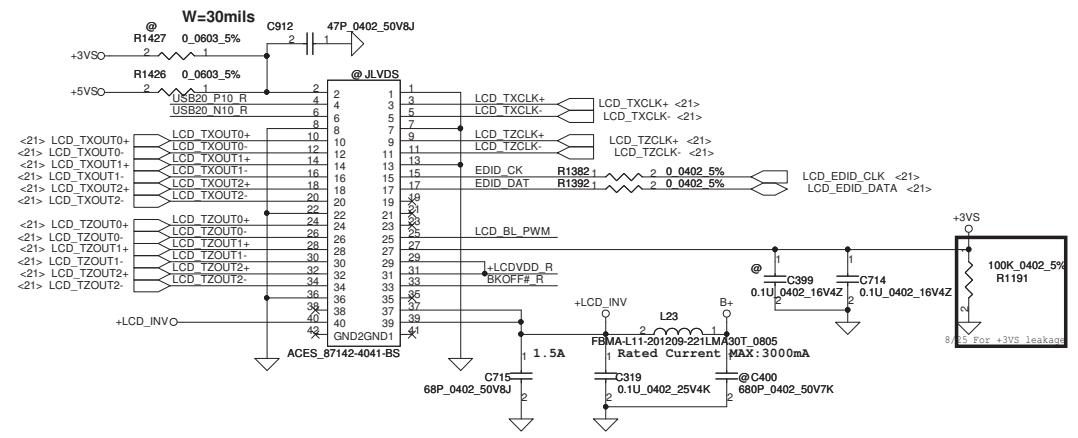
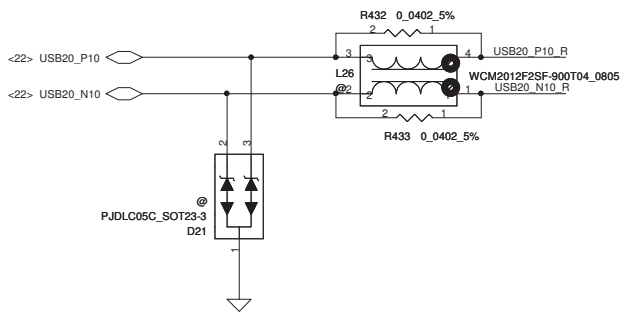
PIN 30	CPU_0	CPU_1
0 (Default)	133MHz	133MHz
1	100MHz	100MHz



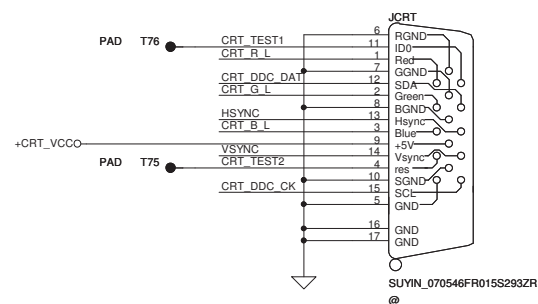
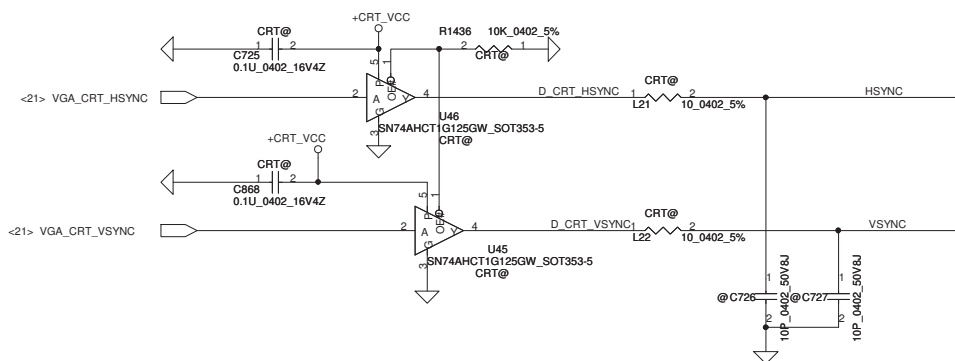
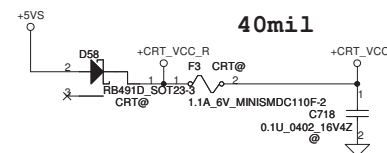
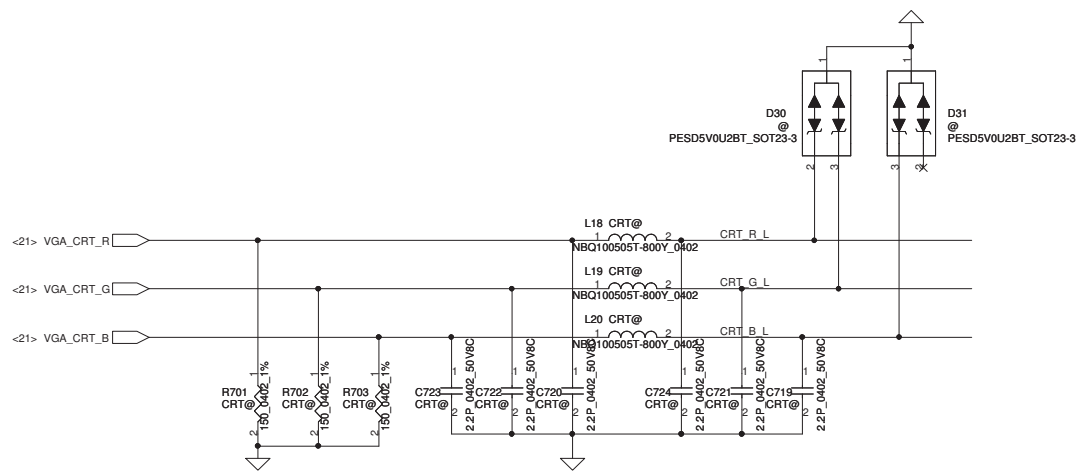
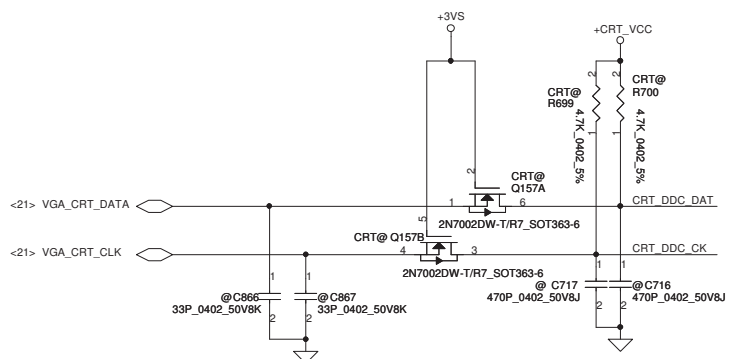
Standard
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SILEGO: SLG8SP587V(WF), SA00002XY10



LCD/PANEL BD. Conn.

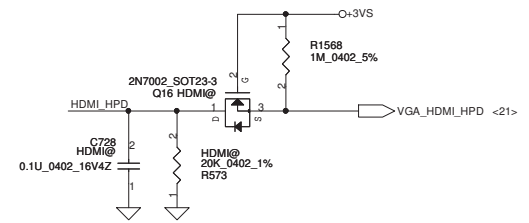
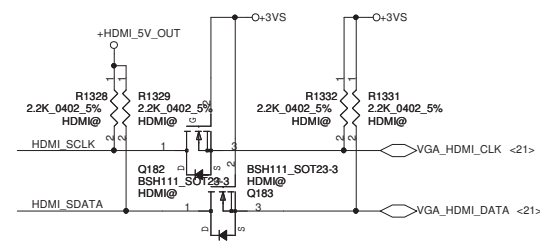
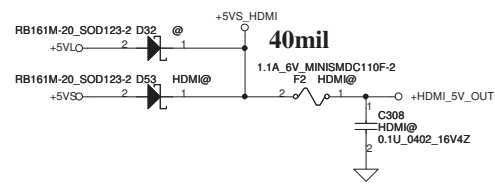
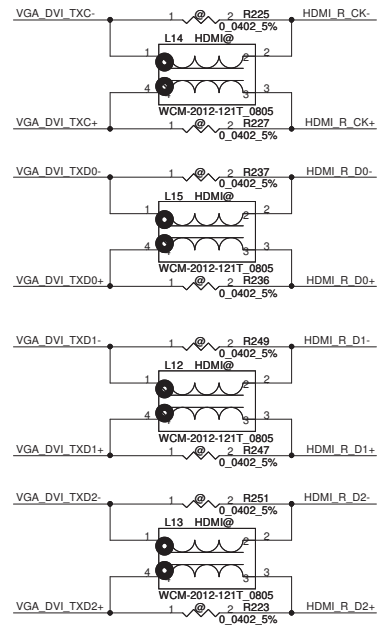


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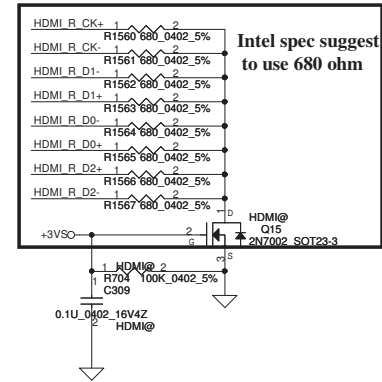


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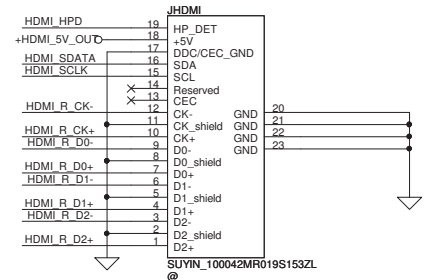
For EMI
Change 90 ohm
P/N:SM07000K00



<21> PCIE_MTX_GRX_HDMI_N3	CV1781	2	0.1U 0402 16V7K HDMI@	VGA DVI TXC-
<21> PCIE_MTX_GRX_HDMI_N0	CV1741	2	0.1U 0402 16V7K HDMI@	VGA DVI TXD2-
<21> PCIE_MTX_GRX_HDMI_N1	CV1721	2	0.1U 0402 16V7K HDMI@	VGA DVI TXD1-
<21> PCIE_MTX_GRX_HDMI_N2	CV1791	2	0.1U 0402 16V7K HDMI@	VGA DVI TXD0-
<21> PCIE_MTX_GRX_HDMI_P3	CV1761	2	0.1U 0402 16V7K HDMI@	VGA DVI TXC+
<21> PCIE_MTX_GRX_HDMI_P0	CV1751	2	0.1U 0402 16V7K HDMI@	VGA DVI TXD2+
<21> PCIE_MTX_GRX_HDMI_P1	CV1731	2	0.1U 0402 16V7K HDMI@	VGA DVI TXD1+
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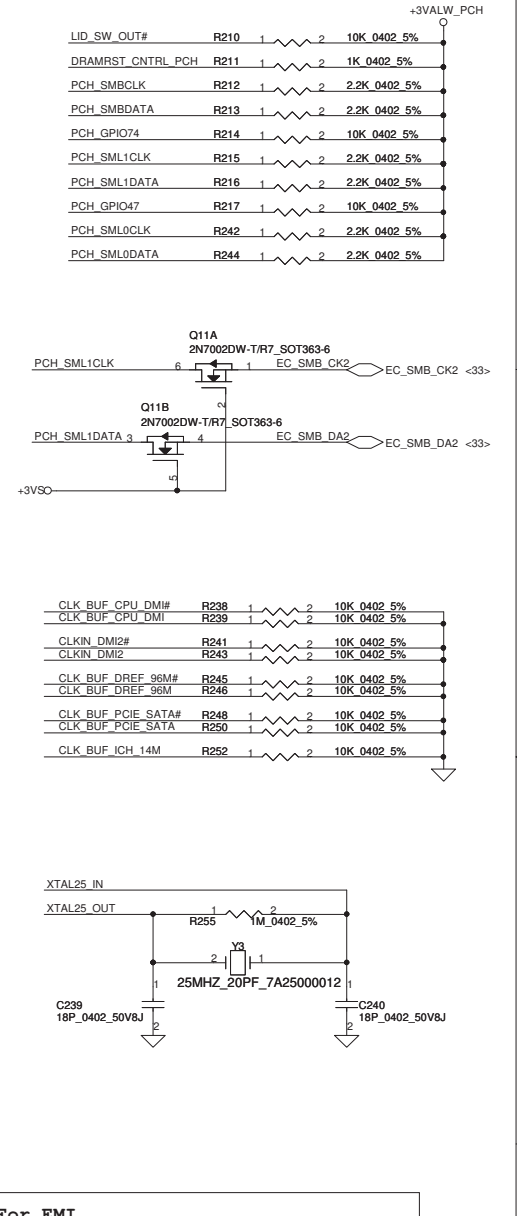
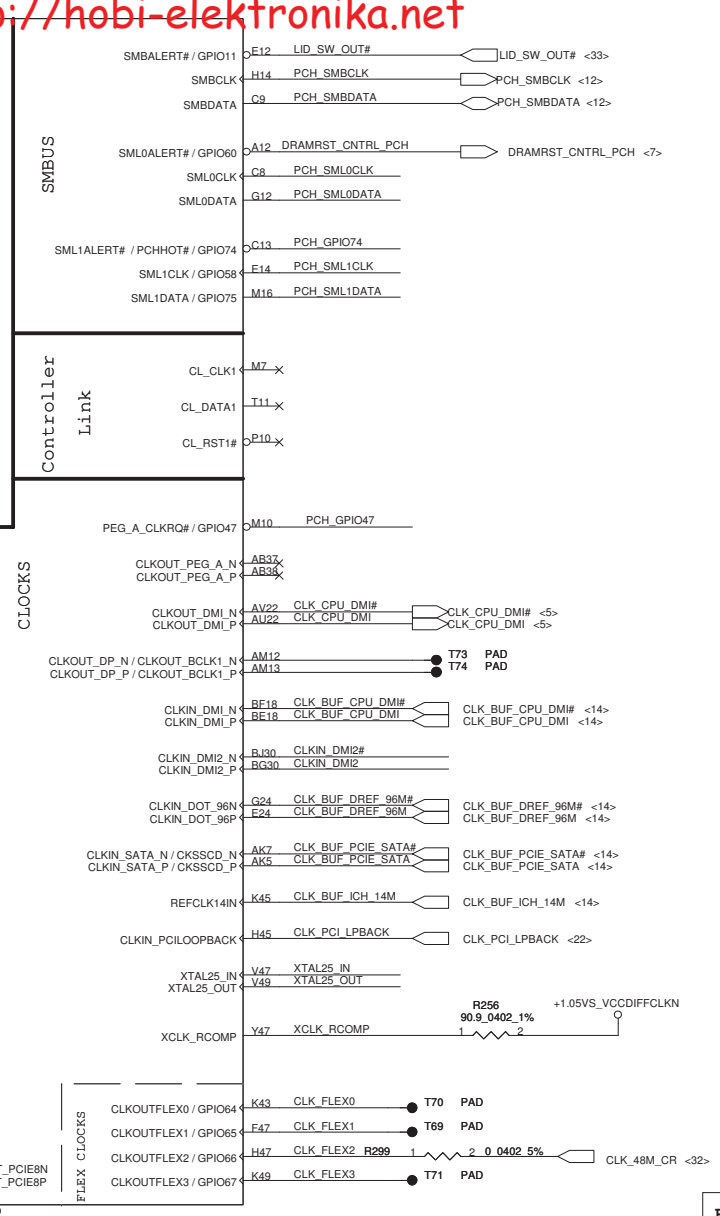
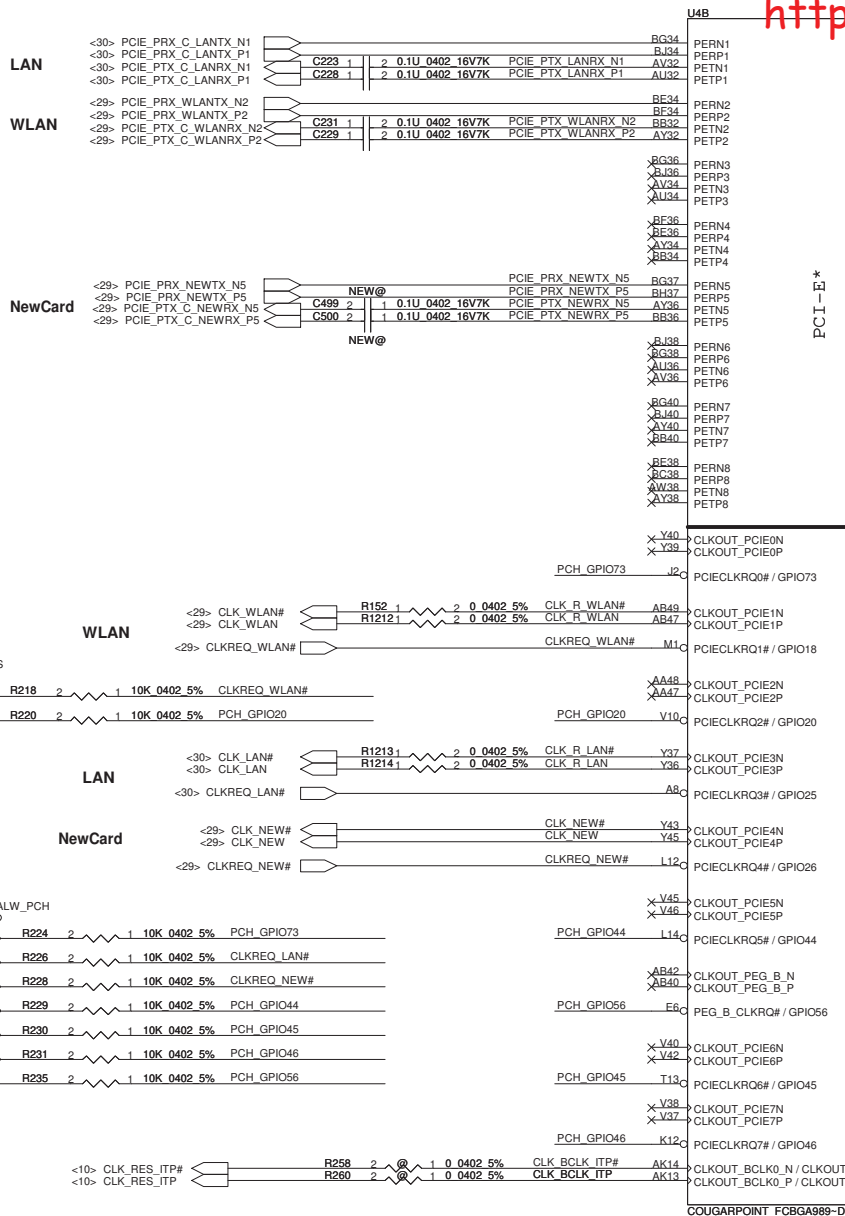
HDMI Connector



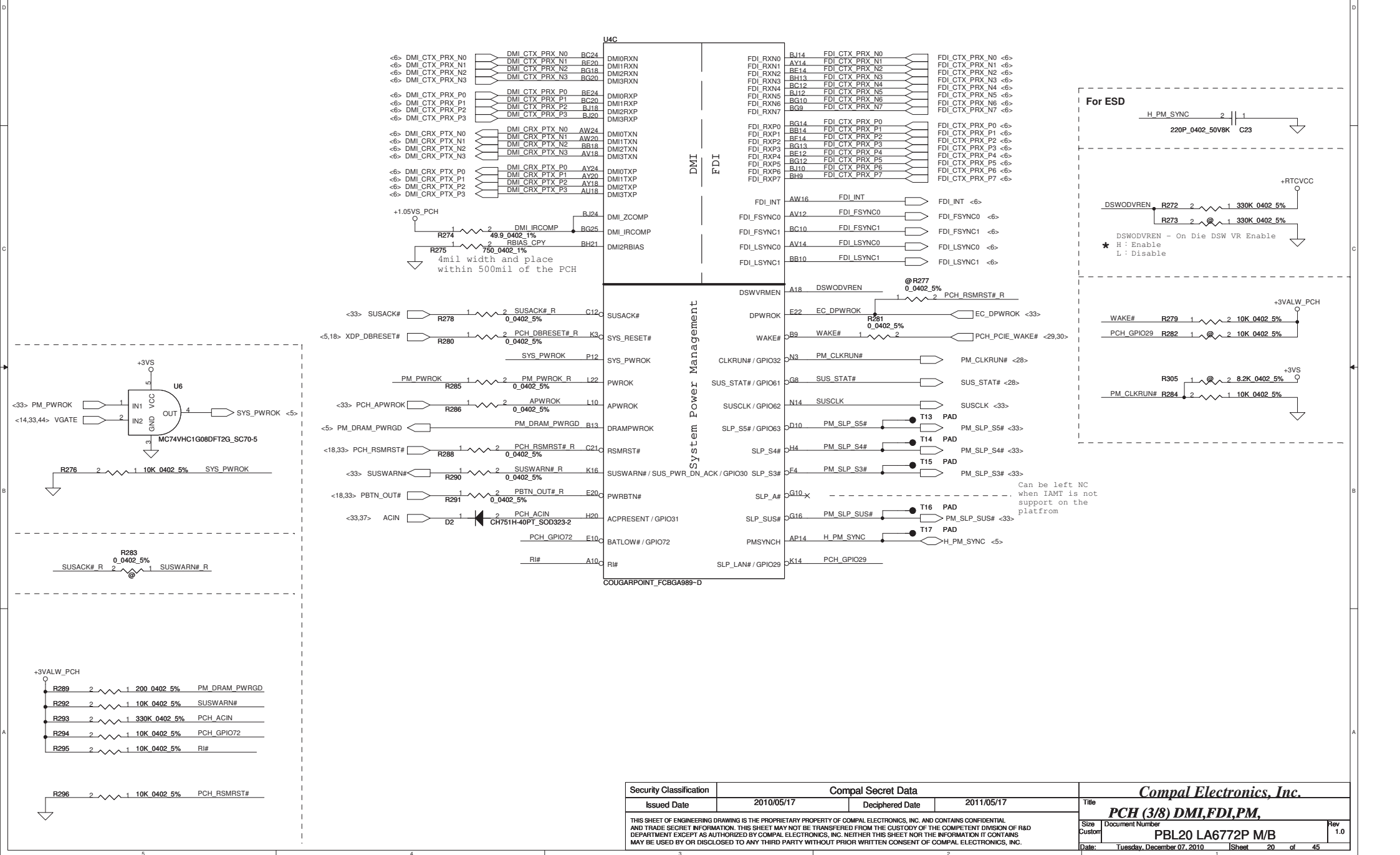
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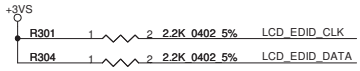
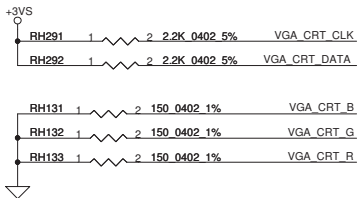
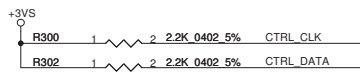
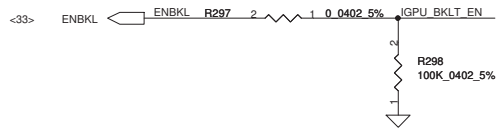
The schematic diagram illustrates the internal connections of the PBL20 LA6772P M/B. It features several key sections:

- INTVRMEN:** Includes integrated VRM enable/disable controls with pull-up resistors (R170, R171) and a note that the signal should always be pulled high.
- HDA_SDO:** Shows the HDA_SDO signal path with a pull-up resistor (R179) and a note about the weak internal pull-down.
- For EMI:** Details the EMI filter network with capacitors (C929) and resistors (R189, R1457).
- W=20mils:** Specifies the trace width for the power planes (RTCBATT, CHGRTC, +RTCVCC).
- Connectors:** Lists various connectors including RTCX1, RTCX2, RTCRST#, INTRUDER#, INTVRMEN, HDA_BIT_CLK, HDA_SYNC, HDA_SPKR, HDA_RST#, HDA_SDIN0, HDA_SDIN1, HDA_SDIN2, HDA_SDIN3, HDA_SDO, HDA_DOCK_EN#, HDA_DOCK_RST#, JTAG_TCK, JTAG_TMS, JTAG_TDI, JTAG_TDO, SPI_CLK, SPI_CS0#, SPI_CS1#, SPI_MOSI, SPI_MISO, SATA0RXN, SATA0RXP, SATA0TXN, SATA0TXP, SATA1RXN, SATA1RXP, SATA1TXN, SATA1TXP, SATA2RXN, SATA2RXP, SATA2TXN, SATA2TXP, SATA3RXN, SATA3RXP, SATA3TXN, SATA3TXP, SATA4RXN, SATA4RXP, SATA4TXN, SATA4TXP, SATA5RXN, SATA5RXP, SATA5TXN, SATA5TXP, SATAICOMPO, SATAICOMPI, SATA3RCOMPO, SATA3COMPI, SATA3RBIAS, SATALED#, SATA0GP / GPIO21, SATA1GP / GPIO19, SATA_LED#, BBS_BIT0_R, SERIRQ, and SATA_LED#.
- Components:** Includes resistors (R151, R170, R171, R176, R177, R178, R179, R180, R181, R184, R185, R186, R187, R188, R189, R190, R191, R192, R193, R194, R195, R196, R197, R198, R199, R200, R201, R202, R203, R204, R205, R206, R207, R208, R209, R210, R211, R212, R213, R214, R215, R216, R217, R218, R219, R220, R221, R222, R223, R224, R225, R226, R227, R228, R229, R230, R231, R232, R233, R234, R235, R236, R237, R238, R239, R240, R241, R242, R243, R244, R245, R246, R247, R248, R249, R250, R251, R252, R253, R254, R255, R256, R257, R258, R259, R260, R261, R262, R263, R264, R265, R266, R267, R268, R269, R270, R271, R272, R273, R274, R275, R276, R277, R278, R279, R280, R281, R282, R283, R284, R285, R286, R287, R288, R289, R290, R291, R292, R293, R294, R295, R296, R297, R298, R299, R300, R301, R302, R303, R304, R305, R306, R307, R308, R309, R310, R311, R312, R313, R314, R315, R316, R317, R318, R319, R320, R321, R322, R323, R324, R325, R326, R327, R328, R329, R330, R331, R332, R333, R334, R335, R336, R337, R338, R339, R340, R341, R342, R343, R344, R345, R346, R347, R348, R349, R350, R351, R352, R353, R354, R355, R356, R357, R358, R359, R360, R361, R362, R363, R364, R365, R366, R367, R368, R369, R370, R371, R372, R373, R374, R375, R376, R377, R378, R379, R380, R381, R382, R383, R384, R385, R386, R387, R388, R389, R390, R391, R392, R393, R394, R395, R396, R397, R398, R399, R400, R401, R402, R403, R404, R405, R406, R407, R408, R409, R410, R411, R412, R413, R414, R415, R416, R417, R418, R419, R420, R421, R422, R423, R424, R425, R426, R427, R428, R429, R430, R431, R432, R433, R434, R435, R436, R437, R438, R439, R440, R441, R442, R443, R444, R445, R446, R447, R448, R449, R450, R451, R452, R453, R454, R455, R456, R457, R458, R459, R460, R461, R462, R463, R464, R465, R466, R467, R468, R469, R470, R471, R472, R473, R474, R475, R476, R477, R478, R479, R480, R481, R482, R483, R484, R485, R486, R487, R488, R489, R490, R491, R492, R493, R494, R495, R496, R497, R498, R499, R500, R501, R502, R503, R504, R505, R506, R507, R508, R509, R510, R511, R512, R513, R514, R515, R516, R517, R518, R519, R520, R521, R522, R523, R524, R525, R526, R527, R528, R529, R530, R531, R532, R533, R534, R535, R536, R537, R538, R539, R540, R541, R542, R543, R544, R545, R546, R547, R548, R549, R550, R551, R552, R553, R554, R555, R556, R557, R558, R559, R560, R561, R562, R563, R564, R565, R566, R567, R568, R569, R570, R571, R572, R573, R574, R575, R576, R577, R578, R579, R580, R581, R582, R583, R584, R585, R586, R587, R588, R589, R590, R591, R592, R593, R594, R595, R596, R597, R598, R599, R600, R601, R602, R603, R604, R605, R606, R607, R608, R609, R610, R611, R612, R613, R614, R615, R616, R617, R618, R619, R620, R621, R622, R623, R624, R625, R626, R627, R628, R629, R630, R631, R632, R633, R634, R635, R636, R637, R638, R639, R640, R641, R642, R643, R644, R645, R646, R647, R648, R649, R650, R651, R652, R653, R654, R655, R656, R657, R658, R659, R660, R661, R662, R663, R664, R665, R666, R667, R668, R669, R670, R671, R672, R673, R674, R675, R676, R677, R678, R679, R680, R681, R682, R683, R684, R685, R686, R687, R688, R689, R690, R691, R692, R693, R694, R695, R696, R697, R698, R699, R700, R701, R702, R703, R704, R705, R706, R707, R708, R709, R710, R711, R712, R713, R714, R715, R716, R717, R718, R719, R720, R721, R722, R723, R724, R725, R726, R727, R728, R729, R730, R731, R732, R733, R734, R735, R736, R737, R738, R739, R740, R741, R742, R743, R744, R745, R746, R747, R748, R749, R750, R751, R752, R753, R754, R755, R756, R757, R758, R759, R760, R761, R762, R763, R764, R765, R766, R767, R768, R769, R770, R771, R772, R773, R774, R775, R776, R777, R778, R779, R780, R781, R782, R783, R784, R785, R786, R787, R788, R789, R790, R791, R792, R793, R794, R795, R796, R797, R798, R799, R800, R801, R802, R803, R804, R805, R806, R807, R808, R809, R810, R811, R812, R813, R814, R815, R816, R817, R818, R819, R820, R821, R822, R823, R824, R825, R826, R827, R828, R829, R830, R831, R832, R833, R834, R835, R836, R837, R838, R839, R840, R841, R842, R843, R844, R845, R846, R847, R848, R849, R850, R851, R852, R853, R854, R855, R856, R857, R858, R859, R860, R861, R862, R863, R864, R865, R866, R867, R868, R869, R870, R871, R872, R873, R874, R8

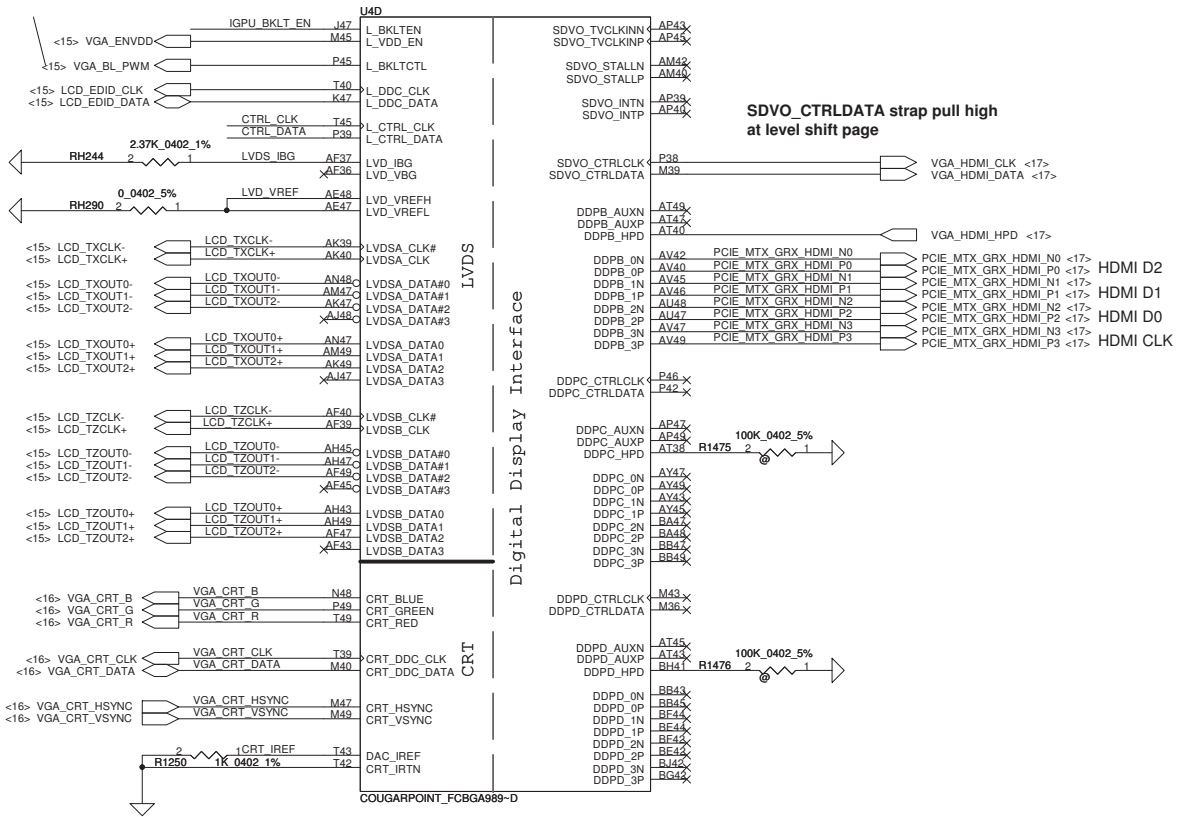


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				Document Number
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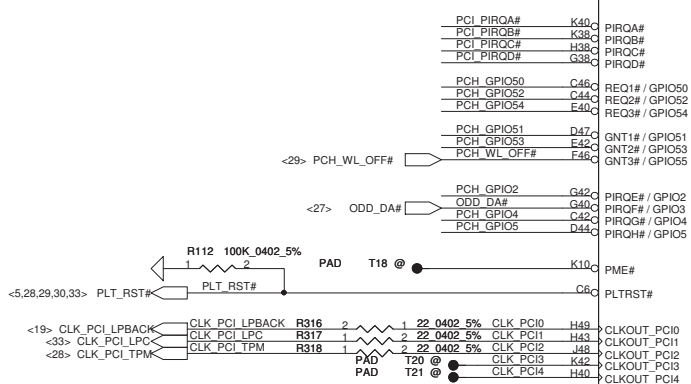
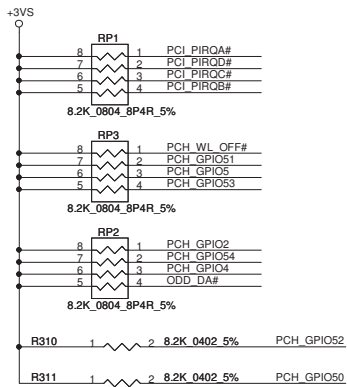


Pull high at LVDS conn side.



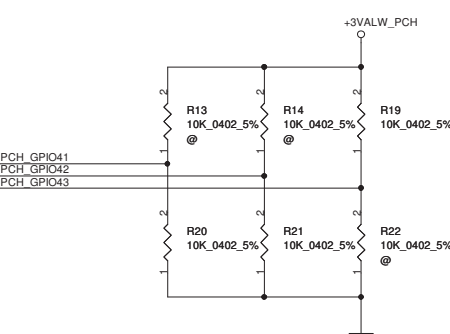
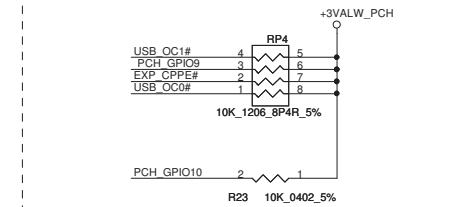
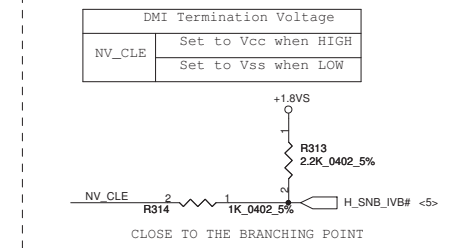
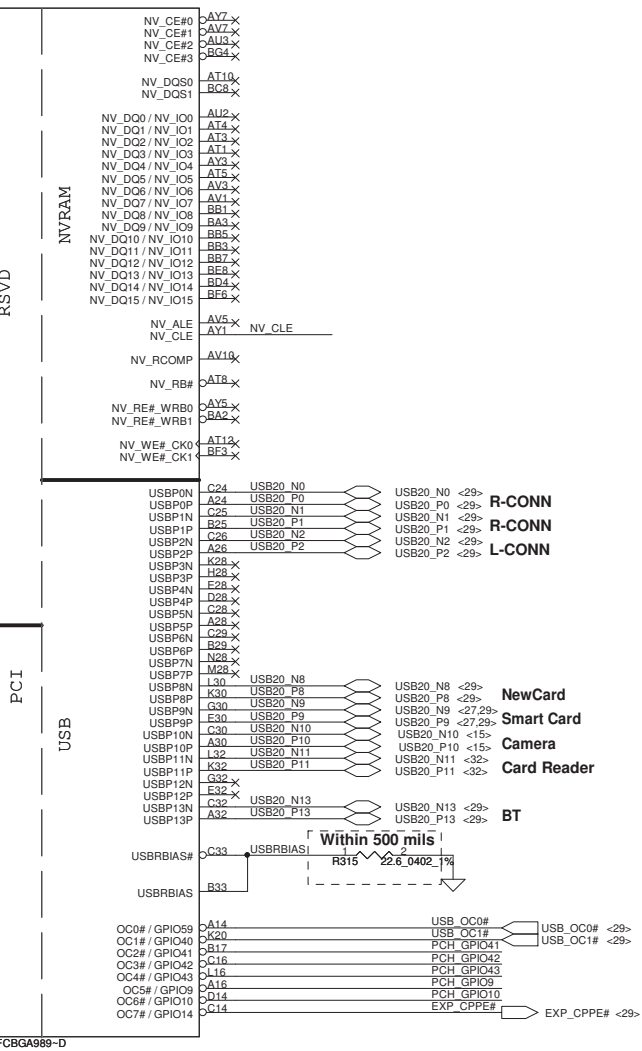
SDVO_CTRLDATA strap pull high at level shift page

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HDMI D0
HDMI CLK

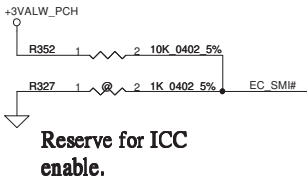


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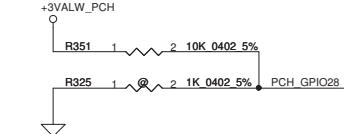
Boot BIOS Strap bit1 BBS1		
	Bit11	Bit10
BBS bit1 /BBS bit0	0	1
	1	0
	1	1
	0	0



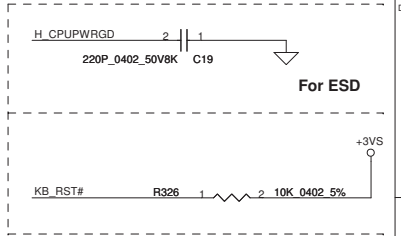
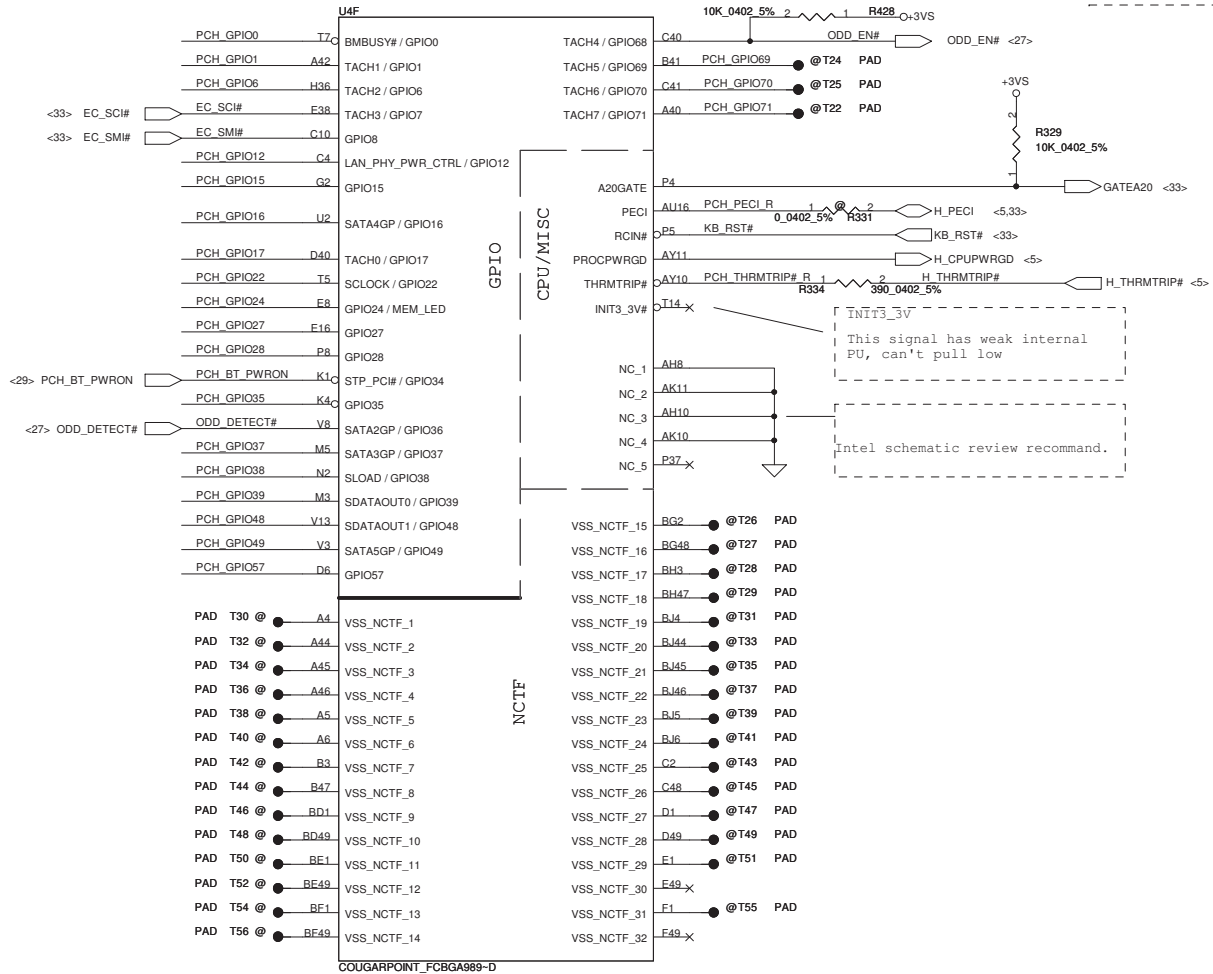
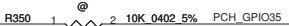
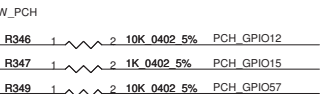
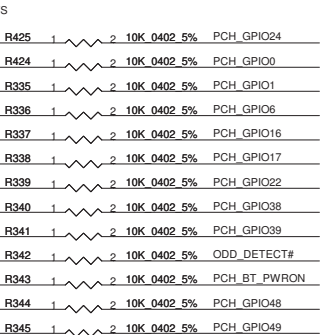
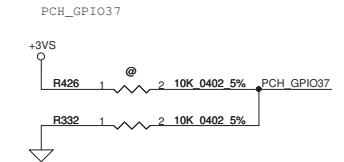
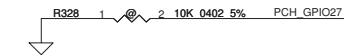
	GPIO43	GPIO42	GPIO41
PBL00	0	0	0
PBL01	0	0	1
PBL10	0	1	0
PBL11	0	1	1
PBL20	1	0	0
PBL21	1	0	1



GPIO28
On-Die PLL Voltage Regulator
This signal has a weak internal pull up
★ H : On-Die voltage regulator enable
L : On-Die PLL Voltage Regulator disable

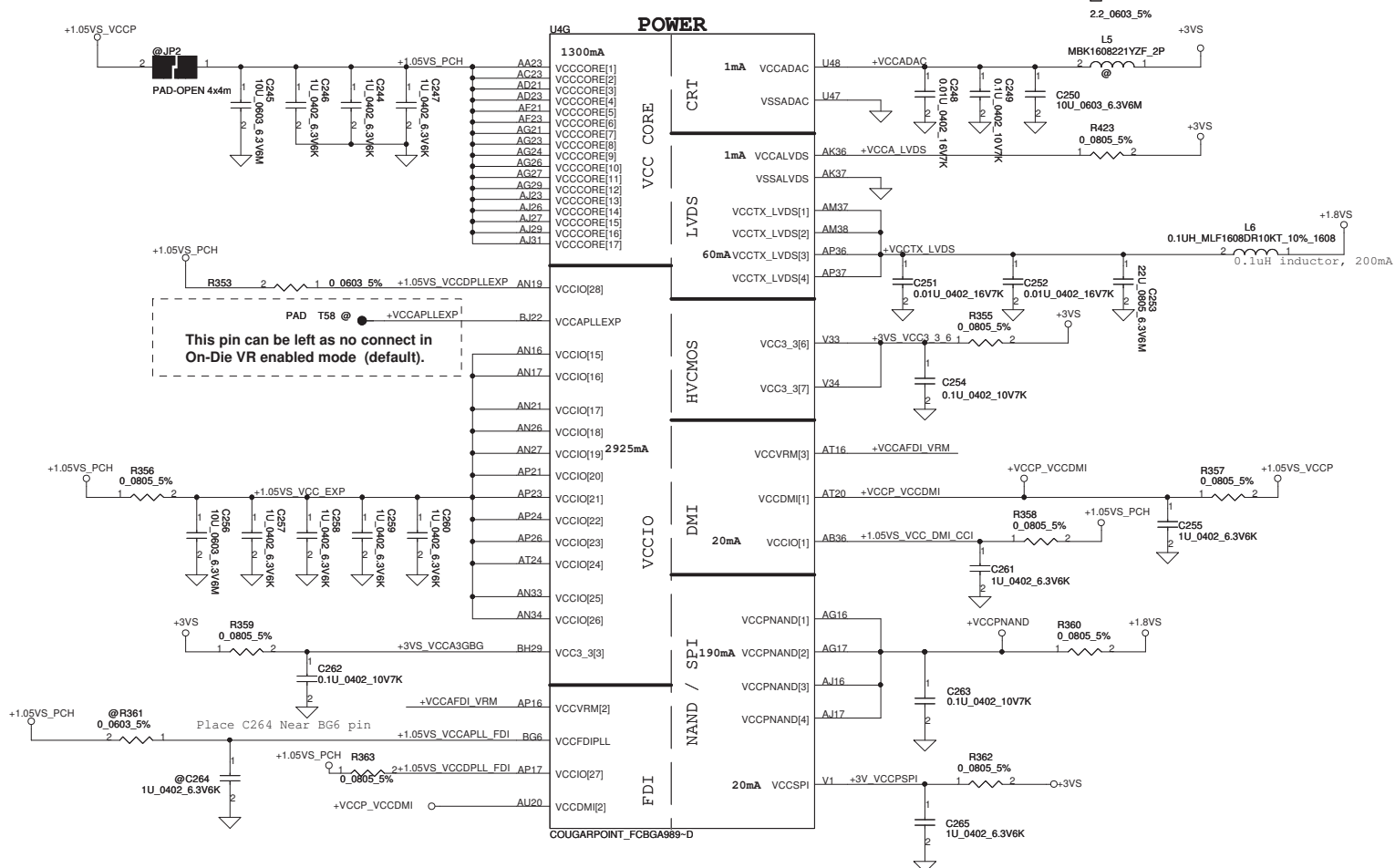


PCH_GPIO27 (Have internal Pull-High)
★ High: VCCVRM VR Enable
Low: VCCVRM VR Disable

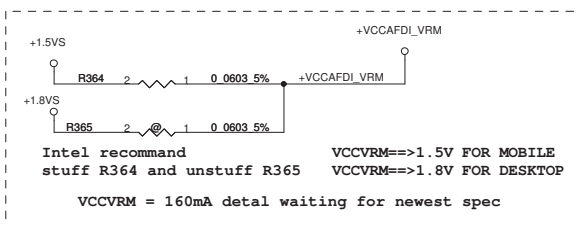


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Deciphered Date				2011/05/17				PCH (6/9) GPIO, CPU, MISC			
Size				Document Number				PBL20 LA6772P M/B			
Date				Tuesday, December 07, 2010				Rev 1.0			
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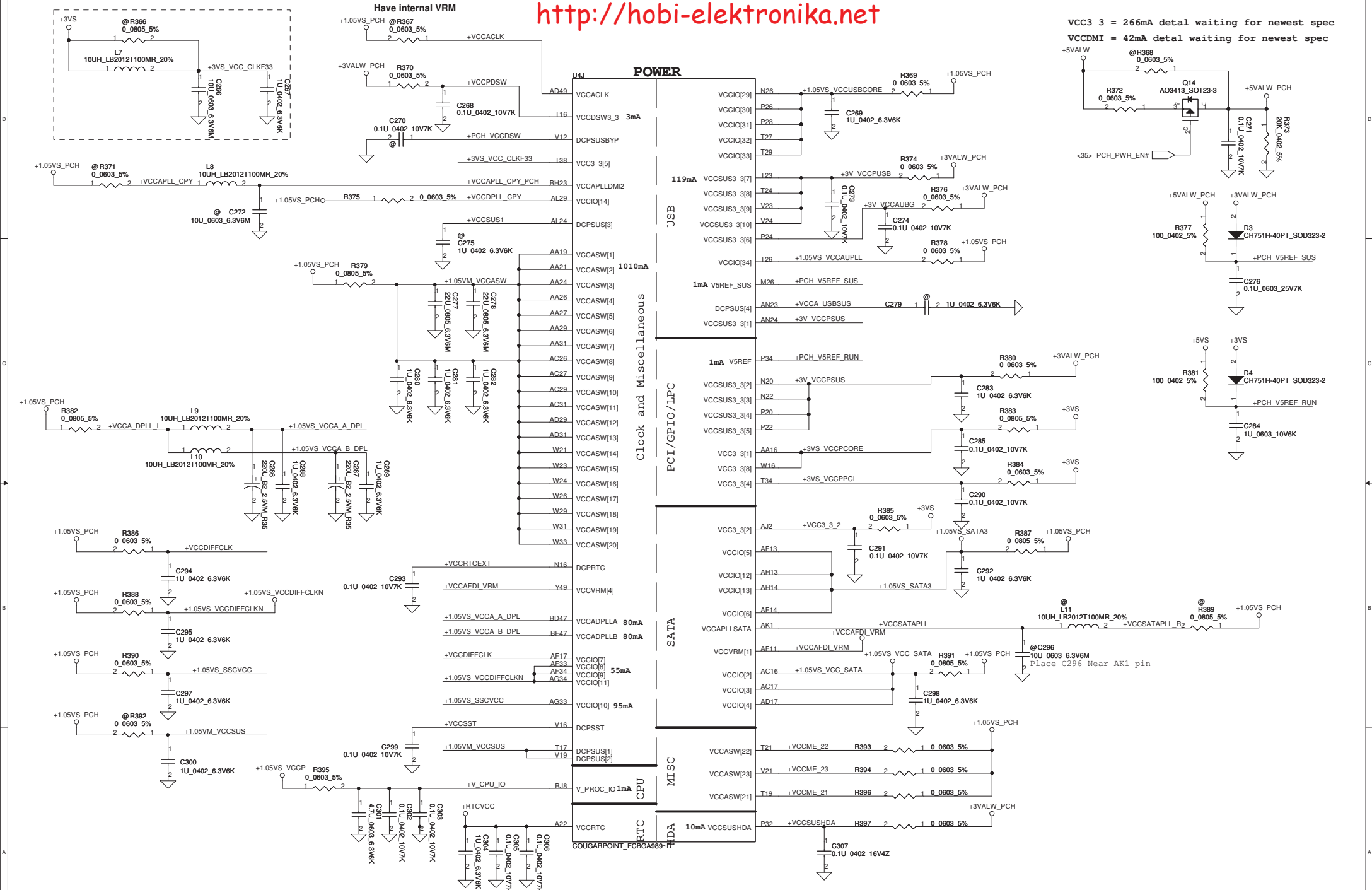
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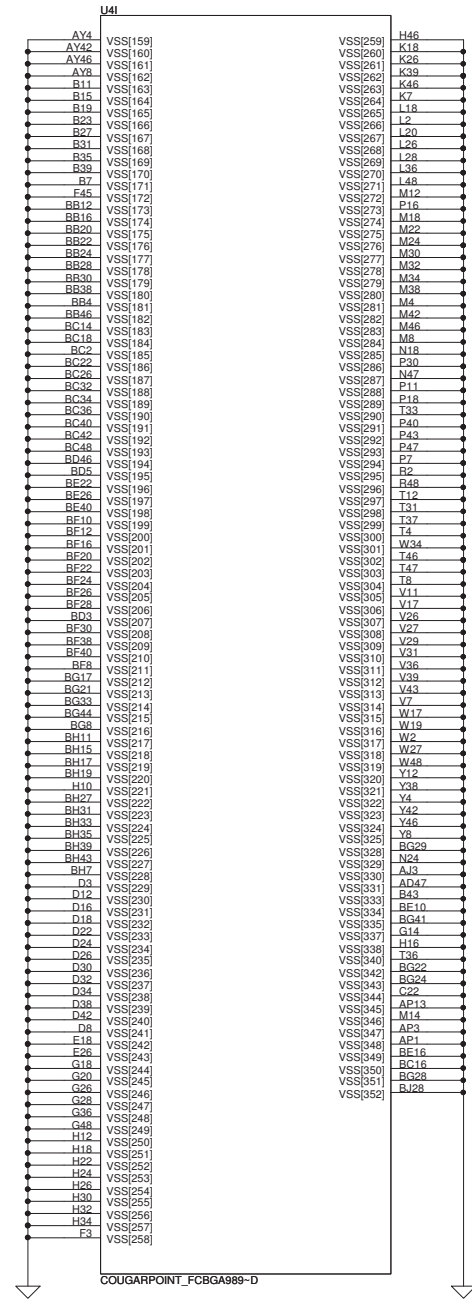
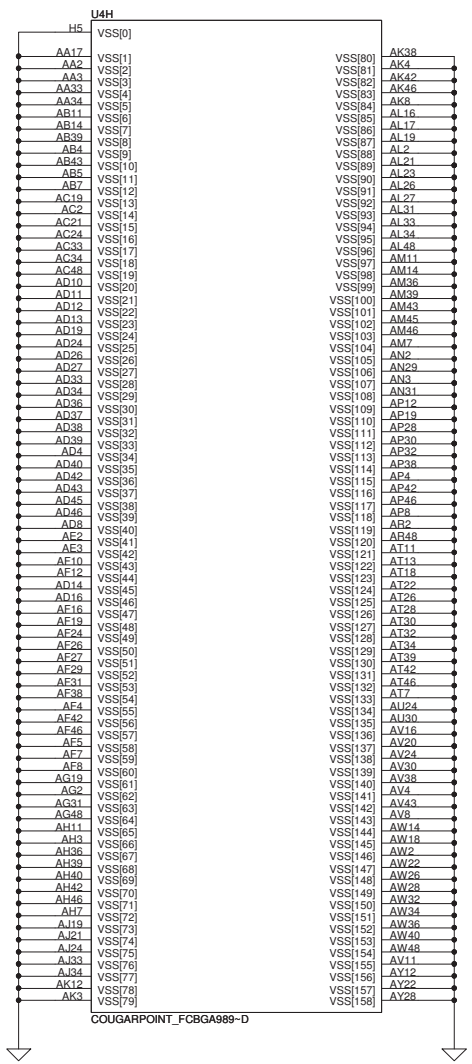
PCH Power Rail Table		
Voltage Rail	Voltage	SO Iccmax Current (A)
V_PROC_IO	1.05	0.001
V5REF	5	0.001
V5REF_Sus	5	0.001
Vcc3_3	3.3	0.266
VccADAC	3.3	0.001
VccADPLLA	1.05	0.08
VccADPLLB	1.05	0.08
VccCore	1.05	1.3
VccDMI	1.05	0.042
VccIO	1.05	2.925
VccASW	1.05	1.01
VccSPI	3.3	0.02
VccDSW	3.3	0.003
VccpNAND	1.8	0.19
VccRTC	3.3	6 uA
VccSus3_3	3.3	0.119
VccSusHDA	3.3 / 1.5	0.01
VccVRM	1.8 / 1.5	0.16
VccCLKDMI	1.05	0.02
VccSSC	1.05	0.095
VccDIFFCLKN	1.05	0.055
VccALVDS	3.3	0.001
VccTX_LVDS	1.8	0.06



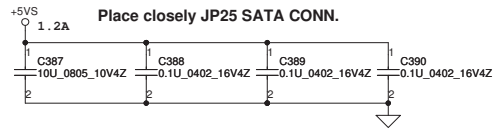
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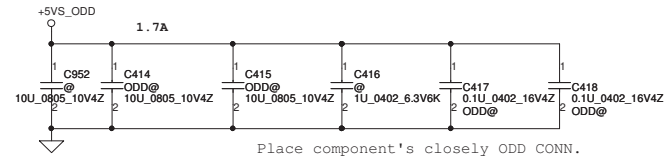
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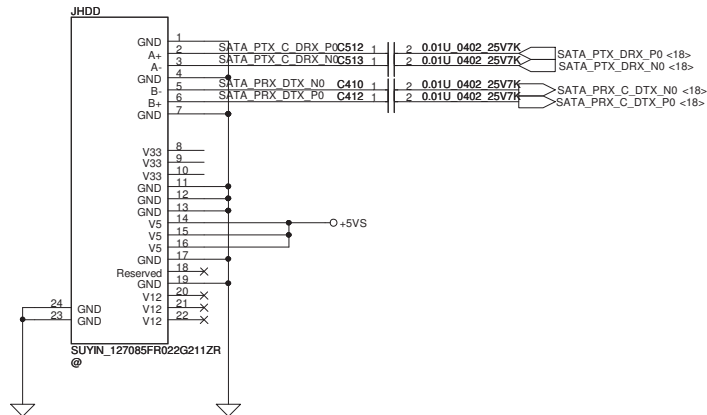
SATA HDD1 Conn.



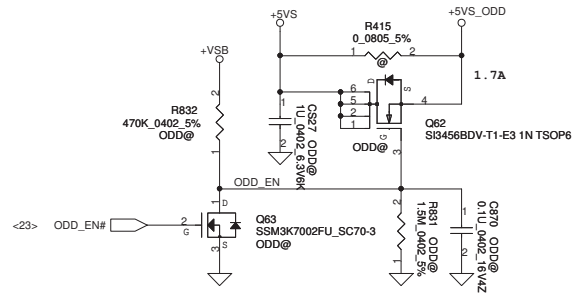
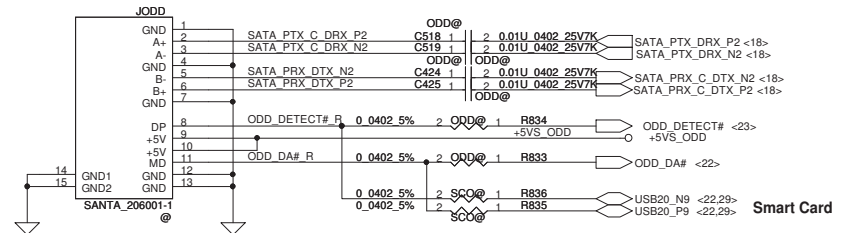
SATA ODD Conn



13.3" HDD

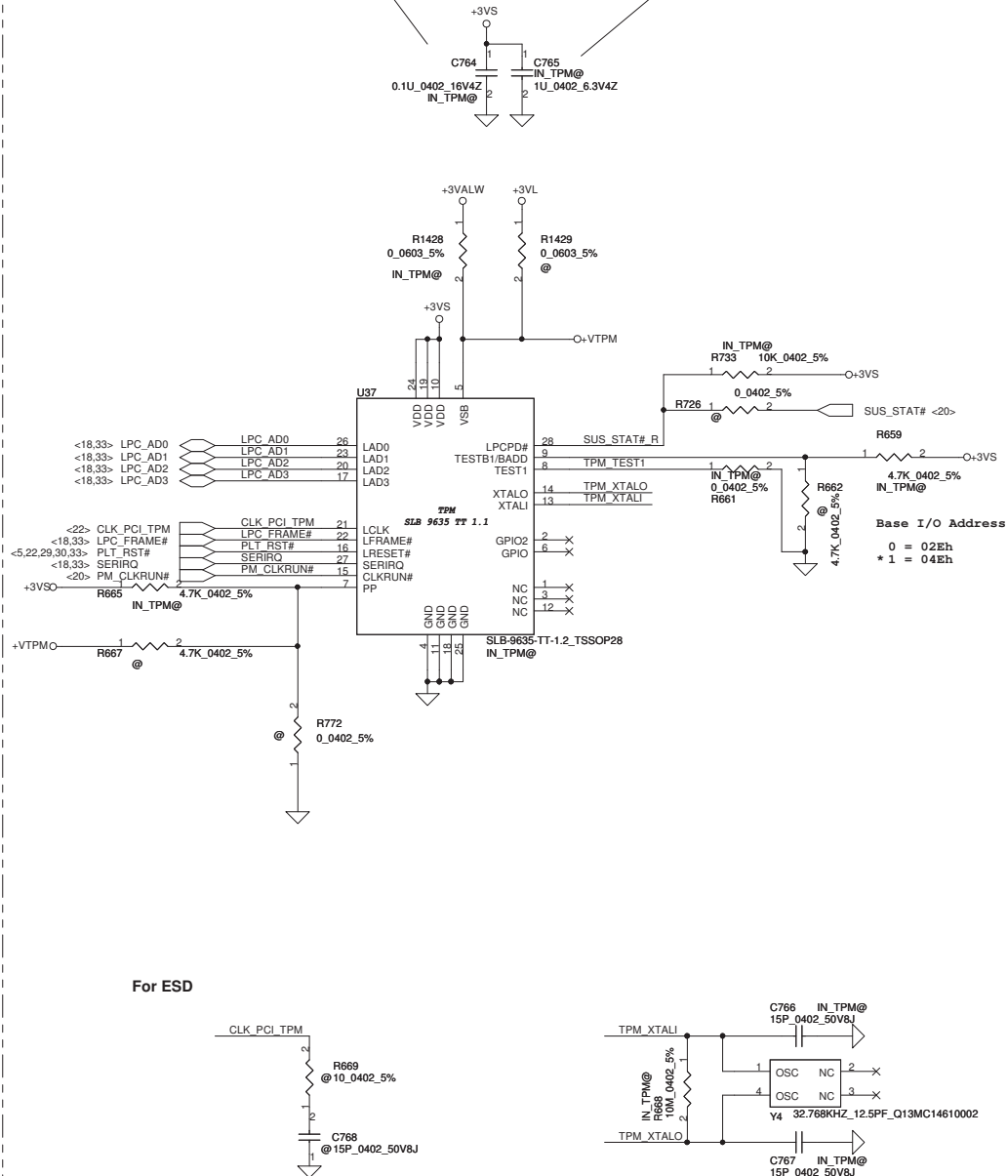


13.3" ODD



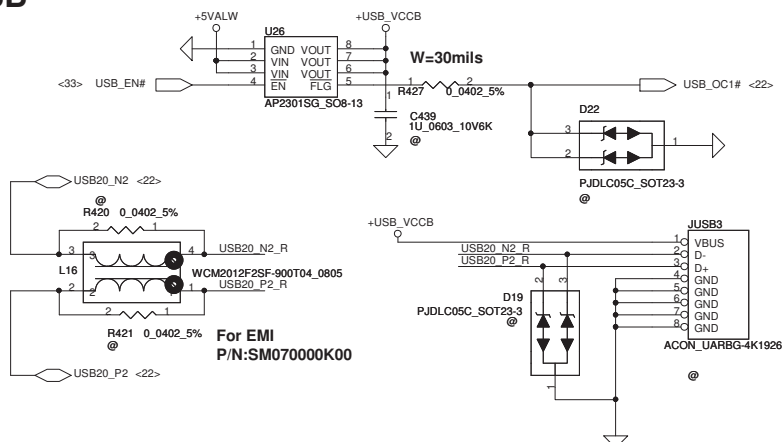
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<http://hobi-elektronika.net>

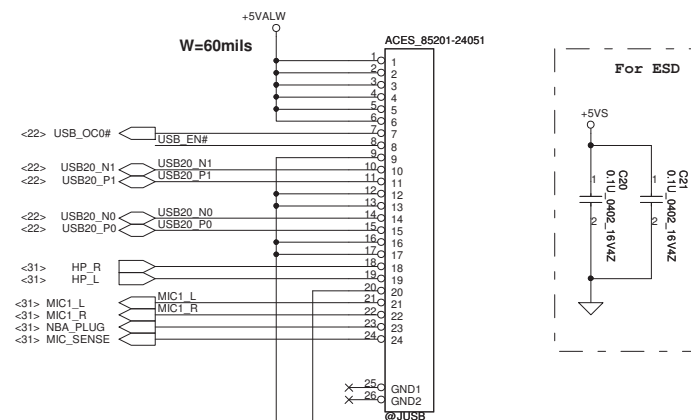


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				Customer	Rev
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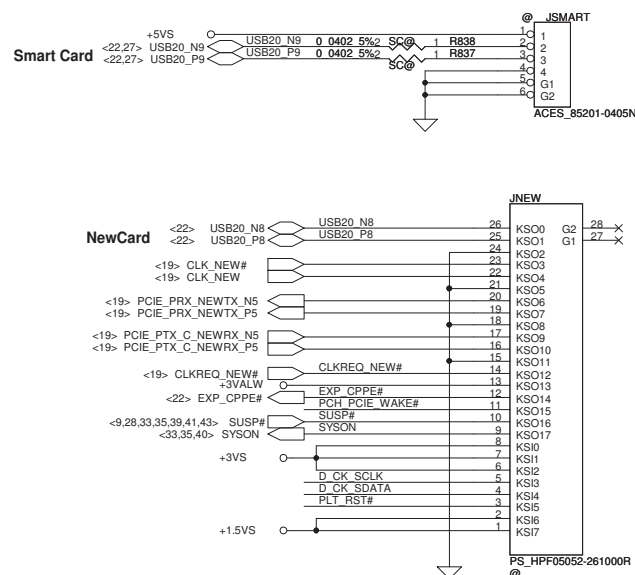
Left USB



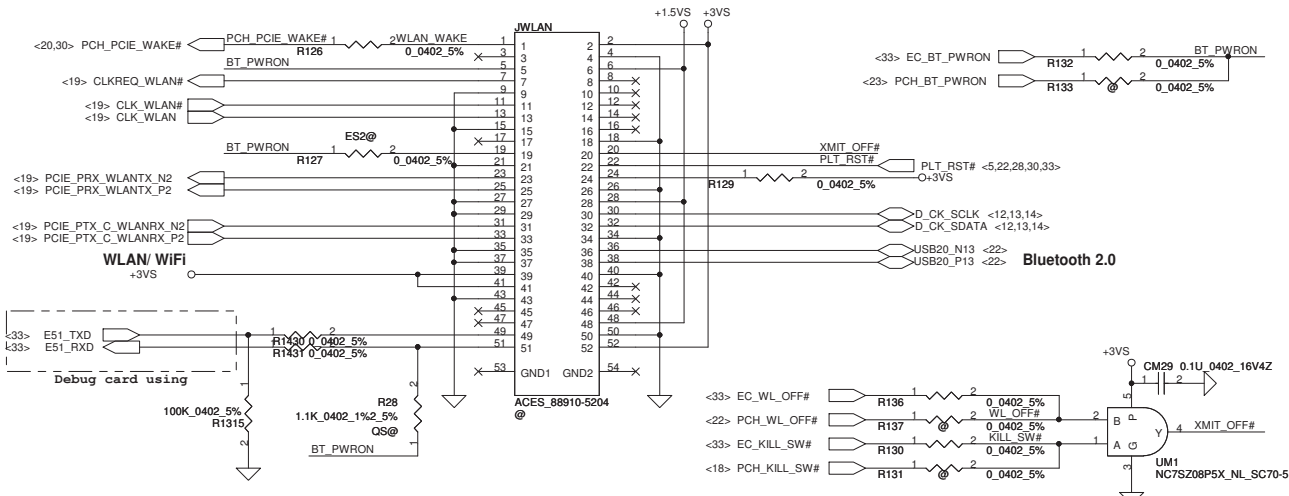
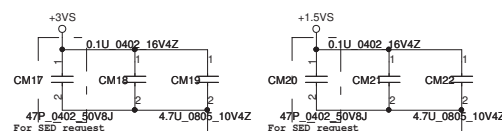
Function Board



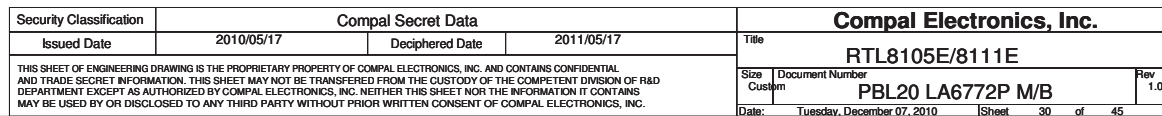
Smart Card and New Card

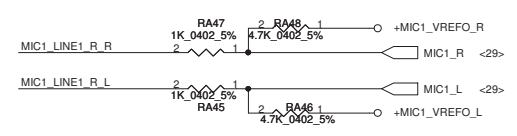
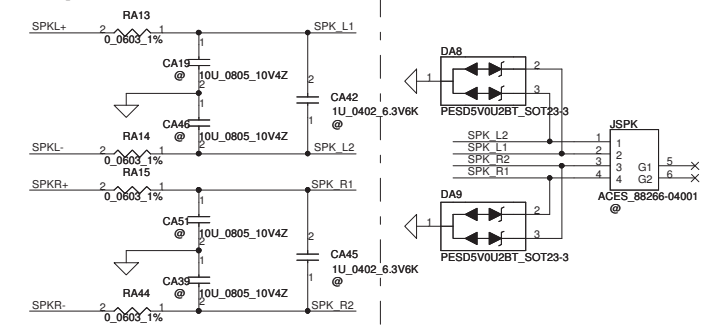
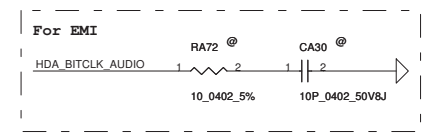


Slot 1 Half PCIe Mini Card-WLAN & BT2.0



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				Custm	PBL20 LA6772P M/B	1.0
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place close to chip

The diagram shows two signal paths, SENSE_A and SENSE_B, connecting external components to a chip. SENSE_A is connected to MIC_SENSE and NBA_PLUG. SENSE_B is connected to an unlabeled input. Each path includes a 20K_0402_1% resistor and a 39.2K_0402_1% resistor. The chip is represented by a large rectangle with pins on the left and right. The text 'place close to chip' is centered above the chip.

<29> MIC_SENSE

1 2

RA18 20K_0402_1%

1 2

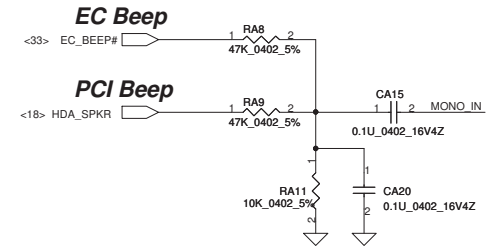
NBA_PLUG

RA16 39.2K_0402_1%

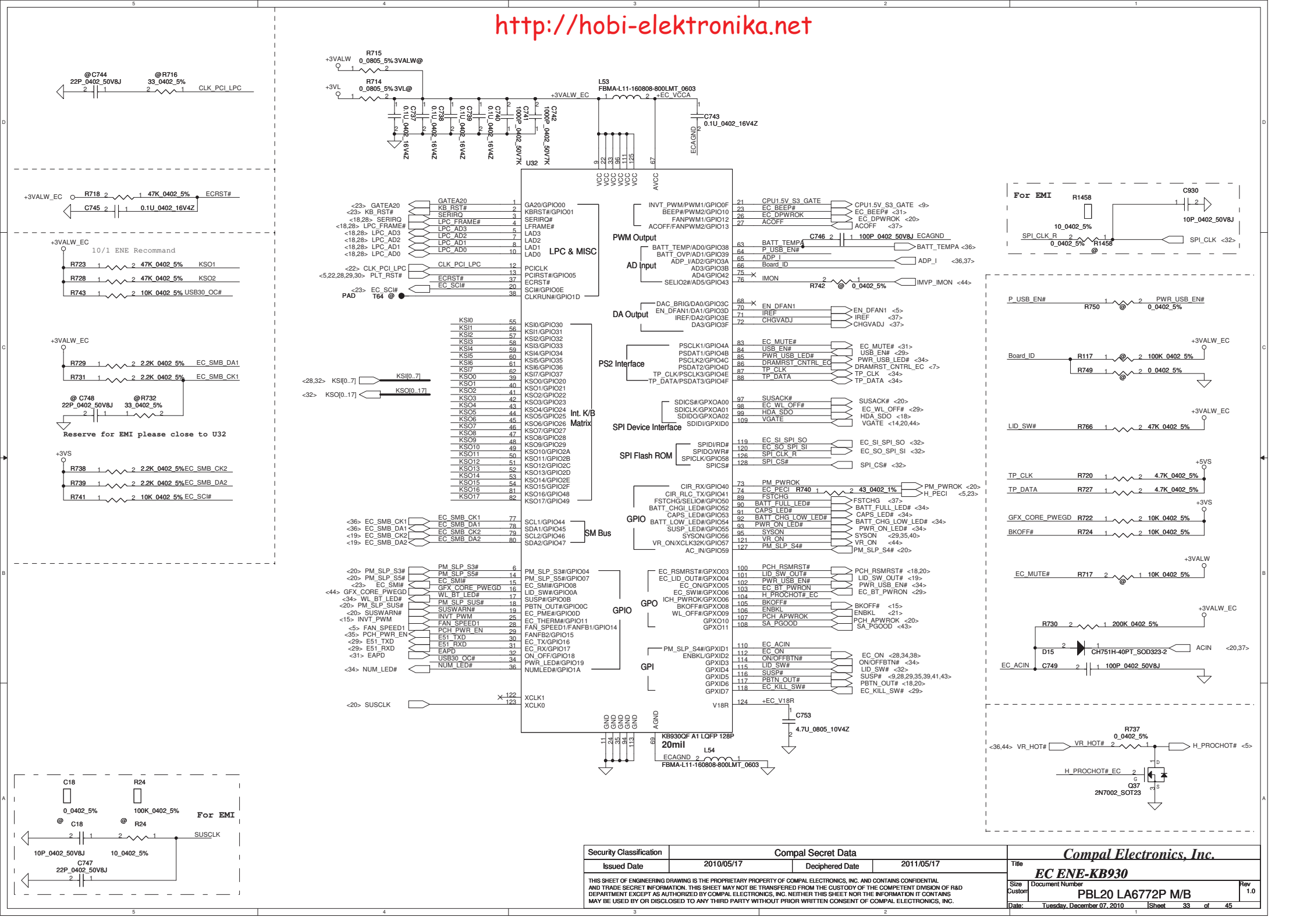
SENSE_A

SENSE_B

RA19 20K_0402_1%

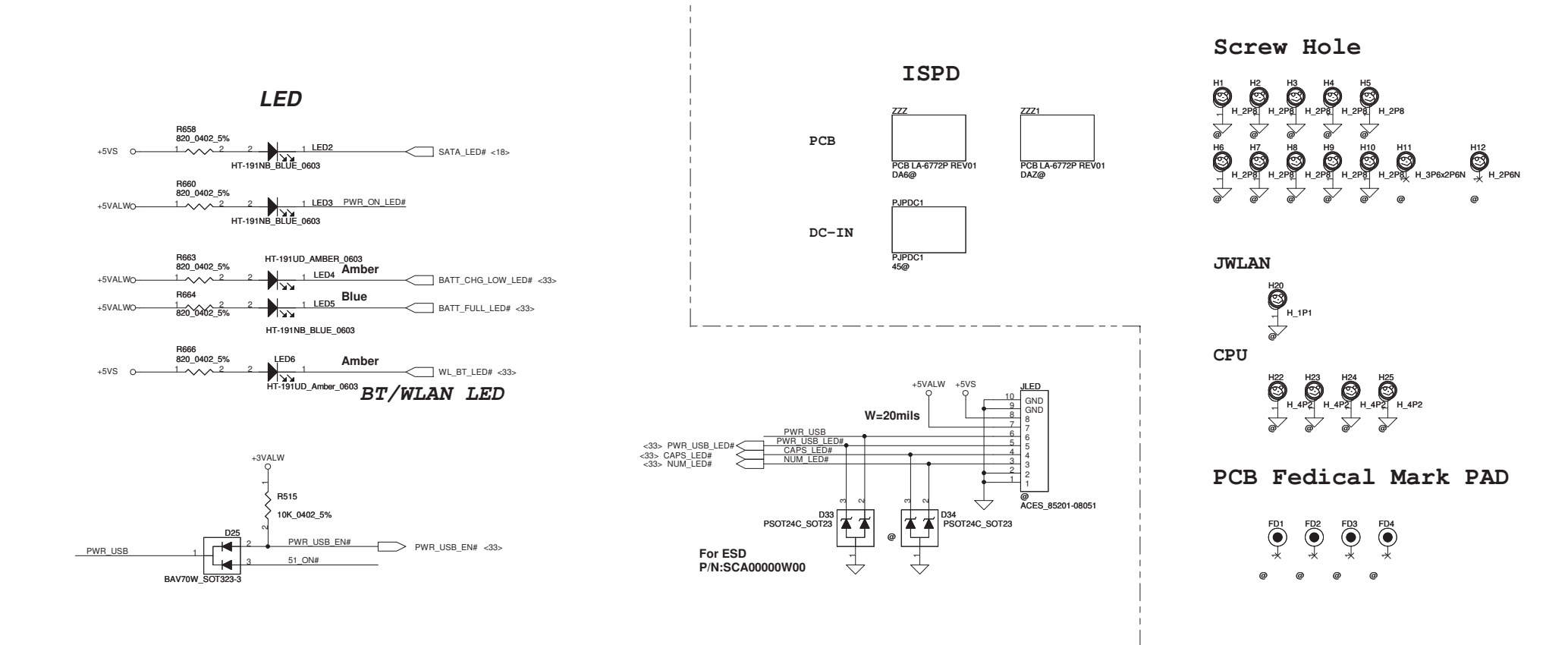
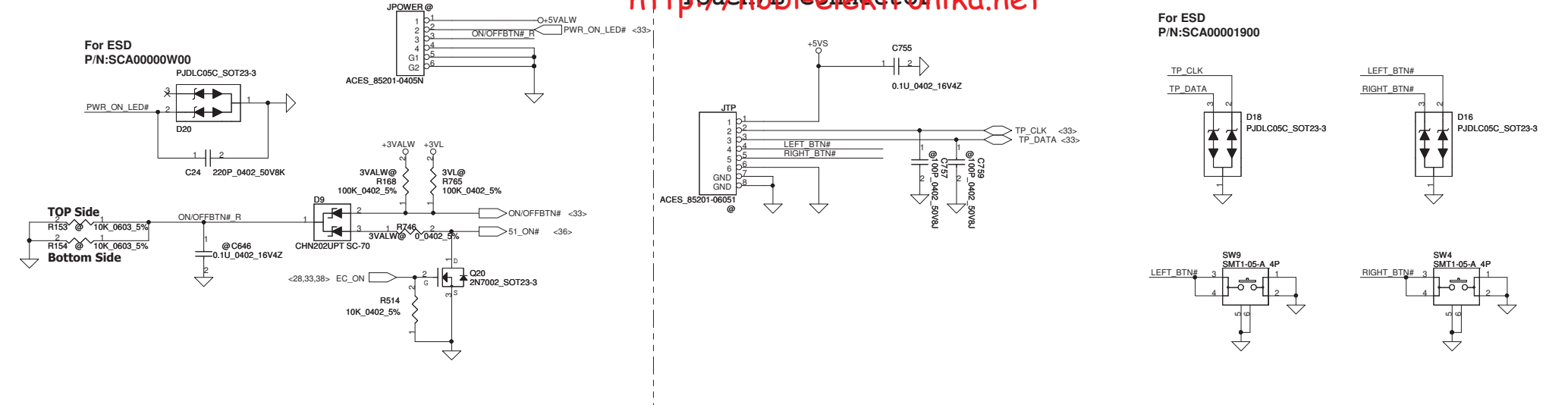


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3	1	2		Date: Tuesday, December 07, 2010	1 Sheet 31 of 45

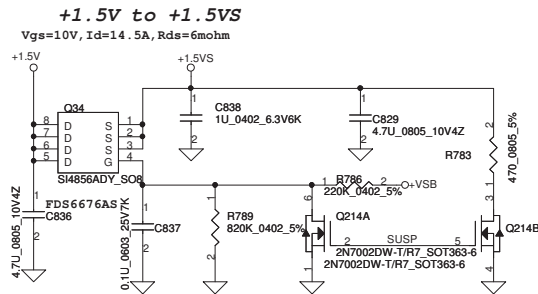
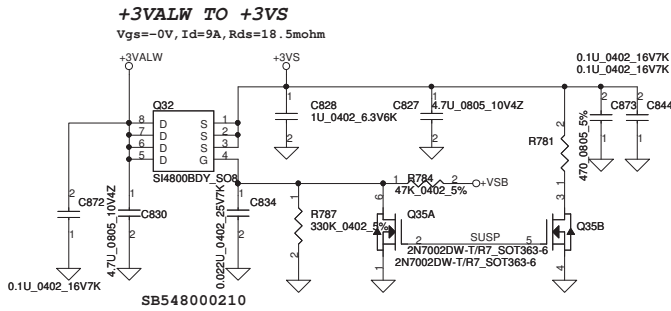
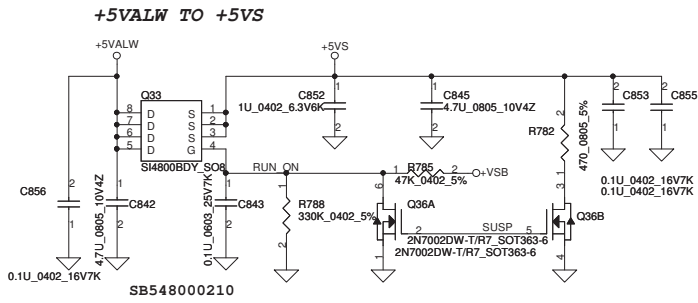


Power Button/ PWR/B

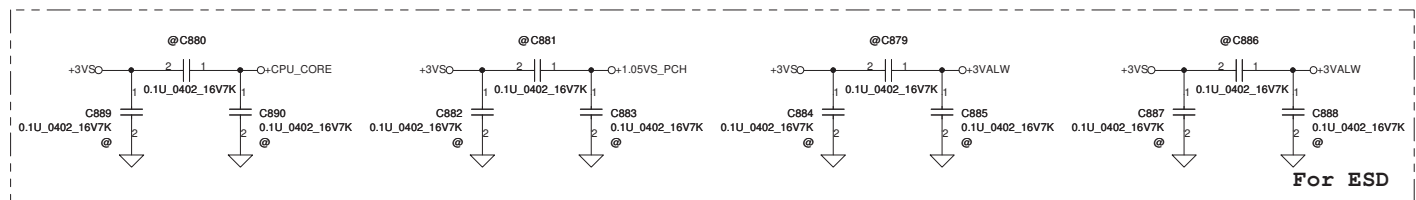
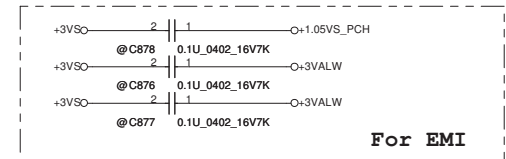
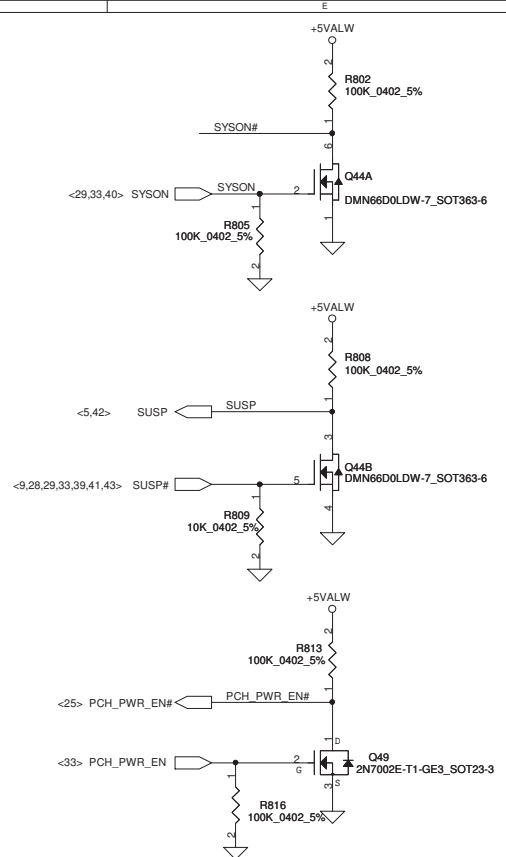
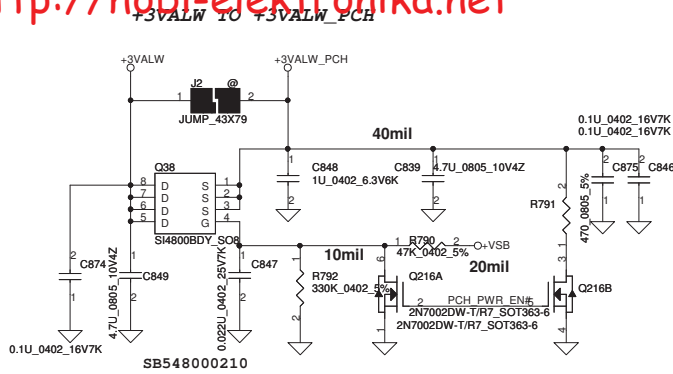
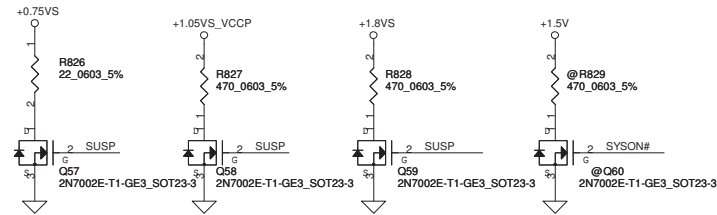
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For ESD  1 2 PWR_USB_LED# C25 220P_0402_50V8K		Security Classification		Compal Secret Data				Compal Electronics, Inc.											
		Issued Date		2010/05/17		Deciphered Date		2011/05/17		Title		Power B/TP/LED							
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												Date:		Tuesday, December 07, 2010		Sheet 34 of 45			



2009/08/14
CP_S3PowerReduction
WhitePaper_Rev0.9
0.75VS speed up discharge

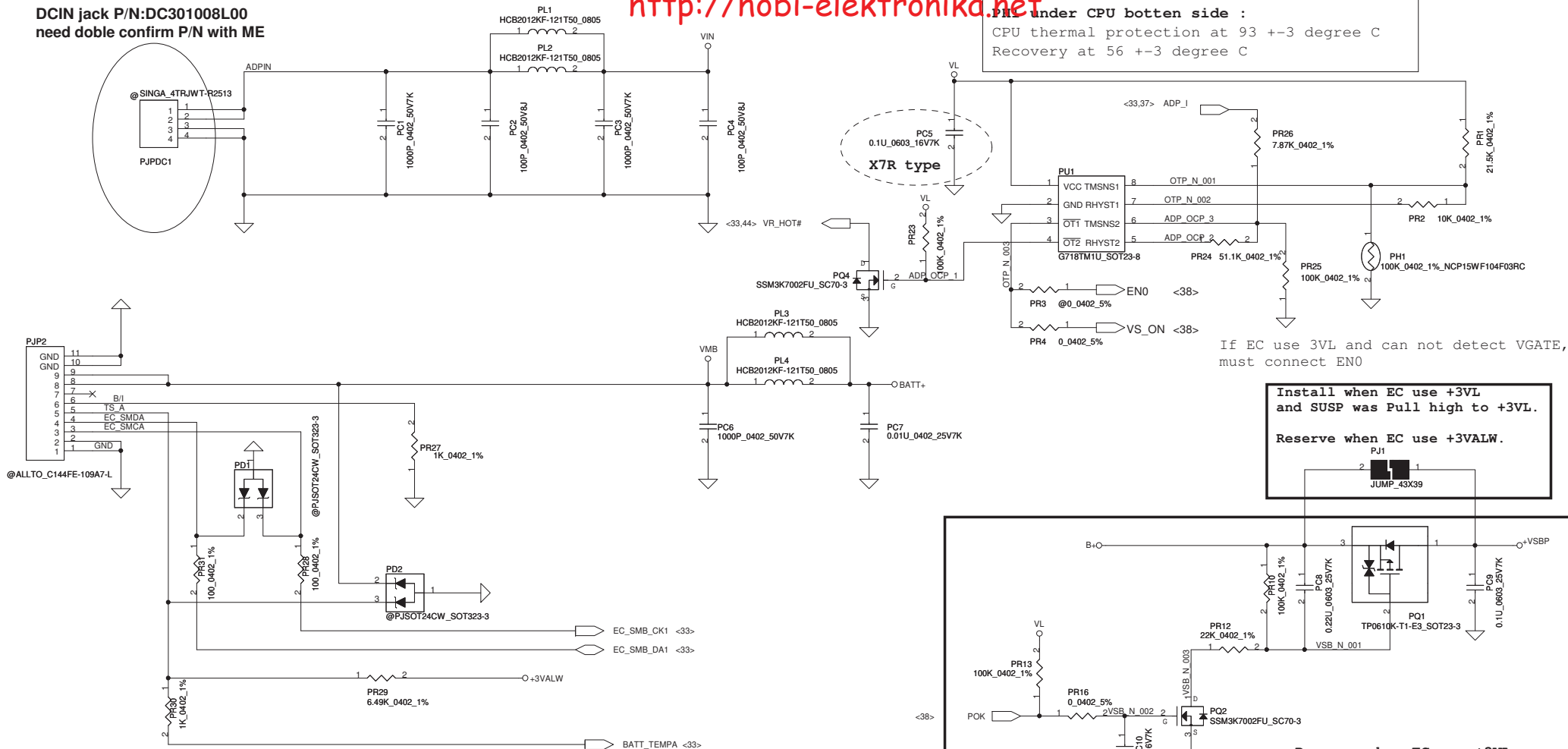


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				DC Interface			
				Size	Document Number	Rev	
				Custom	PBL20 LA6772P M/B	1.0	
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DCIN jack P/N:DC301008L00
need double confirm P/N with ME

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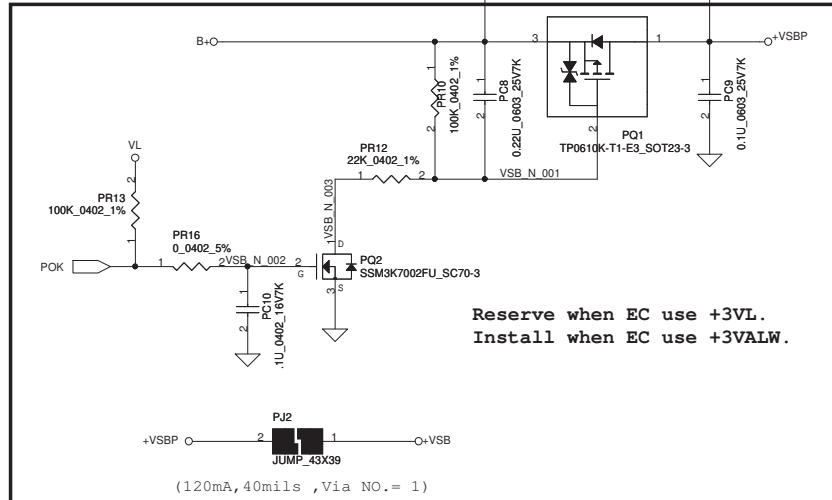
Pin under CPU bottom side :
CPU thermal protection at 93 + -3 degree C
Recovery at 56 + -3 degree C



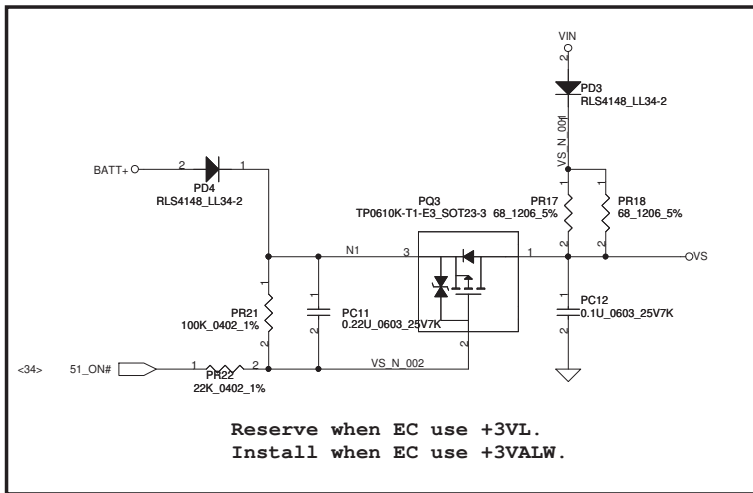
If EC use 3VL and can not detect VGATE,
must connect EN0

Install when EC use +3VL
and SUSP was Pull high to +3VL.

Reserve when EC use +3VALW.

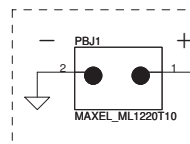


Reserve when EC use +3VL.
Install when EC use +3VALW.



Reserve when EC use +3VL.
Install when EC use +3VALW.

RTC Battery



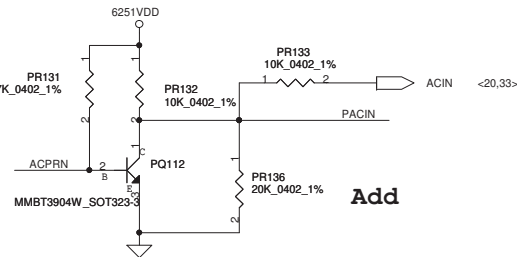
SP093MX0000

Change RTC For Cost Down

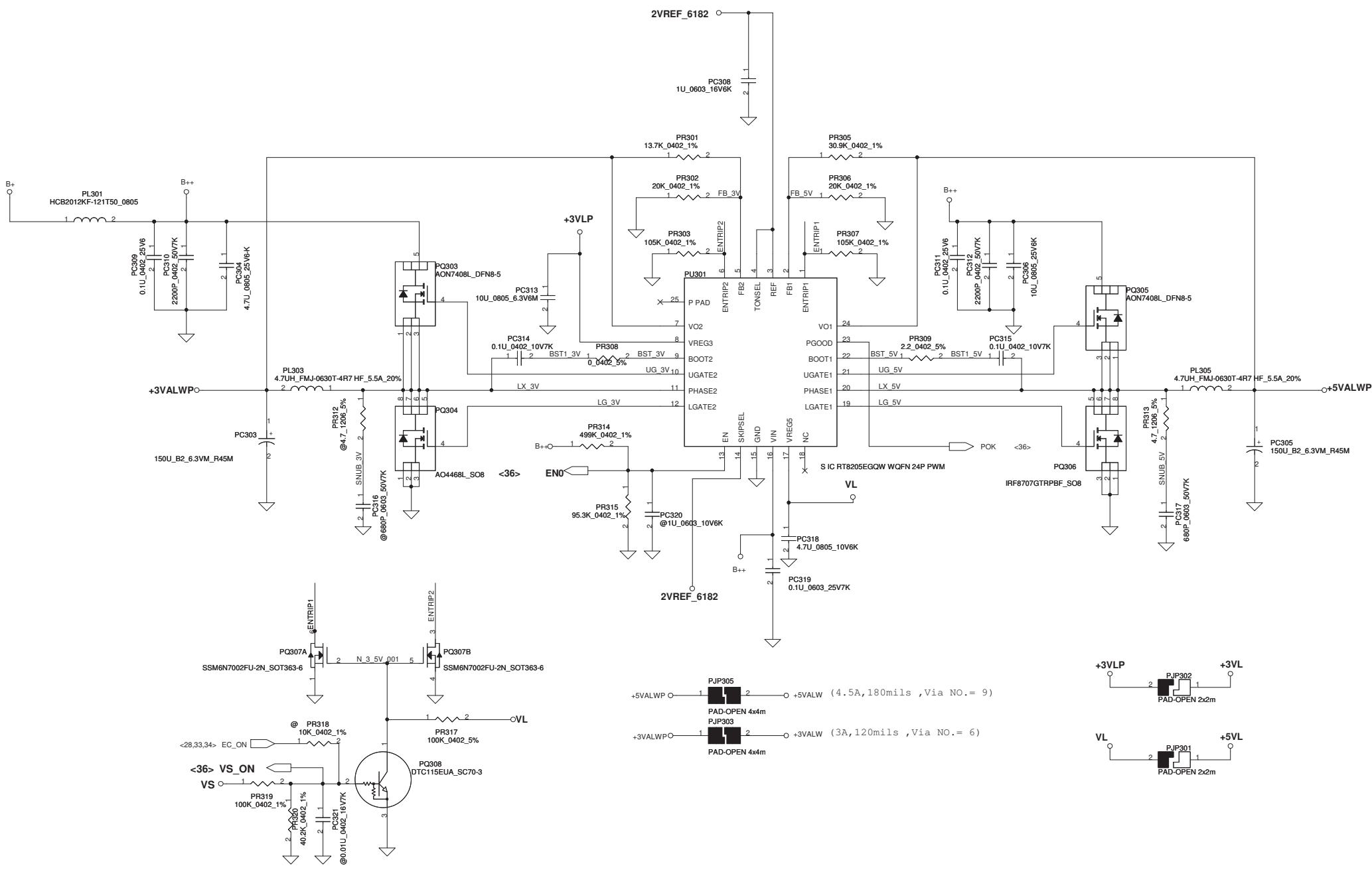


Must close PB1

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				Date	Rev
				Tuesday, December 07, 2010	1.0
				Sheet	45

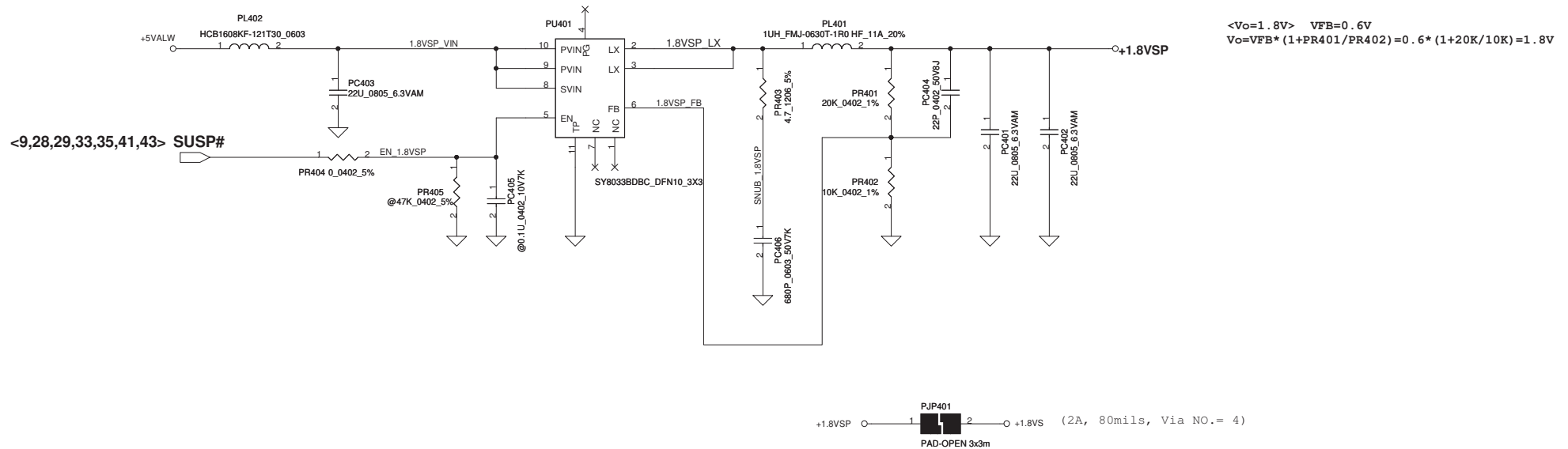


Size	Document Number	Rev
	NCL61 LA-6321P M/B	1.0
Date:	Tuesday, December 07, 2010	Sheet 37 of 45

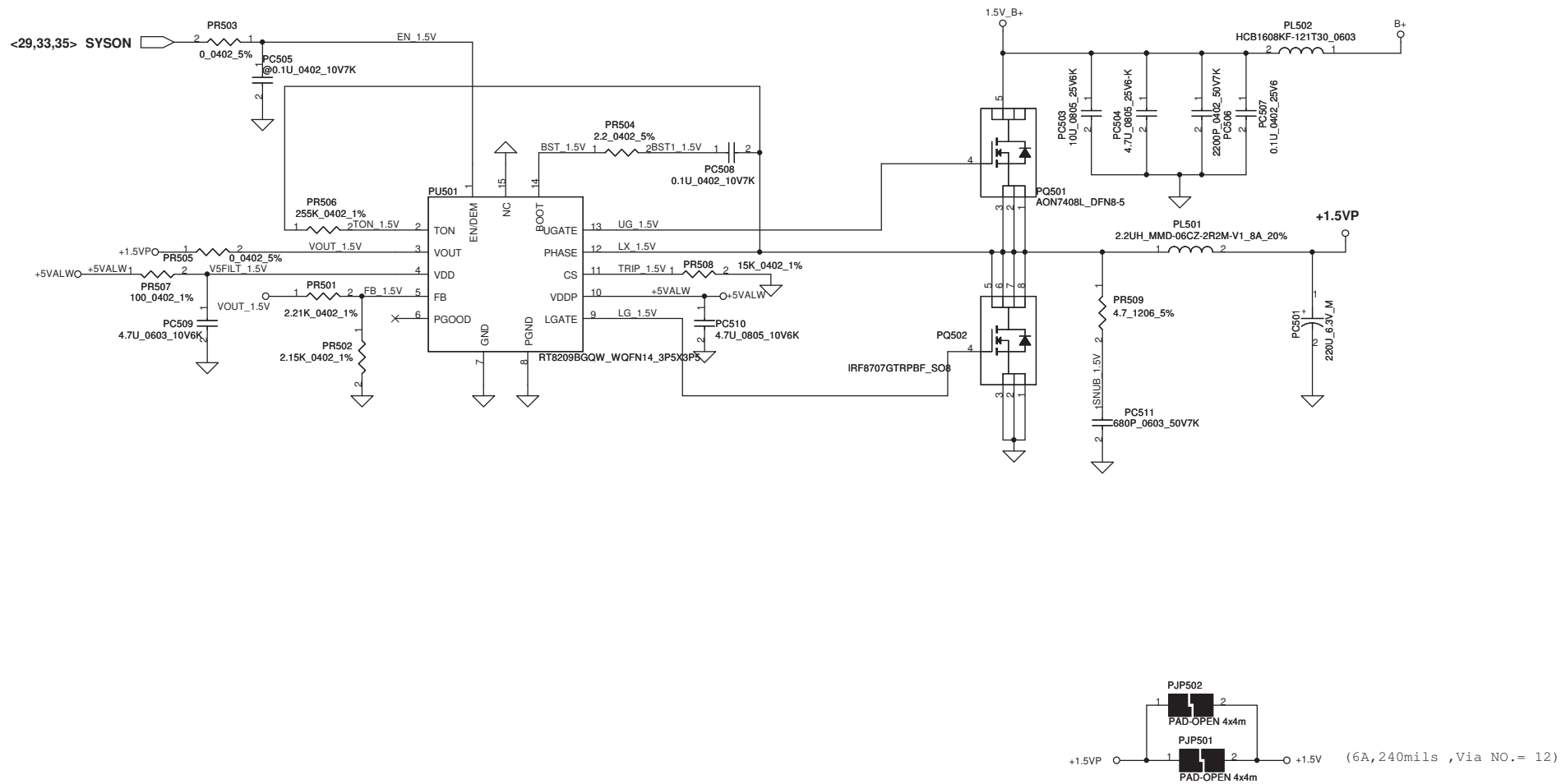


EC:+3VL, reserve PR319, install PR318, PR320 100K
 EC:+3VALW, reserve PR318, install PR319, PR320 40.2K

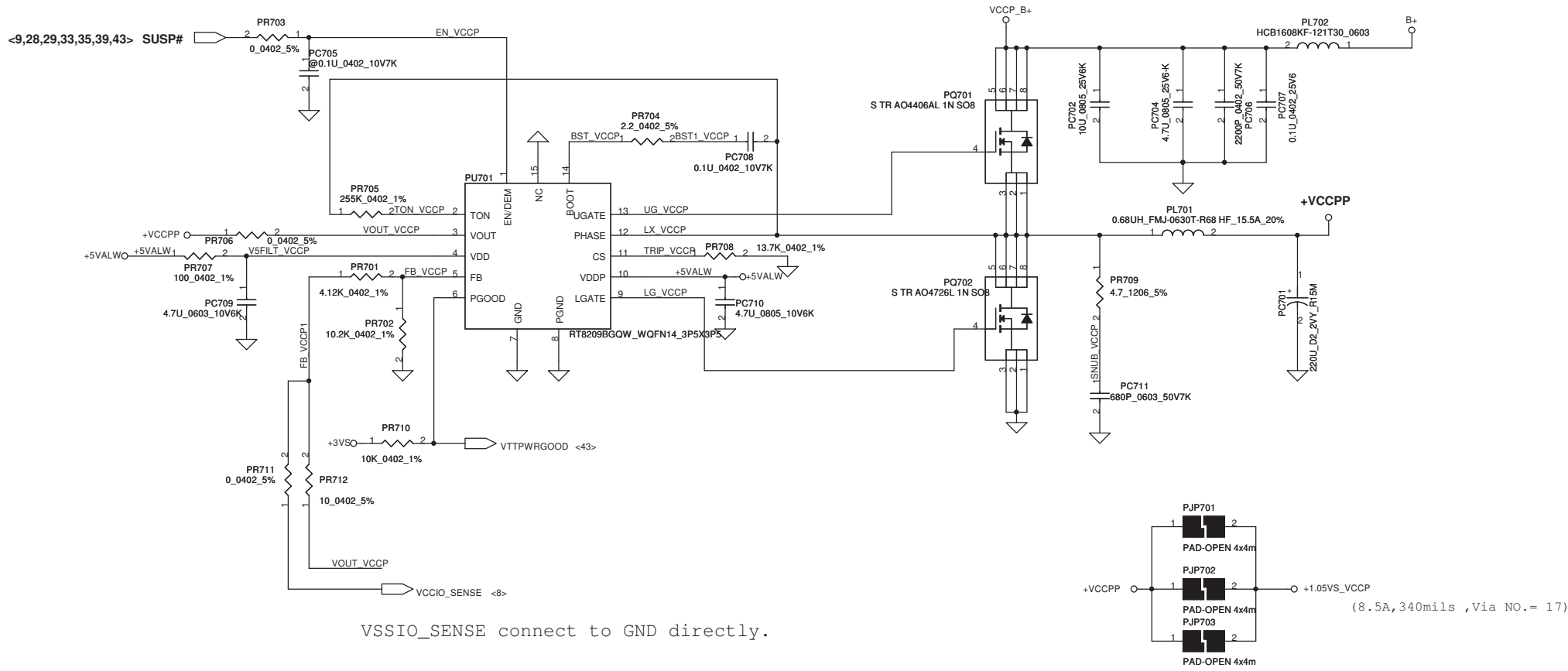
Security Classification		Compal Secret Data		Title	
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				Document Number	1.0
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				Date:	Tuesday, December 07, 2010
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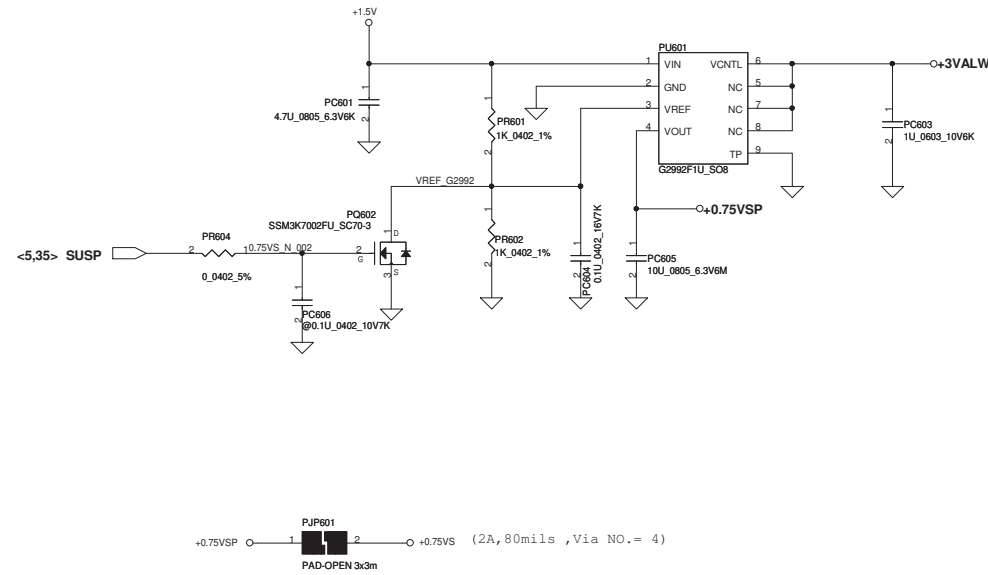


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	NCL61 LA-6321P M/B			1.0	
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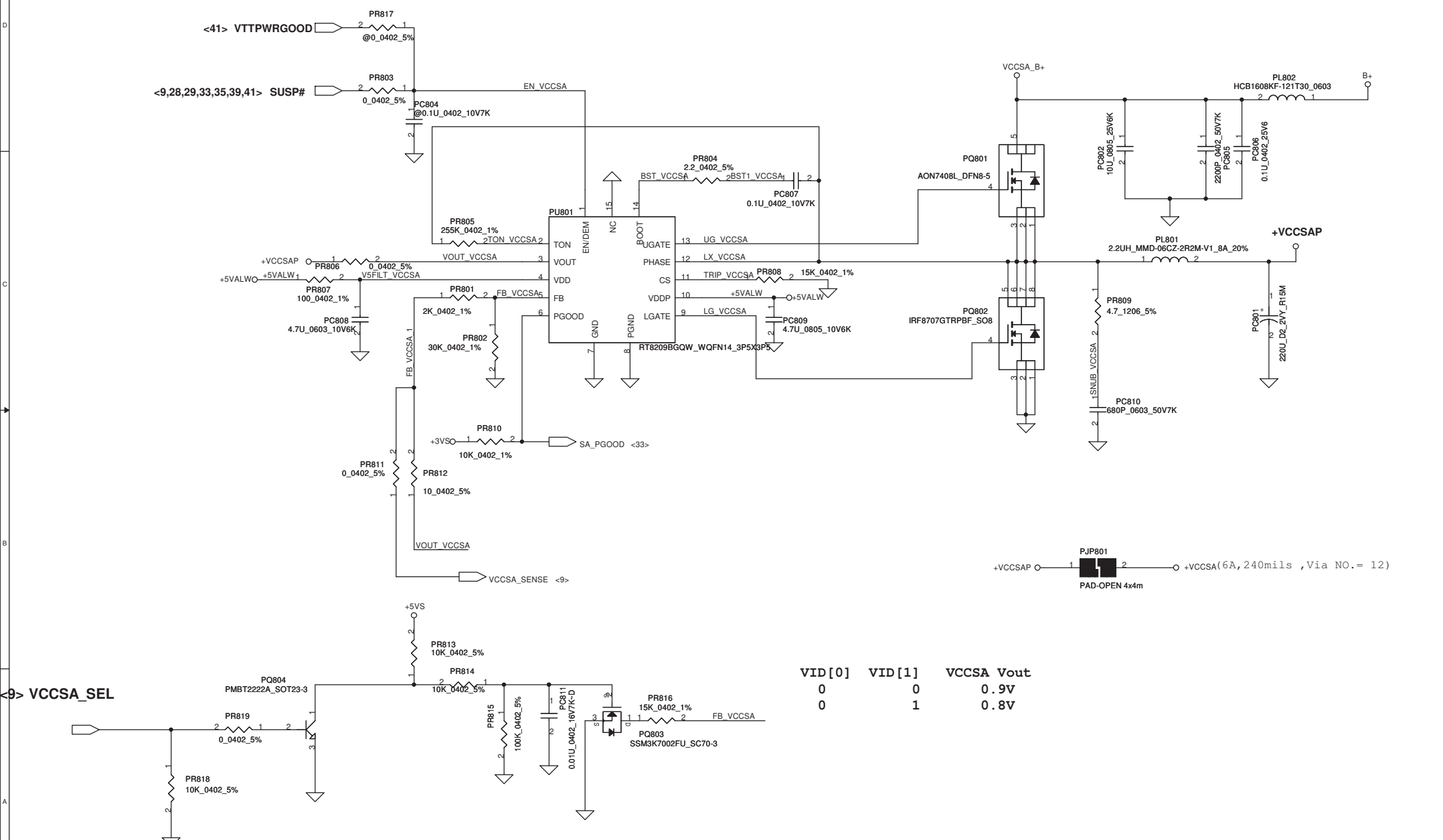


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VID[0]	VID[1]	VCCSA Vout
0	0	0.9V
0	1	0.8V

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		Custom		1.0	
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PIR (Product Improve Record)

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PBL20 LA-6772P SCHEMATIC CHANGE LIST
REVISION CHANGE:

Revision Change: 0.1 to 0.2

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	08/03	17	DELETE U47, C869, R1530 and R572	For HDMI detect issue
2	08/03	17	Change R573 to 20K	For HDMI detect issue
3	08/03	31	Change RA16 to 39.2K	For Headphone detect issue
4	08/03	15	DELETE L25, C320 and C401	For cost down
5	08/04	31	Add RA19	For Headphone detect issue
6	08/20	5	Delete R61, R63, R206, R257, R208, R261, R259 and C16	For remove XDP connector
7	08/20	7	Add C233 and C232	For DDR3 1333 EMI issue
8	08/20	15	Delete L25, C401 and C320	For cost down
9	08/20	17	Delete U10, R405, R407, R410, R705, R409, R408, R406, R411, R403, R404, R402, R221, R253, R254, R412, R413, R414 and R222	Remove level shift for cost down
10	08/20	19	Add C499 and C500	For add new card function
11	08/20	19	Delete R258 and R260	For remove XDP connector
12	08/20	23	Add R425	ODD_EN# pull-up 10K to +3VS
13	08/20	28	Add R303, R46 and R61	For SPI and ME ROM
14	08/20	29	Add JSMART connector	For add smart card connector
15	08/20	29	Add JNEW	For add new card connector
16	08/20	29	Add R127 ,R28 and R129	For support intel W/L
17	08/20	29	Add R132, R133, R136, R137, R130 and R131	For SW GPIO common
18	08/20	33	Delete X1, C751 and C752	For cost down
19	08/20	34	Add D20	For ESD issue

Revision Change: 0.2 to 0.3

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	09/01	34	Add R515	PWR_USB_EN# pull-up 10K to +3VALW
2	09/01	5	Add C16	FOR ESD issue
3	09/02	35	Add C880, C889, C890, C881, C882, C883, C879, C884 ,C885, C886, C887 and C888	FOR ESD issue
4	10/06	34	Add D25	For USB issue
5	10/11	18	Add R172	For Power Saving
6	10/14	27	Add R835 and R836	For smart card with ODD
7	10/14	29	Add R837 and R838	For smart card with ODD
8	10/18	30	Add D6 and D7	For ESD issue
9	10/20	33	Add R743	For common design
10	10/20	22	Add R318	For add TPM 1.2
11	10/20	28	Add C764, C765, U37, R733, R726, R659, R662, R661, R665, R772, R669, C768, R668, C766, C767 and Y4	For add TPM 1.2

Revision Change: 0.3 to 1.0

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	12/01	29	Change JNEW conn.	For ME
1	12/03	33	Add R117, R749 and R750	For EC
1	12/01	29	Add D22	For USB power switch issue

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					Size	Document Number	Rev
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Date: Tuesday, December 07, 2010			Sheet	45	of	45	