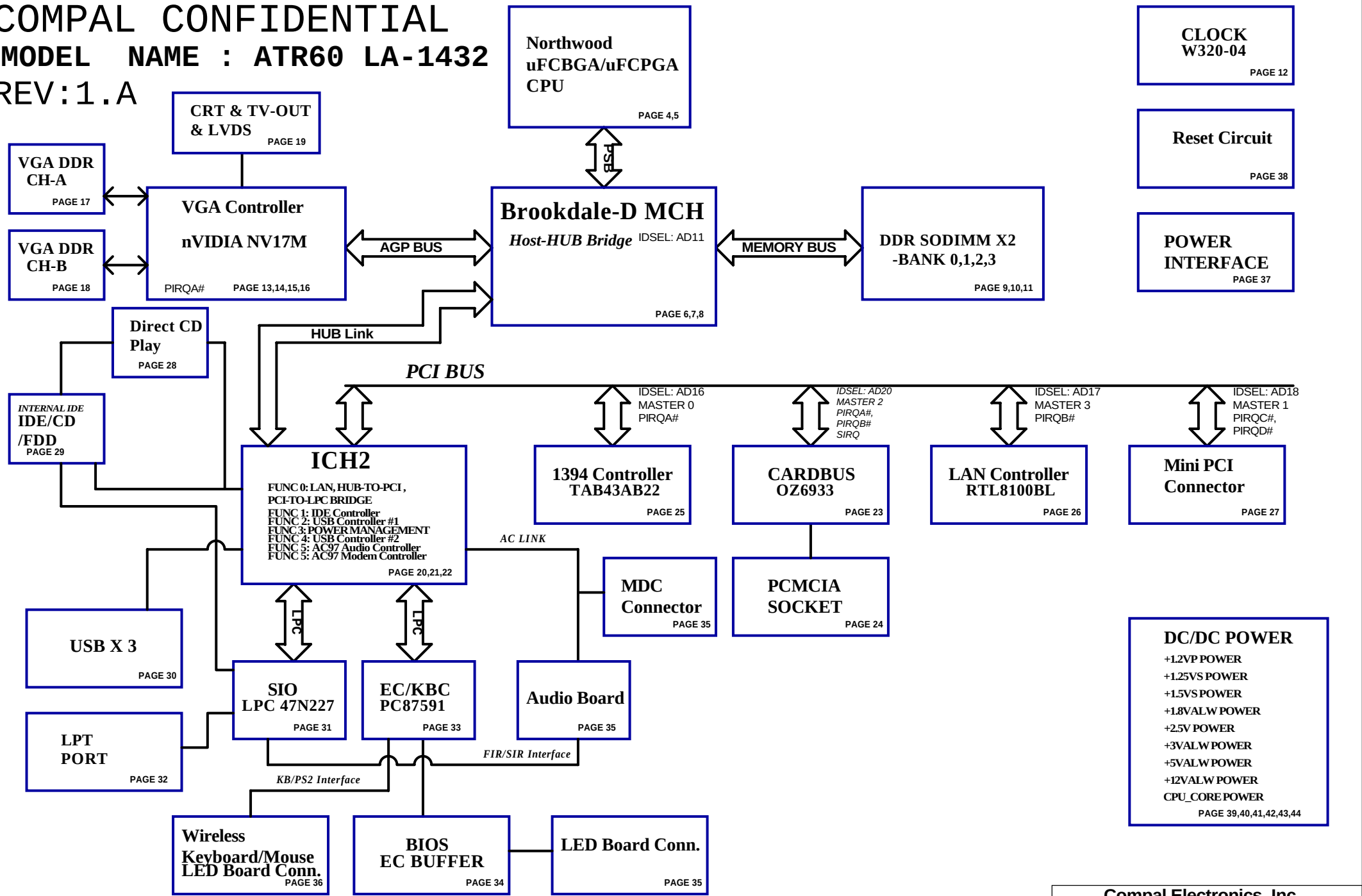


# ATR60 LA-1432 REV1.A Schematics Document

Intel Pentium 4 Processor it the 478-pin Package with 845D chipset

|                          |                  |   |               |
|--------------------------|------------------|---|---------------|
| Title                    |                  |   |               |
| Compal Electronics, Ltd. |                  |   |               |
| Cover Sheet              |                  |   |               |
| Size                     | Document Number  |   | Rev           |
| B                        | LA1432           |   | 1.A           |
| Date:                    | 星期五, 六月 07, 2002 |   | Sheet 1 of 45 |
|                          |                  | E |               |

COMPAL CONFIDENTIAL  
MODEL NAME : ATR60 LA-1432  
REV:1.A



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| ATR60 COVER SHEET        |                  |       |         |
| Size                     | Document Number  | Rev   |         |
| B                        | LA1432           | 1.A   |         |
| Date:                    | 星期五, 六月 07, 2002 | Sheet | 2 of 45 |

Voltage Rails

| Power Plane | Description                                 | S1  | S3  | S5  |
|-------------|---------------------------------------------|-----|-----|-----|
| VIN         | Adapter power supply (19V)                  | N/A | N/A | N/A |
| B+          | AC or battery power rail for power circuit. | N/A | N/A | N/A |
| +CPU_VCC    | Core voltage for CPU                        | ON  | OFF | OFF |
| +1.2VP      | 1.2V switched power rail for CPU AGTL Bus   | ON  | OFF | OFF |
| +1.25VS     | 1.25V switched power rail                   | ON  | OFF | OFF |
| +1.5VS      | AGP 4X                                      | ON  | OFF | OFF |
| +1.8VALW    | 1.8V power rail                             | ON  | ON  | ON* |
| +1.8VS      | 1.8V switched power rail                    | ON  | OFF | OFF |
| +2.5V       | 2.5V power rail                             | ON  | ON  | OFF |
| +2.5VS      | 2.5V switched power rail                    | ON  | OFF | OFF |
| +3VALW      | 3.3V always on power rail                   | ON  | ON  | ON* |
| +3V         | 3.3V power rail                             | ON  | ON  | OFF |
| +3VS        | 3.3V switched power rail                    | ON  | OFF | OFF |
| +5VALW      | 5V always on power rail                     | ON  | ON  | ON* |
| +5V         | 5V power rail                               | ON  | ON  | OFF |
| +5VS        | 5V switched power rail                      | ON  | OFF | OFF |
| +12VALW     | 12V always on power rail                    | ON  | ON  | ON* |
| RTCVCC      | RTC power                                   | ON  | ON  | ON  |

Note : ON\* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

| Device   | IDSEL# | REQ#/GNT# | Interrupts  |
|----------|--------|-----------|-------------|
| VGA      |        |           | PIRQA       |
| CardBus  | AD20   | 2         | PIRQA/PIRQB |
| LAN      | AD17   | 3         | PIRQB       |
| Mini-PCI | AD18   | 1/4       | PIRQC/PIRQD |
| 1394     | AD16   | 0         | PIRQA       |

EC SM Bus1 address

| Device           | Address     | Device        | Address     |
|------------------|-------------|---------------|-------------|
| Smart Battery    | 0001 011X b | MAX1617MEE    | 1001 110X b |
| EEPROM(24C16/02) | 1010 000X b | OZ163         | 0011 0100 b |
| (24C04)          | 1011 000Xb  | Smart Battery | 0001 011X b |
|                  |             | Docking       | 0011 011X b |
|                  |             | DOT Board     | XXXX XXXXb  |

ICH2 SM Bus address

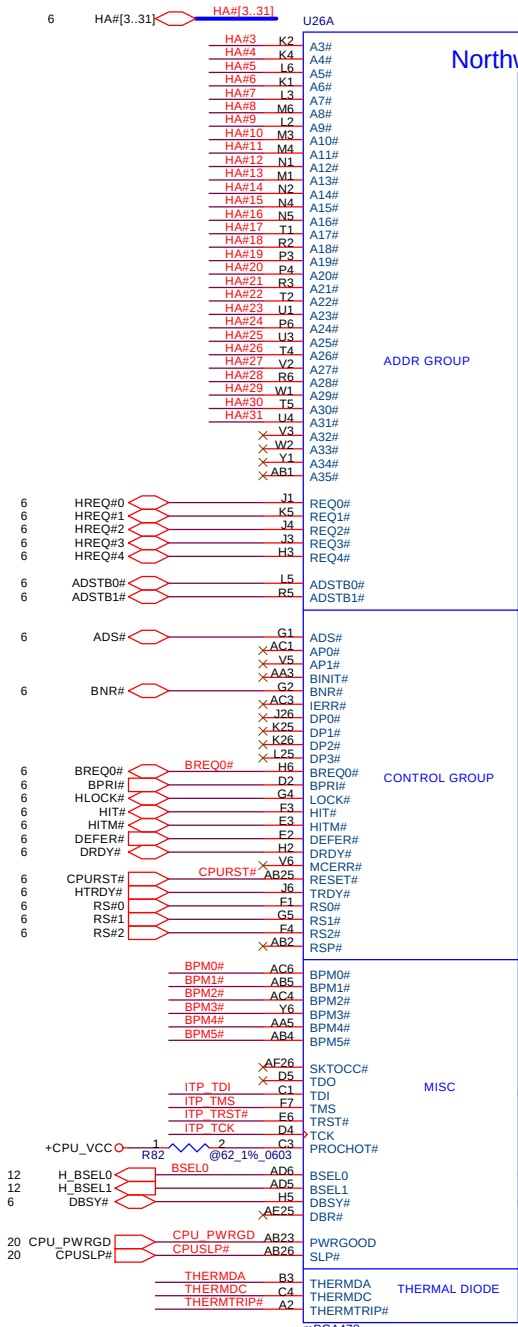
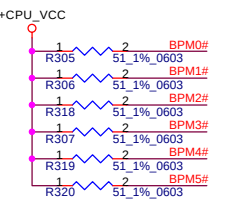
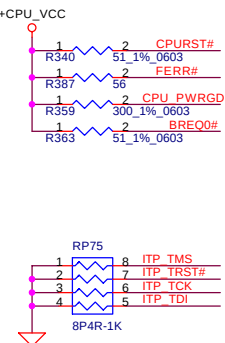
| Device                           | Address    |
|----------------------------------|------------|
| Clock Generator ( ICS9238-50)    | 1101 0000  |
| SDRAM Select ( 74HC4052 )        | 1010 0000  |
| CPU Voltage VID select ( F3565 ) | 0110 111Xb |

Board ID Table for AD channel

|           |             |             |             |             |
|-----------|-------------|-------------|-------------|-------------|
| Vcc       | 3.3V +/- 5% |             |             |             |
| Ra        | 100K +/- 5% |             |             |             |
| Board ID0 | Rb          | VAD_BID min | VAD_BID typ | VAD_BID max |
| 0         | 0           | 0V          | 0V          | 0V          |
| 1         | 47K +/- 5%  | 0.921 V     | 1.055 V     | 1.204 V     |
| 2         | 100K +/- 5% | 1.453 V     | 1.650 V     | 1.759 V     |
| 3         | NC          | 2.5 V       | 3.3 V       | 3.465 V     |

|           |             |             |             |             |
|-----------|-------------|-------------|-------------|-------------|
| Vcc       | 3.3V +/- 5% |             |             |             |
| Rc        | 100K +/- 5% |             |             |             |
| Board ID1 | Rd          | VAD_BID min | VAD_BID typ | VAD_BID max |
| 0         | 0           | 0V          | 0V          | 0V          |
| 1         | 47K +/- 5%  | 0.921 V     | 1.055 V     | 1.204 V     |
| 2         | 100K +/- 5% | 1.453 V     | 1.650 V     | 1.759 V     |
| 3         | NC          | 2.5 V       | 3.3 V       | 3.465 V     |

| Real Board ID | Board ID1 | Board ID0 | PCB Revision |
|---------------|-----------|-----------|--------------|
| 0             | 0         | 0         | 0.1          |
| 1             | 0         | 1         | 1.0          |
| 2             | 0         | 2         |              |
| 3             | 0         | 3         |              |
| 4             | 1         | 0         |              |
| 5             | 1         | 1         |              |
| 6             | 1         | 2         |              |
| 7             | 1         | 3         |              |
| 8             | 2         | 0         |              |
| 9             | 2         | 1         |              |
| 10            | 2         | 2         |              |
| 11            | 2         | 3         |              |
| 12            | 3         | 0         |              |
| 13            | 3         | 1         |              |
| 14            | 3         | 2         |              |
| 15            | 3         | 3         |              |



Northwood

ADDR GROUP

DATA GROUP

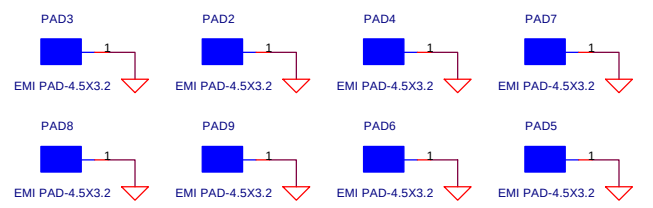
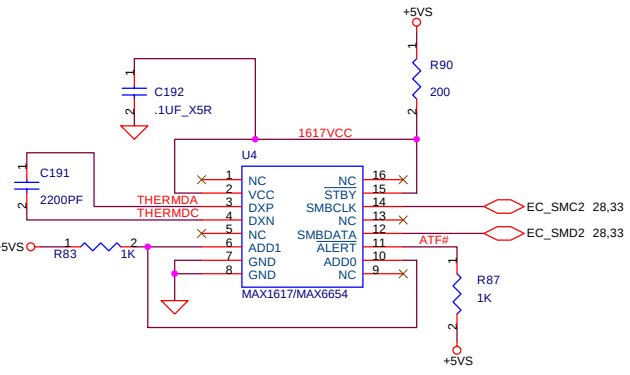
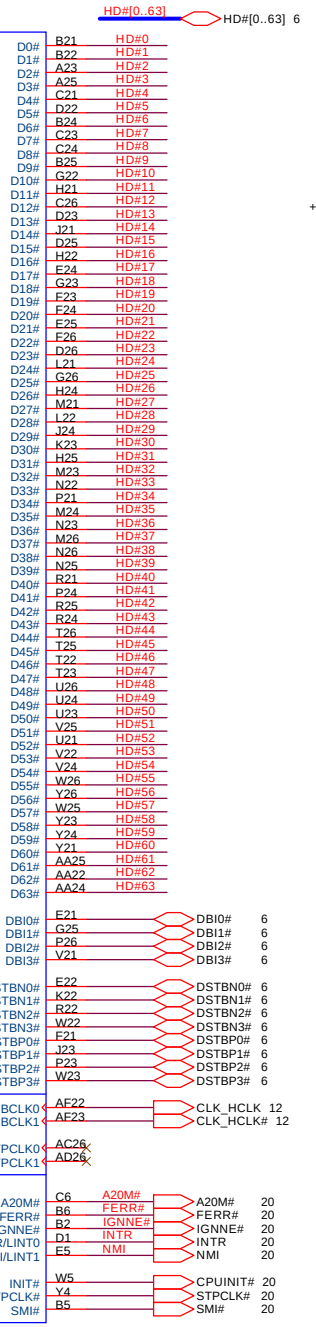
CONTROL GROUP

MISC

HOST CLK

LEGACY CPU

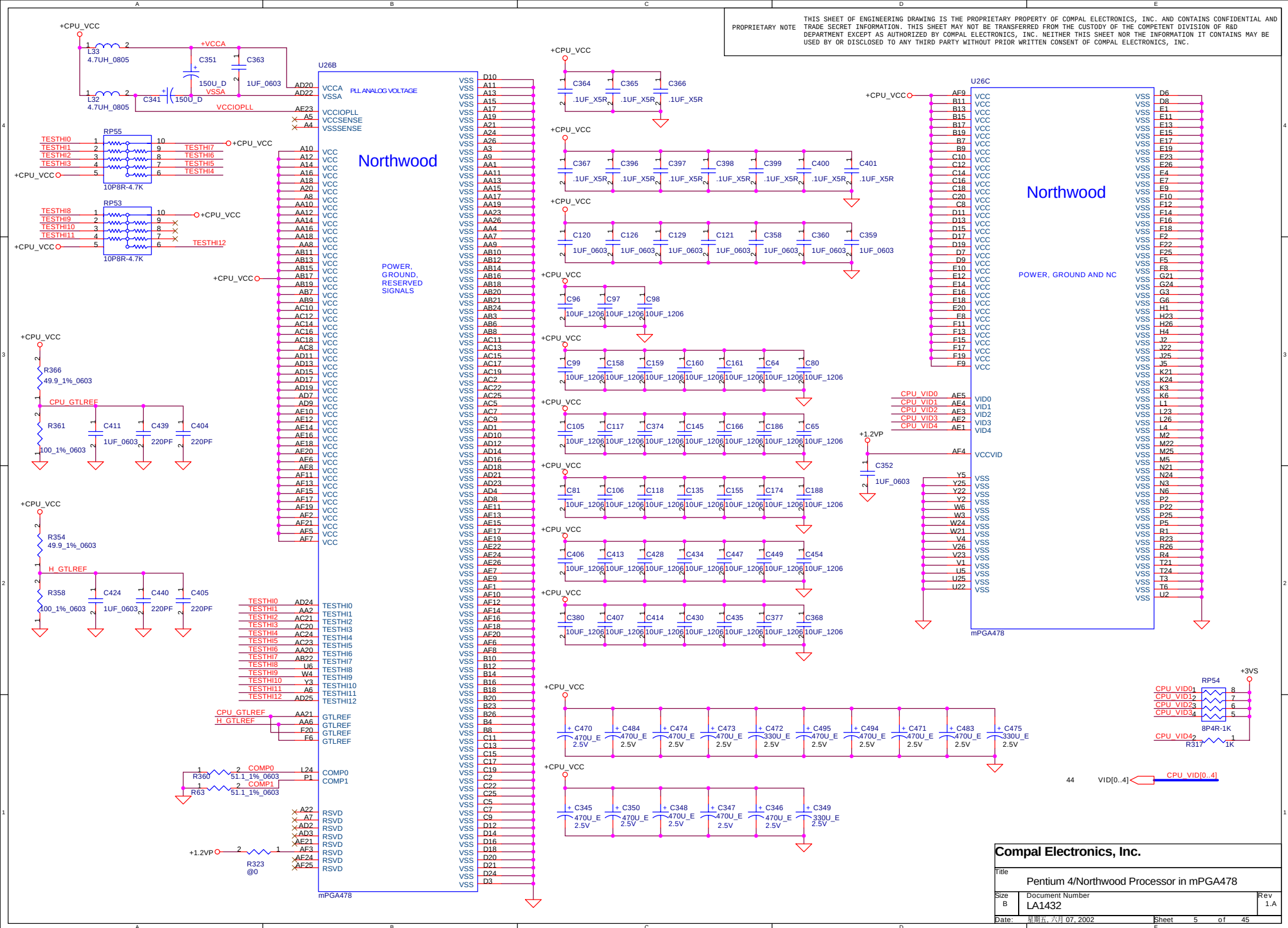
THERMAL DIODE



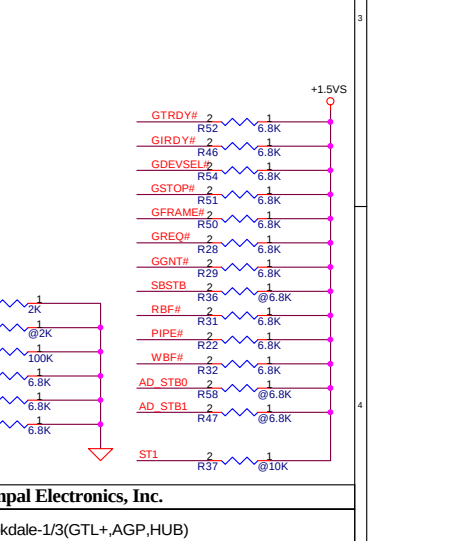
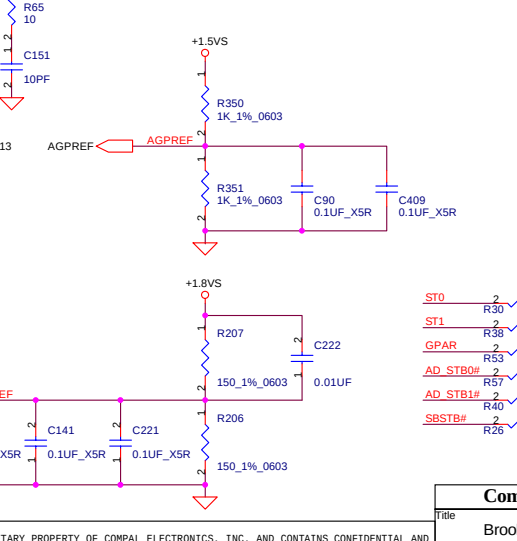
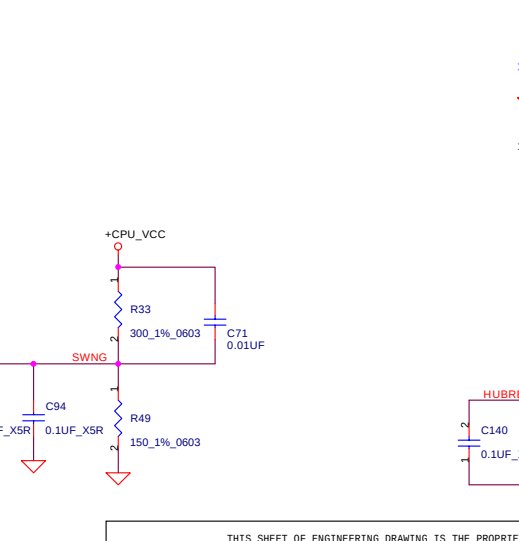
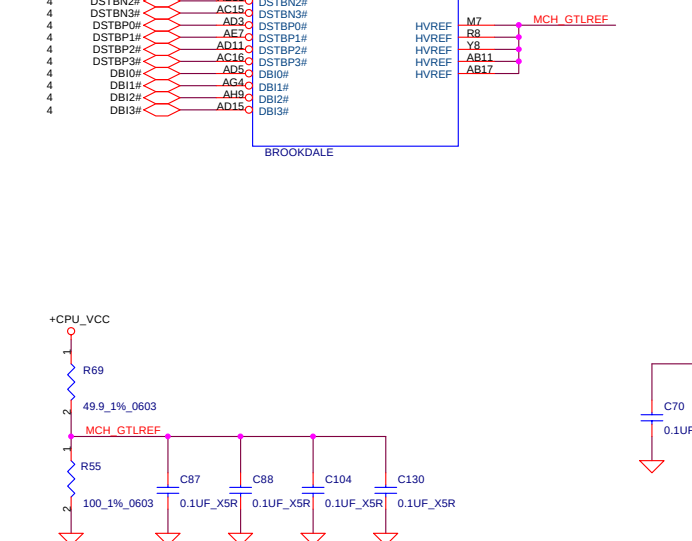
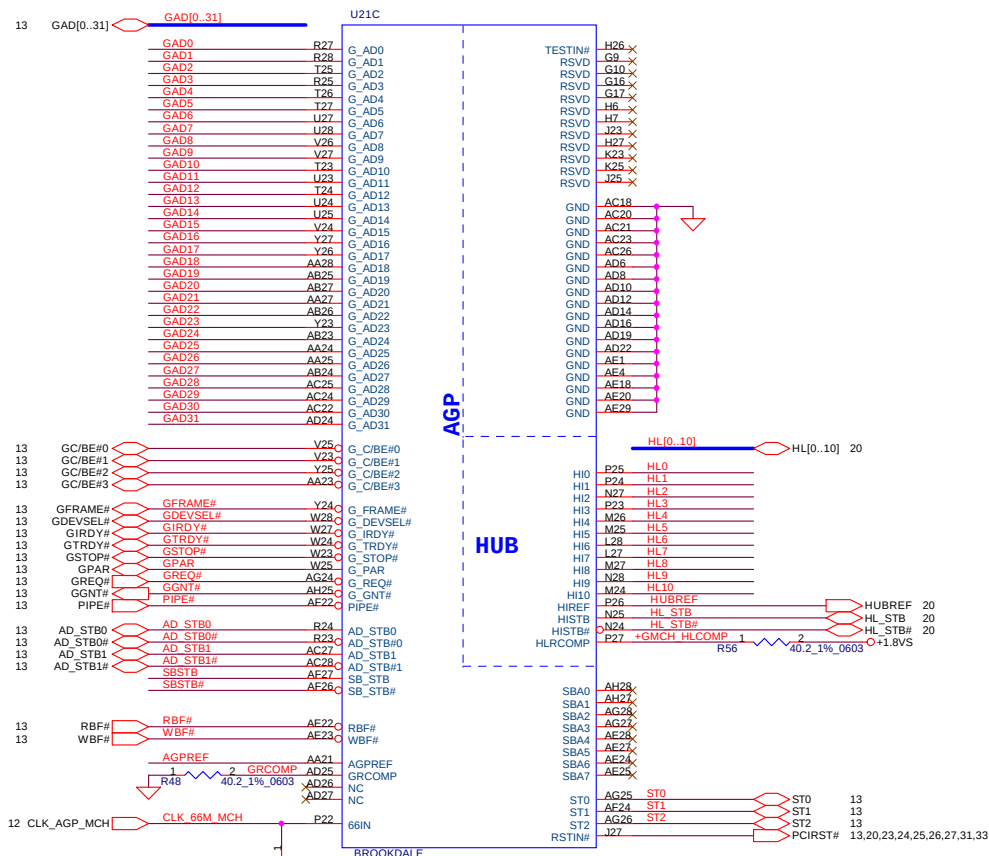
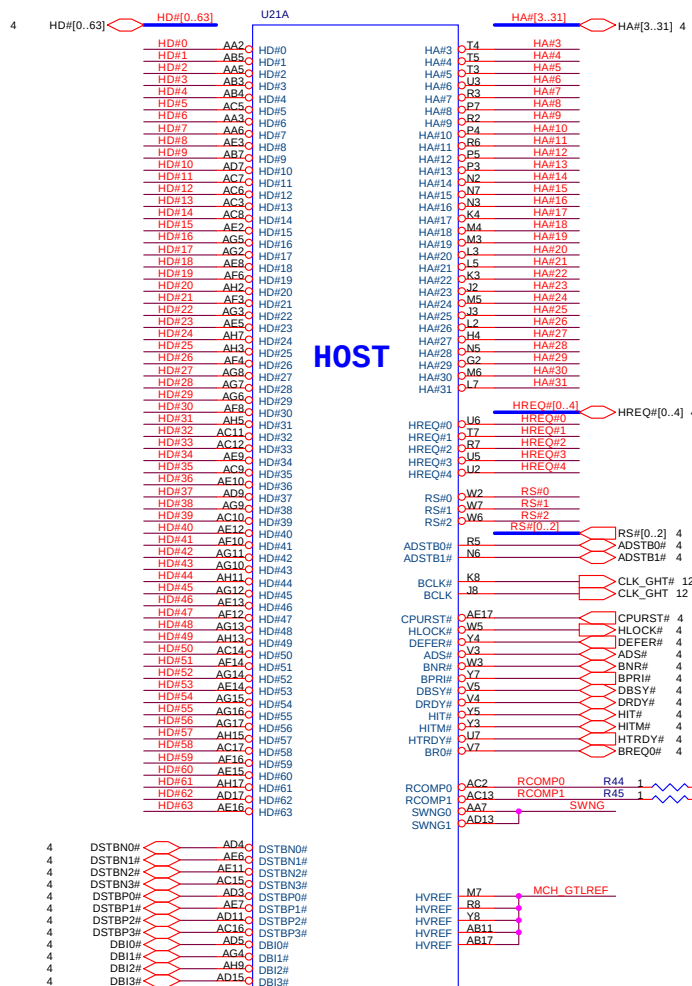
| SELPSB[1:0] | STSEM BUS FREQUENCY |
|-------------|---------------------|
| 00          | 100MHZ              |
| 01          | RESERVED            |
| 10          | RESERVED            |
| 11          | RESERVED            |

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|                                                   |                           |            |
|---------------------------------------------------|---------------------------|------------|
| Compal Electronics, Inc.                          |                           |            |
| Title<br>Pentium 4/Northwood Processor in mPGA478 |                           |            |
| Size<br>B                                         | Document Number<br>LA1432 | Rev<br>1.A |
| Date:<br>星期五, 六月 07, 2002                         | Sheet<br>4                | of<br>45   |



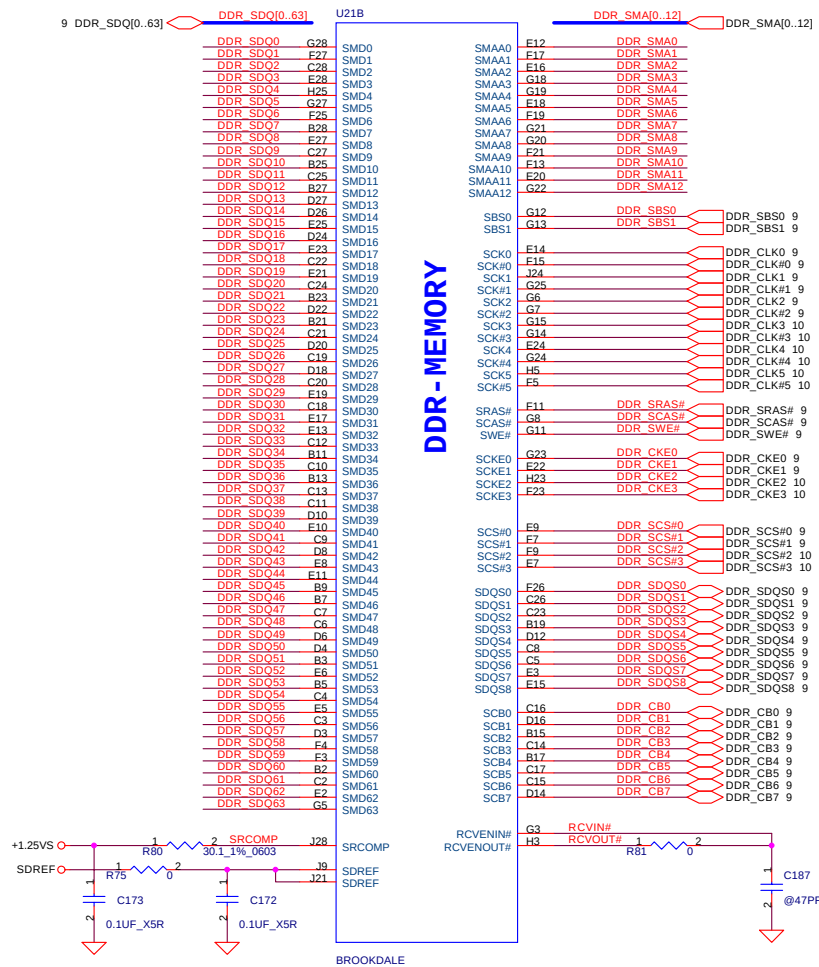
# MC-1/3(GTL+,AGP,HUB)



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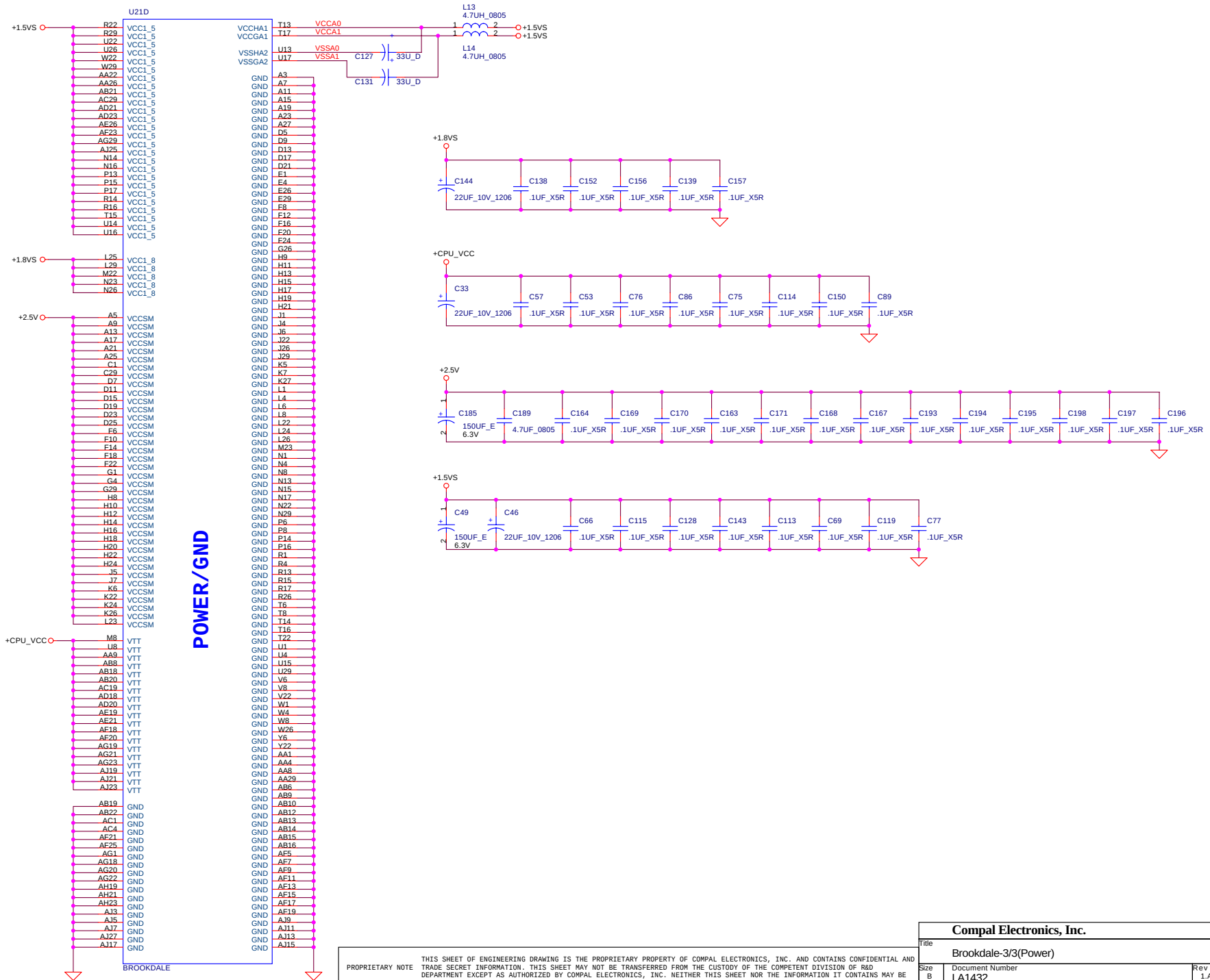
|                                    |                         |          |  |
|------------------------------------|-------------------------|----------|--|
| Compal Electronics, Inc.           |                         |          |  |
| Title: Brookdale-1/3(GTL+,AGP,HUB) |                         |          |  |
| Size: B                            | Document Number: LA1432 | Rev: 1.A |  |
| Date: 星期五, 六月 07, 2002             | Sheet: 6                | of 45    |  |

MCH-2/3(SDRAM)



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|                            |                        |         |
|----------------------------|------------------------|---------|
| Compal Electronics, Inc.   |                        |         |
| Title Brookdale-2/3(SDRAM) |                        |         |
| Size B                     | Document Number LA1432 | Rev 1.A |
| Date: 星期五, 六月 07, 2002     | Sheet 7                | of 45   |

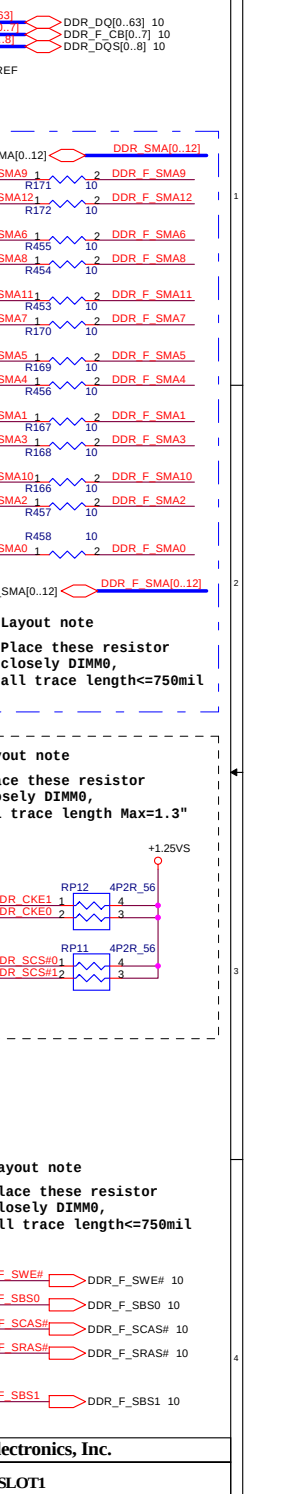
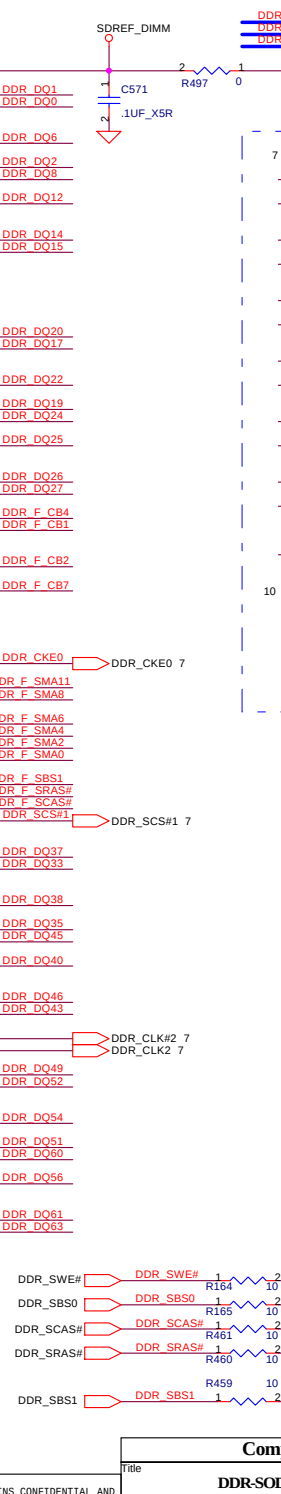
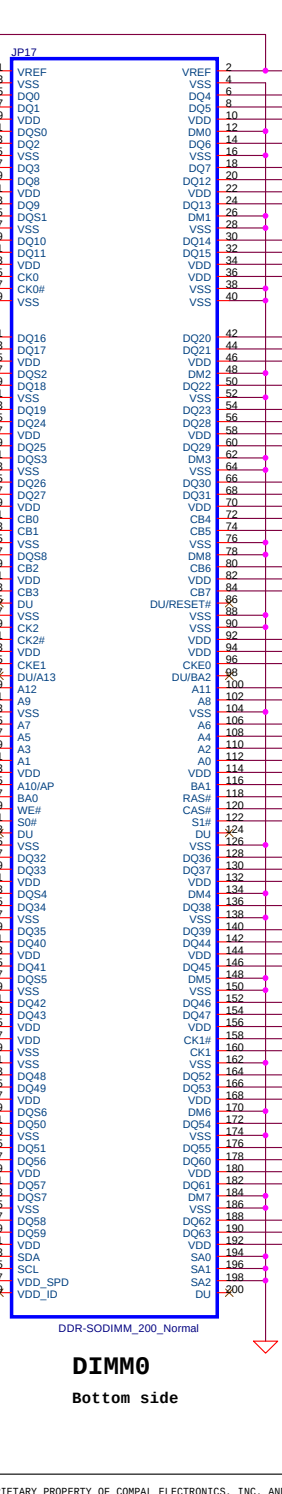
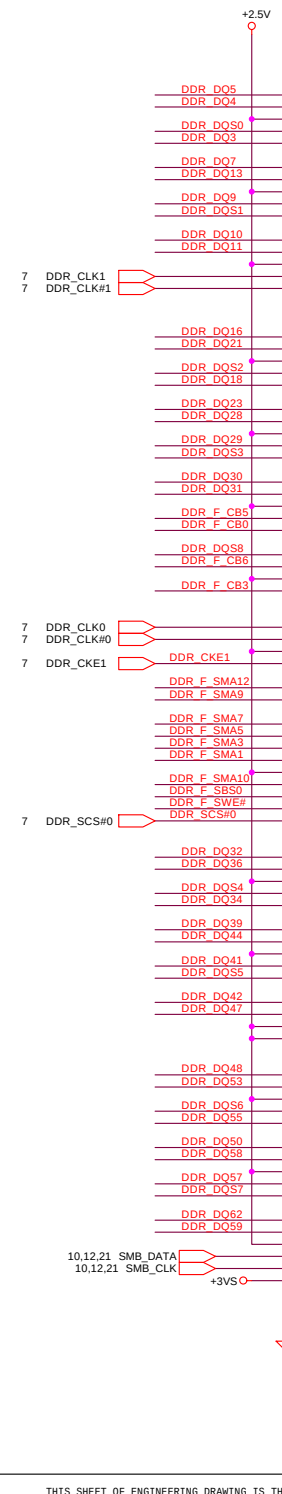
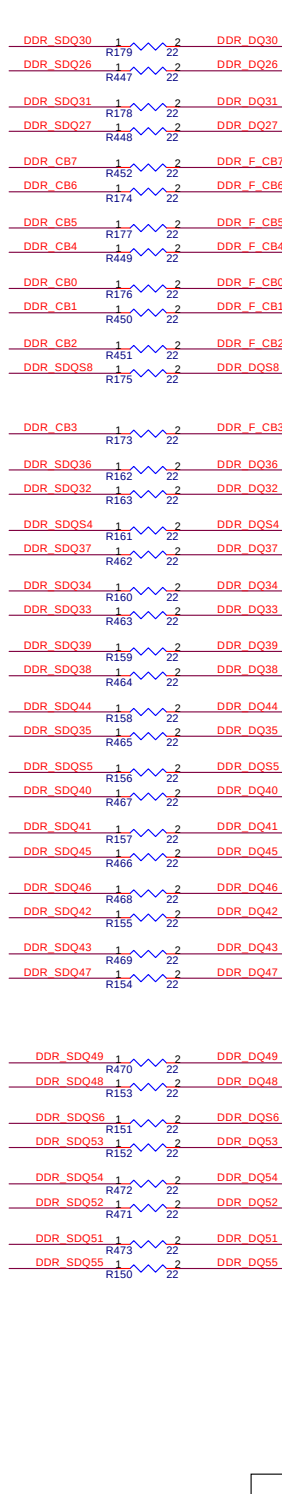
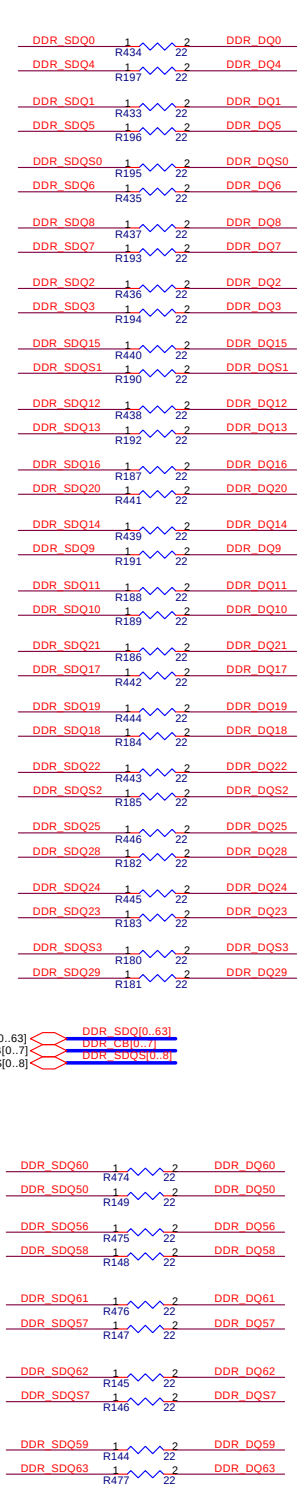


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|--------------------------|------------------|-------|---------|
| Title                    |                  |       |         |
| Brookdale-3/3(Power)     |                  |       |         |
| Size                     | Document Number  | Rev   |         |
| B                        | LA1432           | 1.A   |         |
| Date:                    | 星期五, 六月 07, 2002 | Sheet | 8 of 45 |



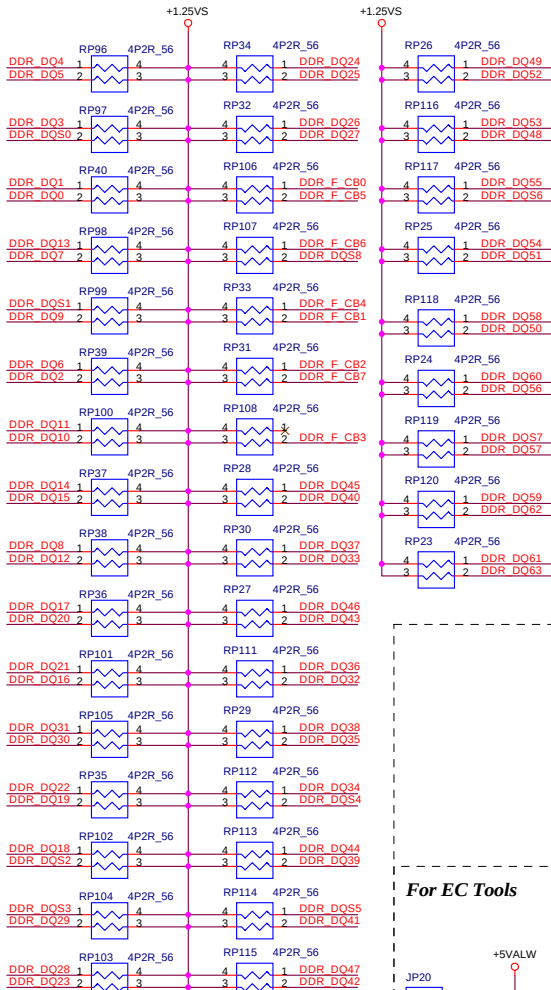
Layout note  
Place these resistor  
closely DIMM0,  
all trace length<750mil



DIMM0  
Bottom side

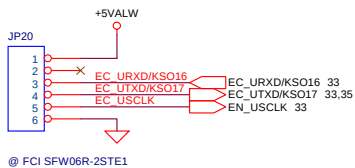
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|--------------------------|------------------|-------|---------|
| DDR-SODIMM SLOT1         |                  |       |         |
| Title                    | Document Number  |       |         |
| Size B                   | LA1432           |       |         |
| Date:                    | 日期五, 六月 07, 2002 | Sheet | 9 of 45 |



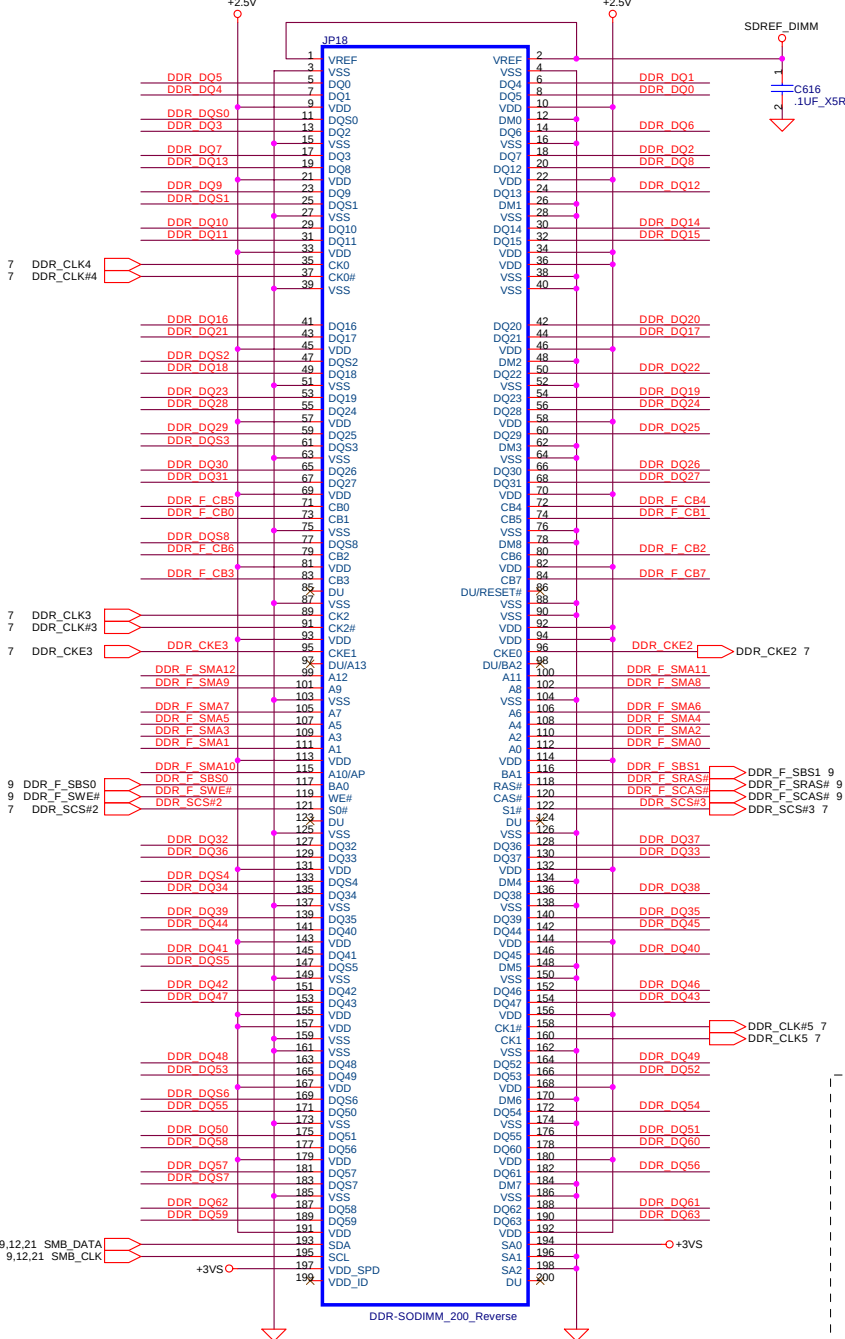
**Layout note**  
Place these resistor  
closely DIMM1,  
all trace  
length<=800mil

### For EC Tools

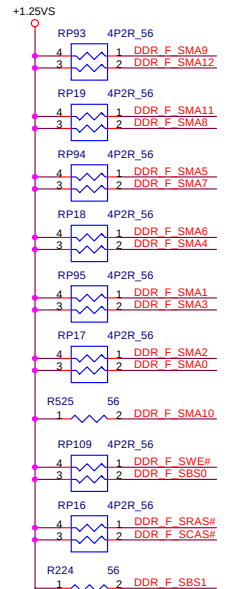


@ FCI SFW06R-2STE1

DDR\_F\_CB[0..7] DDR\_F\_CB[0..7] 9  
DDR\_DQS[0..8] DDR\_DQS[0..8] 9  
DDR\_DQ[0..63] DDR\_DQ[0..63] 9  
DDR\_F\_SMA[0..12] DDR\_F\_SMA[0..12] 9

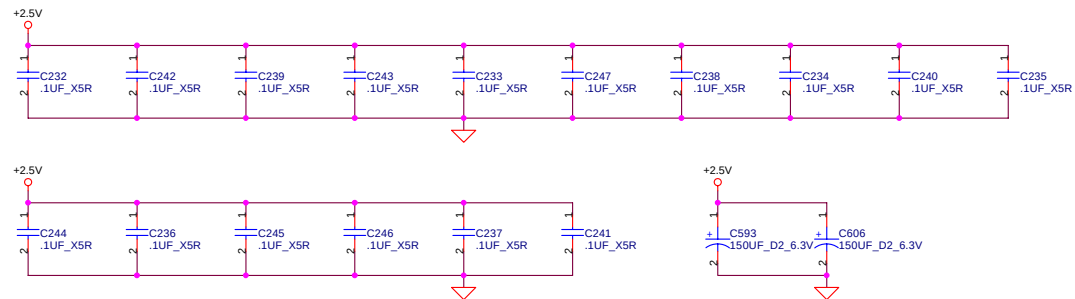


**DIMM1**  
Top side

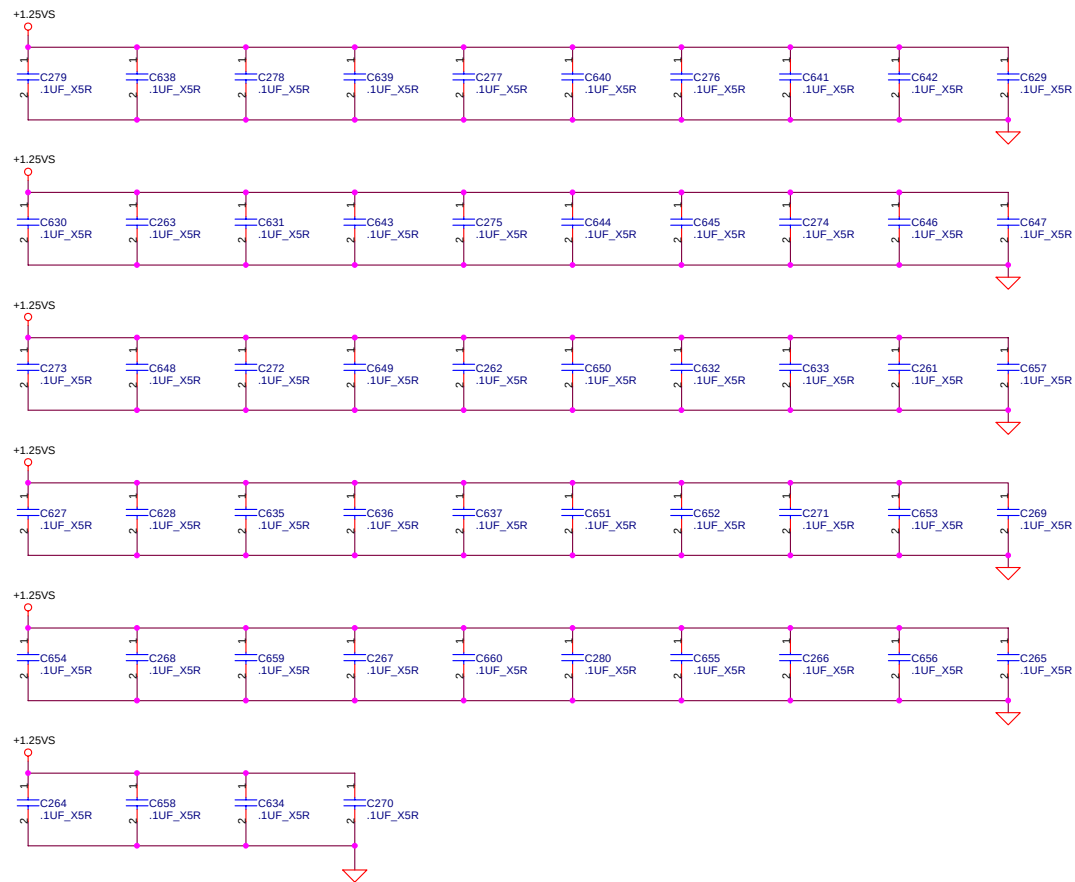


**Layout note**  
Place these resistor  
closely DIMM0,  
all trace length  
Max=1.3"

Layout note :  
Distribute as close as possible  
to DDR-SODIMM.



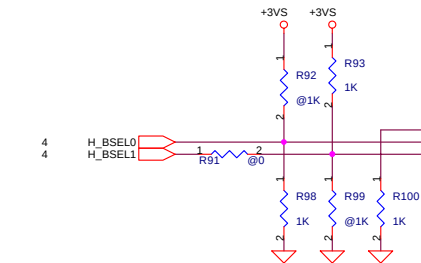
Layout note :  
Place one cap close to every 2 pull up resistors termination to  
+1.25V



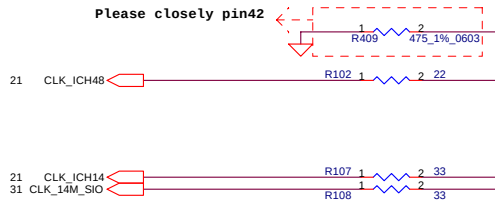
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|--------------------------|------------------|--|----------------|
| Title                    |                  |  |                |
| DDR SODIMM Decoupling    |                  |  |                |
| Size                     | Document Number  |  | Rev            |
| B                        | LA1432           |  | 1.A            |
| Date:                    | 星期五, 六月 07, 2002 |  | Sheet 11 of 45 |

| SEL1 | SEL0 | Function        |
|------|------|-----------------|
| 0    | 0    | 66MHz Host CLK  |
| 0    | 1    | 160MHz Host CLK |
| 1    | 0    | 200MHz Host CLK |
| 1    | 1    | 133MHz Host CLK |

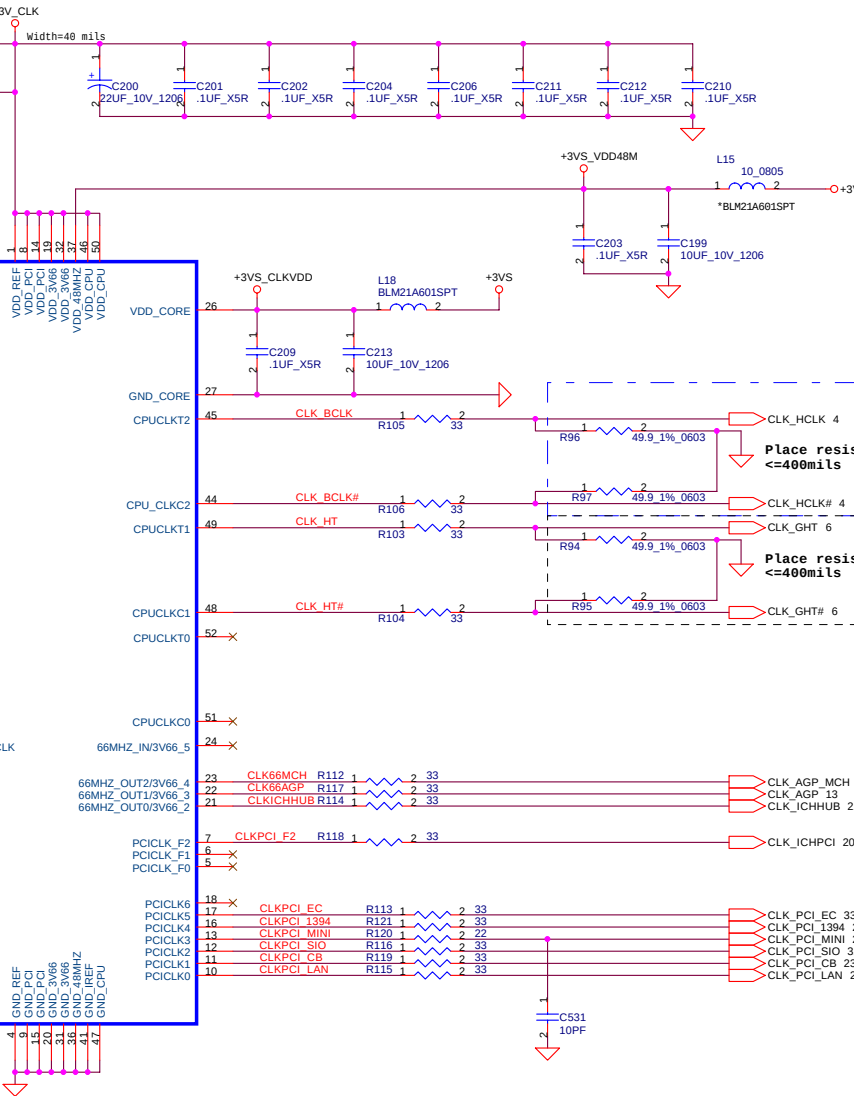
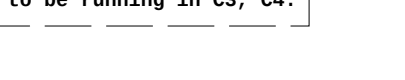
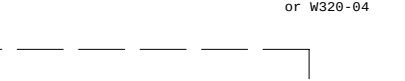
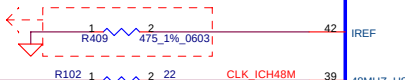
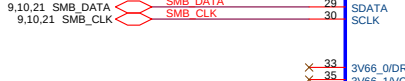
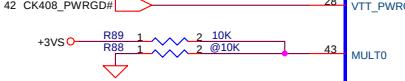
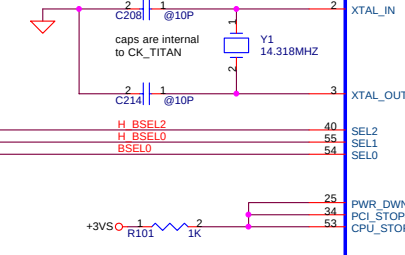


| MULT0 | Reference R, Iref= VDD/(3*Rr) | Output Current |
|-------|-------------------------------|----------------|
| 0     | Rr = 221 1%, Iref = 5.00mA    | Ioh = 4 * Iref |
| 1     | Rr = 475 1%, Iref = 2.32mA    | Ioh = 6 * Iref |



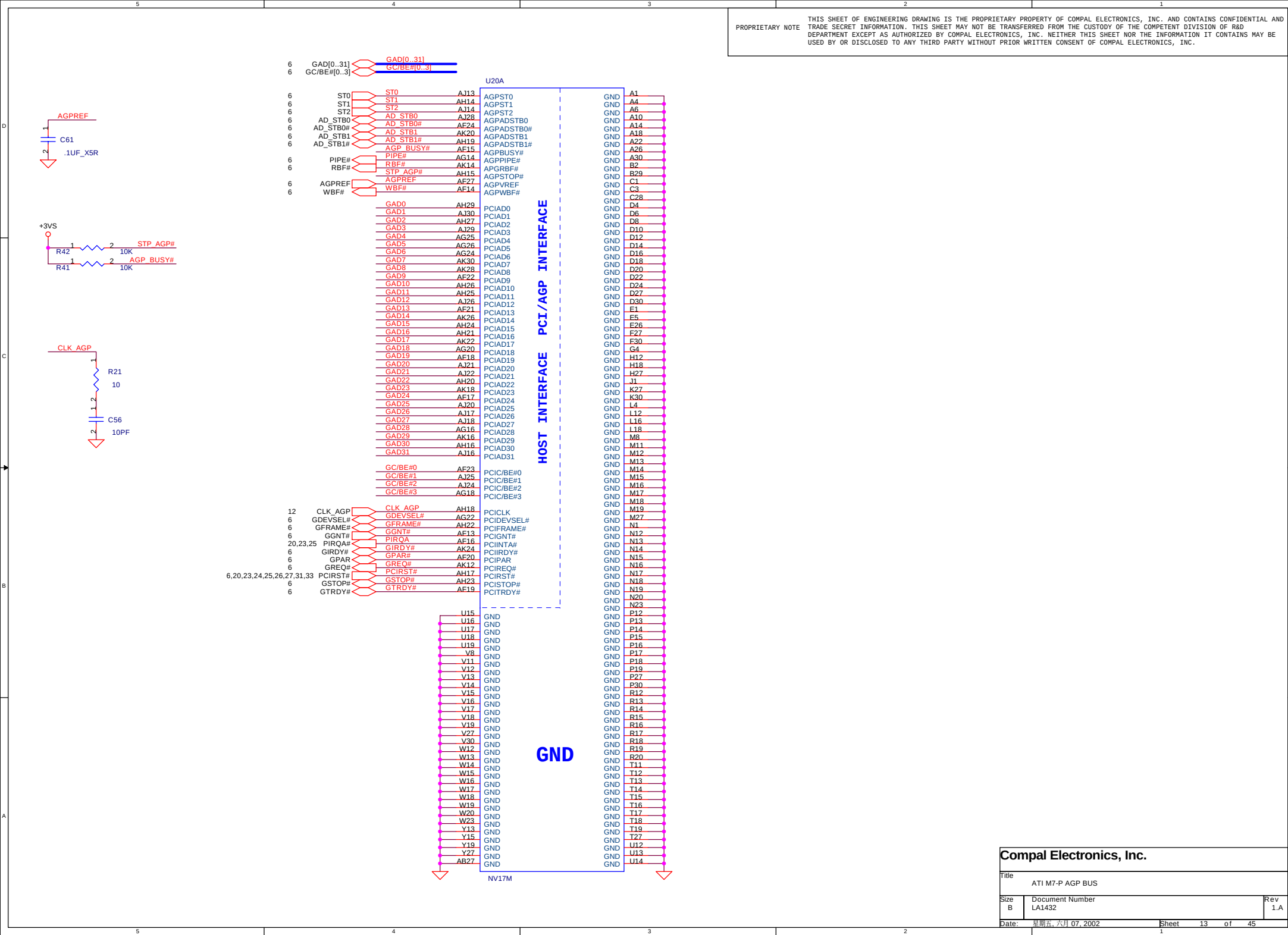
Note:  
CPU\_CLK[2:0] needs to be running in C3, C4.

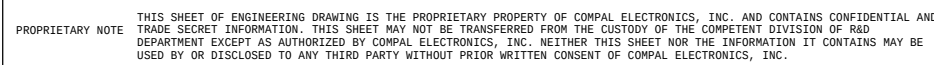
Place Crystal within 500 mils of CK\_Titan

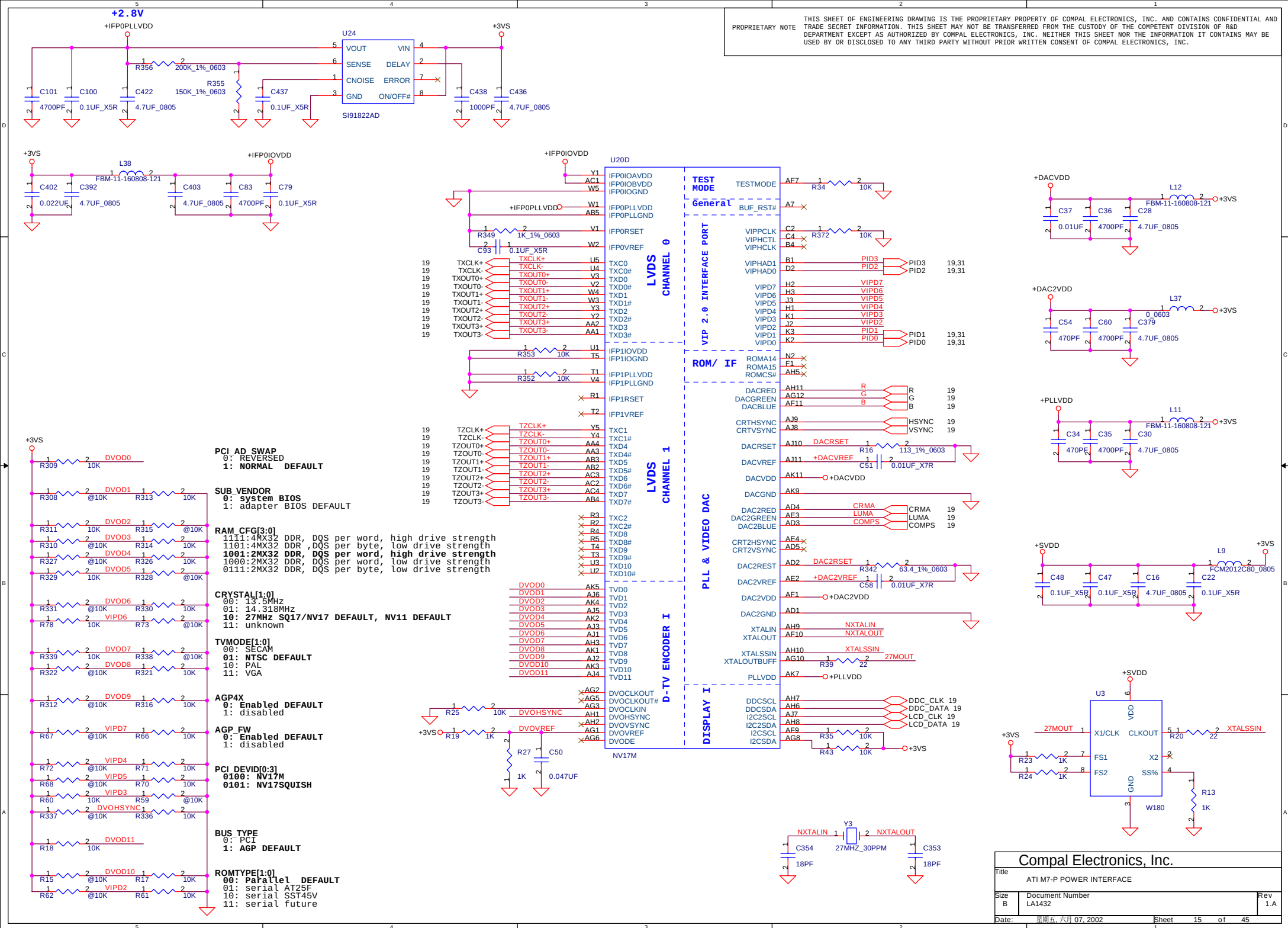


| Compal Electronics, Inc. |                  |       |          |
|--------------------------|------------------|-------|----------|
| Title                    |                  |       |          |
| Clock Generator          |                  |       |          |
| Size                     | Document Number  | Rev   |          |
| B                        | LA1432           | 1.A   |          |
| Date:                    | 星期五, 六月 07, 2002 | Sheet | 12 of 45 |

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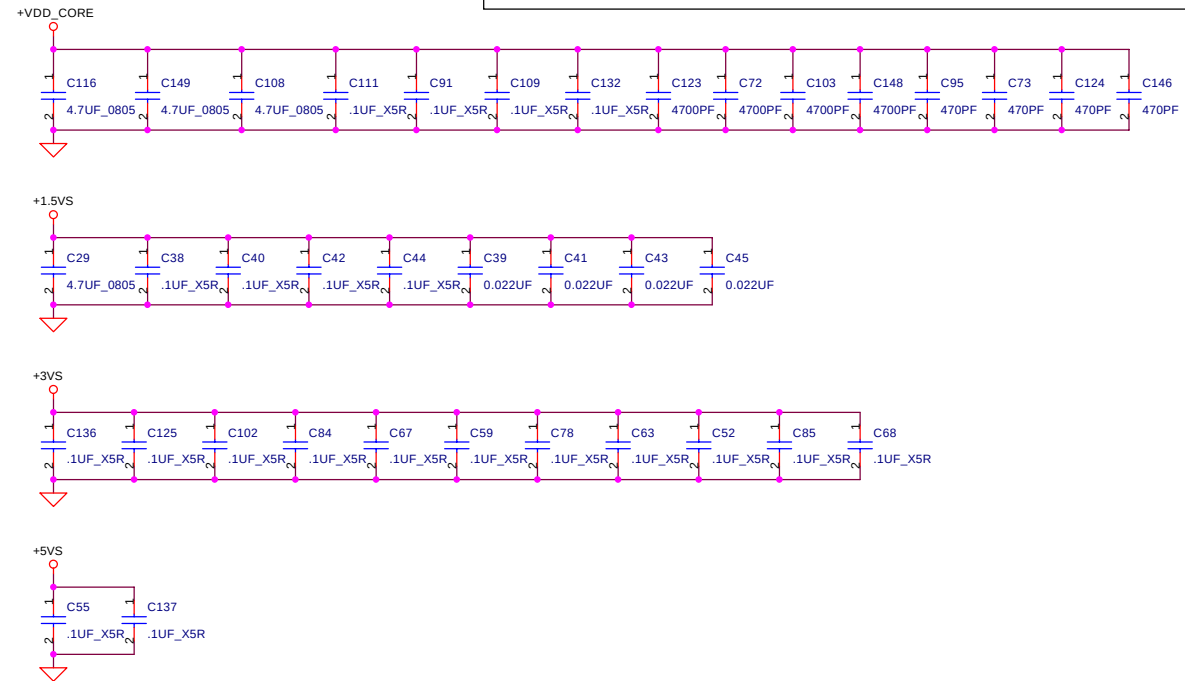
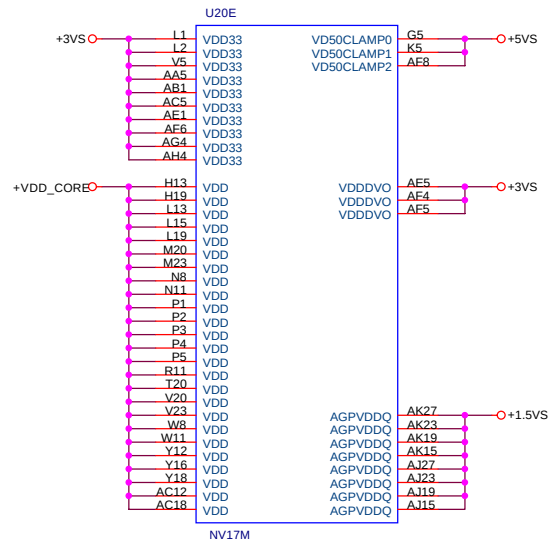




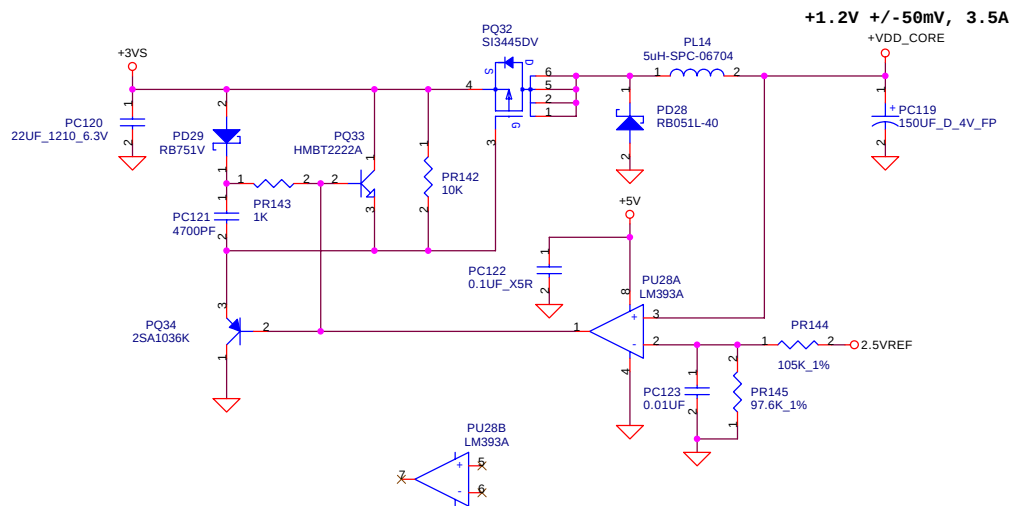




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As close as ppossible to related pin



**Compal Electronics, Inc.**

|       |                    |
|-------|--------------------|
| Title | ATI M7-P Power I/F |
|-------|--------------------|

|           |                           |
|-----------|---------------------------|
| Size<br>B | Document Number<br>LA1432 |
|-----------|---------------------------|

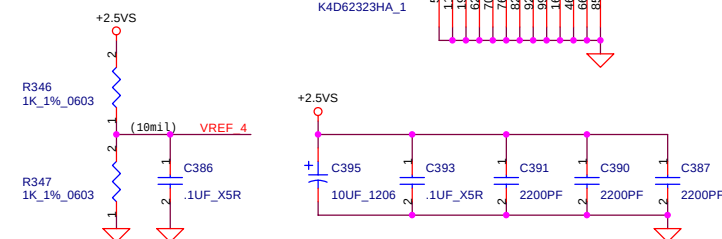
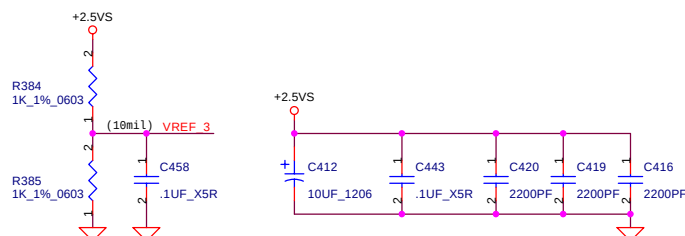
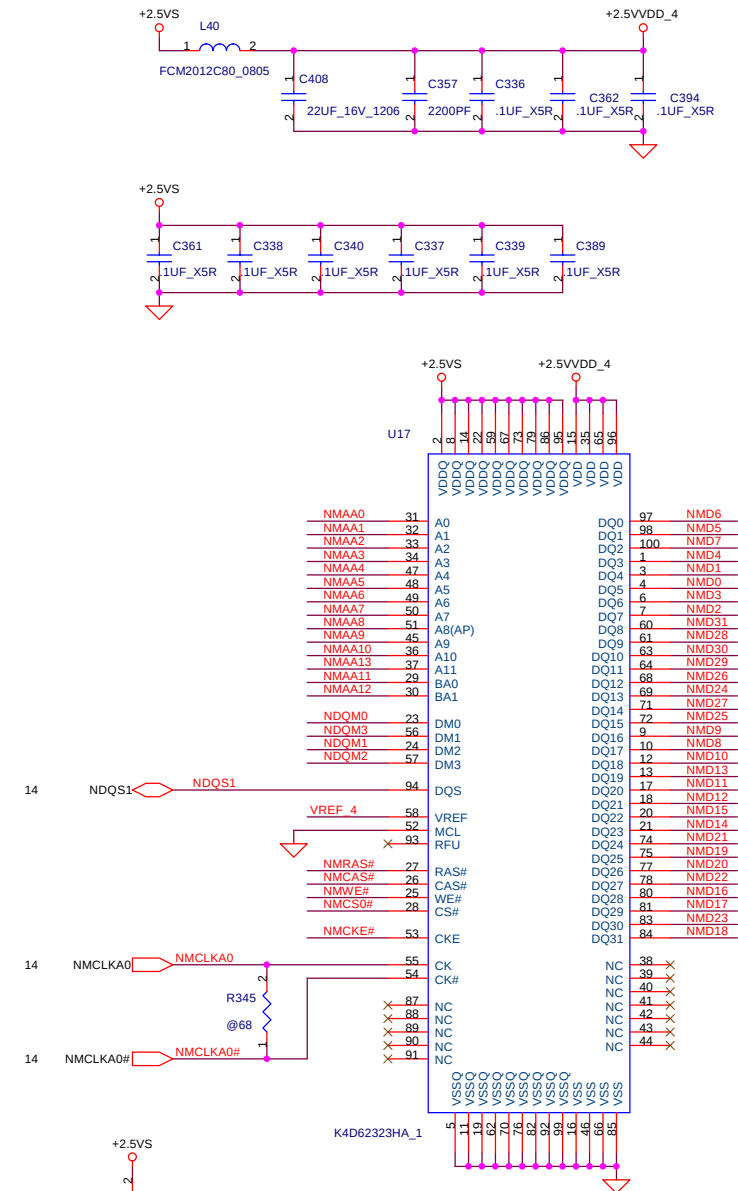
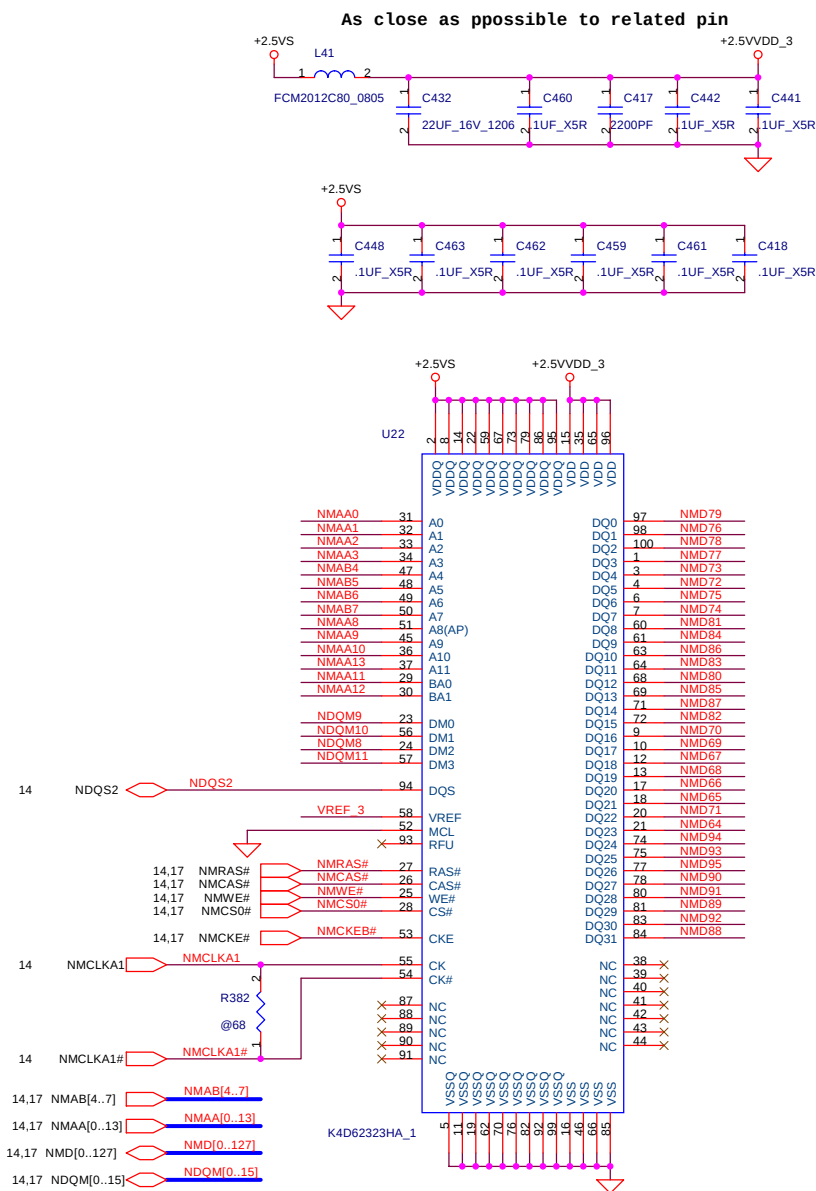
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| Rev |  |
| 1.A |  |

Date: 星期五, 六月 07, 2002

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**Compal Electronics, Inc.**

|       |                           |
|-------|---------------------------|
| Title | ATI DDR CH-B for M7P Only |
|-------|---------------------------|

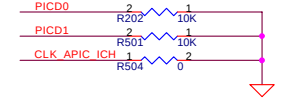
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| Size | Document Number | Rev |
| B    | LA1432          | 1.A |

|       |                  |       |    |    |    |
|-------|------------------|-------|----|----|----|
| Date: | 星期五, 六月 07, 2002 | Sheet | 18 | of | 45 |
|-------|------------------|-------|----|----|----|

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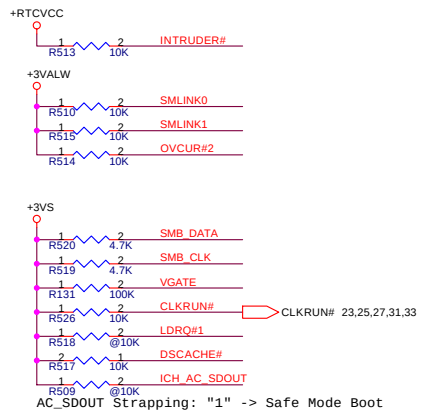
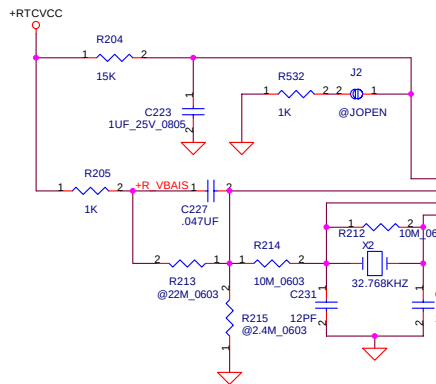
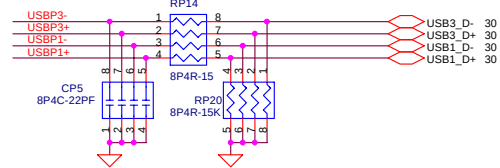
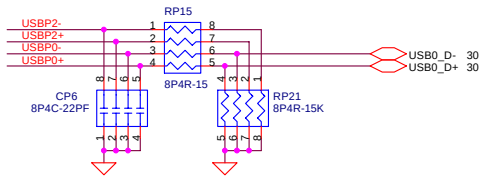
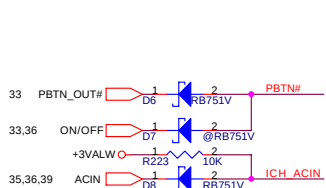
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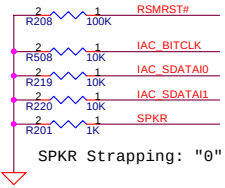
| PCI REQ ASSIGNMENT |                   |
|--------------------|-------------------|
| REQ#0              | 1394              |
| REQ#1              | WLAN              |
| REQ#2              | PCMCIA CONTROLLER |
| REQ#3              | LAN               |
| REQ#4              | NC                |

|                                 |                  |                           |          |
|---------------------------------|------------------|---------------------------|----------|
| <b>Compal Electronics, Inc.</b> |                  |                           |          |
| Title                           |                  | ICH2-A(PCI,HUB,CPU) & FWH |          |
| Size                            | Document Number  | Rev                       |          |
| B                               | LA1432           | 1A                        |          |
| Date:                           | 星期日, 六月 07, 2002 | Sheet                     | 20 of 45 |

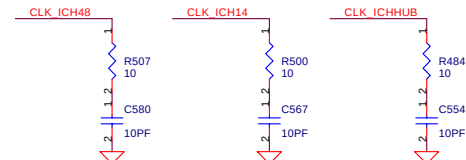
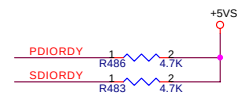
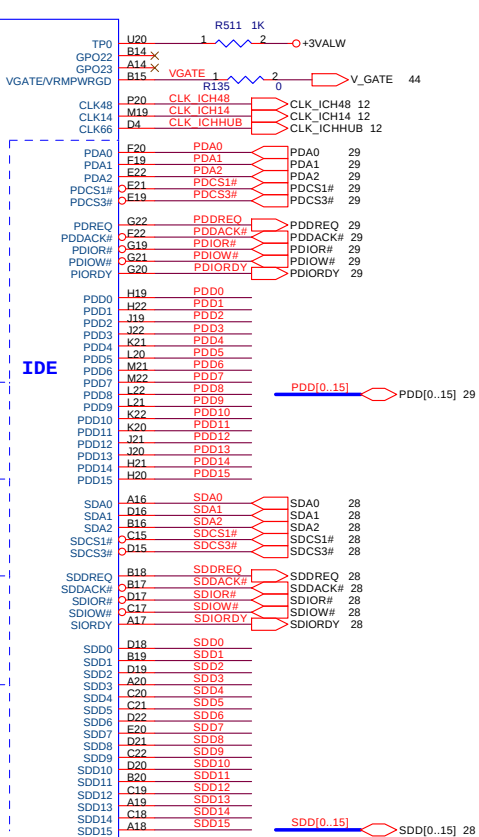
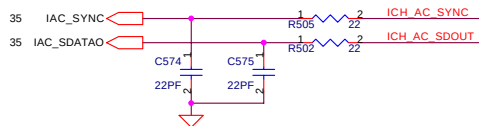
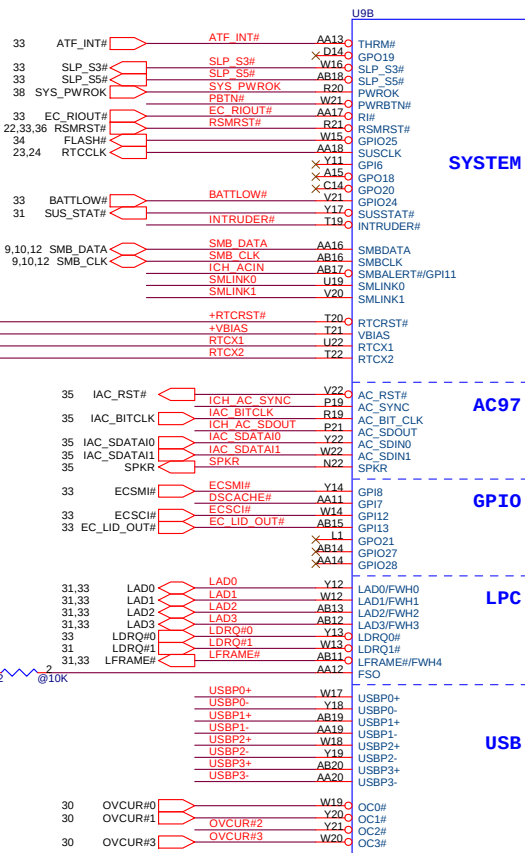
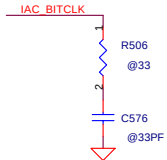
## ICH2-B(IDE,LPC,GPIO)



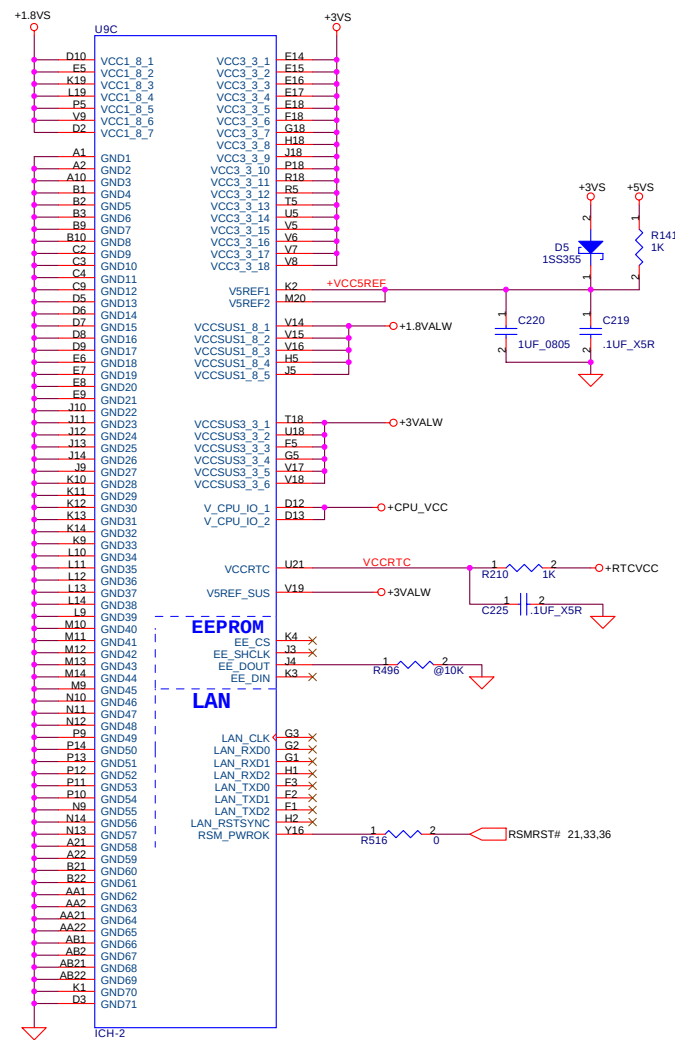
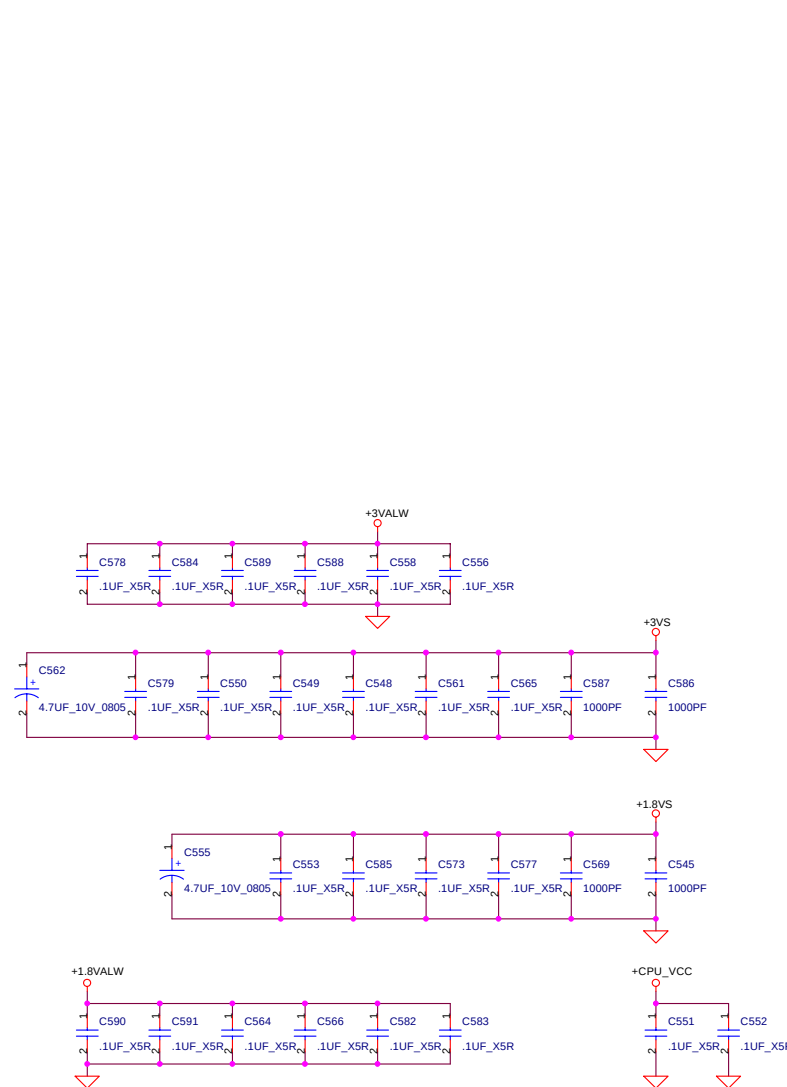
```
AC_SD0UT Strapping: "1" -> Safe Mode Boot
```



SPKR Strapping: "0" -> No Reboot



ICH2-C(LAN,Power)

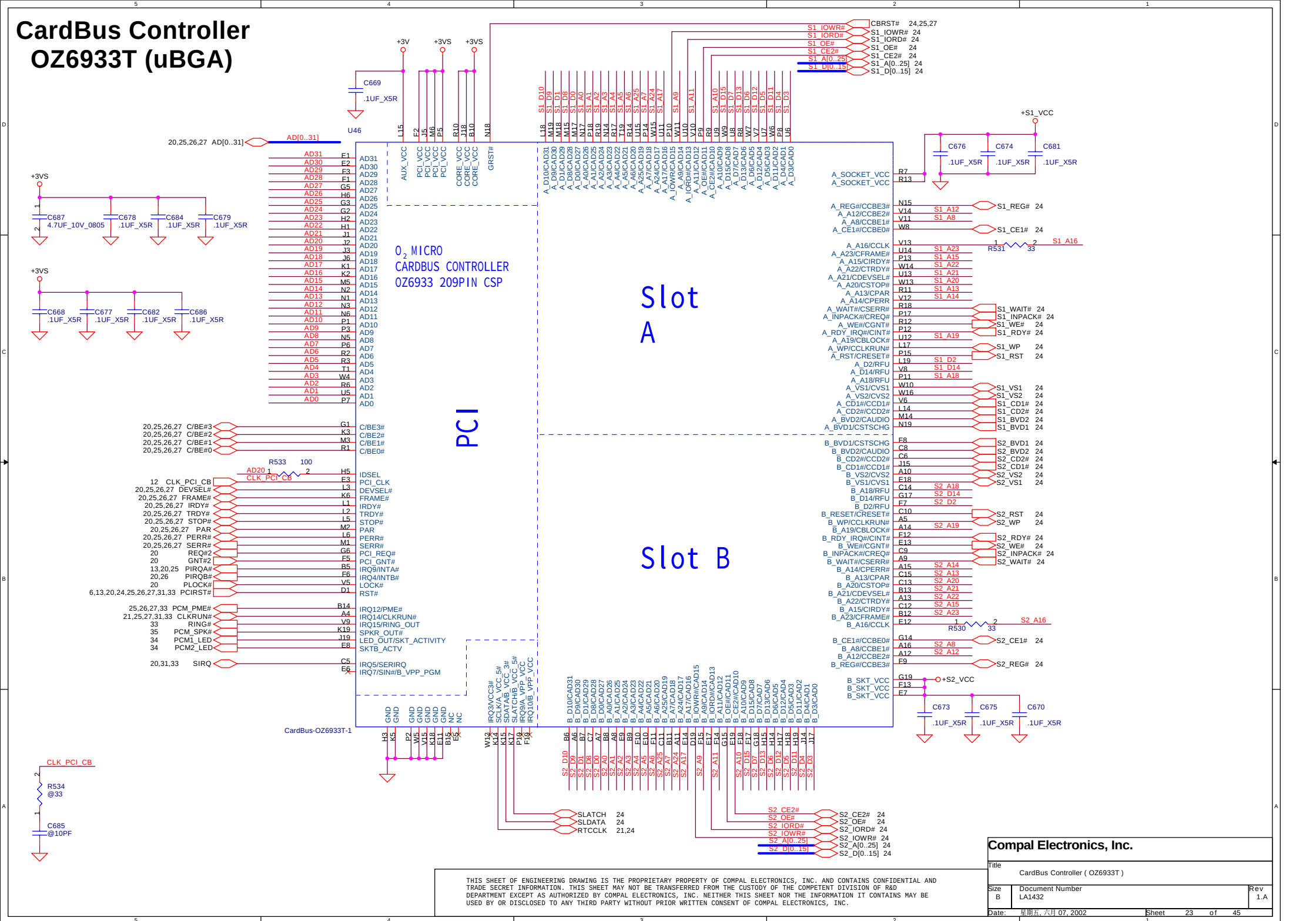


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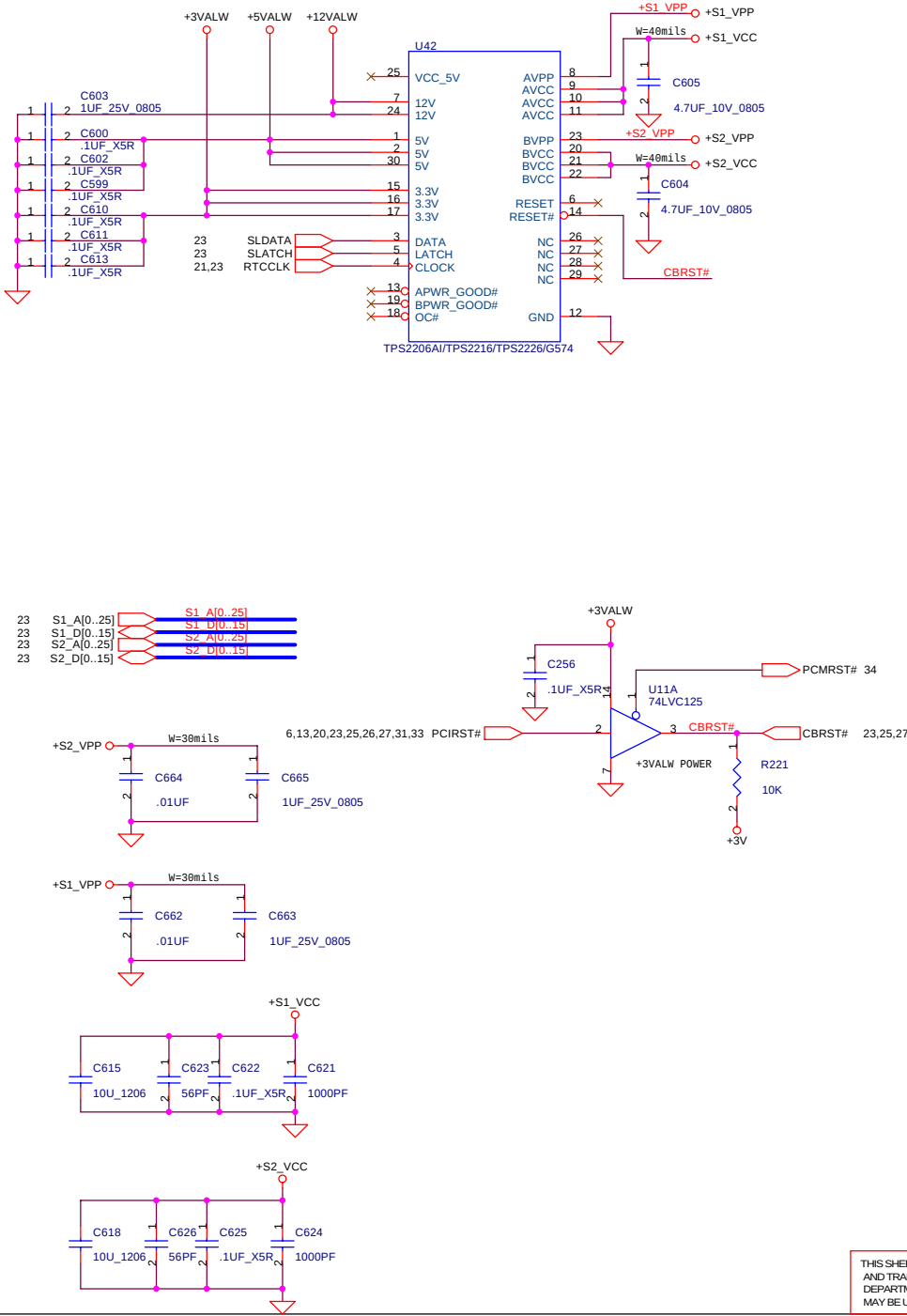
| Compal Electronics, Inc.    |                  |  |                |
|-----------------------------|------------------|--|----------------|
| Title                       |                  |  |                |
| ICH2-C(LAN,Power) & Pull-Up |                  |  |                |
| Size                        | Document Number  |  | Rev            |
| B                           | LA1432           |  | 1.A            |
| Date:                       | 星期五, 六月 07, 2002 |  | Sheet 22 of 45 |

# CardBus Controller

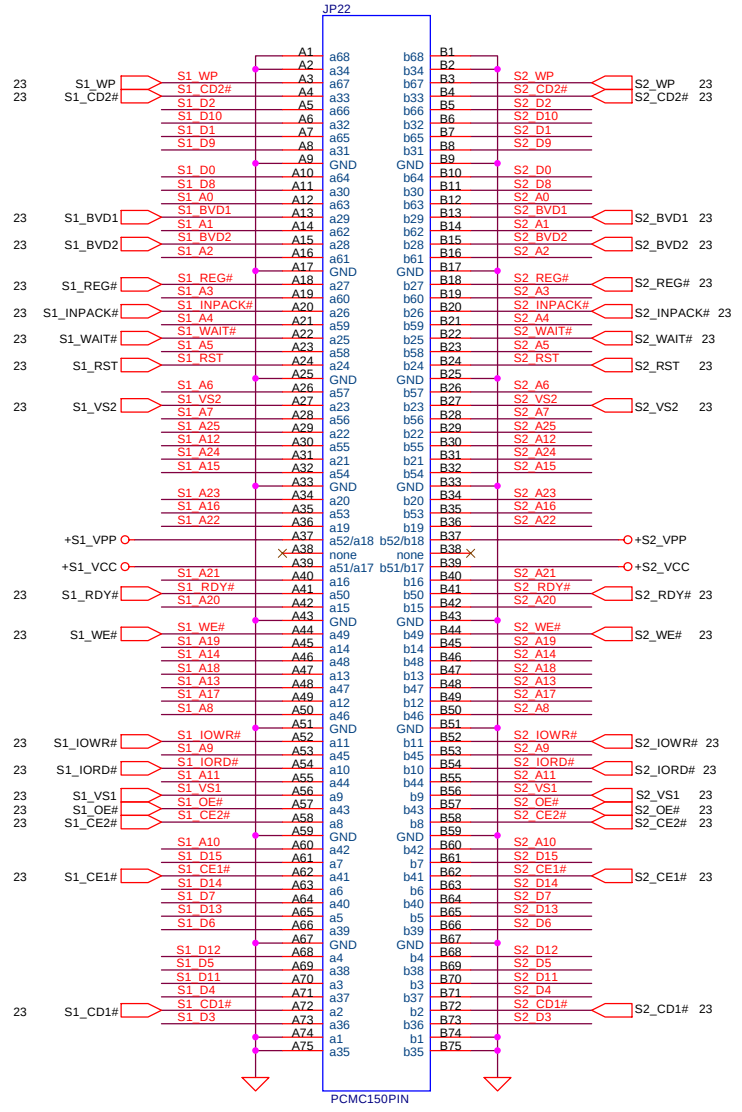
## OZ6933T (uBGA)



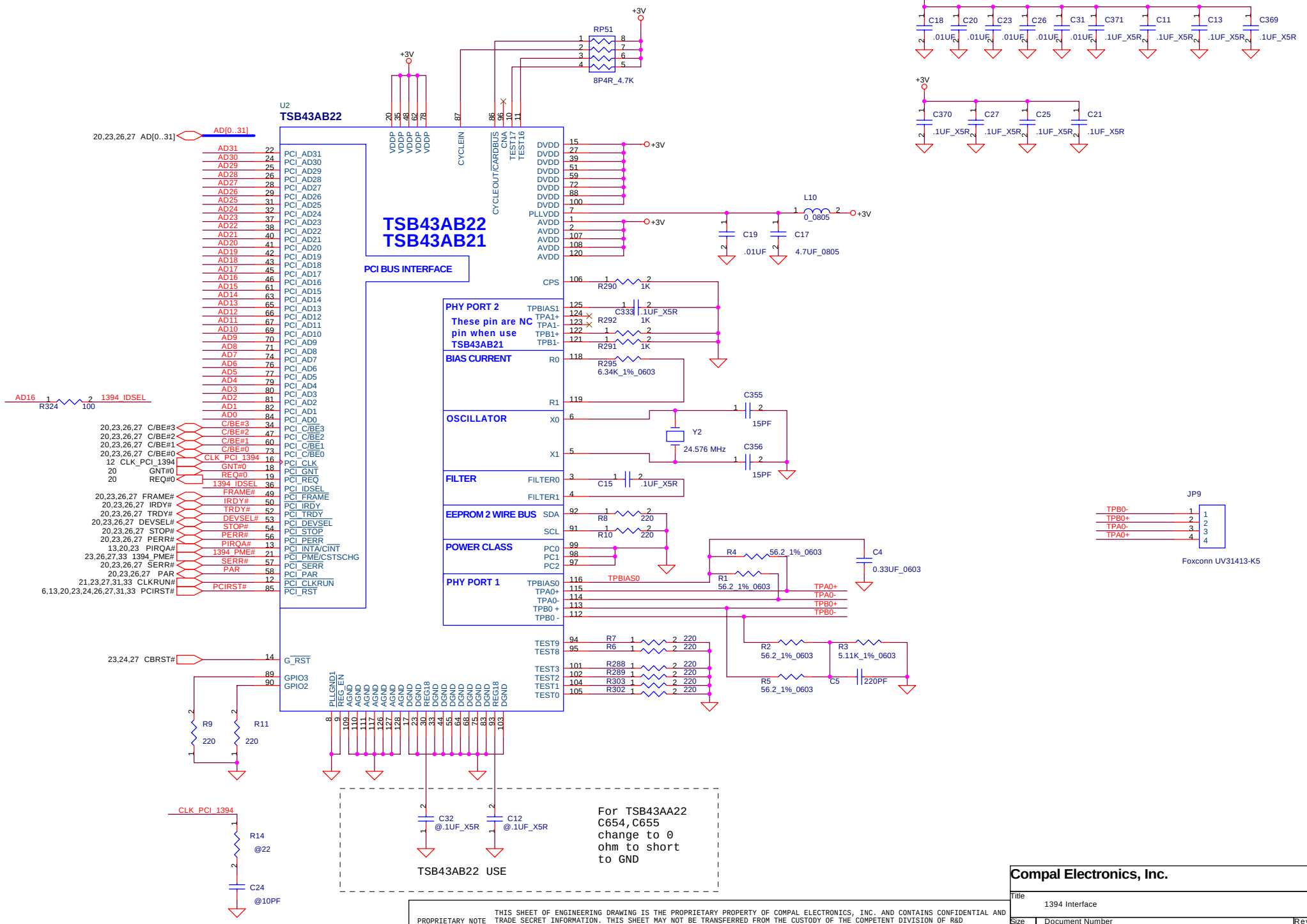
PCMCIA POWER CTRL.



CARDBUS  
SOCKET

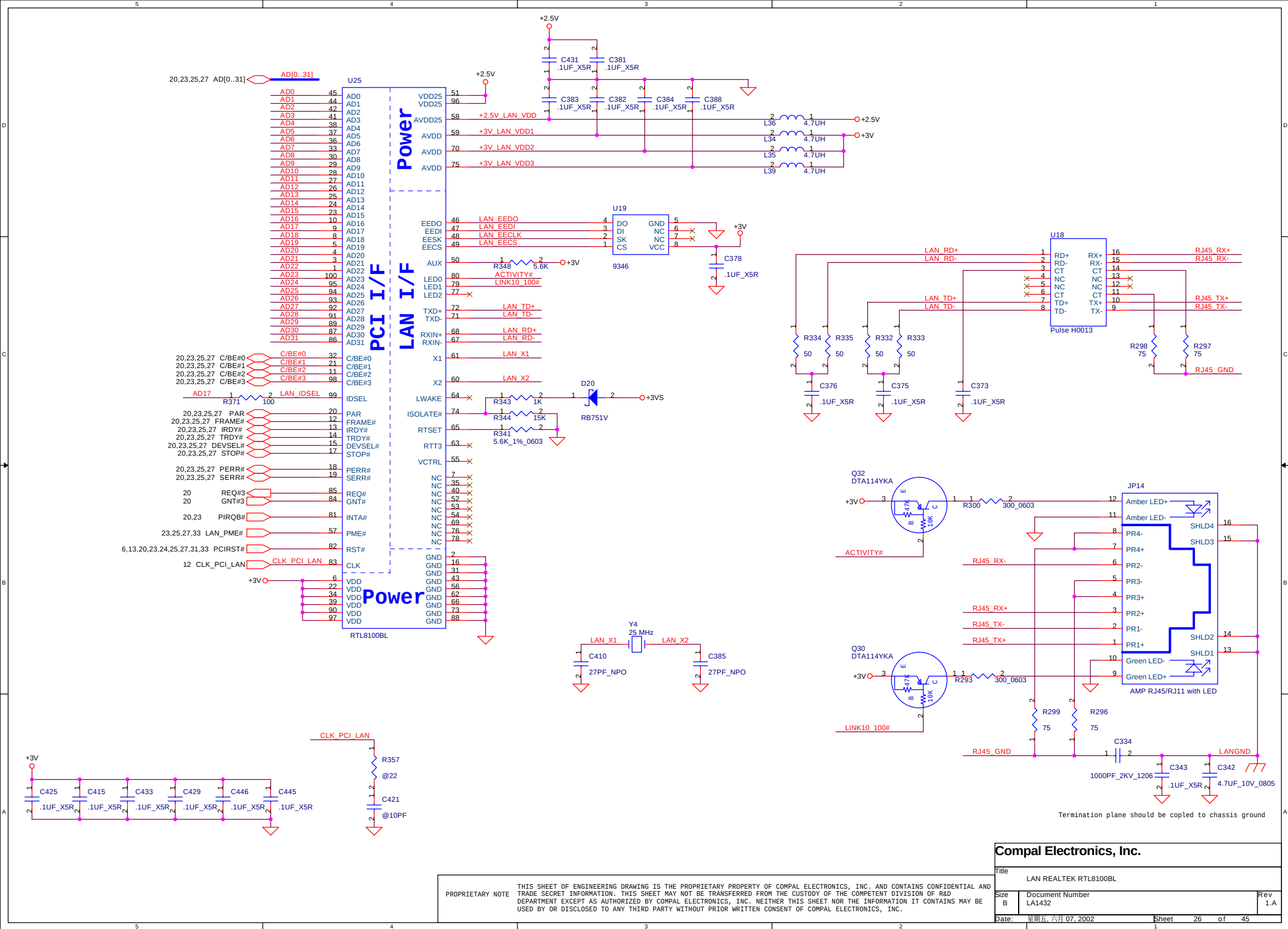


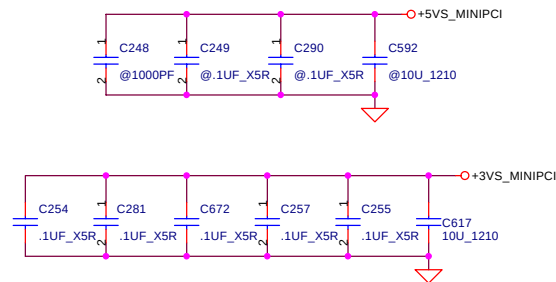
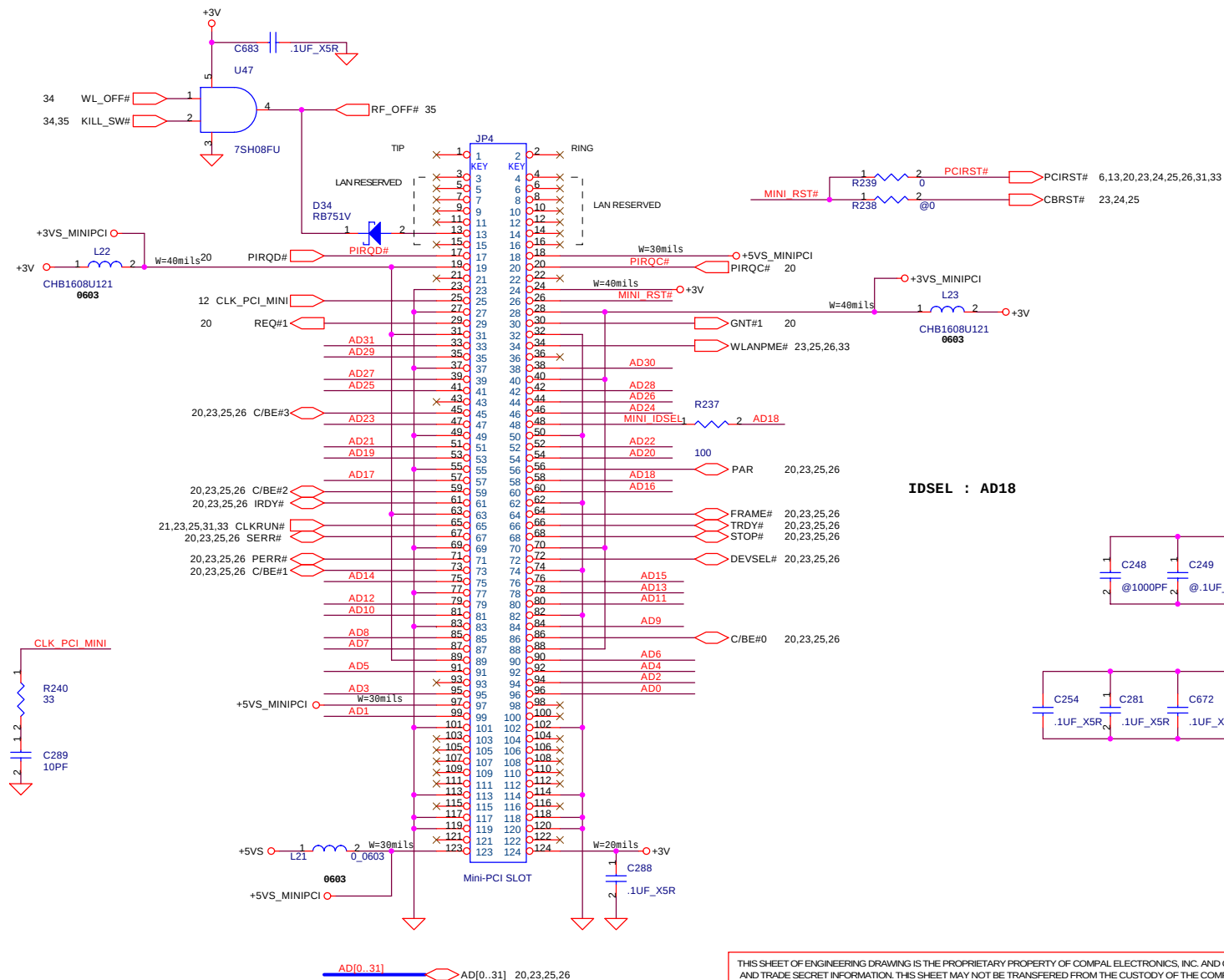




|                          |                        |                |         |
|--------------------------|------------------------|----------------|---------|
| Compal Electronics, Inc. |                        |                |         |
| Title 1394 Interface     |                        |                |         |
| Size B                   | Document Number LA1432 |                | Rev 1.A |
|                          |                        |                |         |
| Date:                    | 星期五, 六月 07, 2002       | Sheet 25 of 45 |         |

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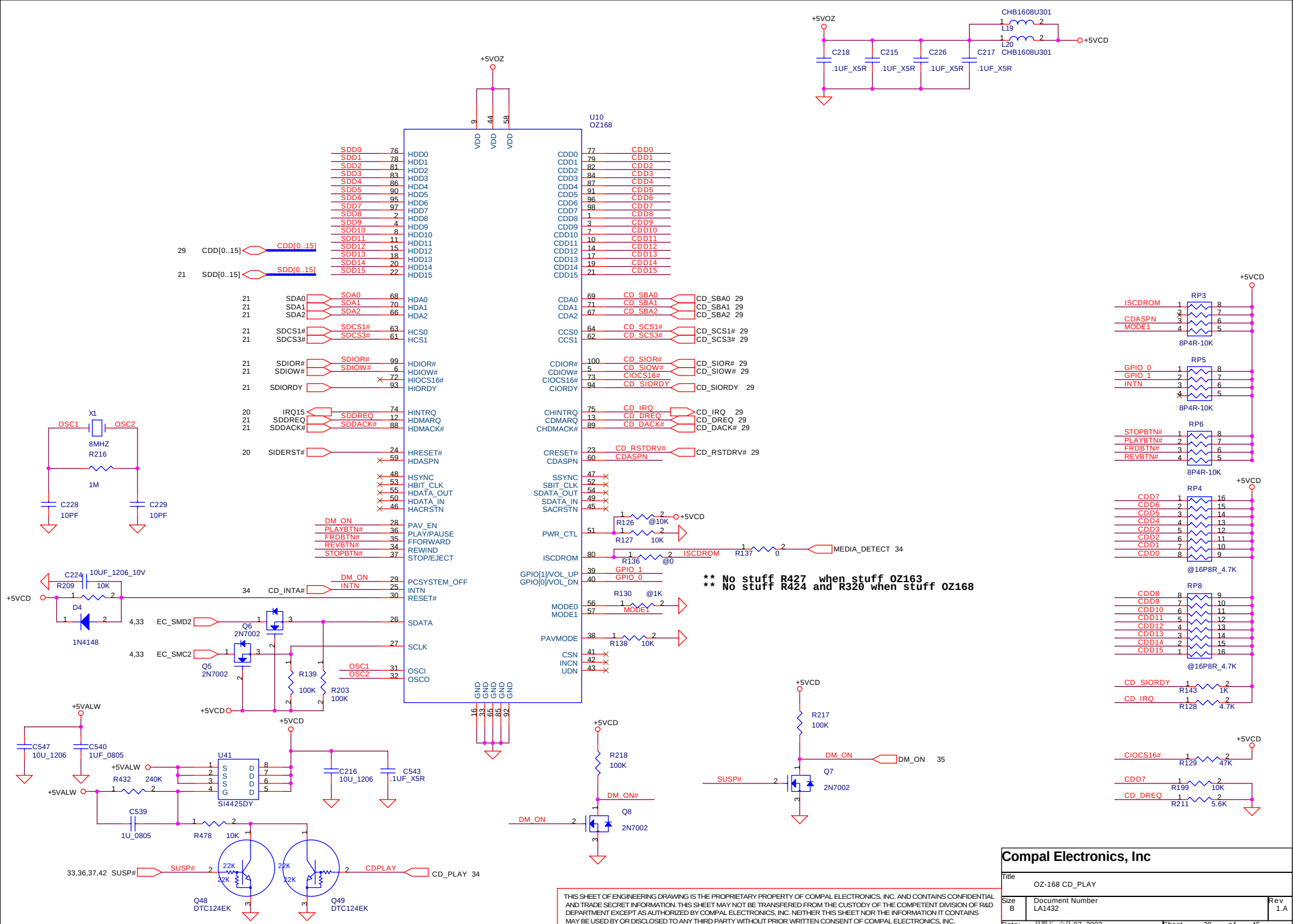




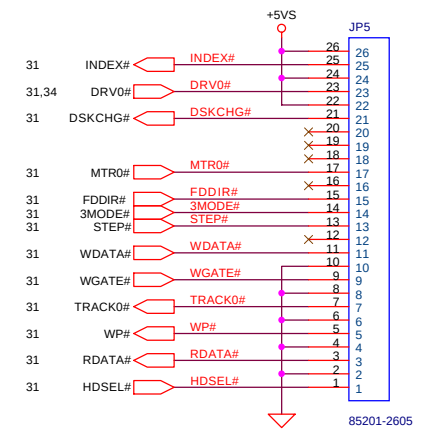
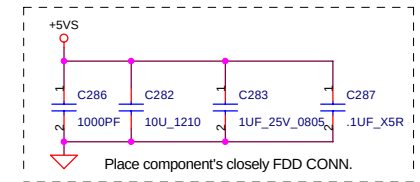
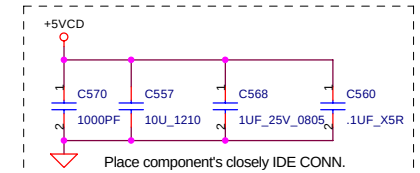
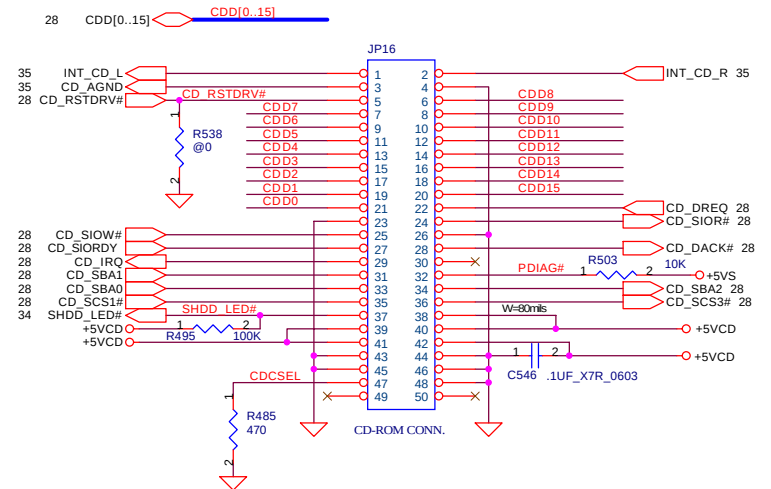
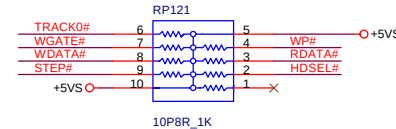
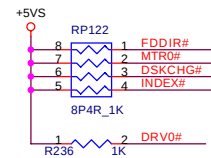
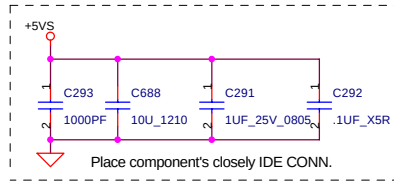
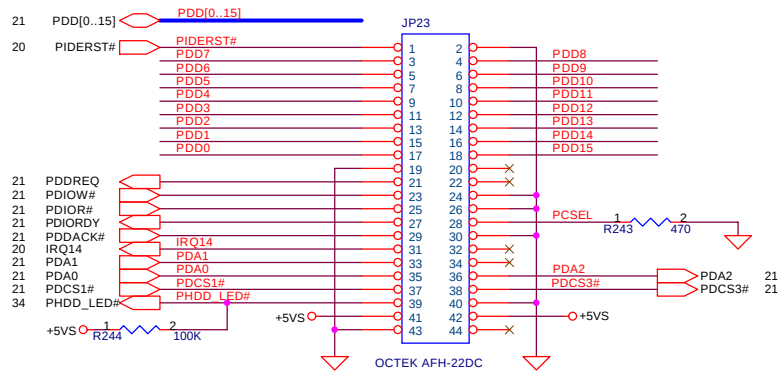
## Compal Electronics, Inc

|       |                  |       |          |
|-------|------------------|-------|----------|
| Title |                  |       | MINI_PCI |
| Size  | Document Number  | Rev   |          |
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| Date: | 星期五, 六月 07, 2002 | Sheet | 27 of 45 |

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# IDE,CD-ROM Module CONN.

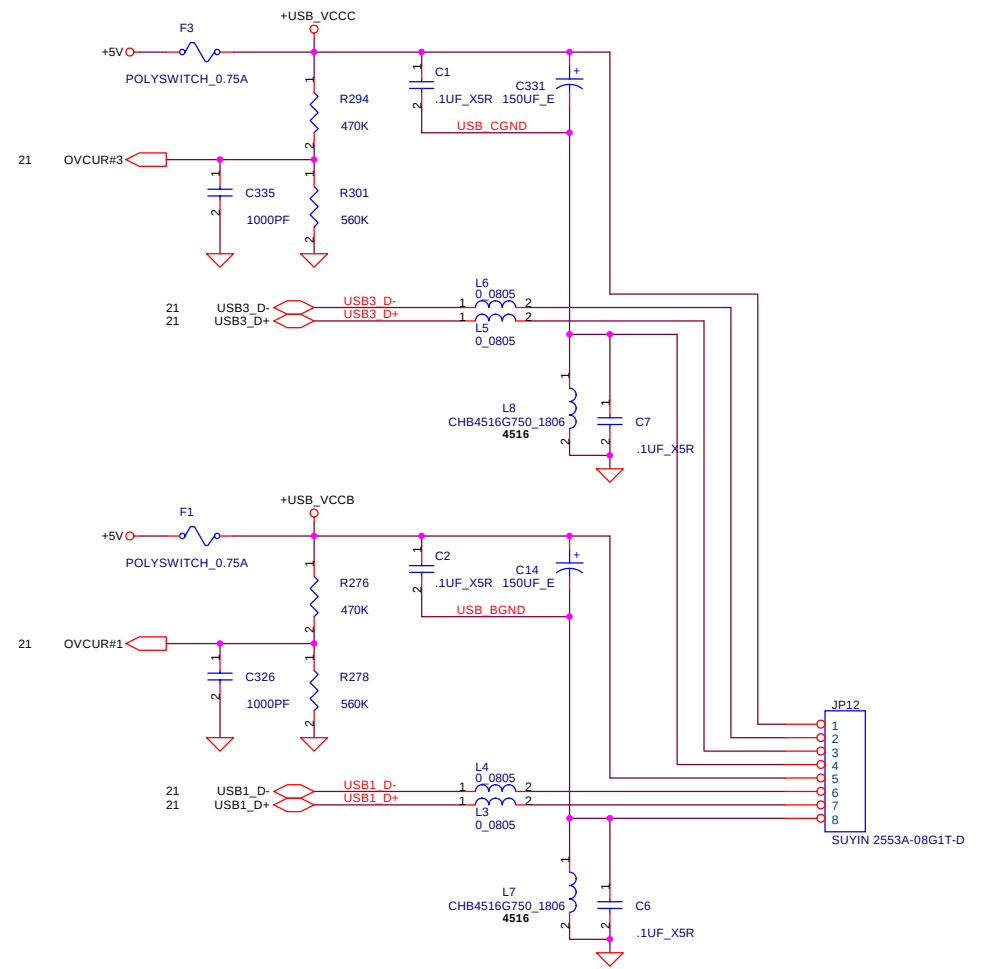
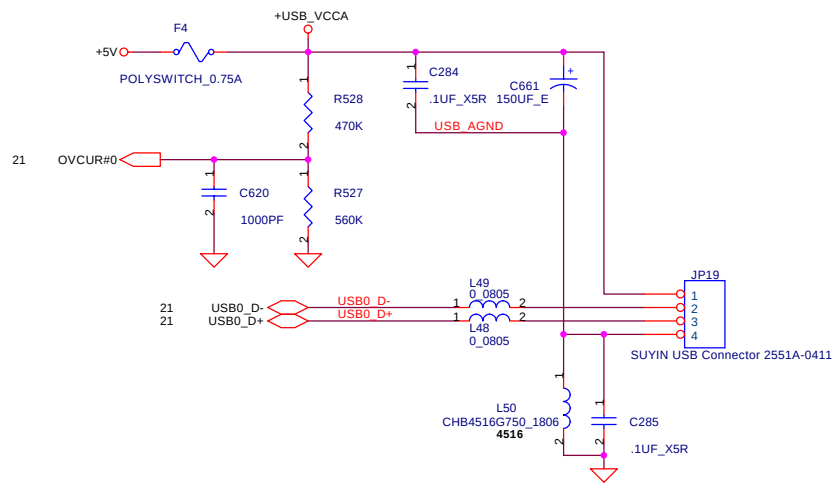


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Compal Electronics, Inc

IDE/ FDD MODULE CONN.

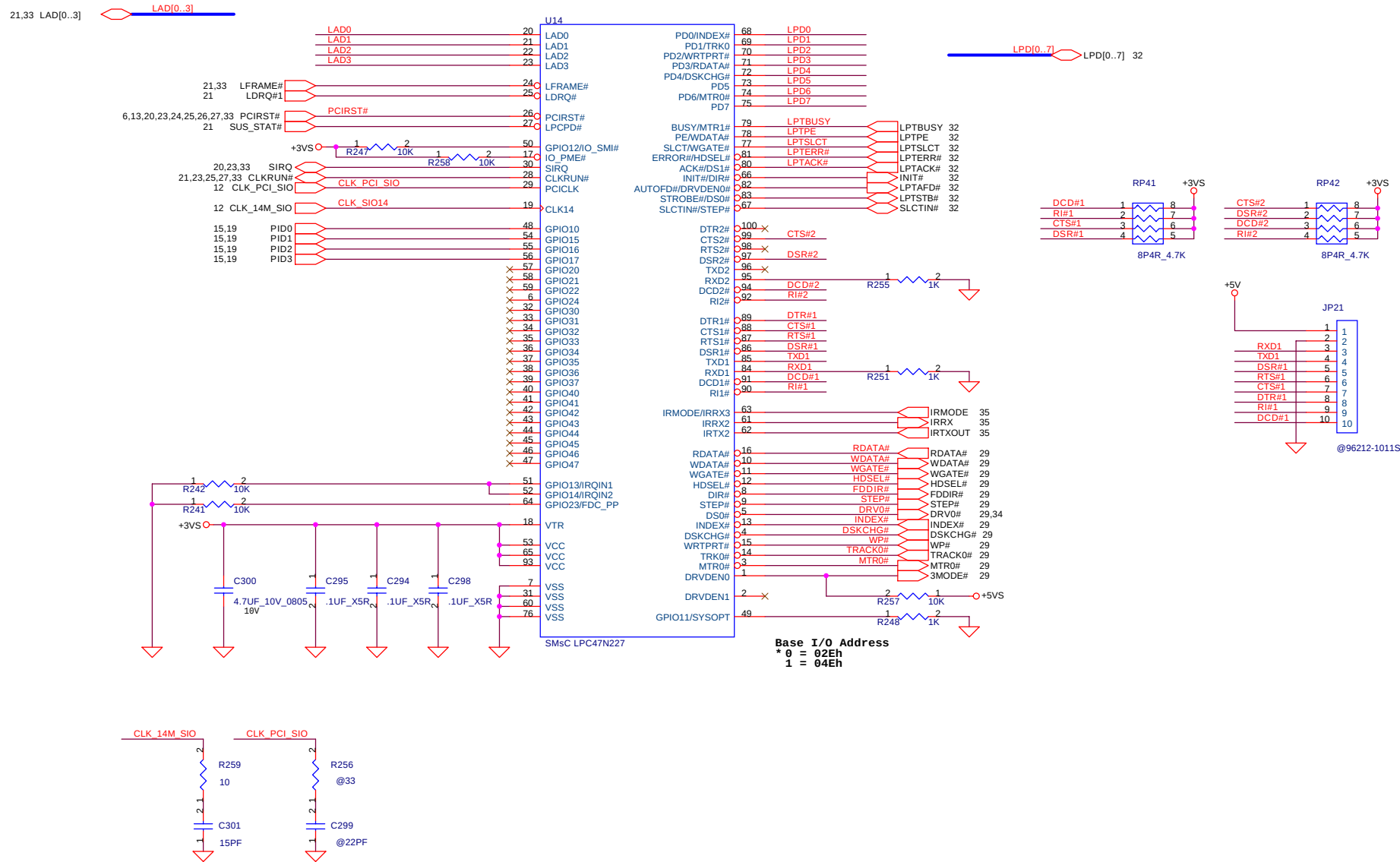
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| Size B                 | Document Number LA1432 | Rev 1.A |
| Date: 星期五, 六月 07, 2002 | Sheet 29 of 45         |         |



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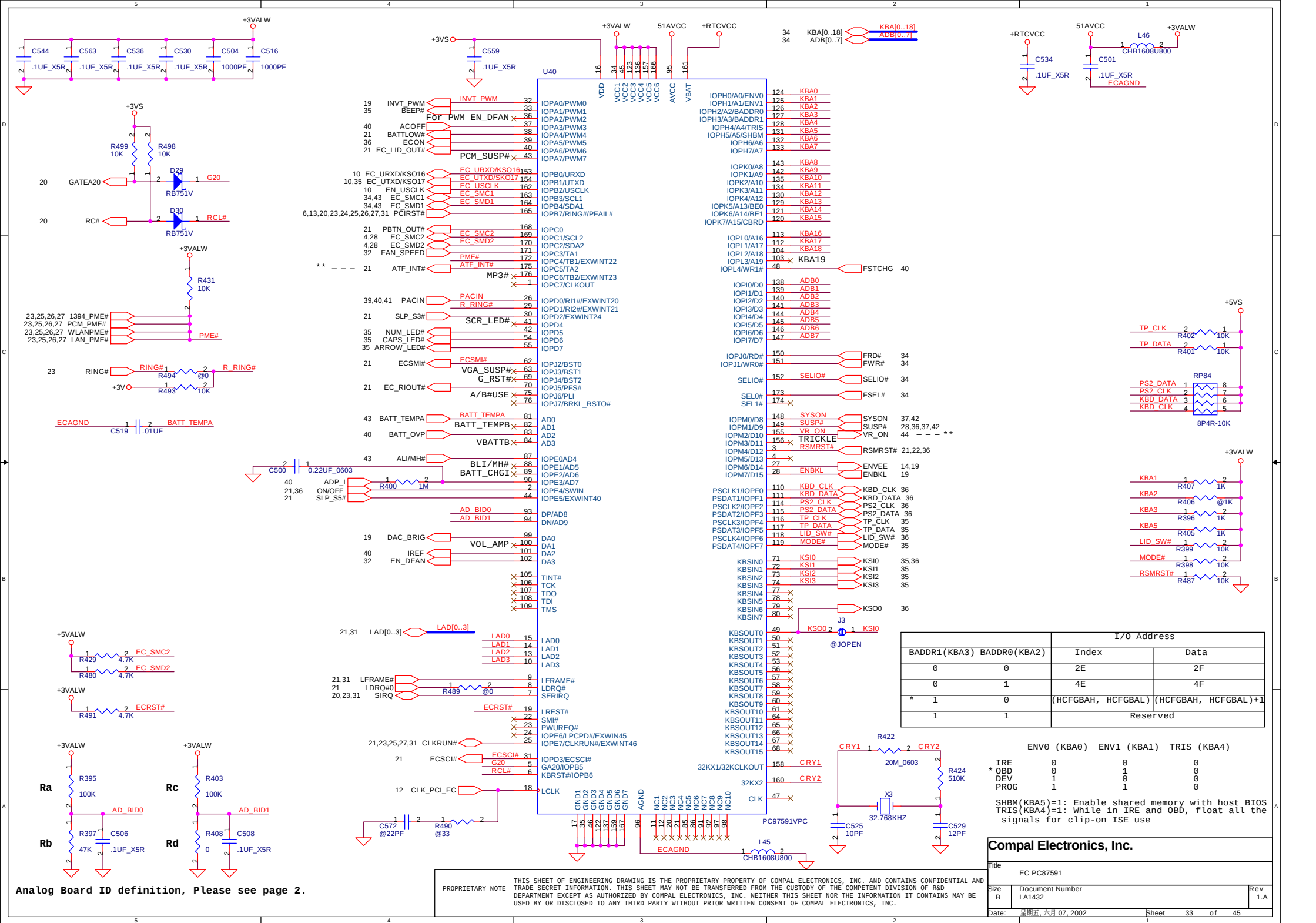
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| Title                   |                  |       |          |
| USB & FIR               |                  |       |          |
| Size                    | Document Number  |       | Rev      |
| B                       | LA1432           |       | 1.A      |
| Date:                   | 星期五, 六月 07, 2002 | Sheet | 30 of 45 |

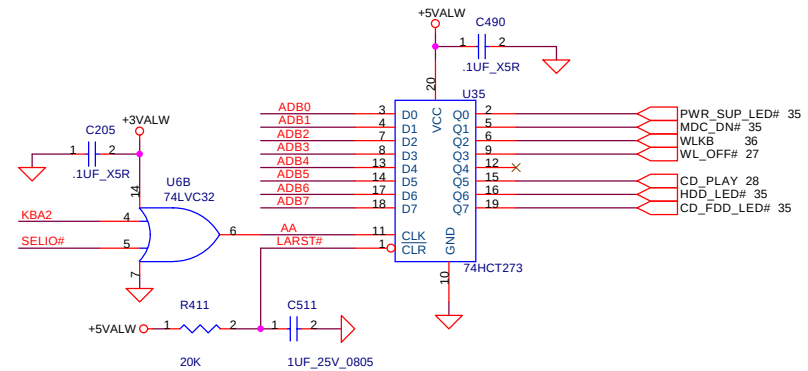
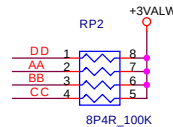
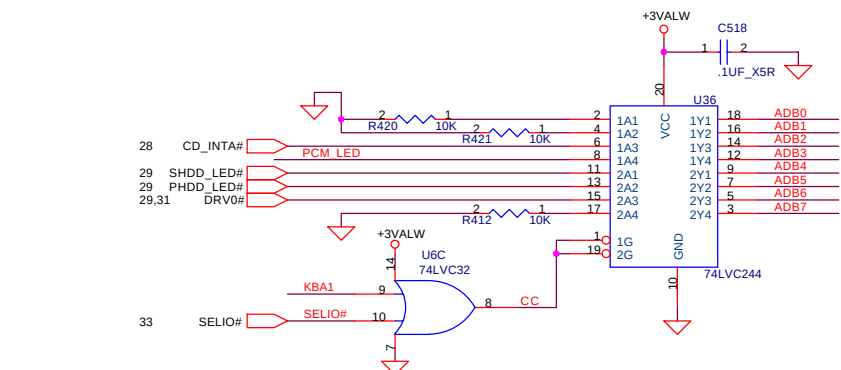
SUPER I/O SMsC FDC47N227



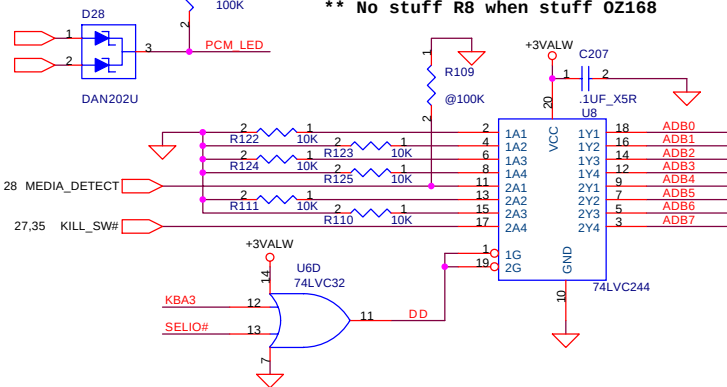




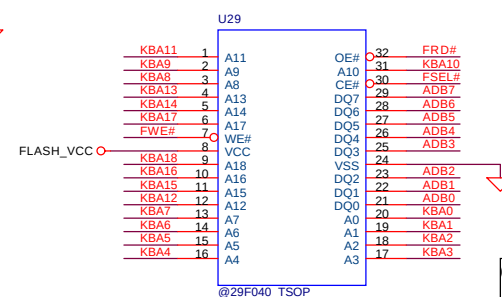
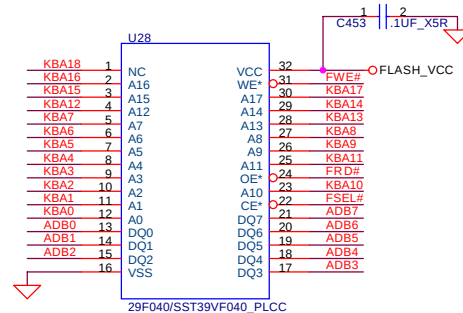
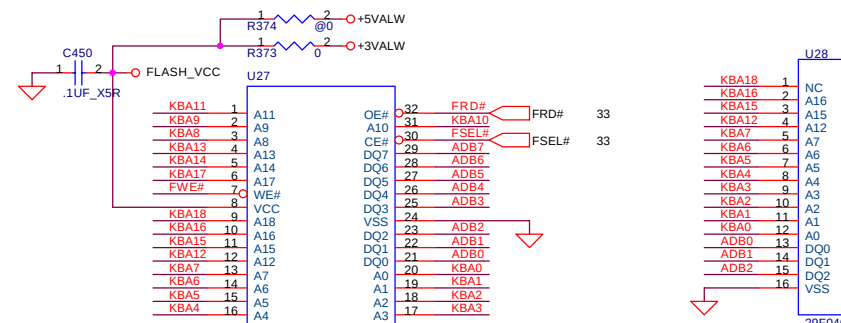
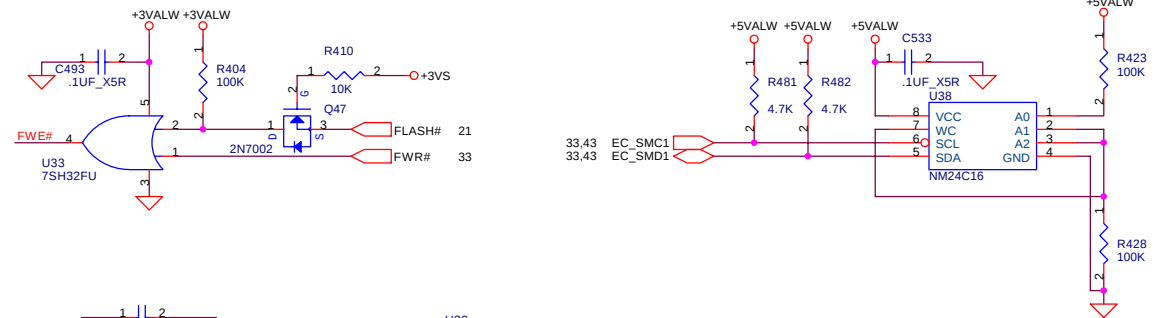
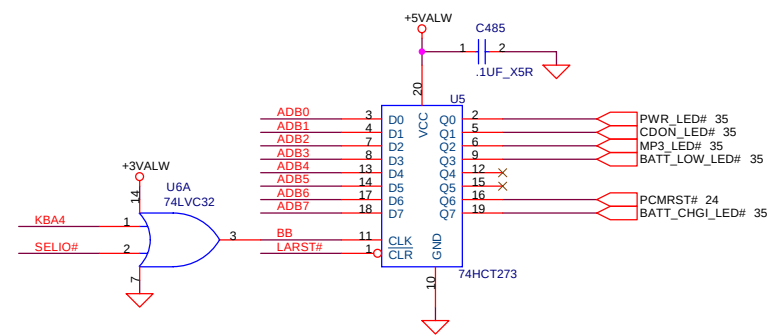




23 PCM1\_LED  
23 PCM2\_LED



\*\* No stuff R8 when stuff 02168



33 KBA[0..18]  
33 ADB[0..7]

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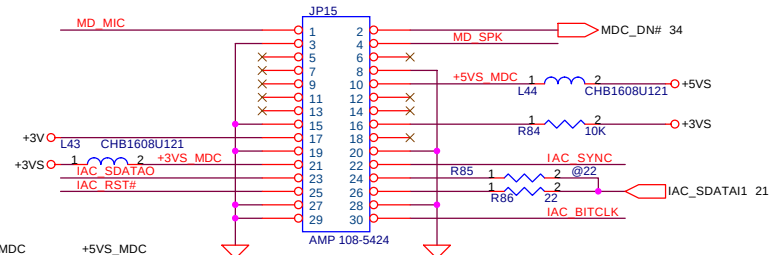
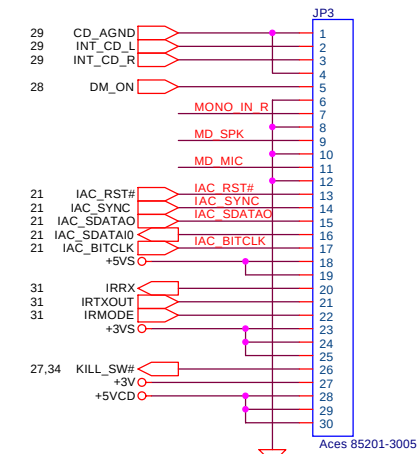
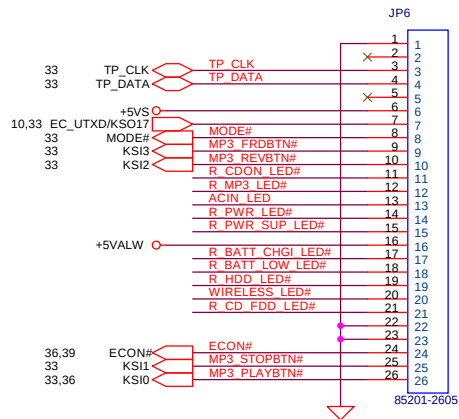
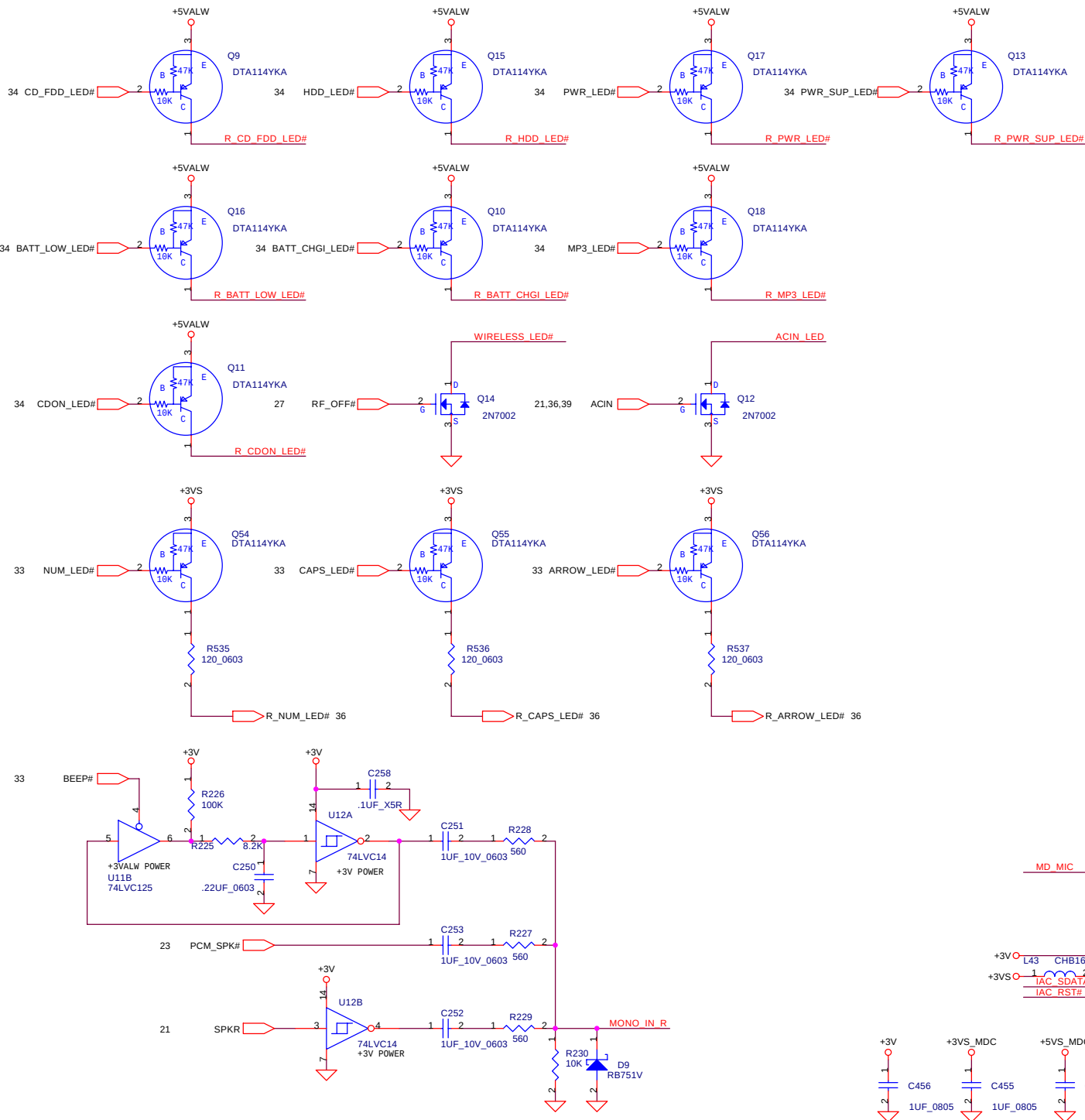
Title BIOS & EXT. I/O PORT

Size B Document Number LA1432

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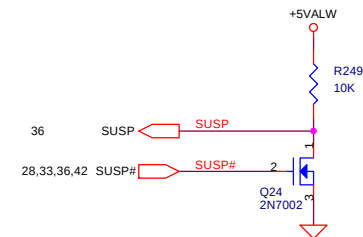
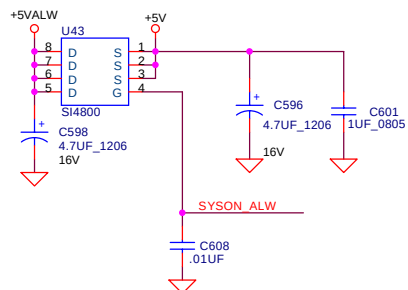
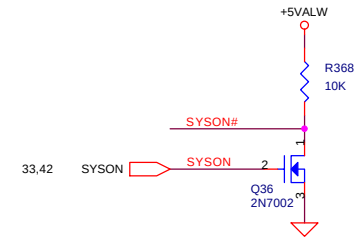
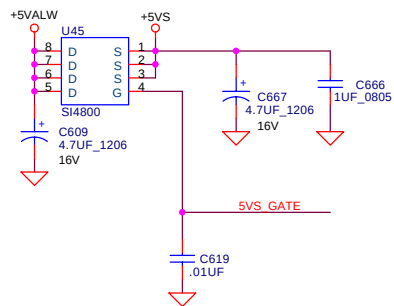
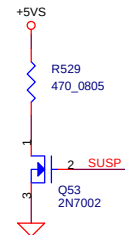
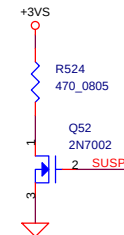
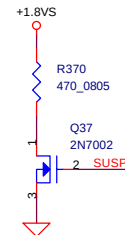
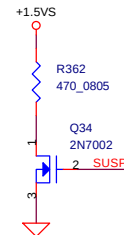
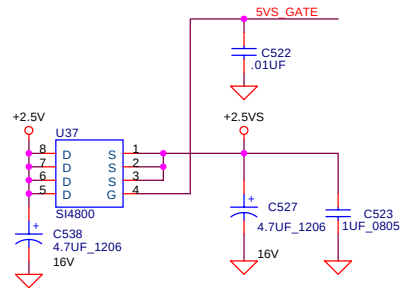
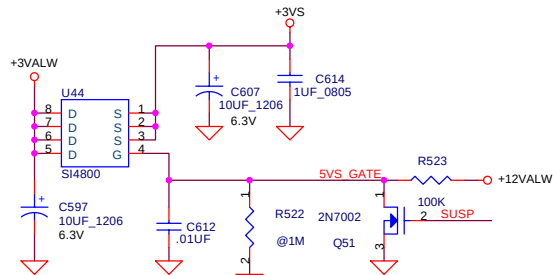
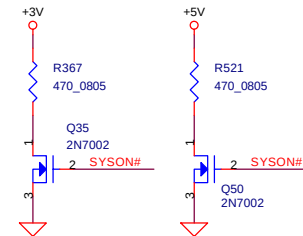
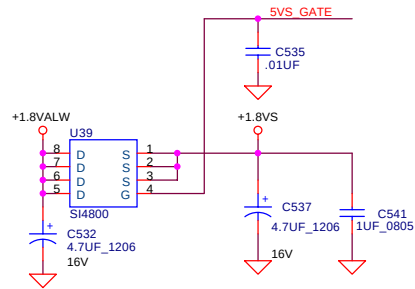
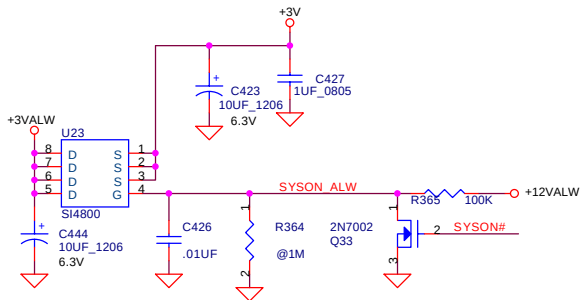


# Compal Electronics, Inc.

|       |                  |       |                       |
|-------|------------------|-------|-----------------------|
| Title |                  |       | Switches & Connectors |
| Size  | Document Number  | Rev   |                       |
| B     | LA1432           | 1.A   |                       |
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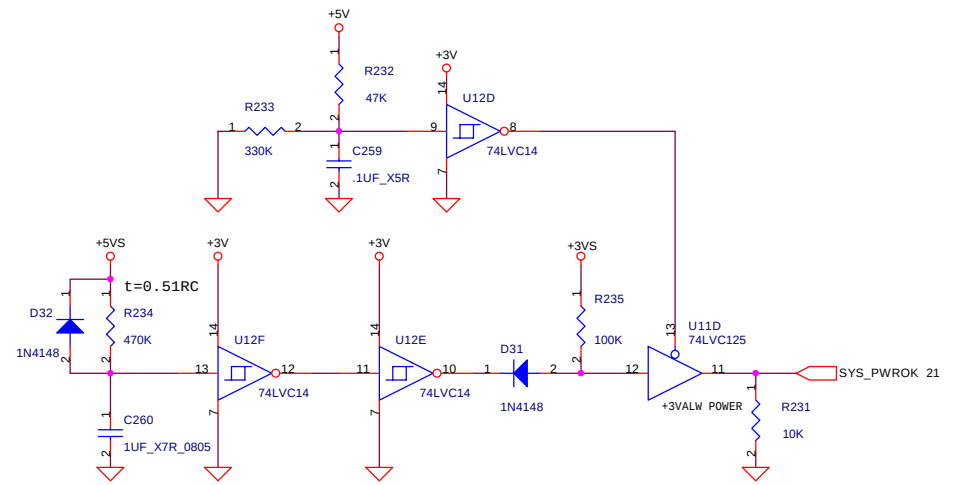
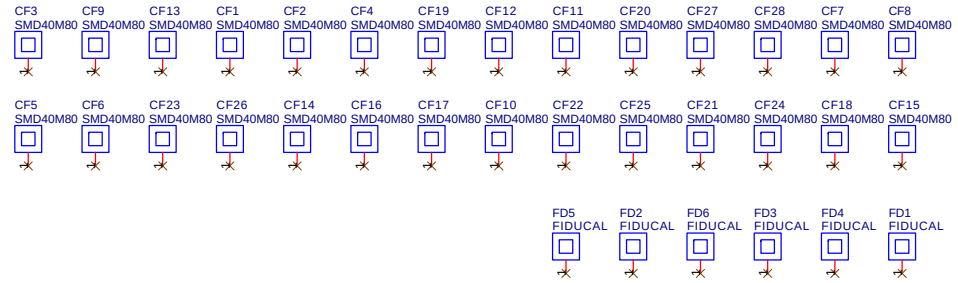
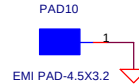
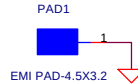
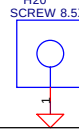
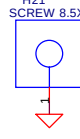
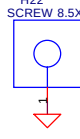
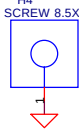
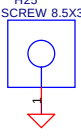
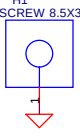
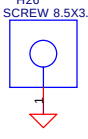
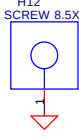
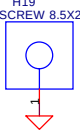
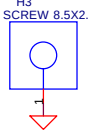
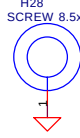
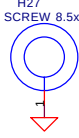
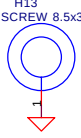
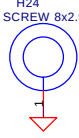
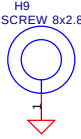
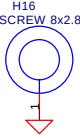
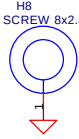
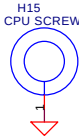
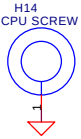
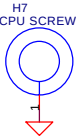
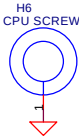
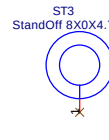
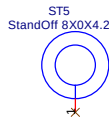
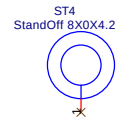
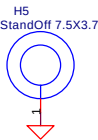
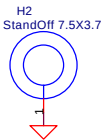
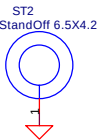
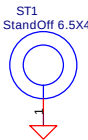
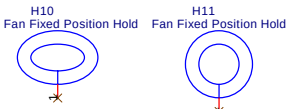
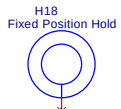
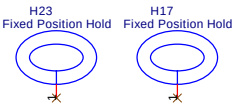
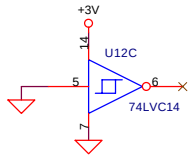




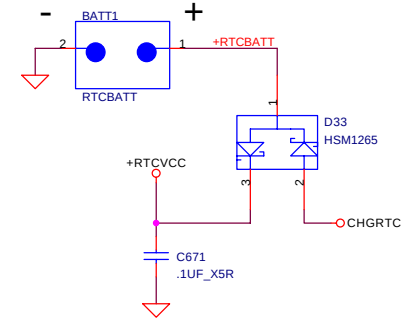
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|       |                  |       |                   |
|-------|------------------|-------|-------------------|
| Title |                  |       | POWER CONTROL CKT |
| Size  | Document Number  | Rev   |                   |
| B     | LA1432           | 1.A   |                   |
| Date: | 星期五, 六月 07, 2002 | Sheet | 37 of 45          |



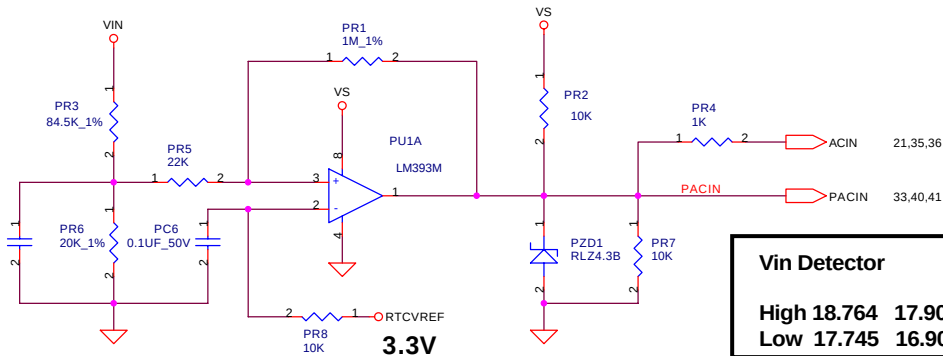
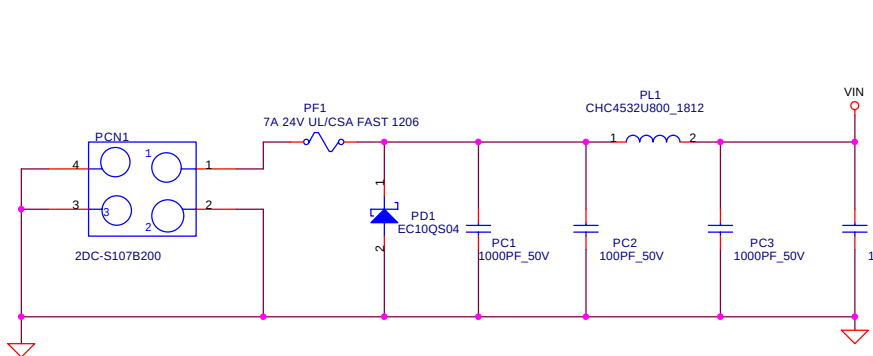
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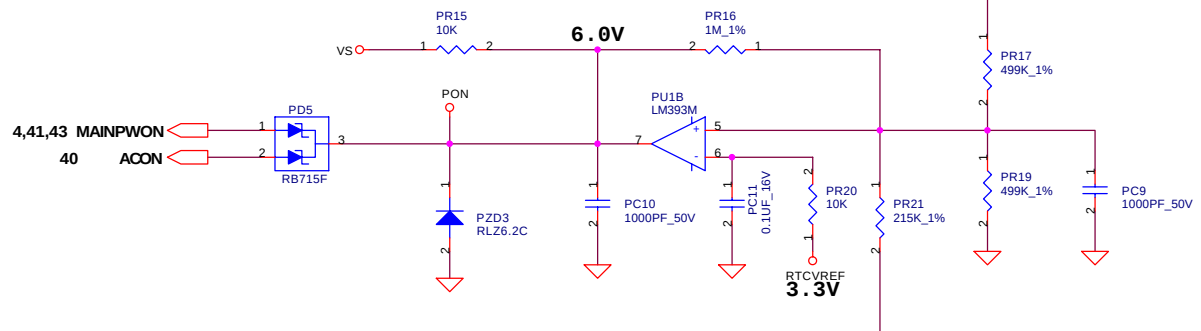
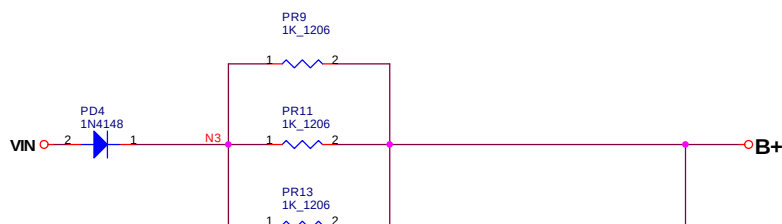
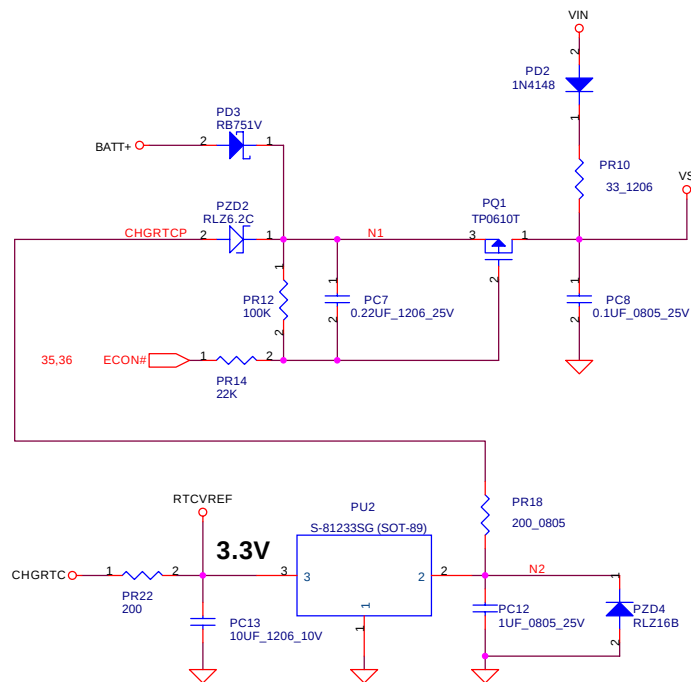
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| Title |                  | RESET |          |
| Size  | Document Number  | Rev   |          |
| B     | LA1432           | 1.A   |          |
| Date: | 星期日, 六月 07, 2002 | Sheet | 38 of 45 |

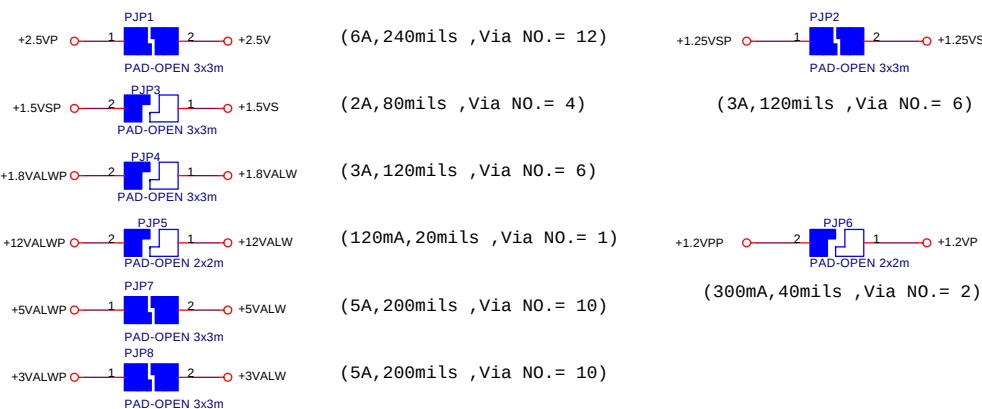
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| Vin Detector |        |               |
|--------------|--------|---------------|
| High         | 18.764 | 17.901 17.063 |
| Low          | 17.745 | 16.903 16.038 |

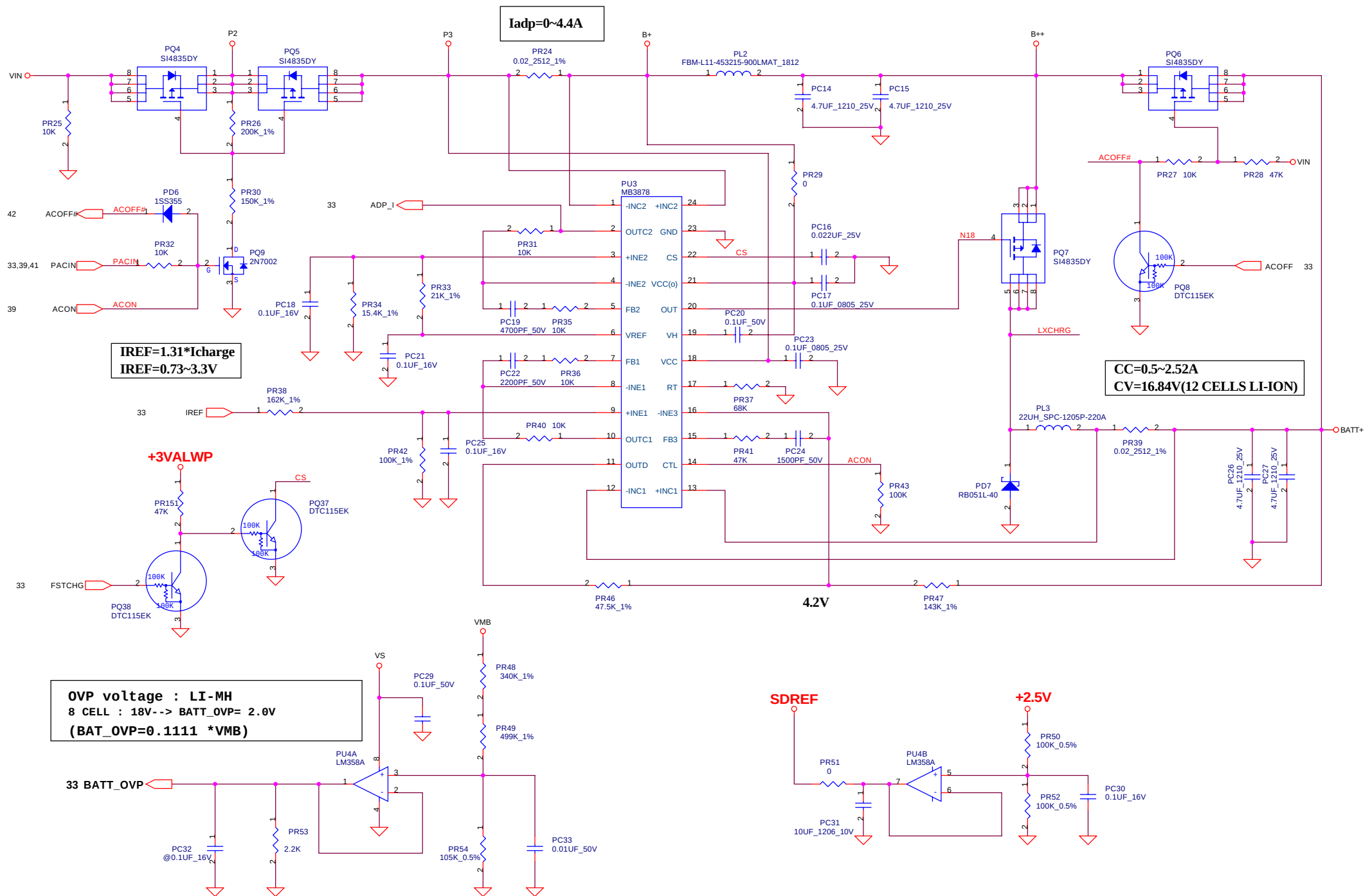


| Precharge detector |       |       |
|--------------------|-------|-------|
| 15.34              | 15.90 | 16.48 |
| 13.13              | 13.71 | 14.20 |



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|--------------------------|------------------|----------------|
| Title                    | DCIN & DETECTOR  |                |
| Size                     | Document Number  | Rev            |
| B                        | LA1432           | 1.A            |
| Date:                    | 星期五, 六月 07, 2002 | Sheet 39 of 45 |

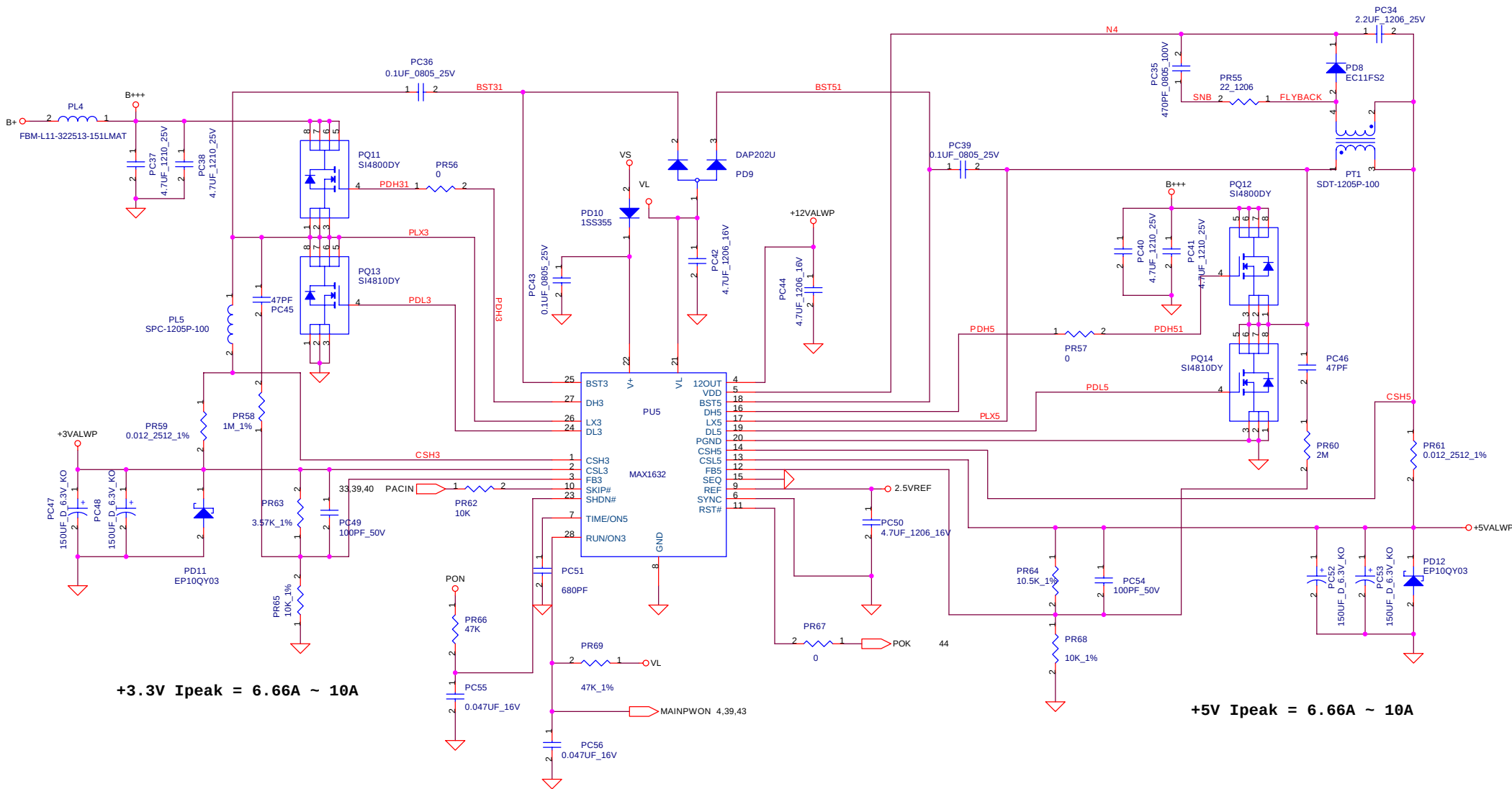


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|                 |                  |       |          |
|-----------------|------------------|-------|----------|
| Title           |                  |       | CHARGER  |
| Size            |                  |       | LA1432   |
| Document Number |                  |       | 1.A      |
| Date:           | 星期五, 六月 07, 2002 | Sheet | 40 of 45 |

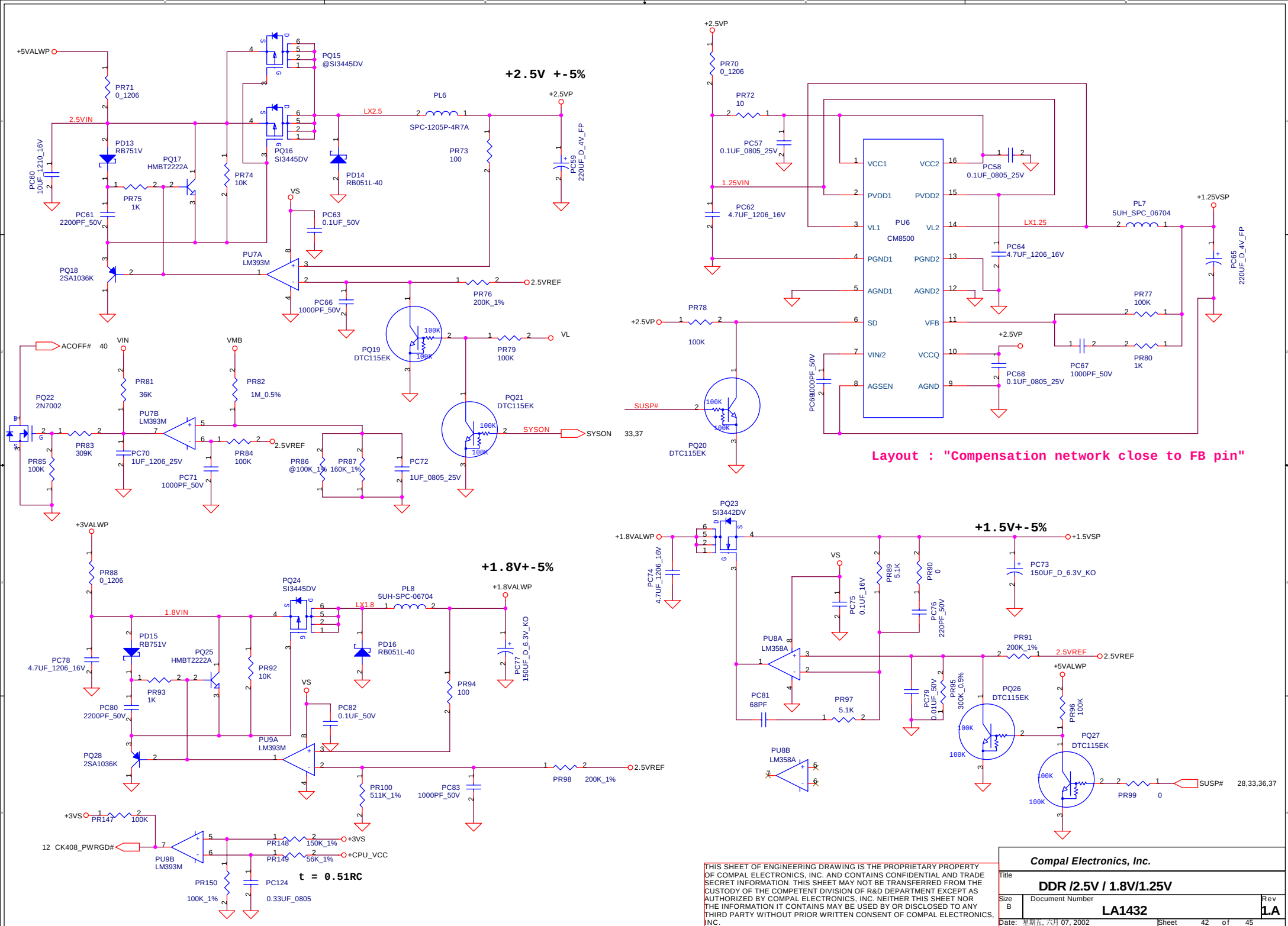




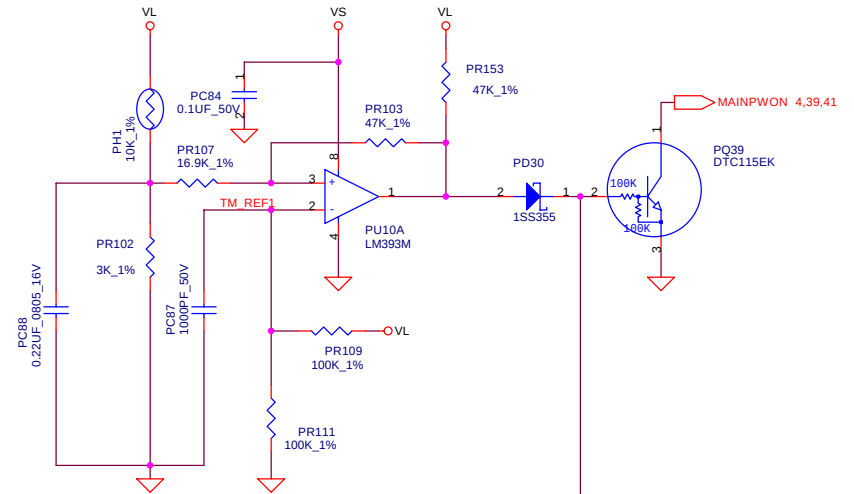
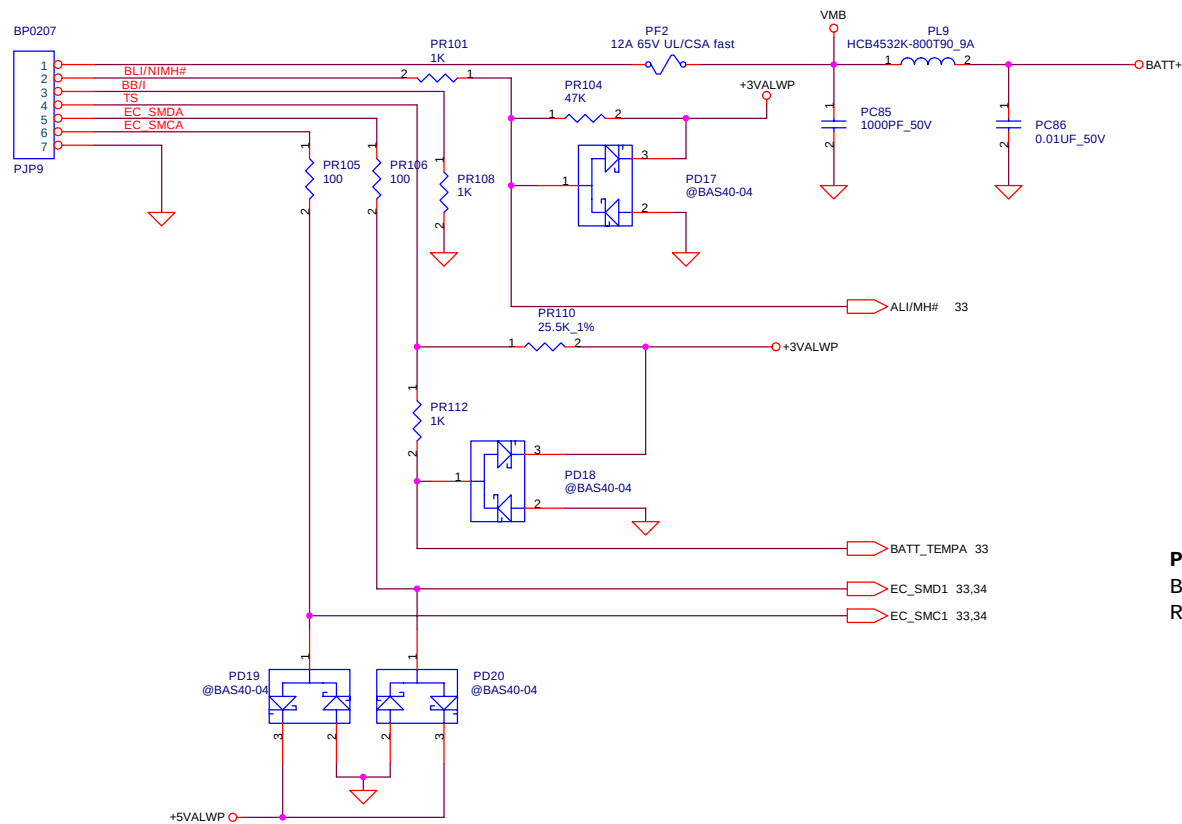
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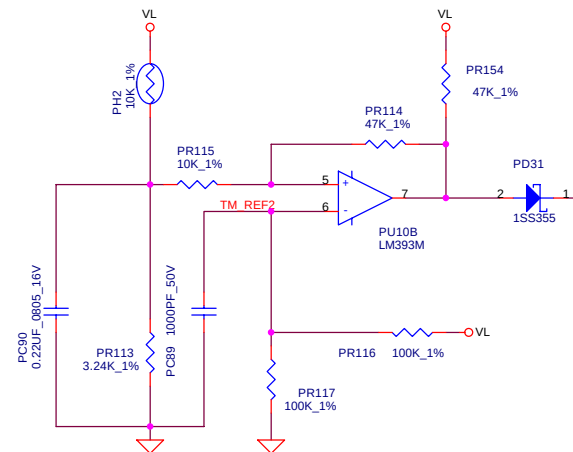
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| Title                  |                 | 5V/3.3V/12V |          |
| Size                   | Document Number | LA1432      |          |
| B                      |                 | 1.A         |          |
| Date: 星期五, 六月 07, 2002 |                 | Sheet       | 41 of 45 |



**PH1 under CPU botten side :**  
 CPU thermal protection at 87 degree C  
 Recovery at 48 degree C



**PH2 near main Battery CONN :**  
 BAT. thermal protection at 73 degree C  
 Recovery at 50(51) degree C



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|       |                  |                    |          |
|-------|------------------|--------------------|----------|
| Title |                  | BATTERY CONN / OTP |          |
| Size  | B                | Document Number    | LA1432   |
| Date: | 星期五, 六月 07, 2002 | Sheet              | 43 of 45 |
| Rev   | 1.A              |                    |          |



# ATR60 PIR LIST

\*\*\*\*\* Rev0.2 PIR List \*\*\*\*\*

04/15/02 Written by Jerry

P26:Delete C372 and Remove +3V at pin6 of U18  
P33: Remove @ at R492 and add @ at R488  
Change value of R397 to 47K

04/26/02 Written by Sam

P14: Remove @ at C134  
P17: Delete RP70, RP77, RP88, RP83, RP81, RP86, RP79, RP69, RP90, RP92  
RP67, RP76, RP87, RP82, RP80, RP85, RP78, RP68, RP89, RP91  
R386, R383  
Remove @ at R413, R414  
P18: Delete RP65, RP64, RP71, RP72, RP62, RP63, RP73, RP66, RP61, RP74  
RP52, RP49, RP57, RP58, RP48, RP47, RP59, RP56, RP50, RP60  
R325, R369  
Remove @ at R345, R382  
P33: Delete R425, R426, R427, R430, R480, R492  
Add U40 Signal Pins for Keyboard Led NUM\_LED# , CAPS\_LED#, ARROW\_LED#  
P35: Add R535, R536 ,R537 ,Q54, Q55, Q56  
P36: Add Q57  
Change D25 to Be PSOT24C, Pin 1 : Floating,Pin 2 : Connect to JP2 Pin 8,  
Pin 3 : Ground  
Change R376 Pin 2 Connect with RTCVREF,Change R390 Pin 2 Connect with RTCVREF,

04/26/02 Written by Jerry

P17,P18: Modify net name R\_NDQM\* TO NDQM\*

05/01/02 Written by Sam

P29: Add @ at R538  
P34: Add U35 Pin 6 Signl for WLKB  
P36: Add R539,R540,D35,Q58  
Del D10  
Change R250 Pin 1 from +12VALW to +5VALW\_RX

05/06/02 Written by Sam  
P35: Q12, Q14 Change Material to 2N7002

05/07/02 Written by Sam  
P15: L37 Change Material to 0\_0603

05/17/02 Written by Sam  
P36: Change J3 footprinter to JUMP\_2MM  
P36: Add JP24 and Change J1,J4 footprinter to JUMP\_2MM

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| Title                    |                  | ATR60 PIR LIST |          |
| Size B                   | Document Number  | Rev            |          |
|                          | LA1432           | 1.A            |          |
| Date:                    | 星期五, 六月 07, 2002 | Sheet          | 45 of 45 |