

Compal confidential

ISRAA LA-3441P Schematics Document

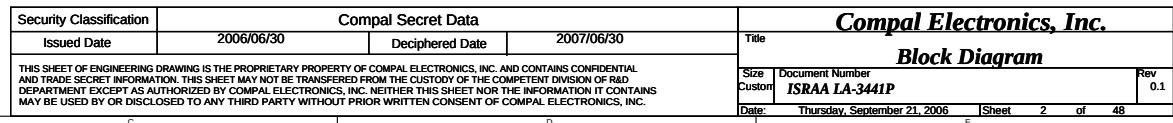
Mobile Merom uFCPGA with Intel
Crestline_PM+ICH8-M core logic

2006-09-21

REV:0.1

Security Classification		Compal Secret Data		Title	
Issued Date	2006/06/30	Deciphered Date	2007/06/30	Cover Sheet	
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Santa Rosa Platform



Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (18.5V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05Vs	1.05V power rail for Processor I/O and MCH/ICH core power	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDRII Vtt	ON	OFF	OFF
+1.5VS	1.5V switched power rail for PCI-E interface	ON	OFF	OFF
+1.8V	1.8V power rail for DDRII	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail for MCH video PLL	ON	OFF	OFF
+1.25Vs	1.25Vs power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+RTC_VCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

DEVICE	PCI Device ID	IDSEL #	REQ/GNT #	PIRQ
1394	D0	AD20	2	A,B,C
CARD BUS	D4	AD20	2	A,B,C
SIN1	D4	AD20	2	A,B,C

KB910 I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
SM1 24C16	A0H	1 0 1 0 0 0 0 X b
SM1 SMART BATTERY	16H	0 0 0 1 0 1 1 X b
SM2 ADM0132	98H	1 0 0 1 1 0 0 X b
CPU THERMAL MONITOR		

ICH8-M SM Bus address

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 1%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0.100 V
1	8.2K +/- 1%	0.216 V	0.250 V	0.289 V
2	18K +/- 1%	0.436 V	0.503 V	0.538 V
3	33K +/- 1%	0.712 V	0.819 V	0.875 V
4	56K +/- 1%	1.036 V	1.185 V	1.264 V
5	100K +/- 1%	1.453 V	1.650 V	1.759 V
6	200K +/- 1%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	
1	
2	
3	
4	
5	
6	
7	

SKU ID Table

SKU ID	SKU
0	10
1	10G
2	10H
3	
4	
5	
6	
7	

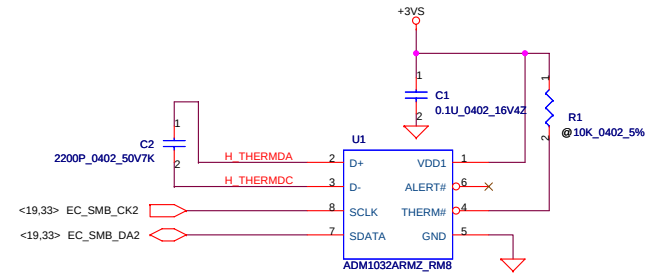
BTO Option Table

BTO Item	BOM Structure
2ND HDD	2HDD@
LAN	100M@ 1000M@
WLAN	WLAN@
NB	GM@ PM@
BT	BT@
MIC	MIC@
CIR	CIR@
FINGER PRINT	FP@
Express Card	NEWCARD@
PCMCIA Card	PCMCIA@
Camera	Camera@
Robson	Robson@
HDMI	1392@ 1932@ M72M@
SPEAKER	

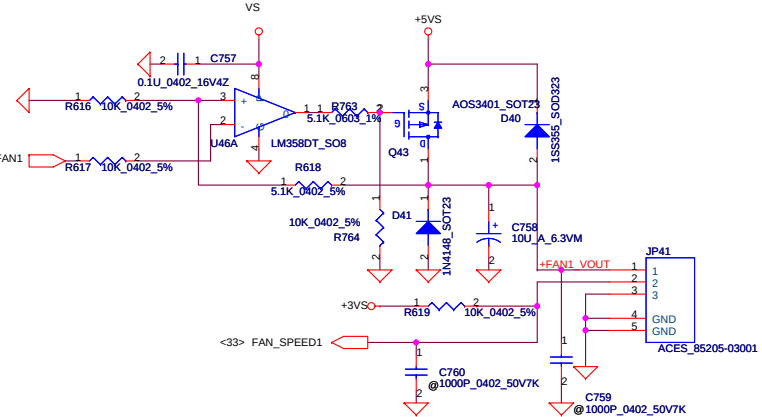
USB PORT LIST

PORT	DEVICE
0	RIGHT USB Port (Saml Board)
1	RIGHT USB Port (Saml Board)
2	RIGHT USB Port (Saml Board)
3	RIGHT USB Port (Saml Board)
4	LEFT USB Port
5	LEFT USB Port
6	Fingerprint
7	Blue Tooth
8	Internal Camera
9	Express Card

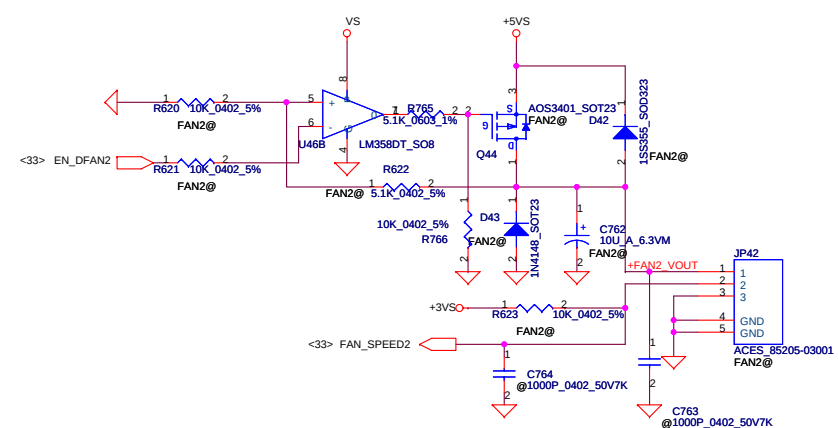
Thermal Sensor ADM1032ARM



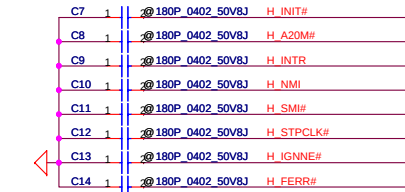
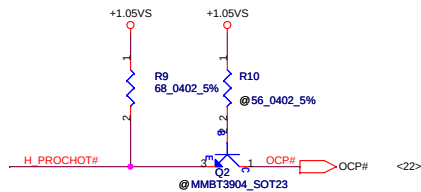
FAN 1



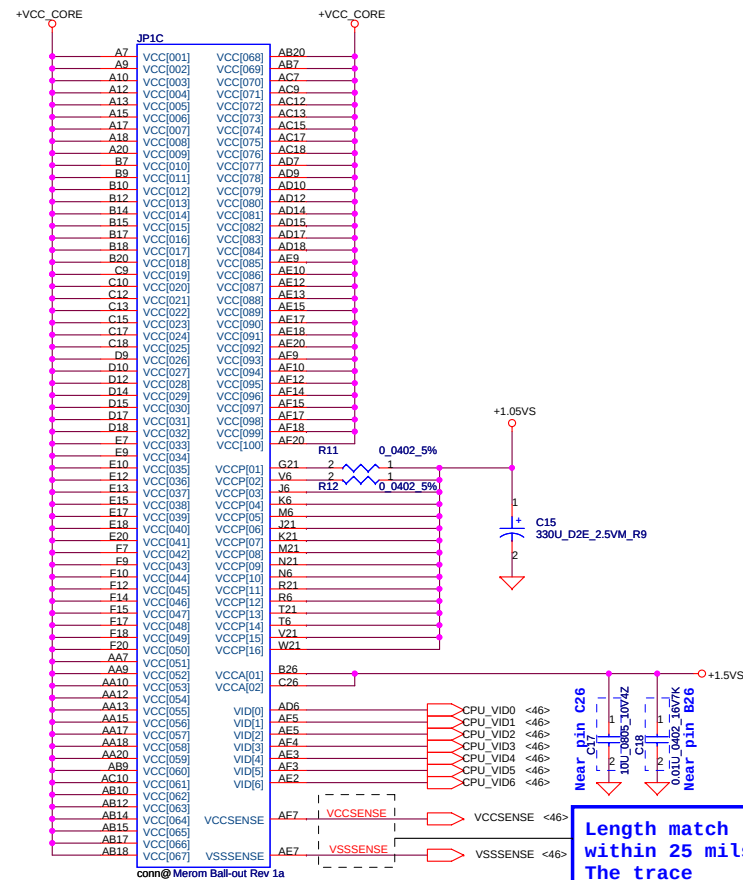
FAN 2



Place Caps Close to CPU Socket



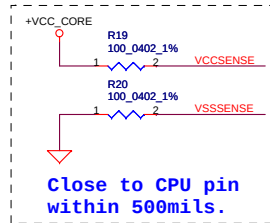
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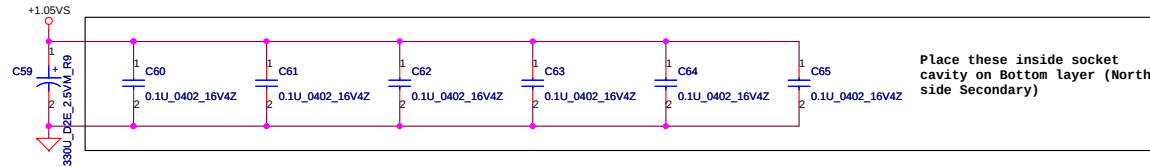
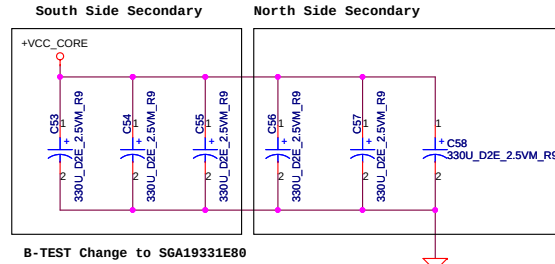
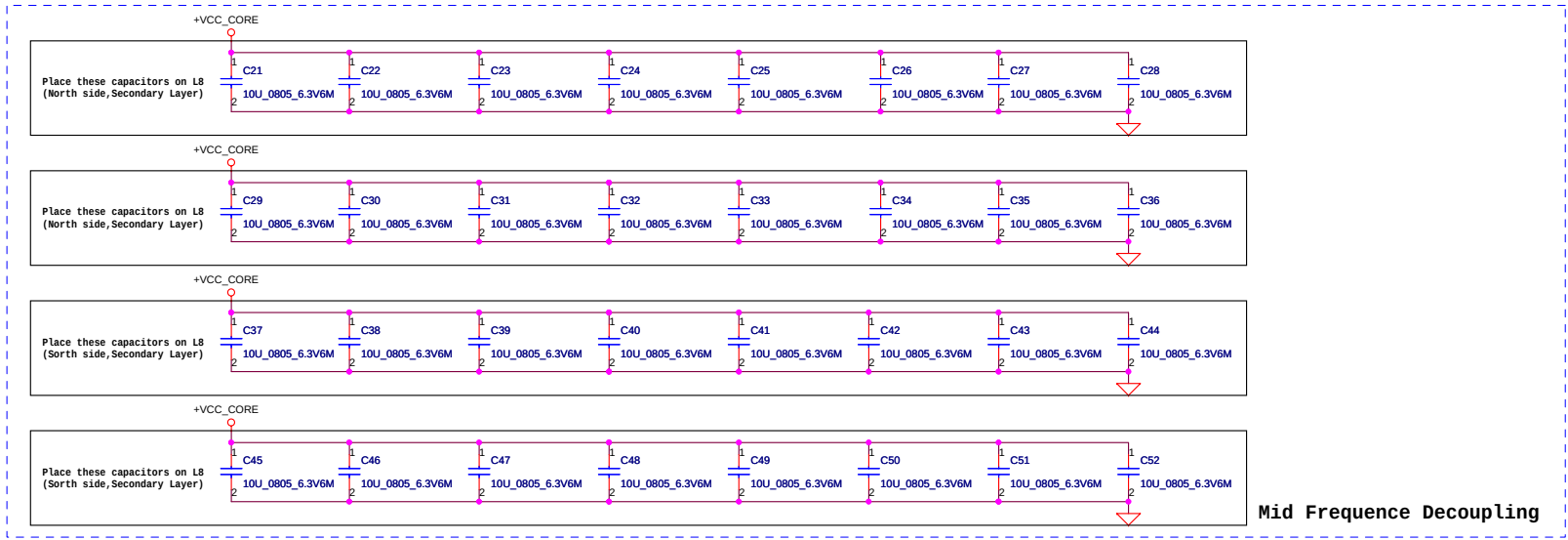
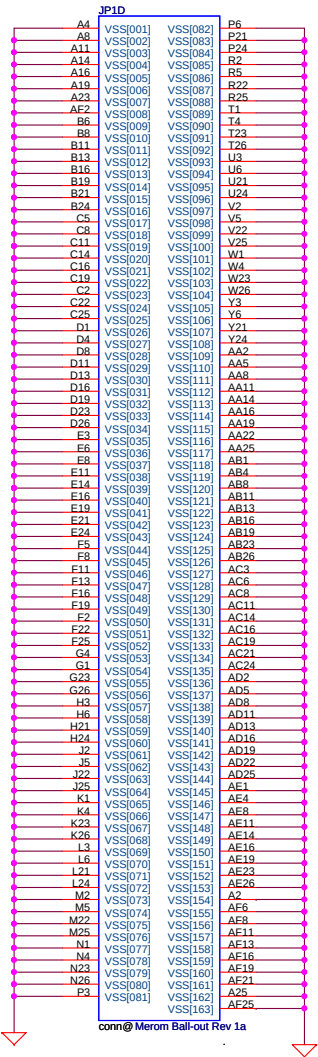
conn@ Merom Ball-out Rev 1a

Resistor placed within 0.5" of CPU pin. Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.

Length match
within 25 mils.
The trace
width/space/other
is 20/7/25.



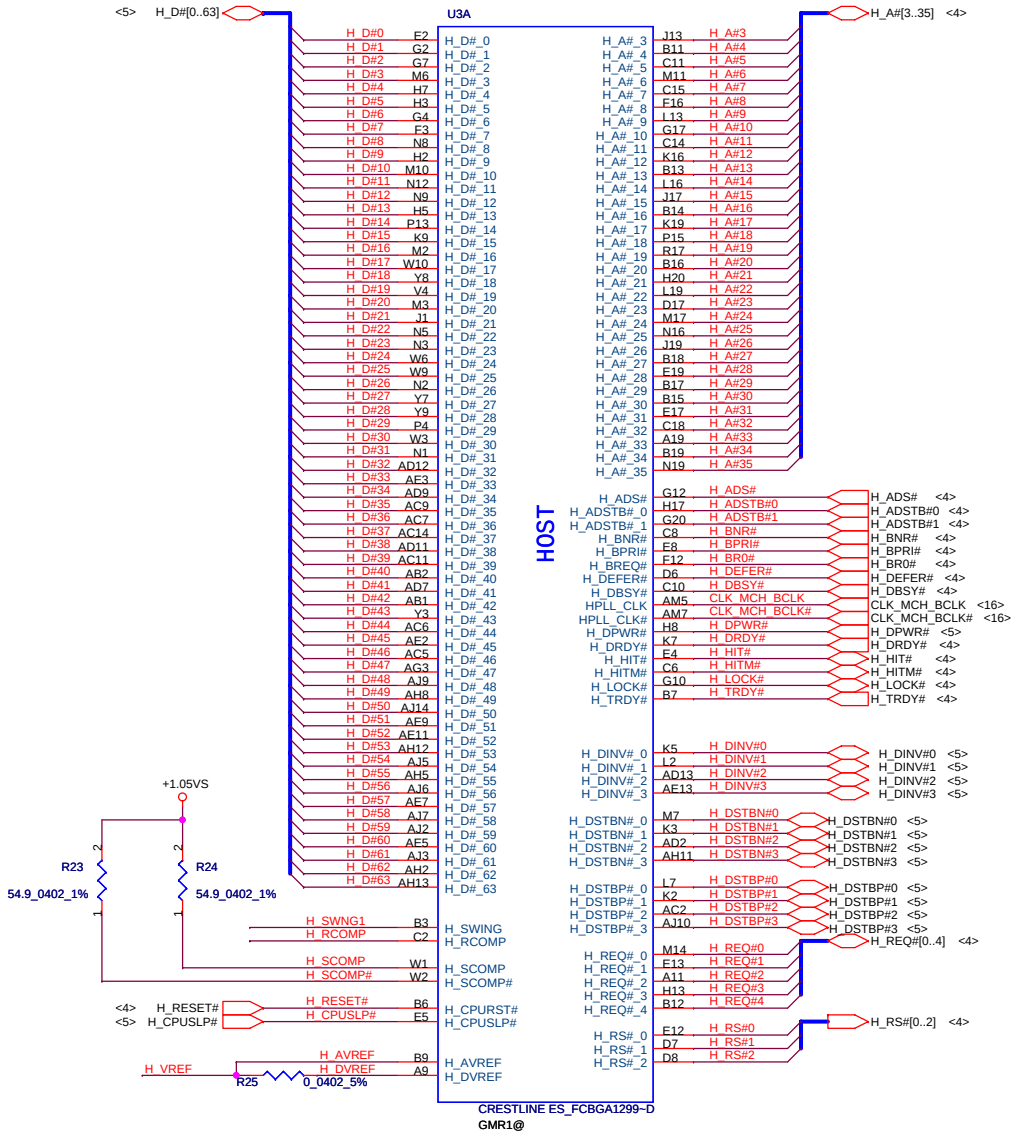
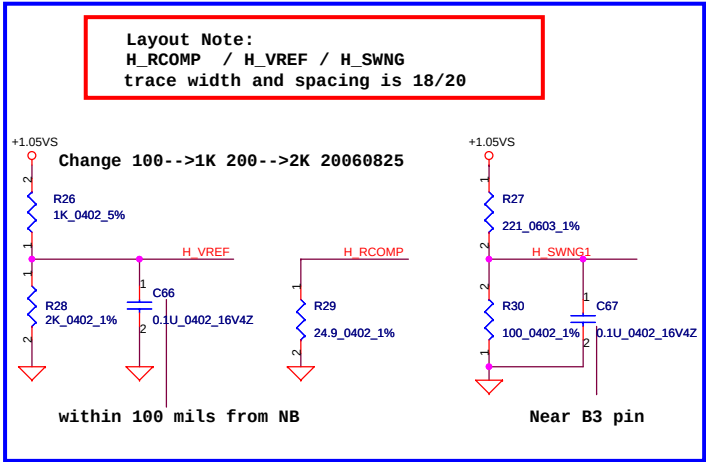
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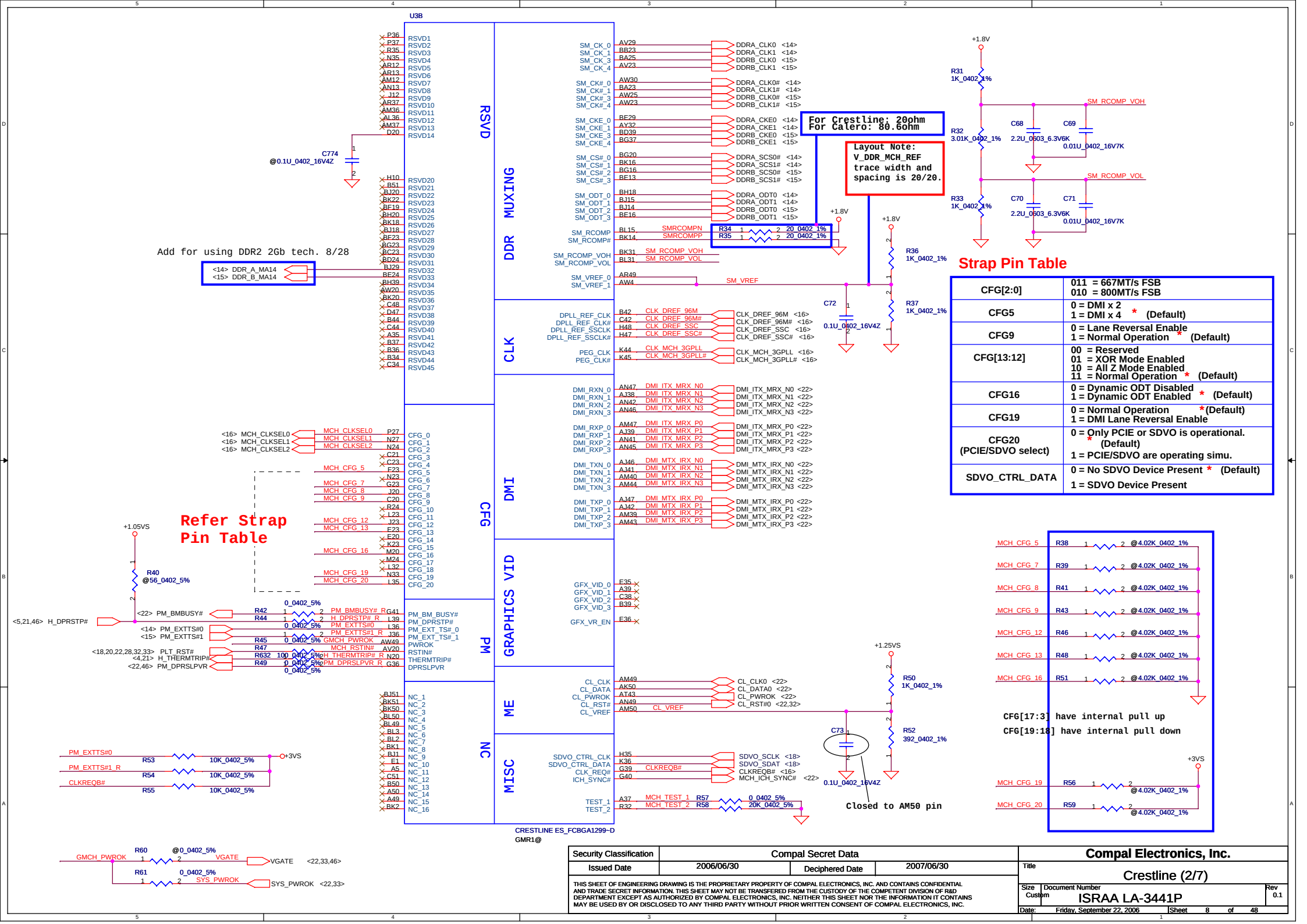
ESR <= 1.5m ohm
Capacitor > 1980uF

330uF ESR 7m ohm X 6 PCS

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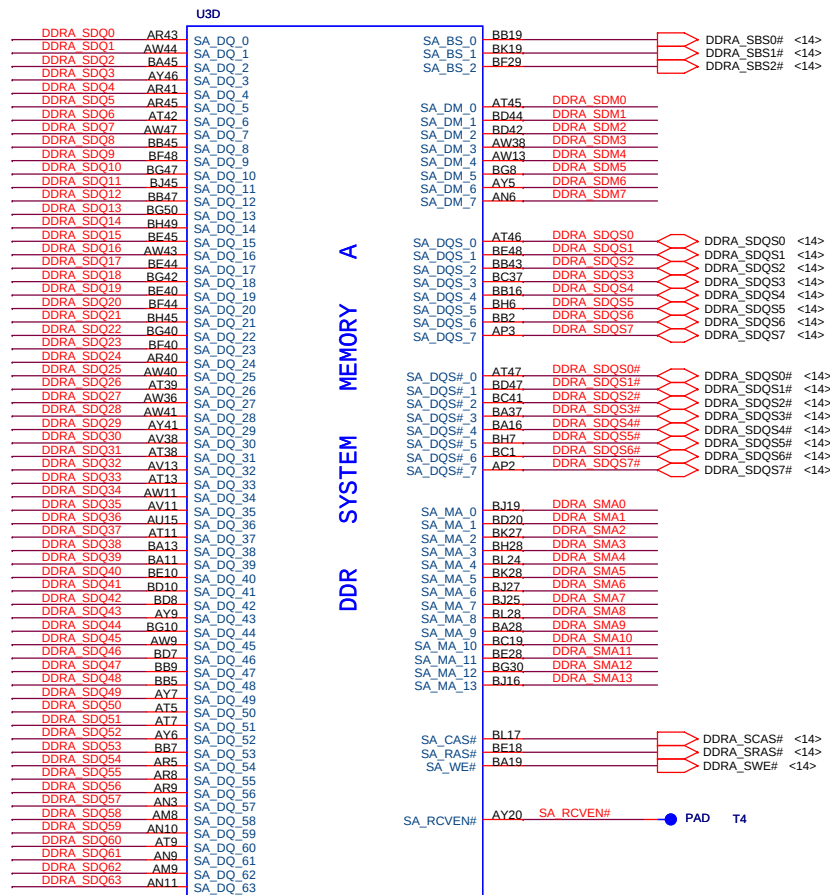


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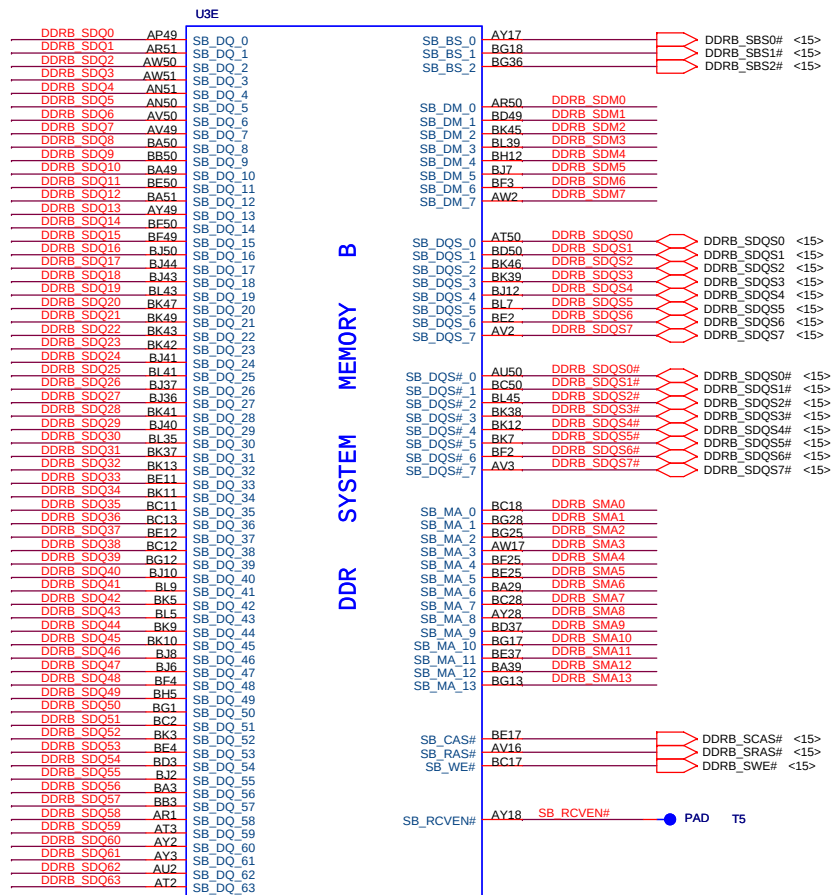


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<14> DDRA_SDM[0..7] DDRA_SDM[0..7]
<14> DDRA_SMA[0..13] DDRA_SMA[0..13]

<15> DDRB_SDQ[0..63] DDRB_SDQ[0..63]
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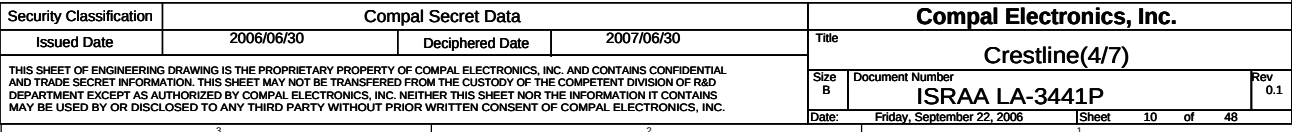


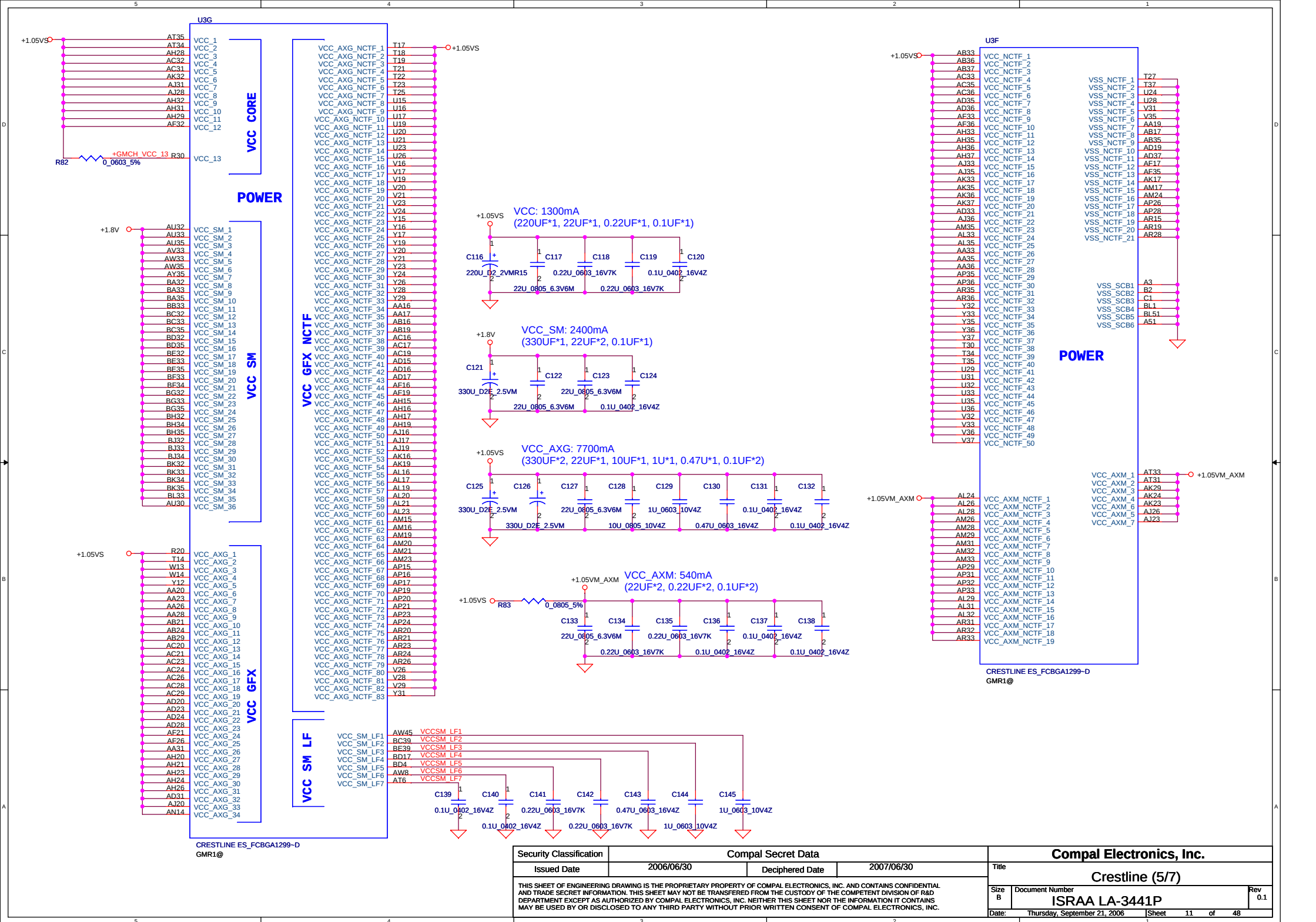
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GMR1@

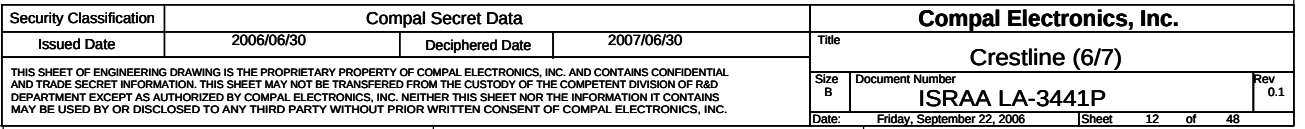


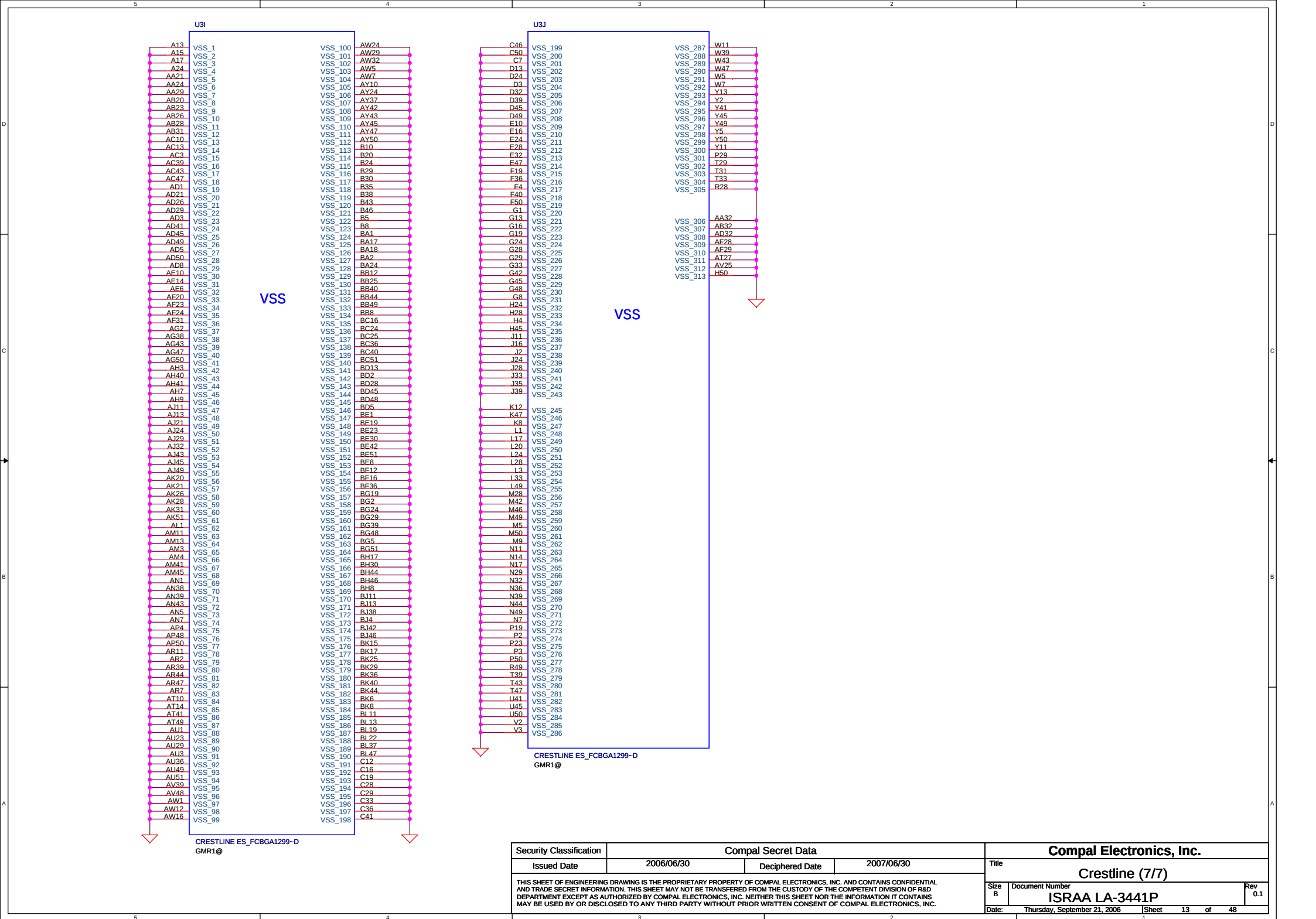
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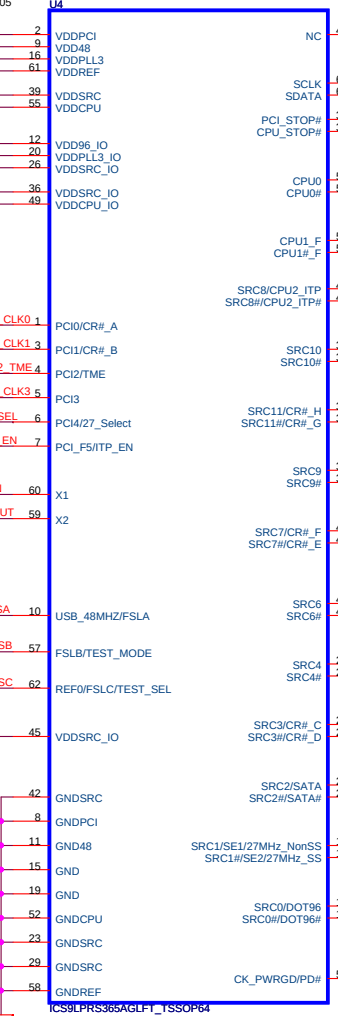
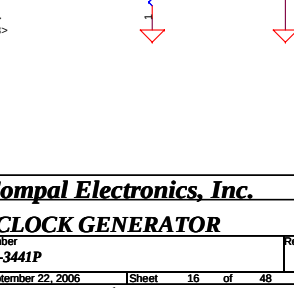
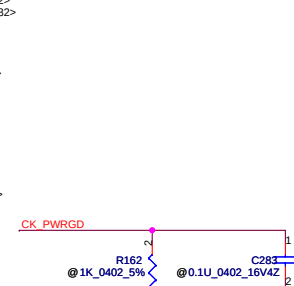
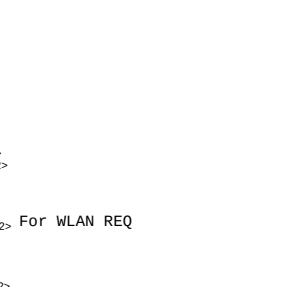
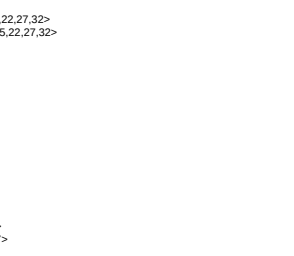
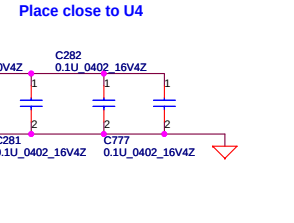
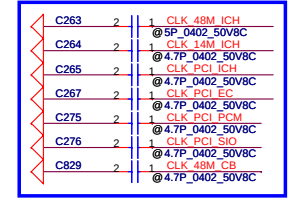
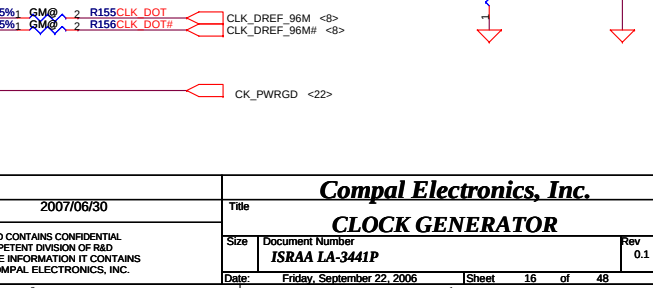
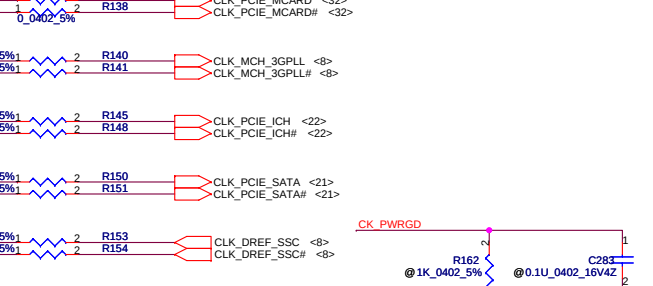
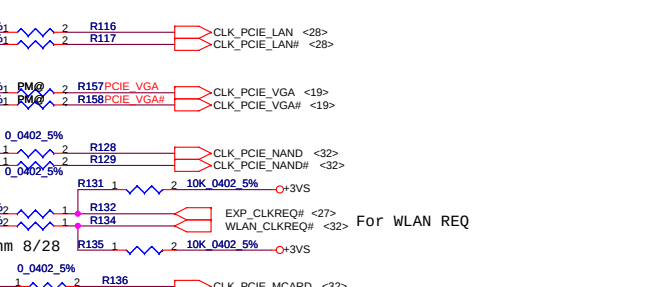
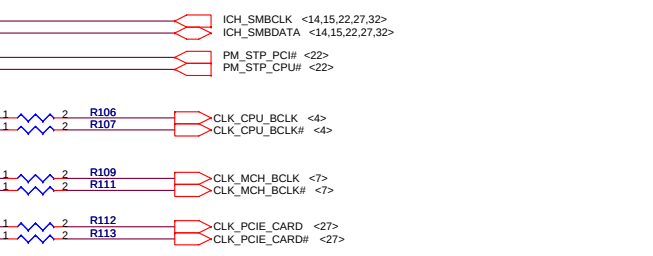
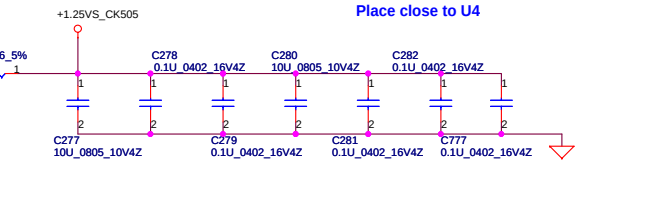
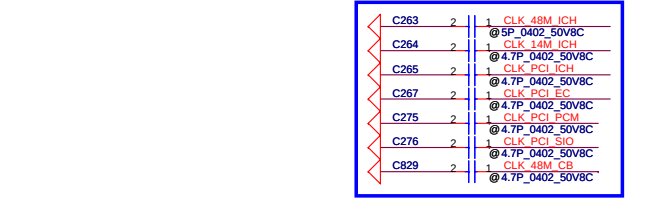
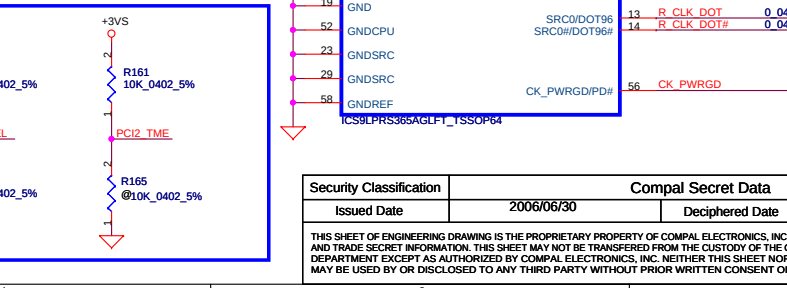
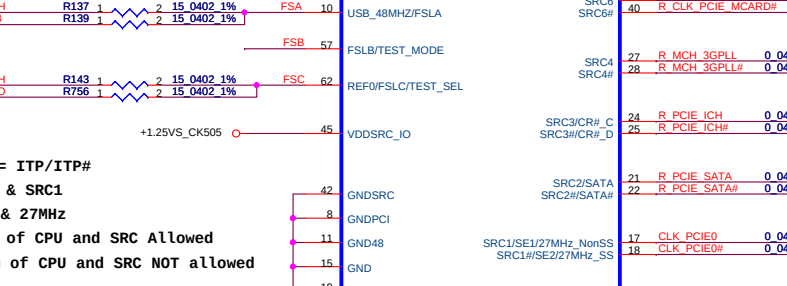
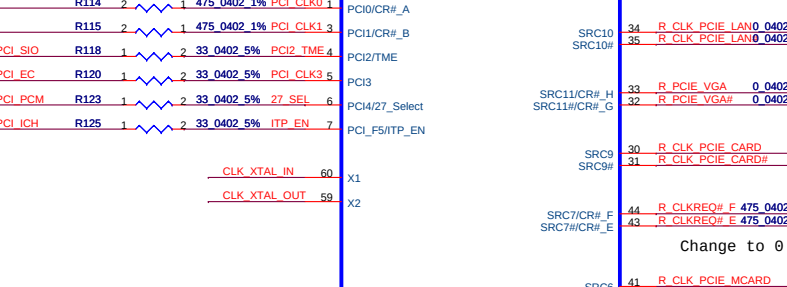
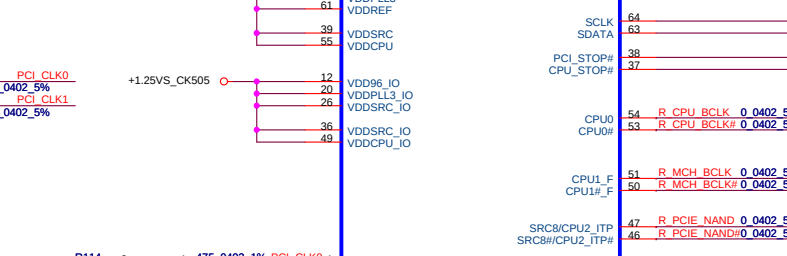
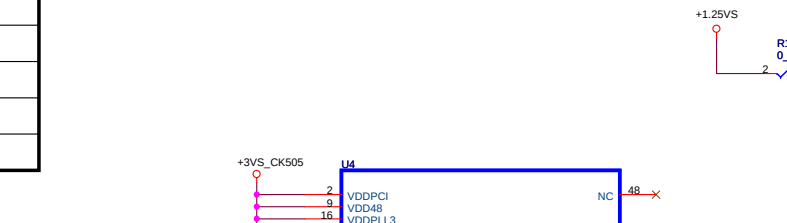
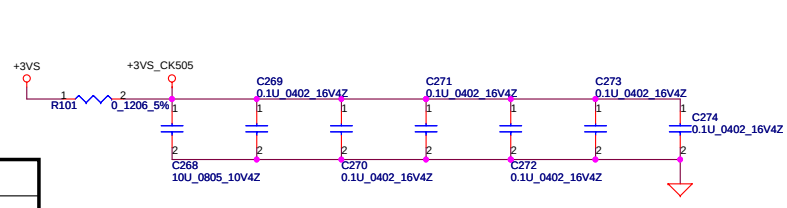
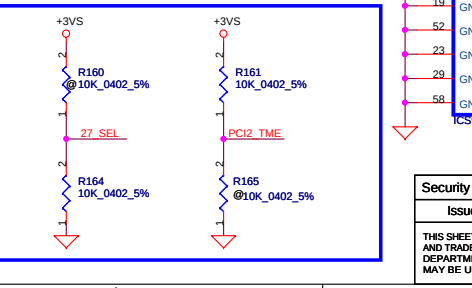
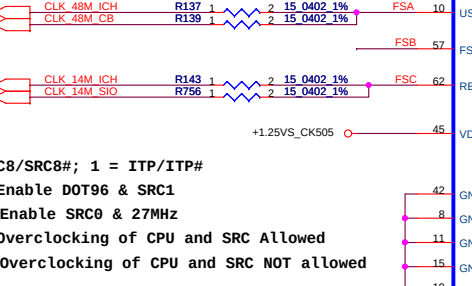
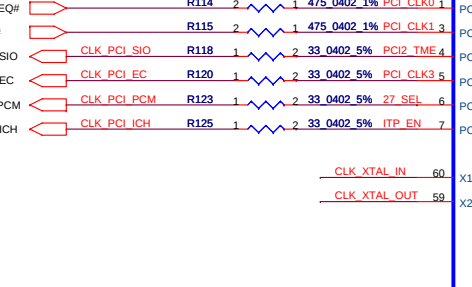
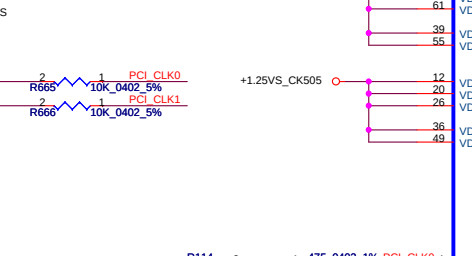
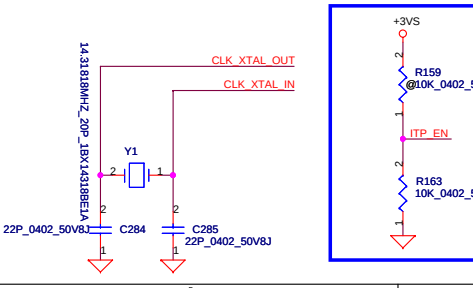
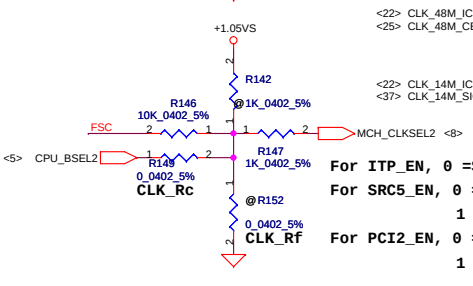
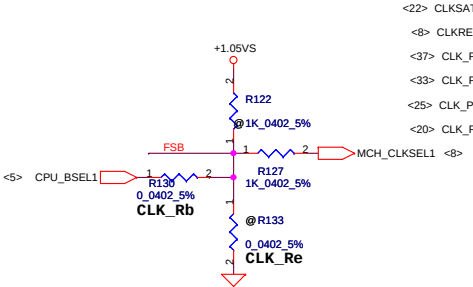
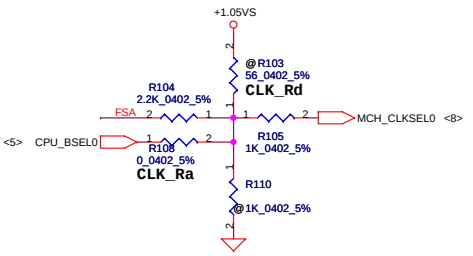


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FSLC	FSLB	FSLA	CPU	SRC	PCI
CLKSEL2	CLKSEL1	CLKSEL0	MHZ	MHZ	MHZ
0	1	0	200	100	33.3
0	1	1	166	100	33.3

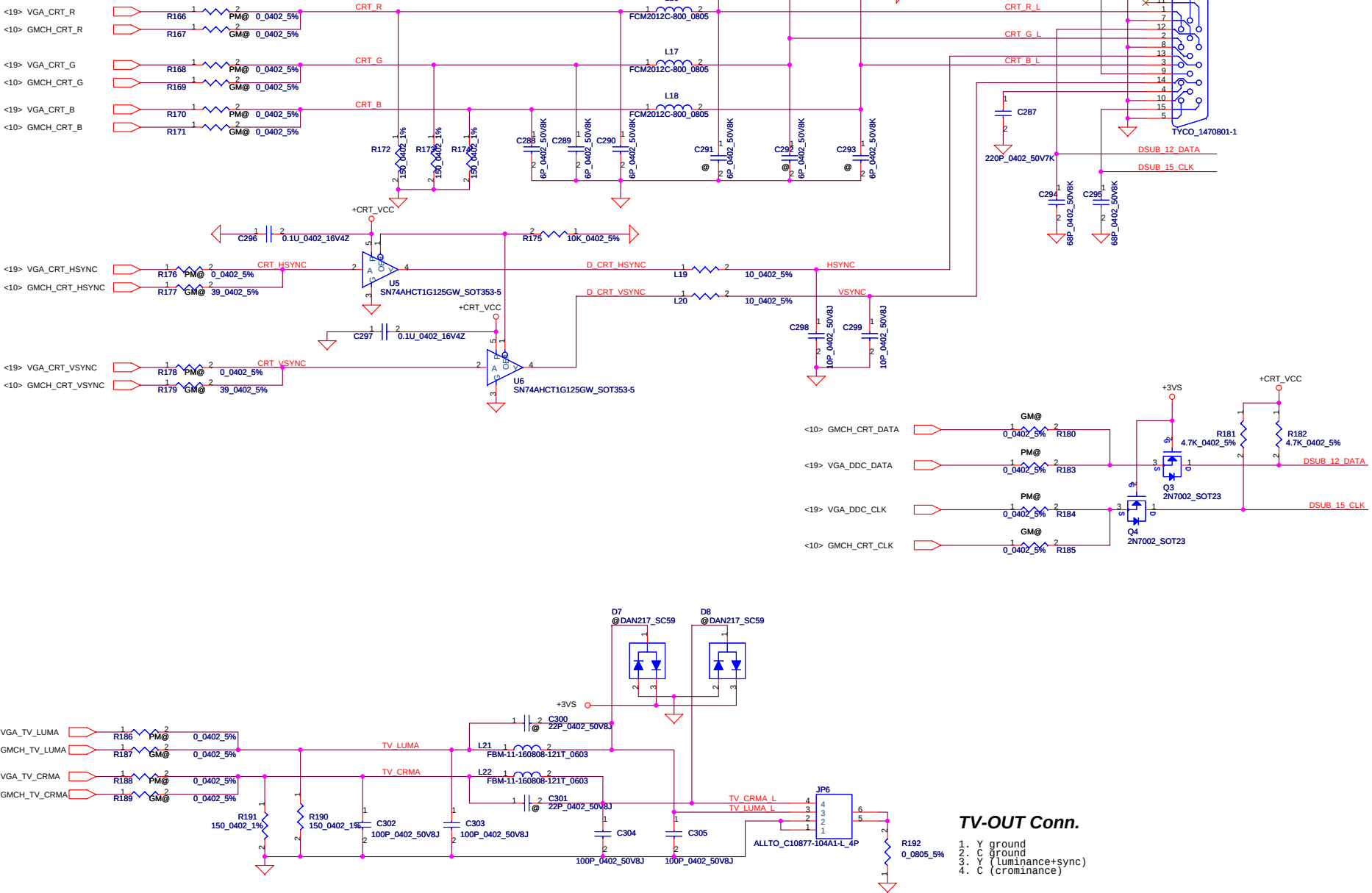
FSB Frequency Set:

CPU Driven	Stuff	R108	R130	R149
* (Default)	No Stuff	R103	R110	R122 R152
667MHz	Stuff	R103	R110	R149 R152
	No Stuff	R108	R122	R130
800MHz	Stuff			R149 R152
	No Stuff	R103	R108	R110 R122 R130



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CRT CONNECTOR

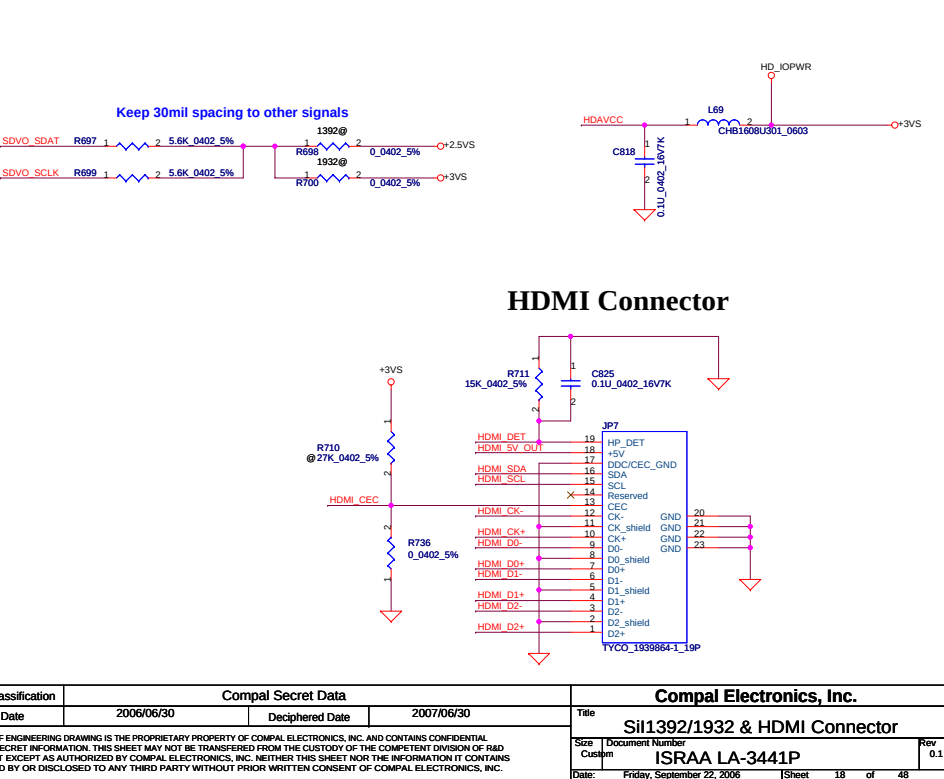
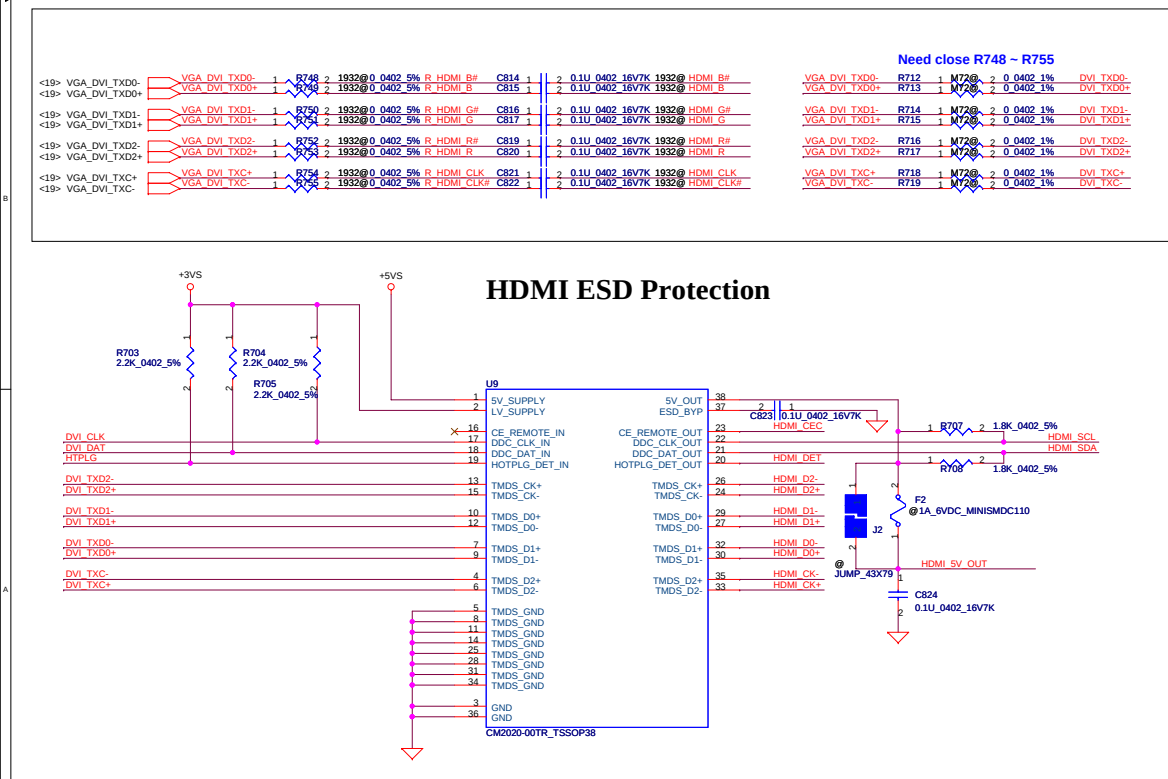
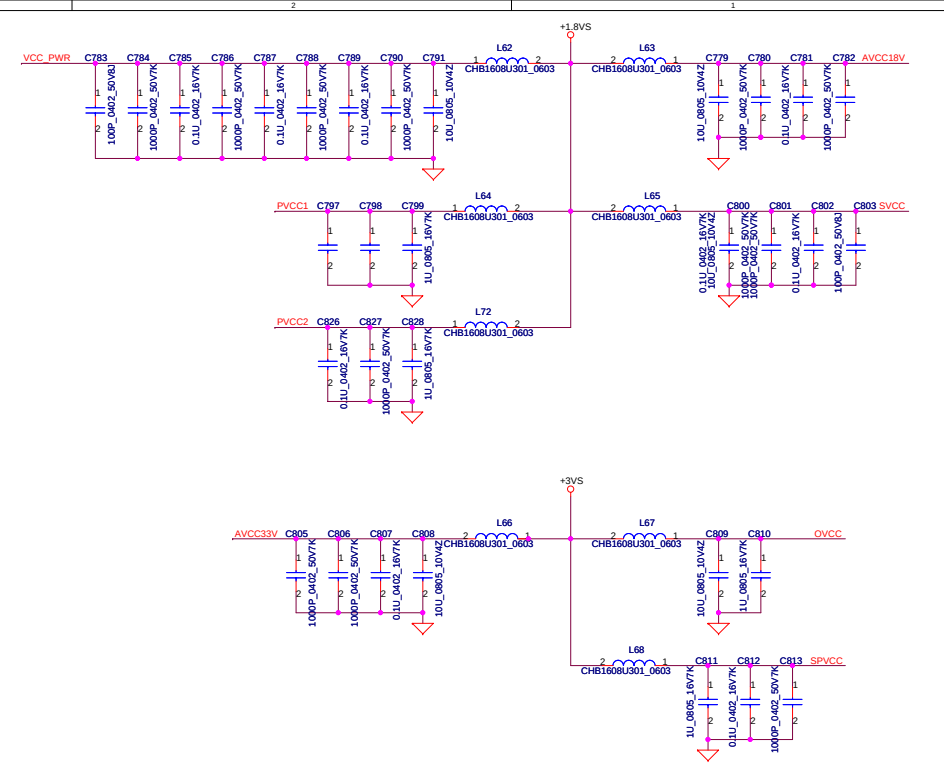
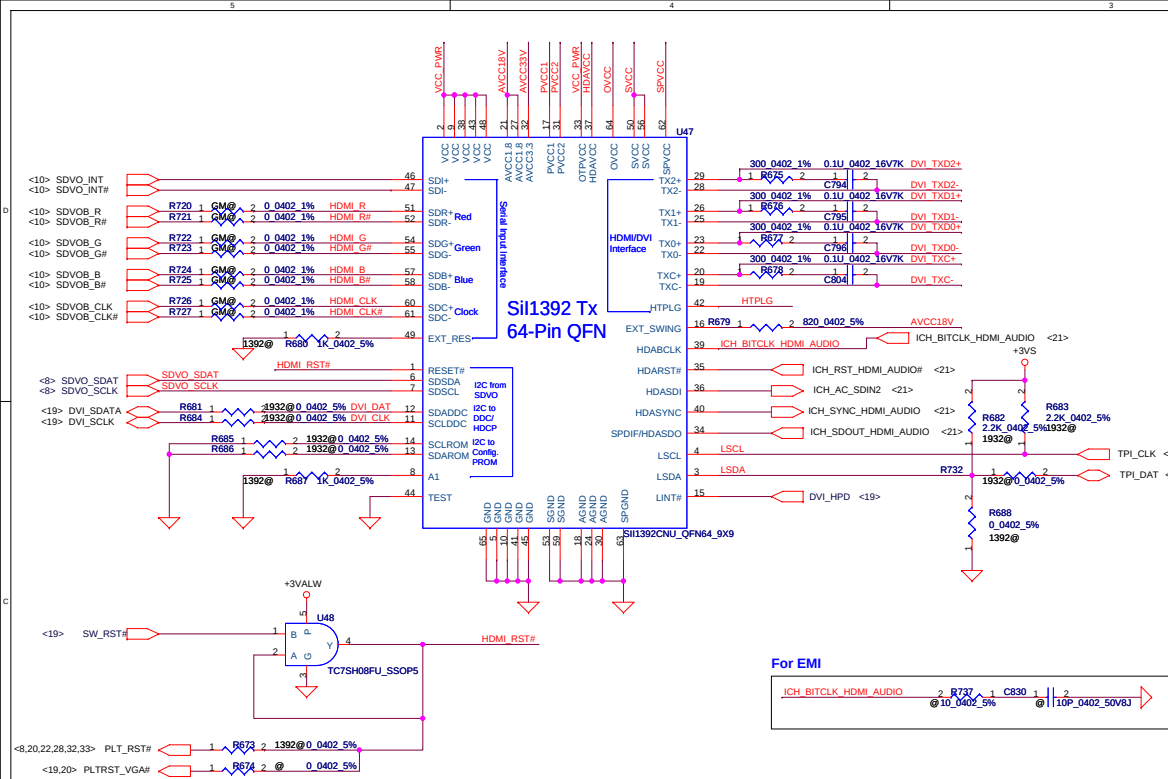


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								Size		Document Number		Rev	
										ISRAA LA-3441P		0.1	
								Date:		Friday, September 22, 2006		Sheet 17 of 48	

CRT Conn.

TV-OUT Conn.

- 1. Y ground
- 2. C ground
- 3. Y (luminance+sync)
- 4. C (chrominance)



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Issued Date		Deciphered Date		Si11392/1932 & HDMI Connector	
2006/06/30		2007/06/30		ISRAA LA-3441P	
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Size		Document Number		Rev	
Cusum		18		0.1	
Sheet		18		of 48	

[illegible]

B+ for NVIDIA VGA BOARD US

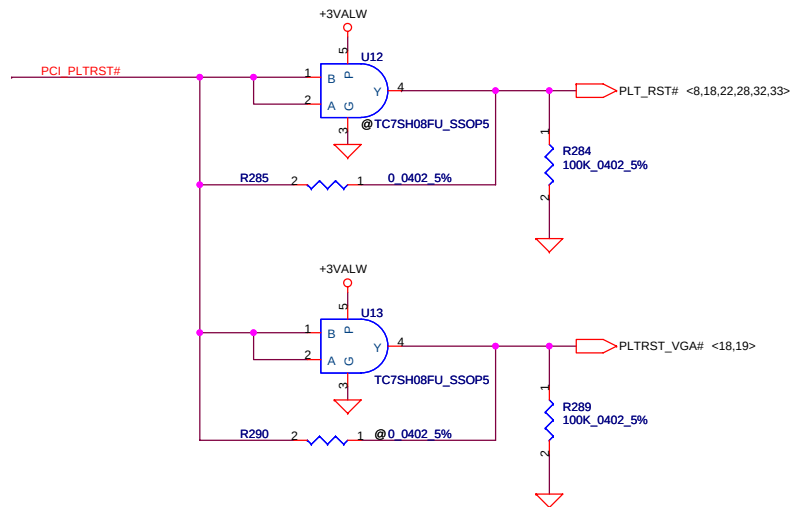
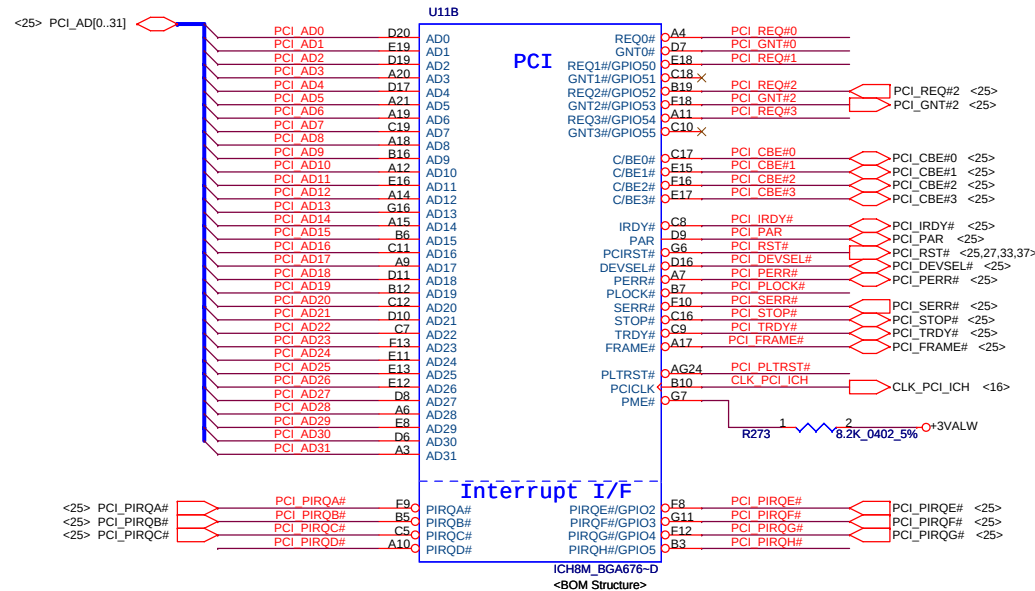
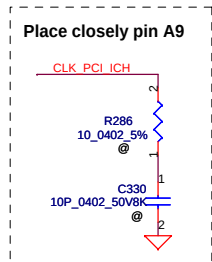
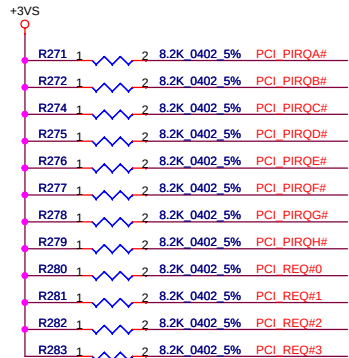
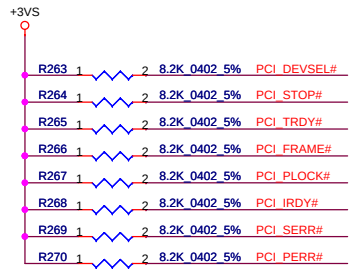
Pin-to-pin comparison diagram for ACES 88386-1K71. The diagram shows two columns of pins, JPB (left) and JPB (right), with their corresponding functions. The left column (JPB) lists pins 1 through 199, and the right column (JPB) lists pins 2 through 200. Functions include power supplies (+2.5VS, +1.5VS, +1.8VS, +3VS, +5VALW), clock signals (TPI_CLK, CLK_PCIE_VGA, CLK_PCIE_VGA#), data signals (TPI_DAT, EC_SMB_CK2, EC_SMB_DA2), and various control and status signals (PCEI GTX C.MRX, PCIE MTX C.GRX, VGA TV CRMA, VGA ENVD, VGA ENBKL, VGA CRT VSYNC, VGA CRT HSYNC, VGA DDC CLK, VGA DDC DATA, VGA CRT R, VGA CRT G, VGA CRT B).

JPB (Left)	JPB (Right)
1	2
3	4
5	6
7	8
9	10
11	12
13	14
15	16
17	18
19	20
21	22
23	24
25	26
27	28
29	30
31	32
33	34
35	36
37	38
39	40
41	42
43	44
45	46
47	48
49	50
51	52
53	54
55	56
57	58
59	60
61	62
63	64
65	66
67	68
69	70
71	72
73	74
75	76
77	78
79	80
81	82
83	84
85	86
87	88
89	90
91	92
93	94
95	96
97	98
99	100
101	102
103	104
105	106
107	108
109	110
111	112
113	114
115	116
117	118
119	120
121	122
123	124
125	126
127	128
129	130
131	132
133	134
135	136
137	138
139	140
141	142
143	144
145	146
147	148
149	150
151	152
153	154
155	156
157	158
159	160
161	162
163	164
165	166
167	168
169	170
171	172
173	174
175	176
177	178
179	180
181	182
183	184
185	186
187	188
189	190
191	192
193	194
195	196
197	198
199	200

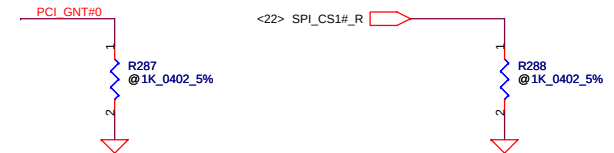
Functions and signals shown in the diagram:

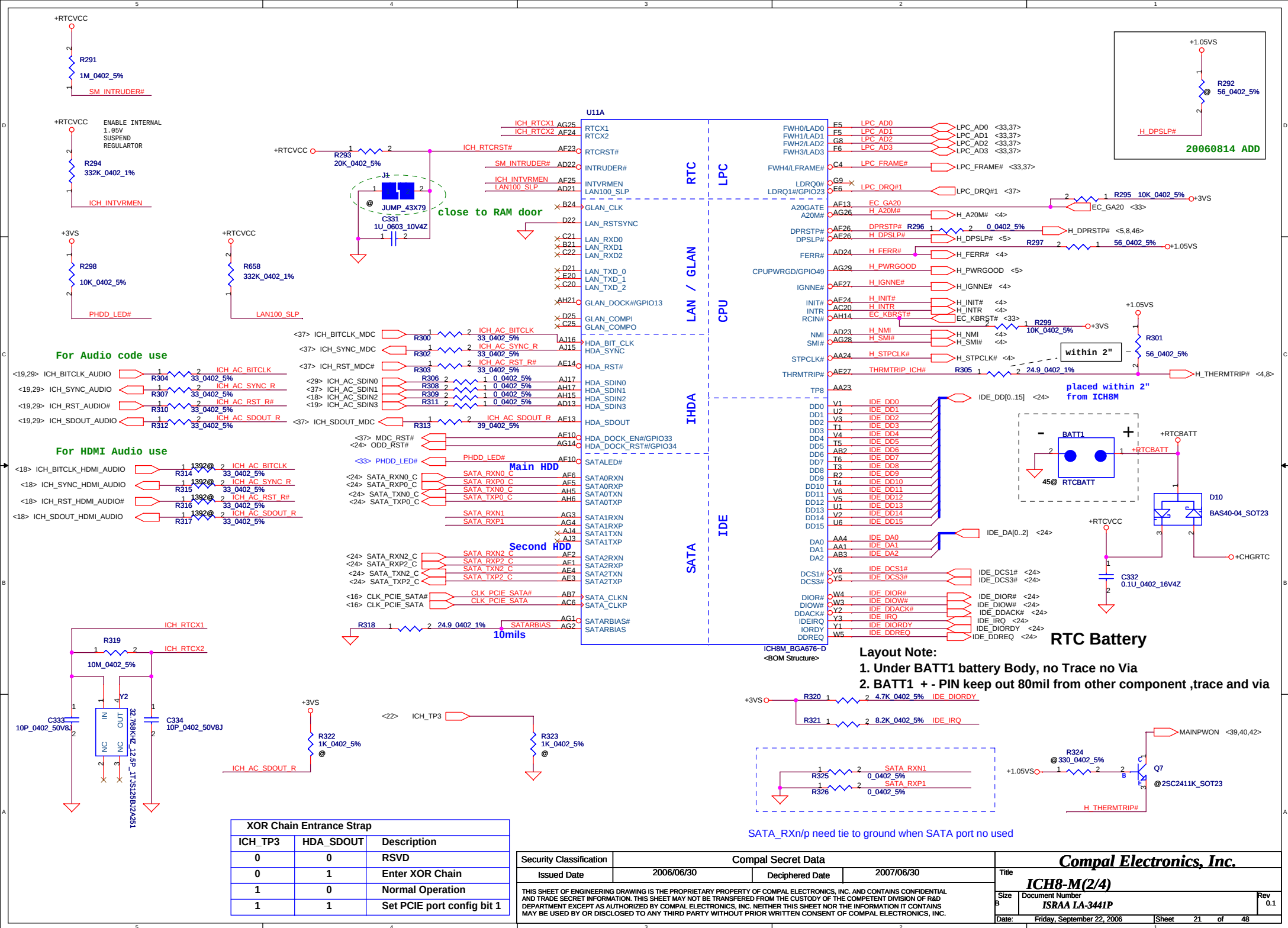
- Power Supplies:** +2.5VS, +1.5VS, +1.8VS, +3VS, +5VALW
- Clock Signals:** TPI_CLK, CLK_PCIE_VGA, CLK_PCIE_VGA#
- Data Signals:** TPI_DAT, EC_SMB_CK2, EC_SMB_DA2
- Control and Status Signals:** PCEI GTX C.MRX, PCIE MTX C.GRX, VGA TV CRMA, VGA ENVD, VGA ENBKL, VGA CRT VSYNC, VGA CRT HSYNC, VGA DDC CLK, VGA DDC DATA, VGA CRT R, VGA CRT G, VGA CRT B
- Other Signals:** R222, R223, R626, R627, VGA LCD DATA, VGA LCD CLK, VGA TXCLK, VGA TXCLK, VGA TXOUT0, VGA TXOUT1, VGA TXOUT2, VGA TXOUT3, VGA TXOUT4, VGA TXOUT5, VGA TXOUT6, VGA TXOUT7, VGA TXOUT8, VGA TXOUT9, VGA TXOUT10, VGA TXOUT11, VGA TXOUT12, VGA TXOUT13, VGA TXOUT14, VGA TXOUT15, VGA TXOUT16, VGA TXOUT17, VGA TXOUT18, VGA TXOUT19, VGA TXOUT20, VGA TXOUT21, VGA TXOUT22, VGA TXOUT23, VGA TXOUT24, VGA TXOUT25, VGA TXOUT26, VGA TXOUT27, VGA TXOUT28, VGA TXOUT29, VGA TXOUT30, VGA TXOUT31, VGA TXOUT32, VGA TXOUT33, VGA TXOUT34, VGA TXOUT35, VGA TXOUT36, VGA TXOUT37, VGA TXOUT38, VGA TXOUT39, VGA TXOUT40, VGA TXOUT41, VGA TXOUT42, VGA TXOUT43, VGA TXOUT44, VGA TXOUT45, VGA TXOUT46, VGA TXOUT47, VGA TXOUT48, VGA TXOUT49, VGA TXOUT50, VGA TXOUT51, VGA TXOUT52, VGA TXOUT53, VGA TXOUT54, VGA TXOUT55, VGA TXOUT56, VGA TXOUT57, VGA TXOUT58, VGA TXOUT59, VGA TXOUT60, VGA TXOUT61, VGA TXOUT62, VGA TXOUT63, VGA TXOUT64, VGA TXOUT65, VGA TXOUT66, VGA TXOUT67, VGA TXOUT68, VGA TXOUT69, VGA TXOUT70, VGA TXOUT71, VGA TXOUT72, VGA TXOUT73, VGA TXOUT74, VGA TXOUT75, VGA TXOUT76, VGA TXOUT77, VGA TXOUT78, VGA TXOUT79, VGA TXOUT80, VGA TXOUT81, VGA TXOUT82, VGA TXOUT83, VGA TXOUT84, VGA TXOUT85, VGA TXOUT86, VGA TXOUT87, VGA TXOUT88, VGA TXOUT89, VGA TXOUT90, VGA TXOUT91, VGA TXOUT92, VGA TXOUT93, VGA TXOUT94, VGA TXOUT95, VGA TXOUT96, VGA TXOUT97, VGA TXOUT98, VGA TXOUT99, VGA TXOUT100, VGA TXOUT101, VGA TXOUT102, VGA TXOUT103, VGA TXOUT104, VGA TXOUT105, VGA TXOUT106, VGA TXOUT107, VGA TXOUT108, VGA TXOUT109, VGA TXOUT110, VGA TXOUT111, VGA TXOUT112, VGA TXOUT113, VGA TXOUT114, VGA TXOUT115, VGA TXOUT116, VGA TXOUT117, VGA TXOUT118, VGA TXOUT119, VGA TXOUT120, VGA TXOUT121, VGA TXOUT122, VGA TXOUT123, VGA TXOUT124, VGA TXOUT125, VGA TXOUT126, VGA TXOUT127, VGA TXOUT128, VGA TXOUT129, VGA TXOUT130, VGA TXOUT131, VGA TXOUT132, VGA TXOUT133, VGA TXOUT134, VGA TXOUT135, VGA TXOUT136, VGA TXOUT137, VGA TXOUT138, VGA TXOUT139, VGA TXOUT140, VGA TXOUT141, VGA TXOUT142, VGA TXOUT143, VGA TXOUT144, VGA TXOUT145, VGA TXOUT146, VGA TXOUT147, VGA TXOUT148, VGA TXOUT149, VGA TXOUT150, VGA TXOUT151, VGA TXOUT152, VGA TXOUT153, VGA TXOUT154, VGA TXOUT155, VGA TXOUT156, VGA TXOUT157, VGA TXOUT158, VGA TXOUT159, VGA TXOUT160, VGA TXOUT161, VGA TXOUT162, VGA TXOUT163, VGA TXOUT164, VGA TXOUT165, VGA TXOUT166, VGA TXOUT167, VGA TXOUT168, VGA TXOUT169, VGA TXOUT170, VGA TXOUT171, VGA TXOUT172, VGA TXOUT173, VGA TXOUT174, VGA TXOUT175, VGA TXOUT176, VGA TXOUT177, VGA TXOUT178, VGA TXOUT179, VGA TXOUT180, VGA TXOUT18

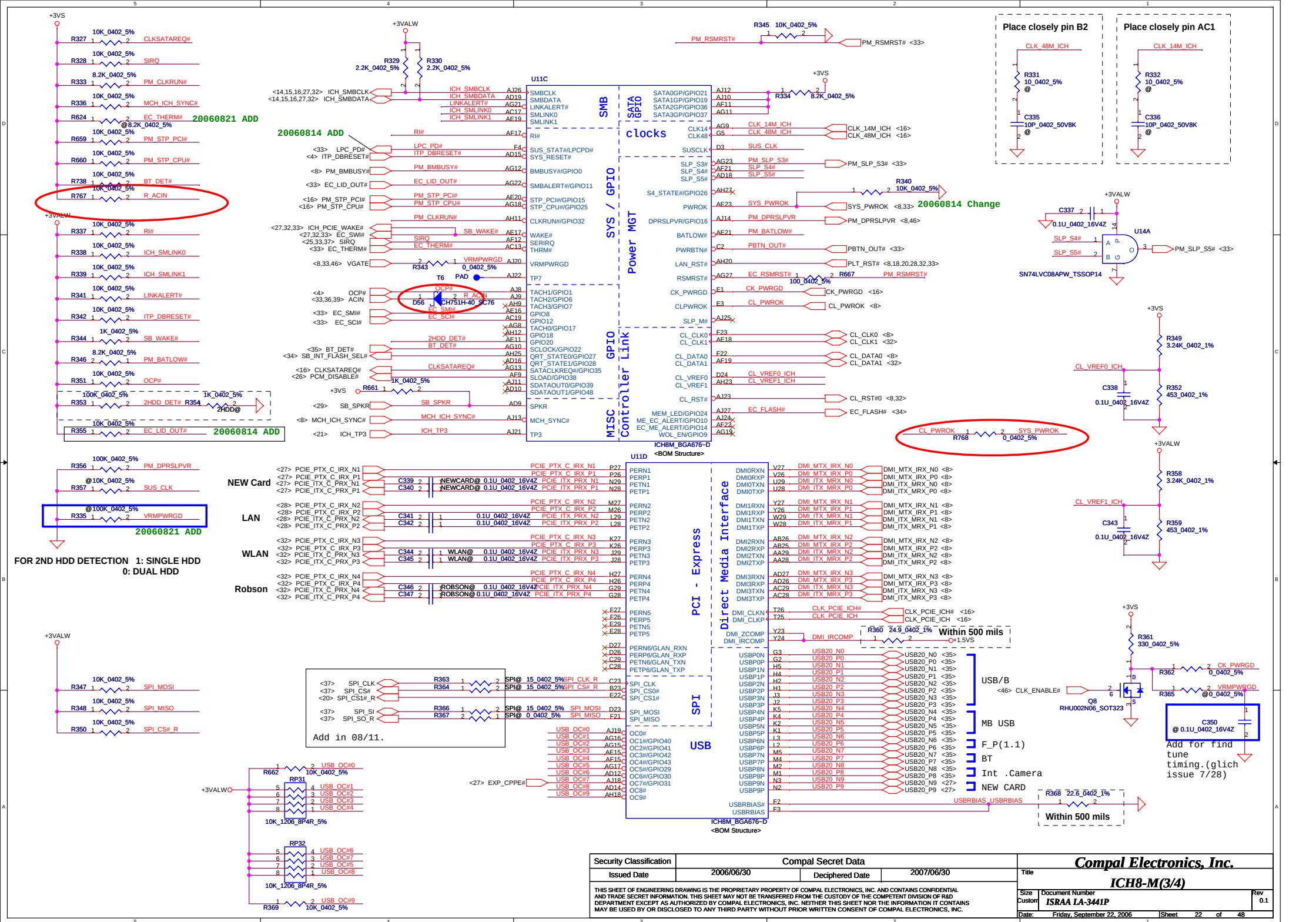
Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2006/06/30	Deciphered Date	2007/06/30	Title VGA CONN		
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				Date:	Friday, September 22, 2006	Sheet 19 of 48

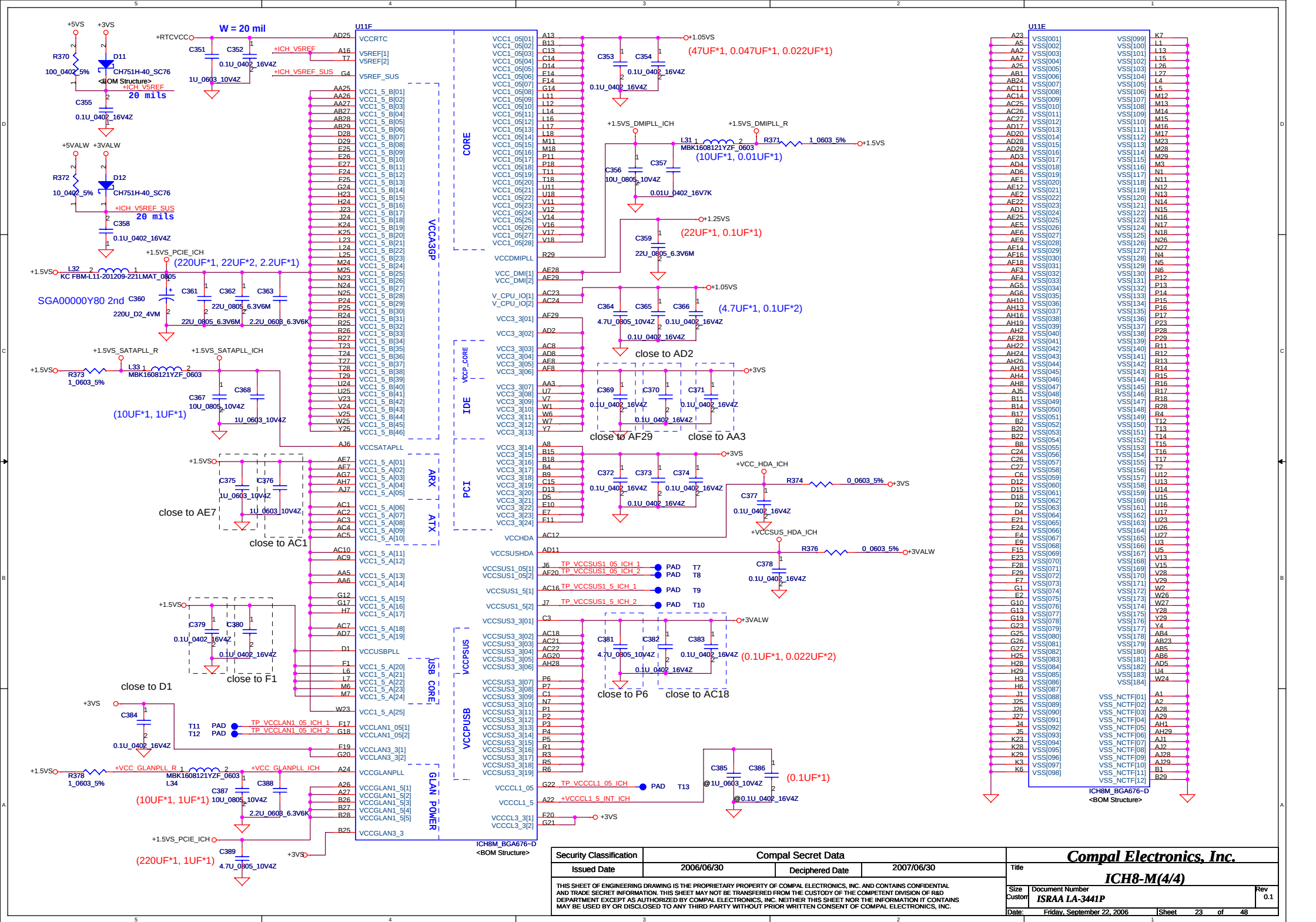


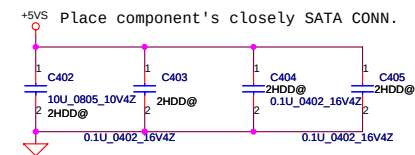
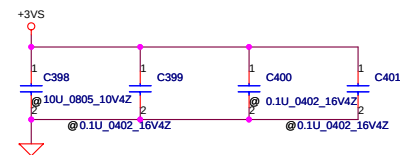
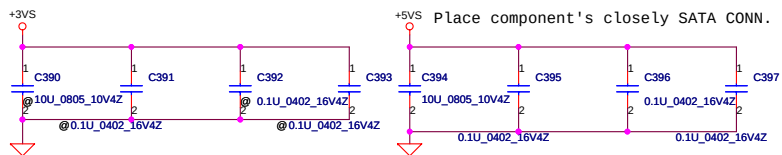
Boot BIOS Strap		
PCI_GNT0#	SPI_CS#0	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC *



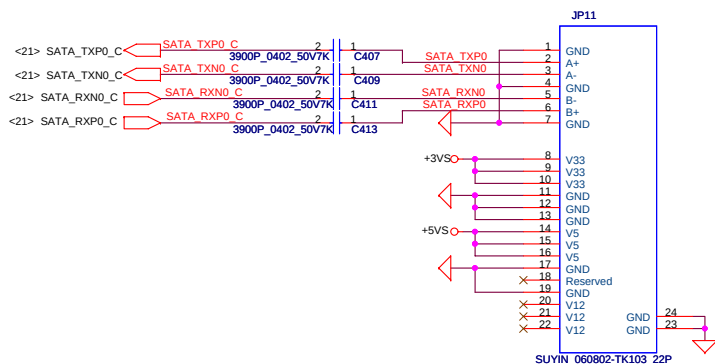




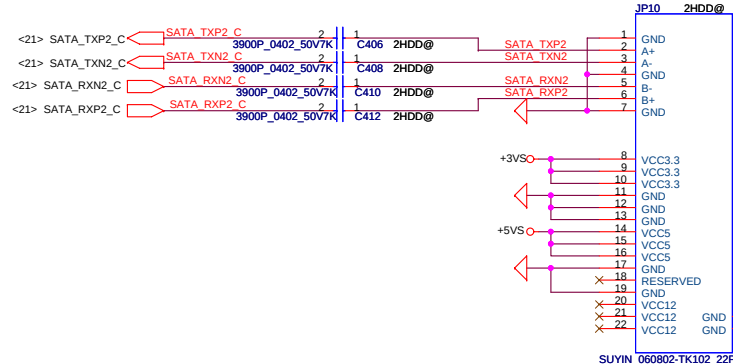




SATA HDD-1 Conn.(5.2mm)



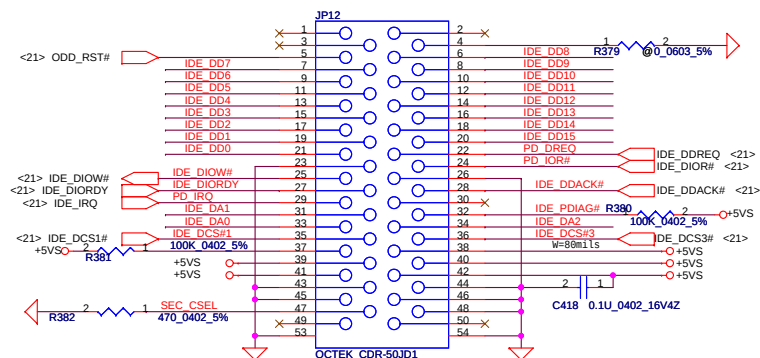
SATA HDD-2 Conn.(11.8mm)



Placea caps. near ODD CONN.



ODD CONN



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				Size B	Document Number	Rev 0
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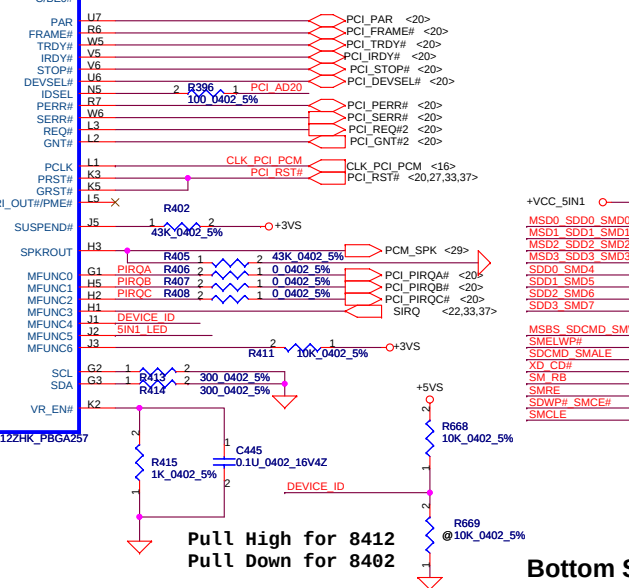
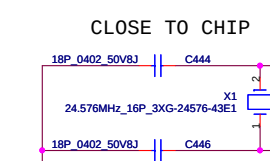
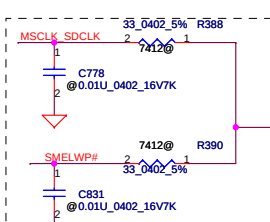
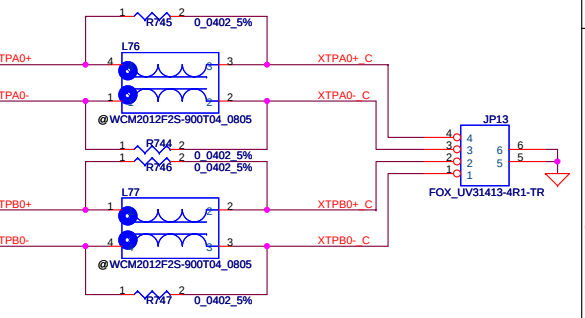


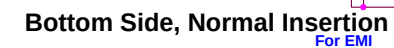
Diagram illustrating the relationship between security classification codes and their corresponding PCI codes:

- PIRQA R417 2 → PCI_PIRQE# <20>
- PIRQB R418 2 → PCI_PIRQF# <20>
- PIRQC R419 2 → PCI_PIRQG# <20>

The codes are also associated with a '2' and a '0 0402 5%' label.



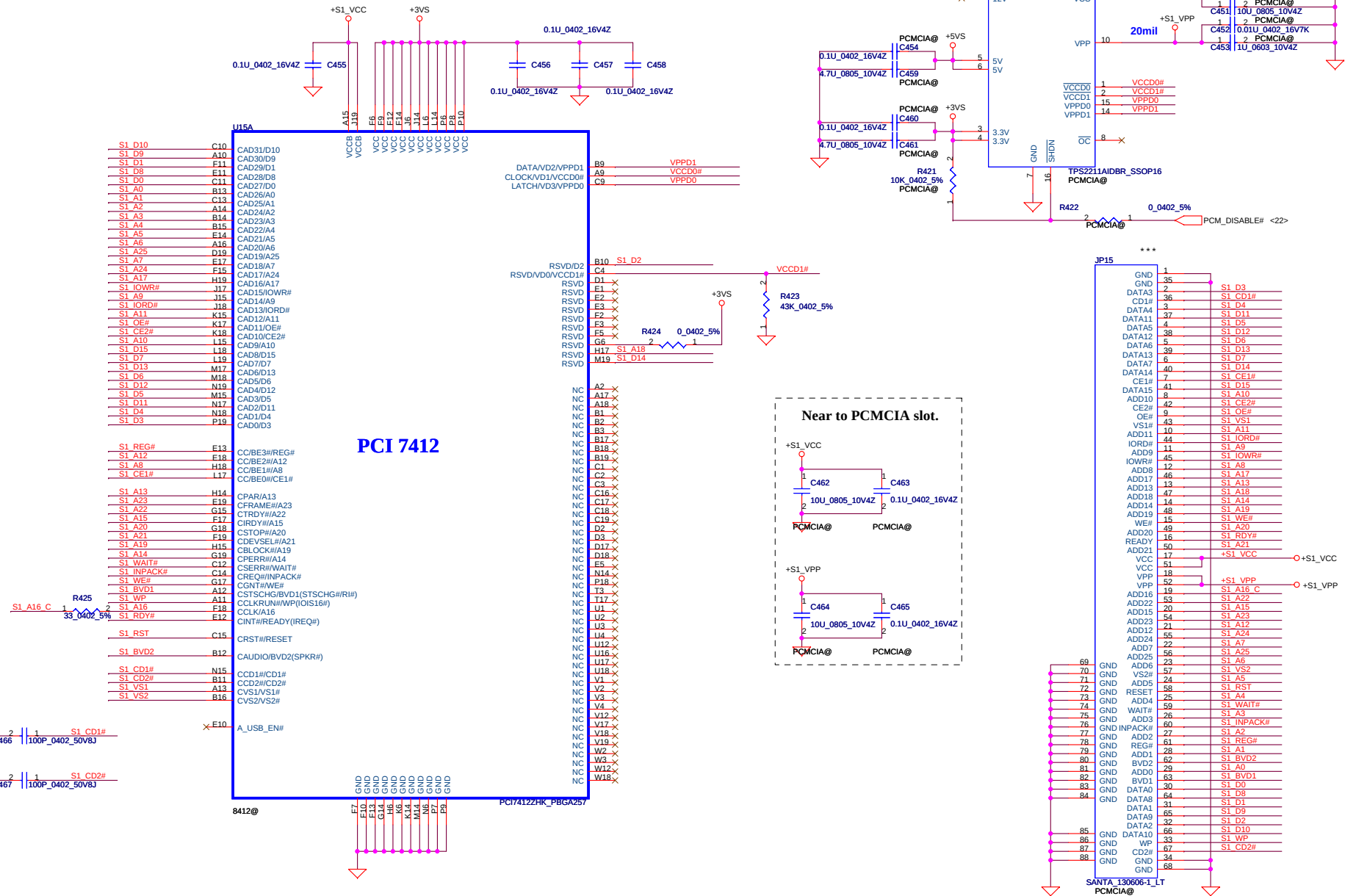
+3VS 6 In 1 Card Power Switch



Secret Data		Compal Electronics, Inc. Title PCI8412/PCI/1394 CONN/CARD SLOT	
Entered Date	2007/06/30		
ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL STUDY OF THE COMPETENT DIVISION OF R&D		Size	Document Number

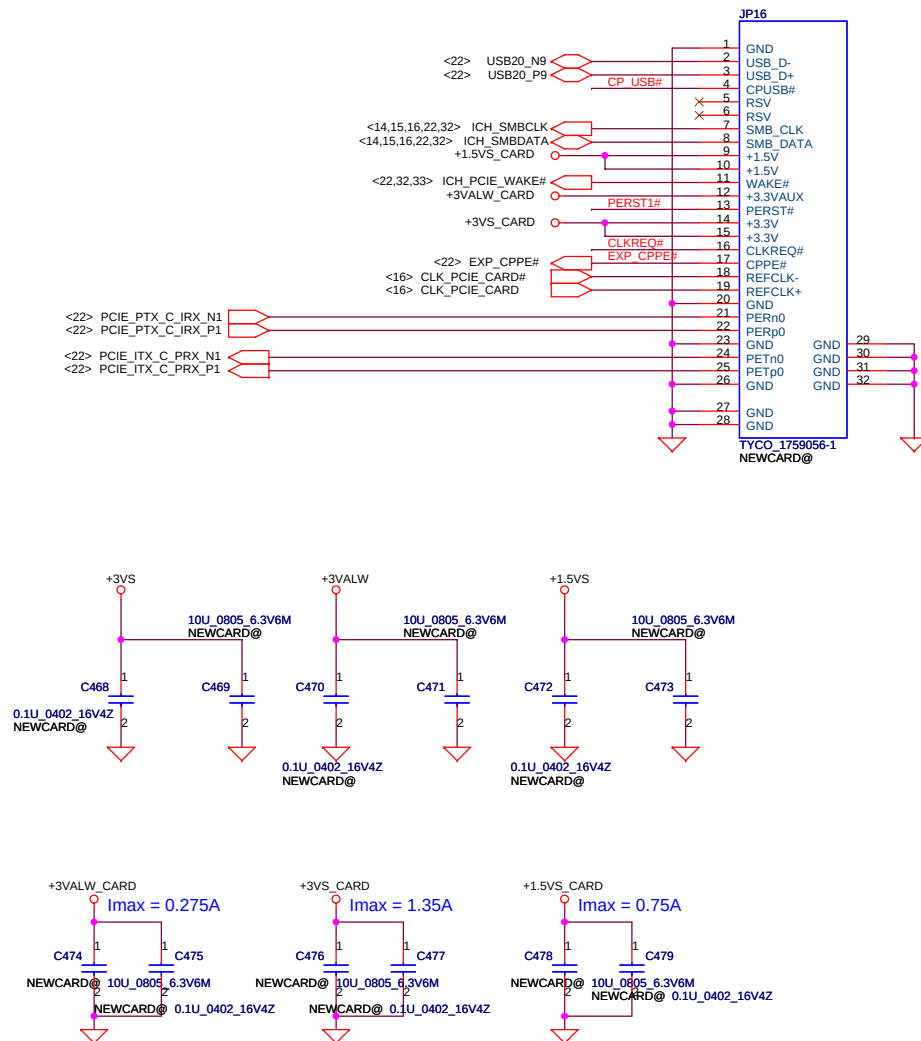
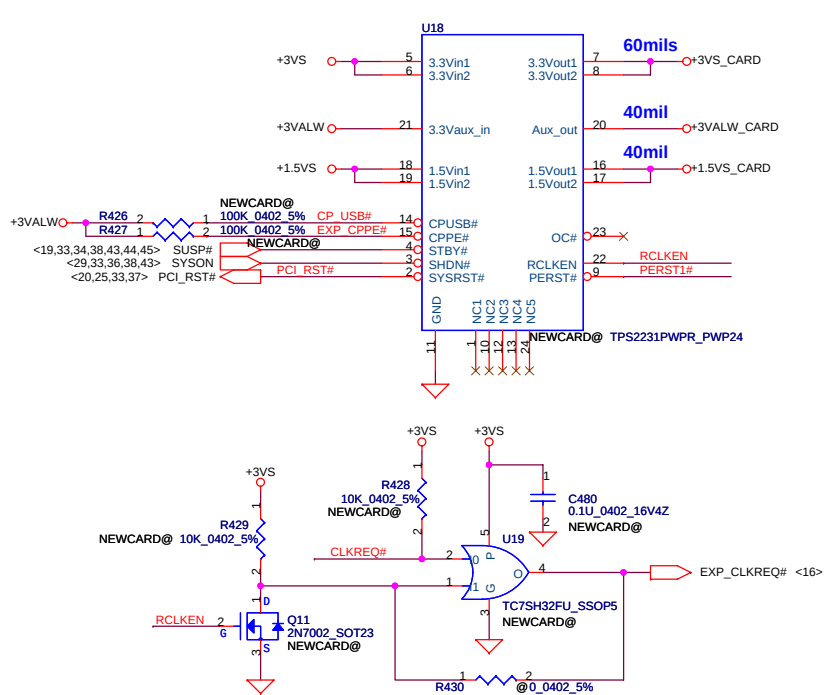
Compal Electronics, Inc.			
PCI8412/PCI/1394 CONN/CARD SLOT			
Size	Document Number	Rev	
Custom	ISRAA LA-3441P	0.1	
Date:	Friday, September 22, 2006	Sheet	25 of 48

CardBus Power Switch



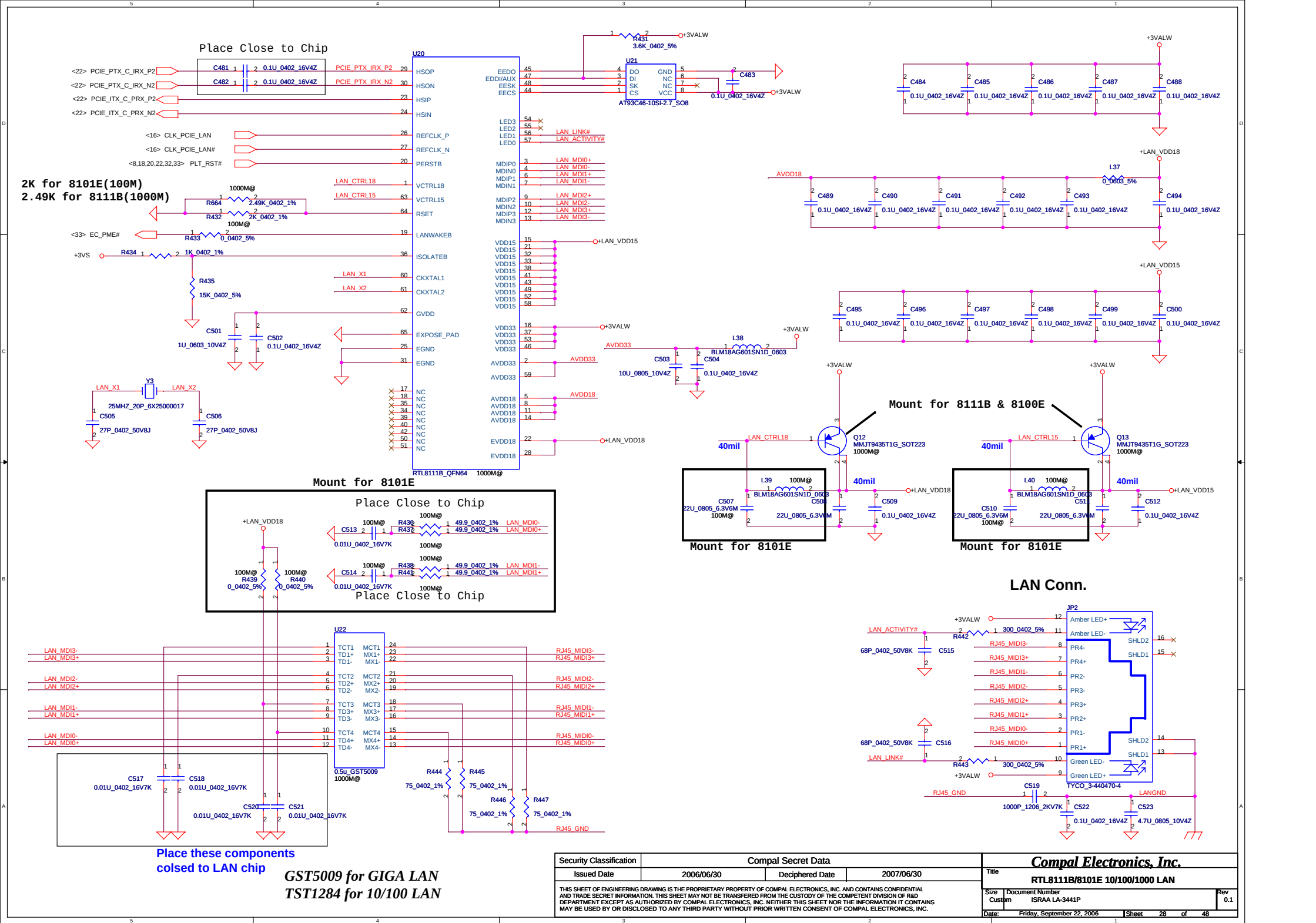
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2006/06/30	Deciphered Date	2007/06/30	Title	PCI7412/CB/CB SLOT	
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New Card Power Switch



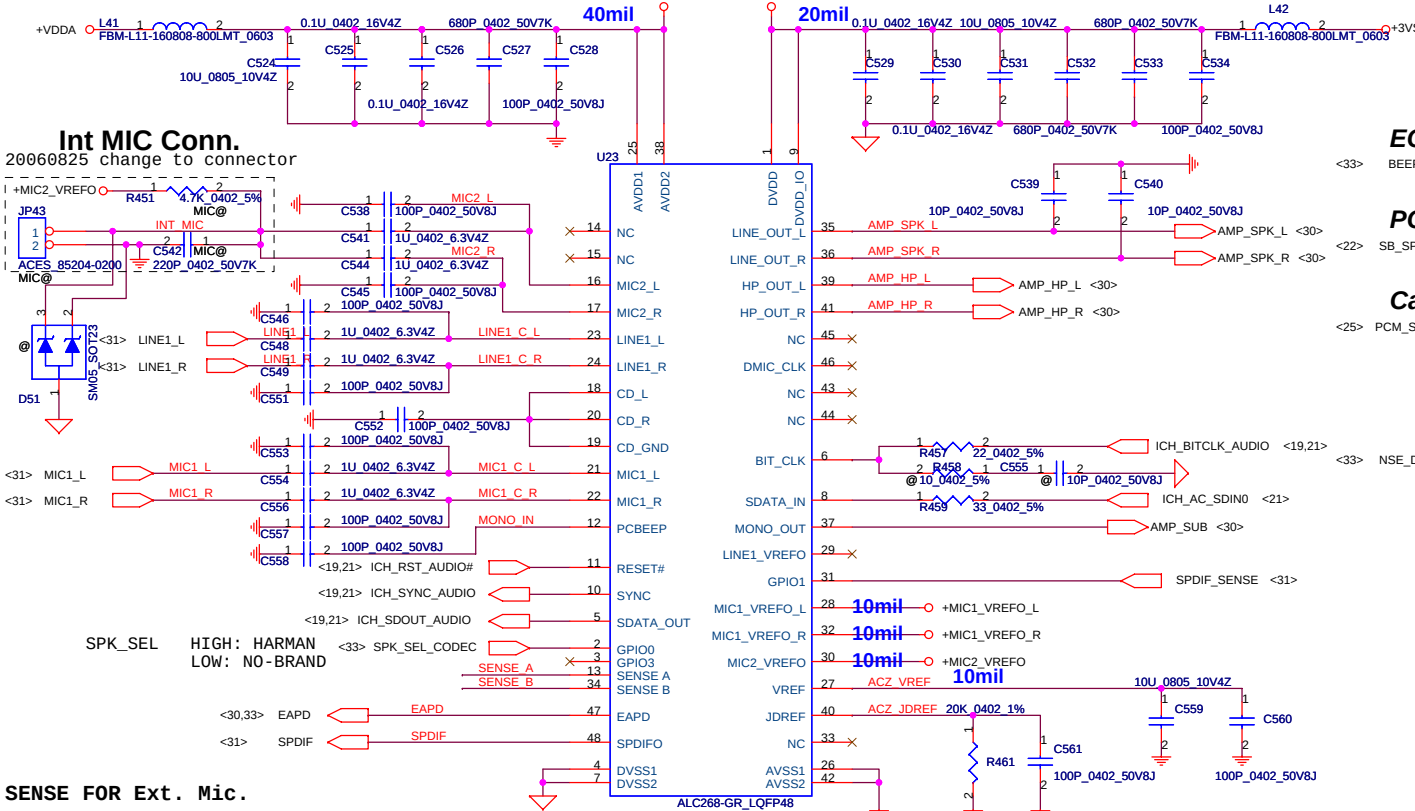
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Compal Electronics, Inc.			
Title			
New Card Socket			
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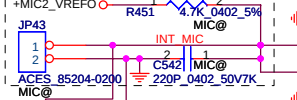
HD Audio Codec

+AVDD_AC97 +3VS_DVDD



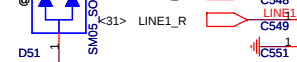
Int MIC Conn.

20060825 change to connector



SPK_SEL

HIGH: HARMAN LOW: NO-BRAND



SENSE FOR Ext. Mic.

<31> MIC_SENSE



SENSE FOR Int. Mic.

For EMI



SENSE FOR HP

<31> HP_SENSE



SENSE FOR LINE

<31> LINE_SENSE



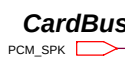
EC BEEP

<33> BEEP#



PCI BEEP

<22> SB_SPKR



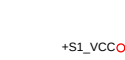
CardBus BEEP

<25> PCM_SPK



Regulator for CODEC

+S1_VCC



Adjustable Output

+VDDA 4.75V



Moat Bridge

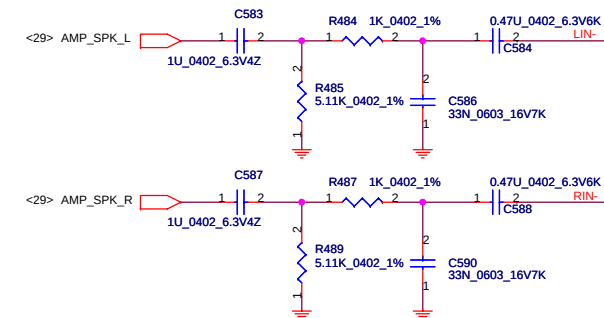
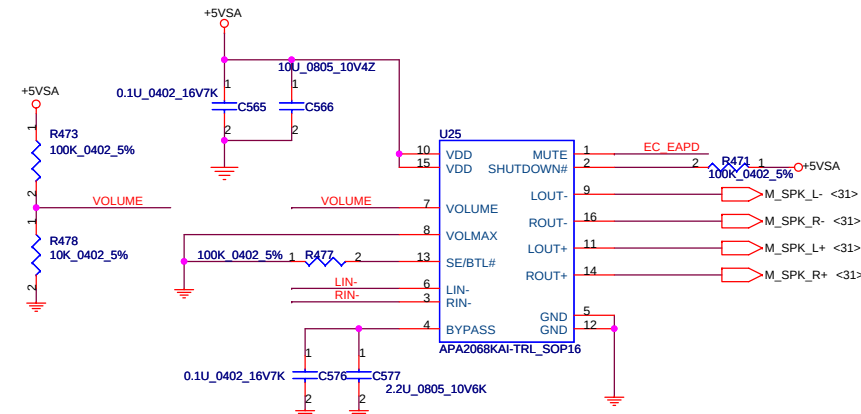
<27,33,36,38,43> SYSON



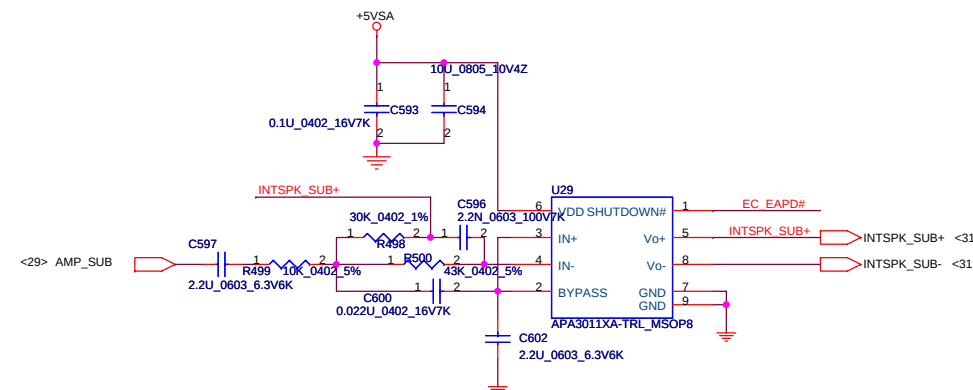
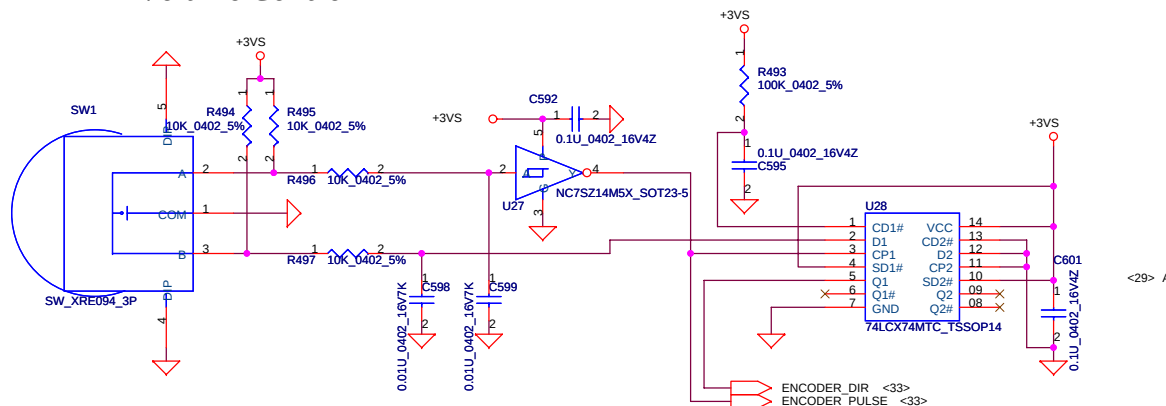
Sense Pin	Impedance	Codec Signals
SENSE A	39.2K	PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
	10K	PORT-C (PIN 23, 24)
	5.1K	PORT-D (PIN 35, 36)
SENSE B	39.2K	PORT-E (PIN 14, 15)
	20K	PORT-F (PIN 16, 17)
	10K	PORT-G (PIN 43, 44)
	5.1K	PORT-H (PIN 45, 46)

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APA2068 Medium Range Amplifier

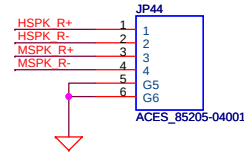
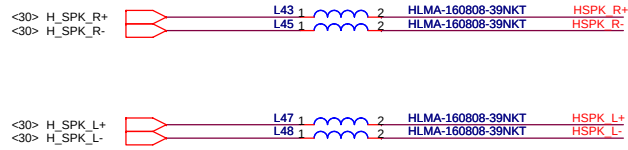


APA3011 Subwoofer Amplifier

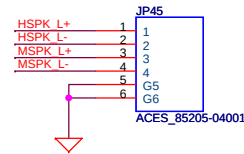
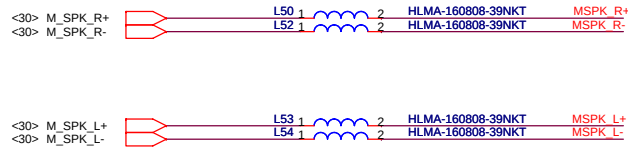


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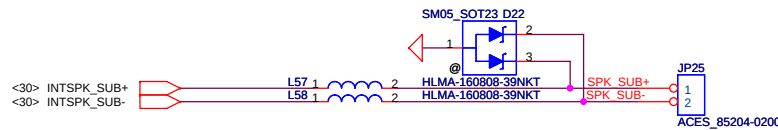
Tweeter Conn.



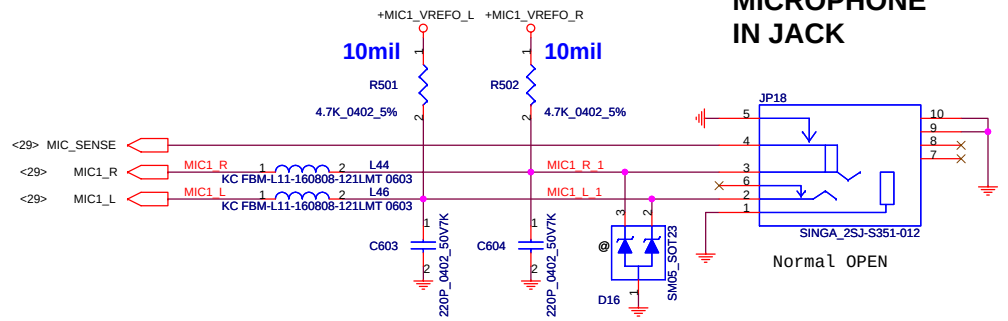
Medium SPK Conn.



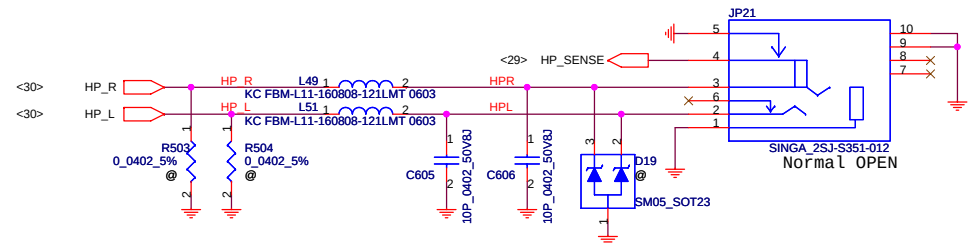
Sub-woofer Conn.



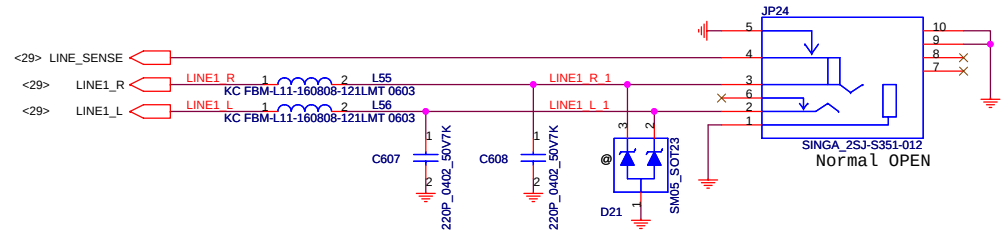
MICROPHONE IN JACK



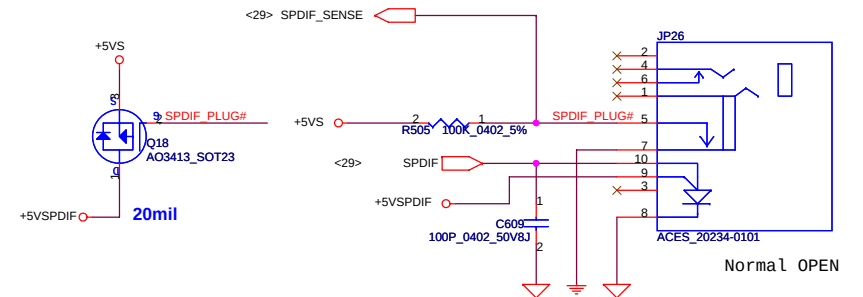
HEADPHONE OUT JACK



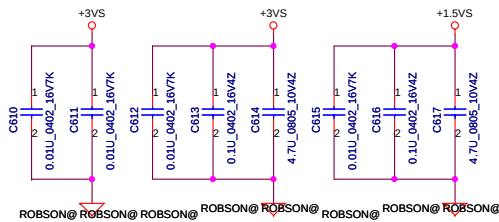
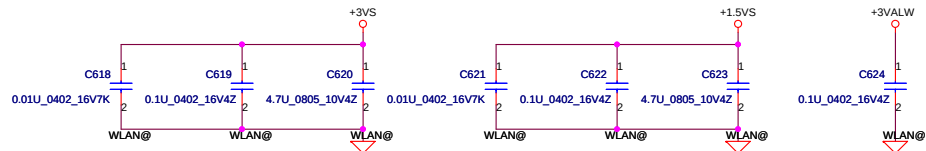
LINE IN JACK



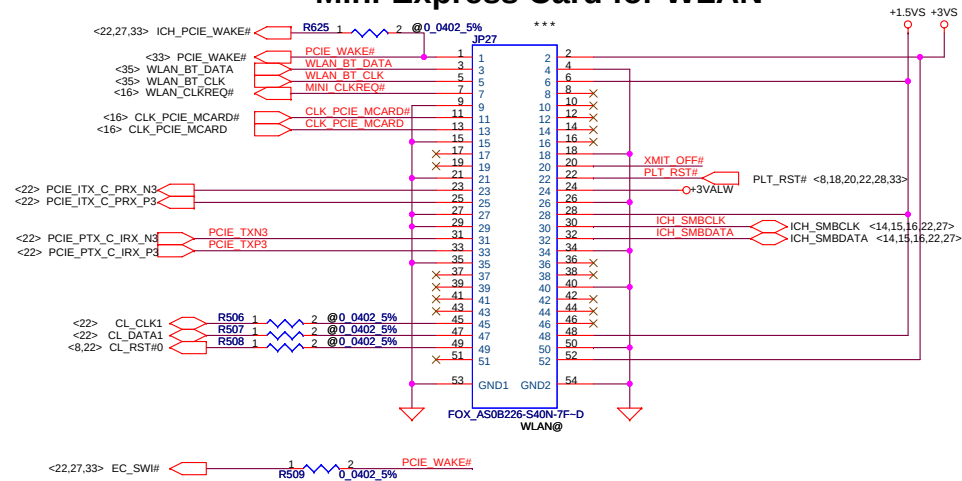
S/PDIF OUT JACK



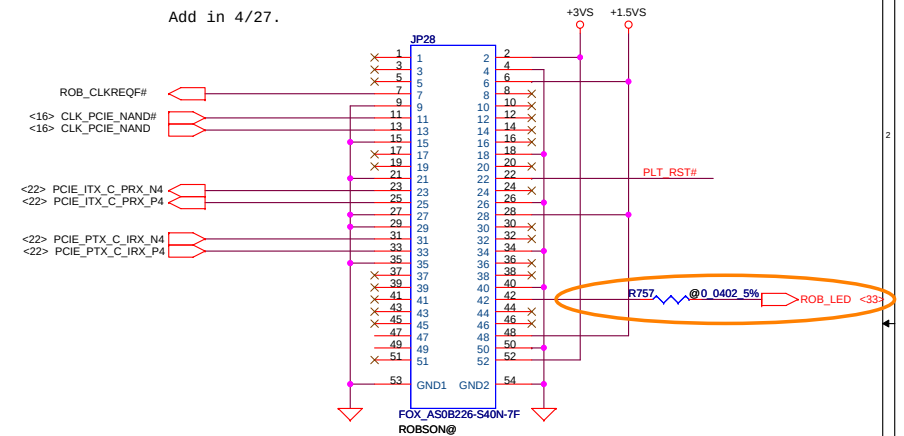
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/06/30	Deciphered Date	2007/06/30	Title	<Title> Audio Jack/MIC/SPDIF
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				Sheet	31 of 48
				Rev	0.1



Mini-Express Card for WLAN



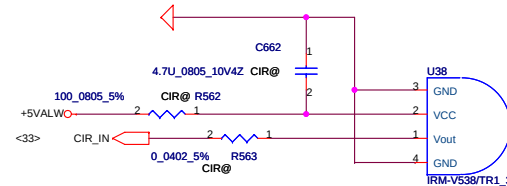
NAND mini Card(Robson support)



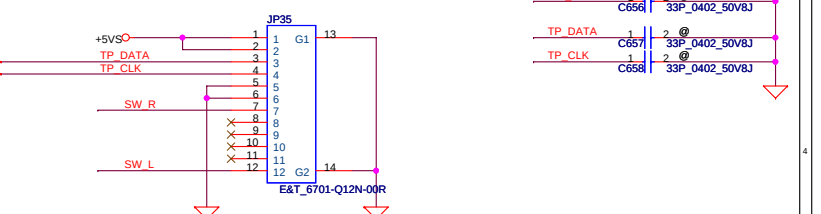
TP Button



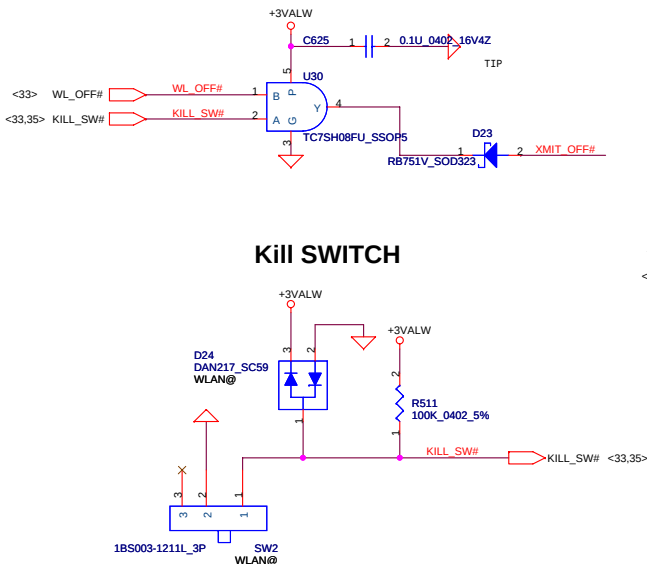
CIR



TP CONN.

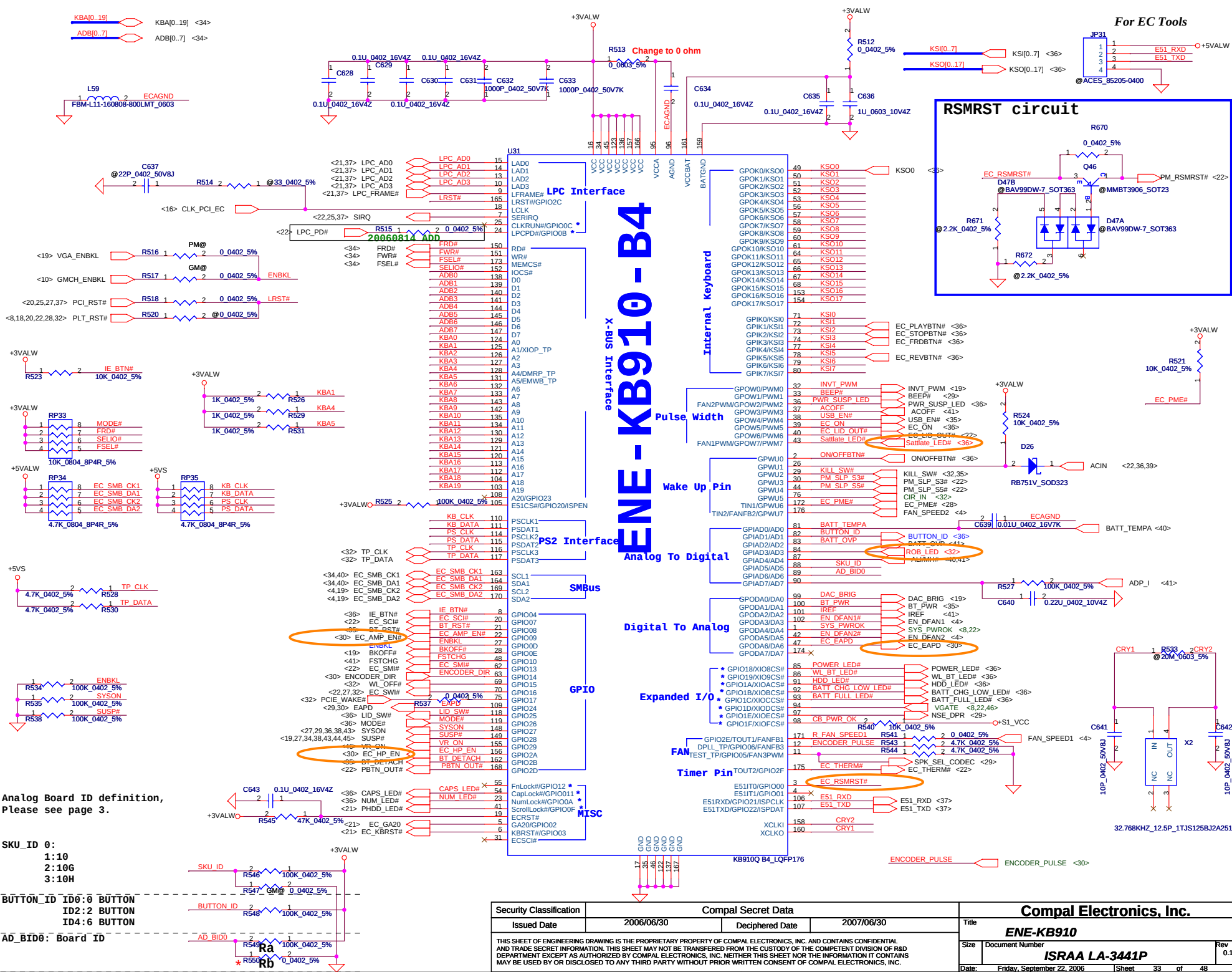


Kill SWITCH

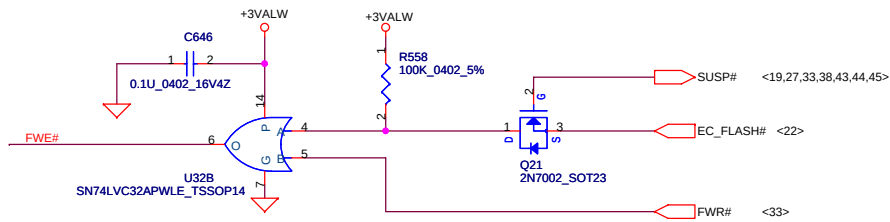
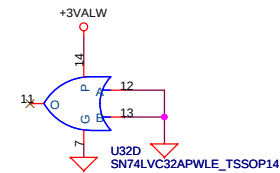
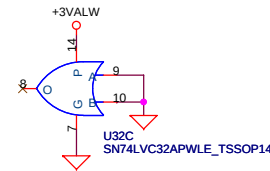
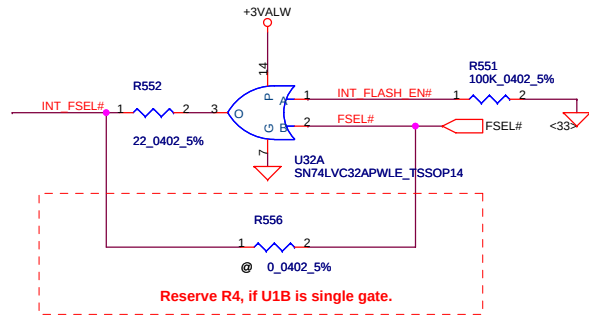


Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2006/06/30				Title			
Deciphered Date				2007/06/30				Mini-Card/CIR/TP Conn			
Size				Document Number				ISRAA LA-3441P			
Date				Friday, September 22, 2006				Rev 0.1			
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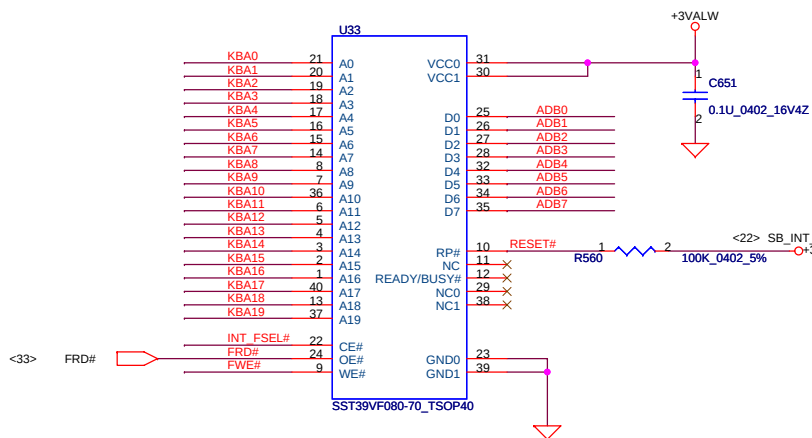
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2006/06/30	Deciphered Date	2007/06/30	Title	ENE-KB910
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Size	Document Number	ISRAA LA-3441P		Rev	0.1
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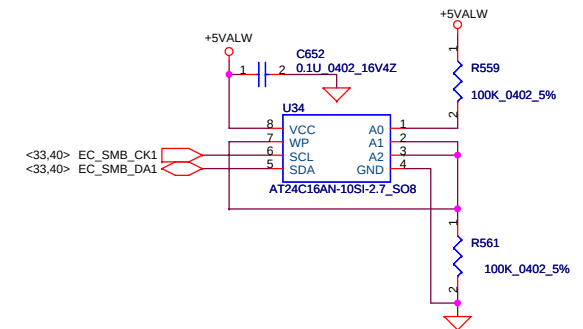
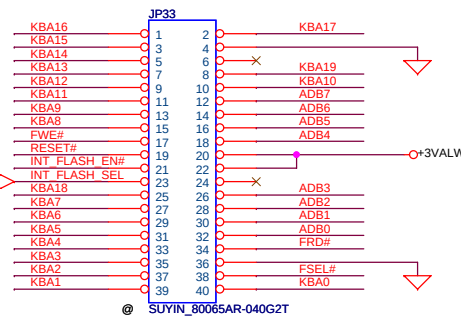
Module ID
Indication for polarity of reset
Reset input High Active --> Low ,
Reset input Low Active --> Open

<33> KBA[0..19] KBA[0..19]
<33> ADB[0..7] ADB[0..7]

1MB Flash ROM

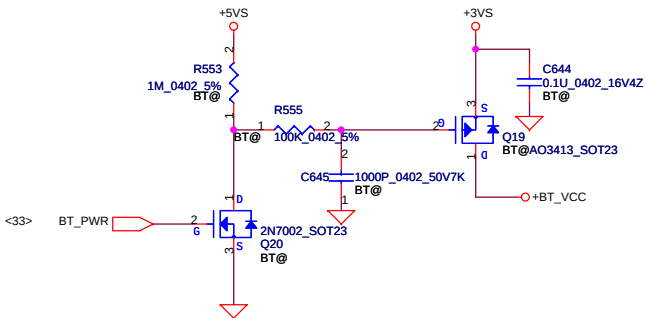


1MB ROM Socket

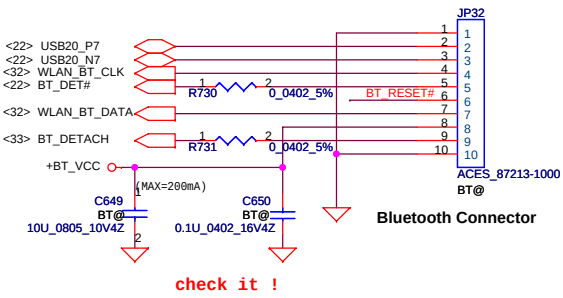
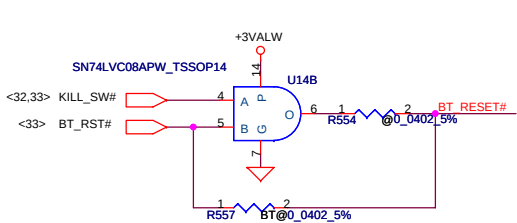


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BlueTooth Interface



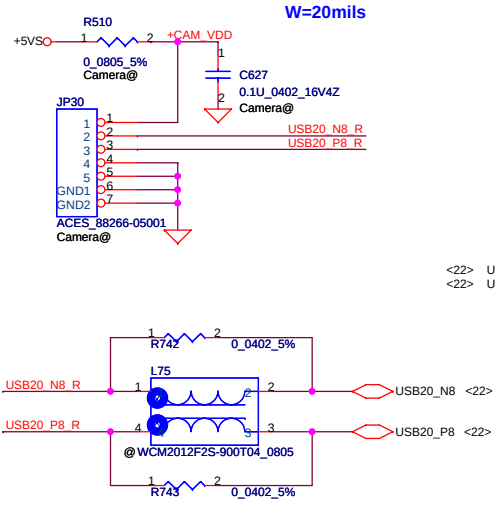
Change at 8/30



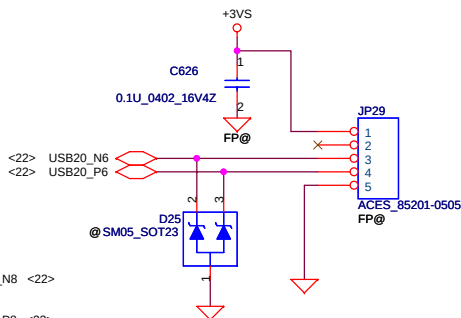
check it !

Bluetooth Connector

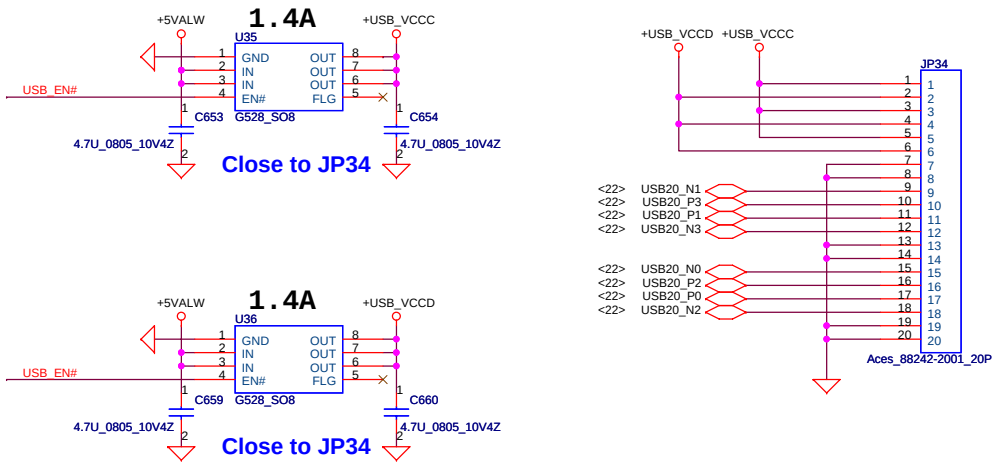
Int. Camera Conn



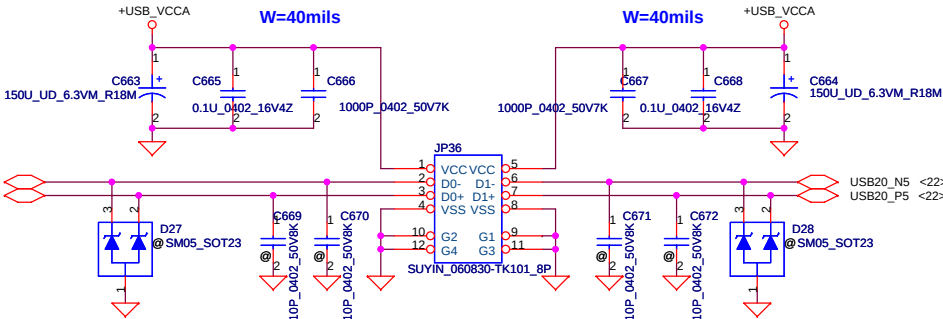
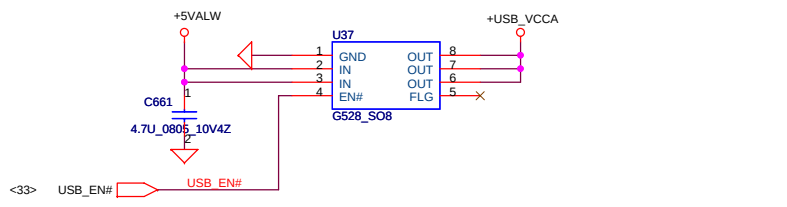
Finger printer Conn



USB Board Right side 2x2 Port

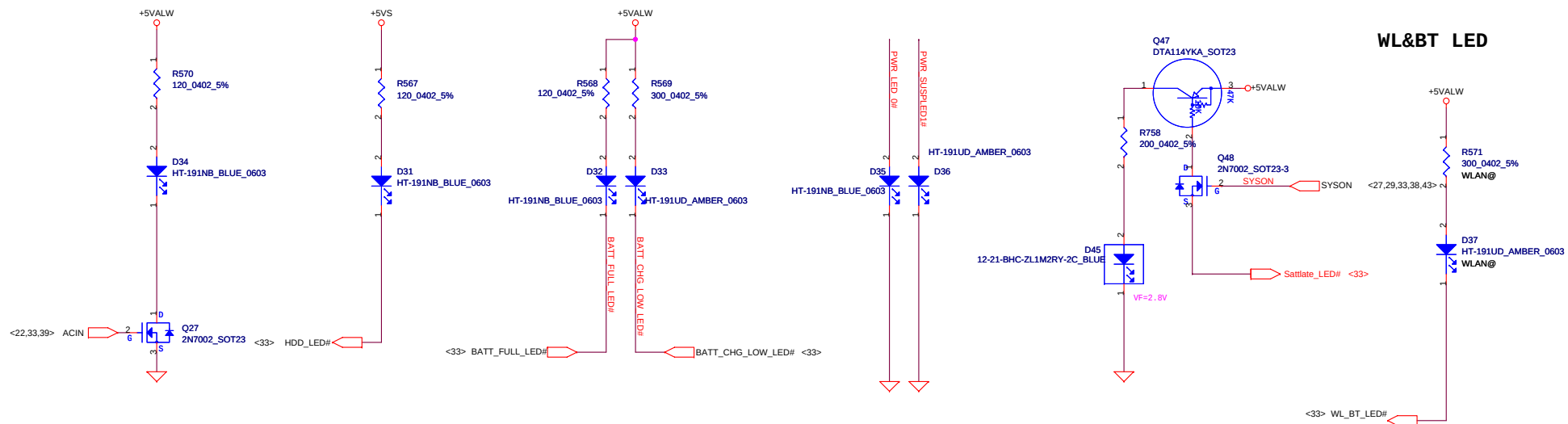
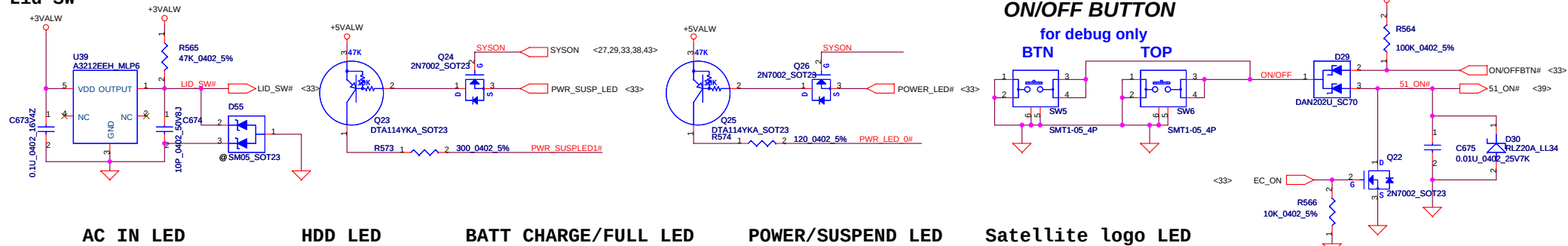


USB Conn Left side 1x2 Port

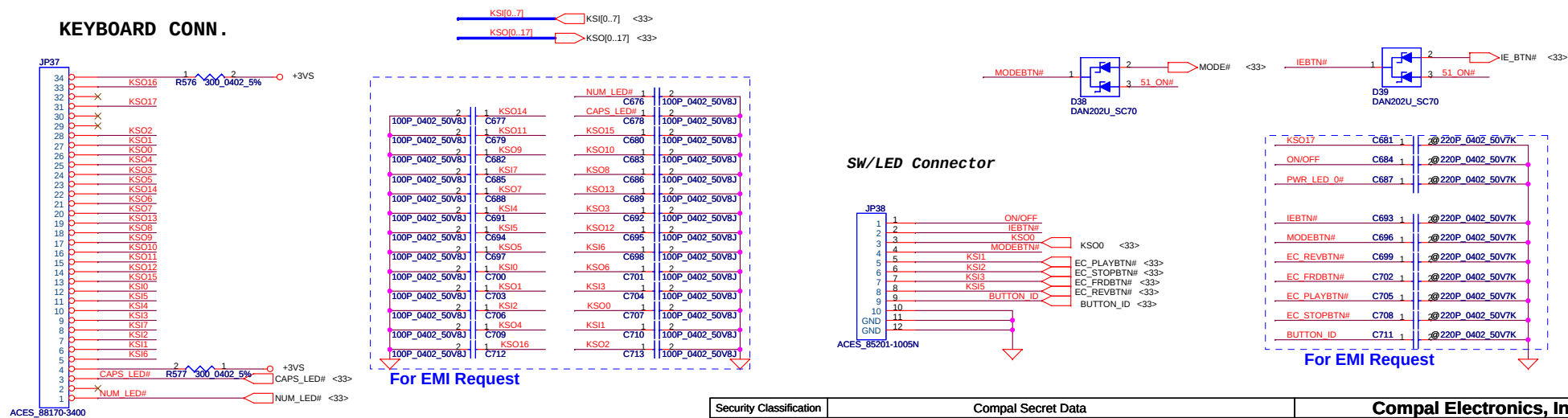


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2006/06/30		2007/06/30		USB/BT/F_P/Int. Ca Conn	
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Lid SW



KEYBOARD CONN.



20070718 change new KB define

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Under DDR ME Assignment Area

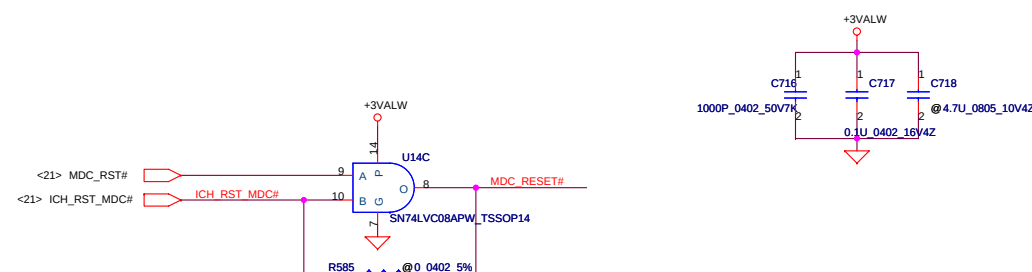


Diagram illustrating the JP39 connector for MDC Rev1.5. The connector is a 18-pin component (JP39) with the following connections:

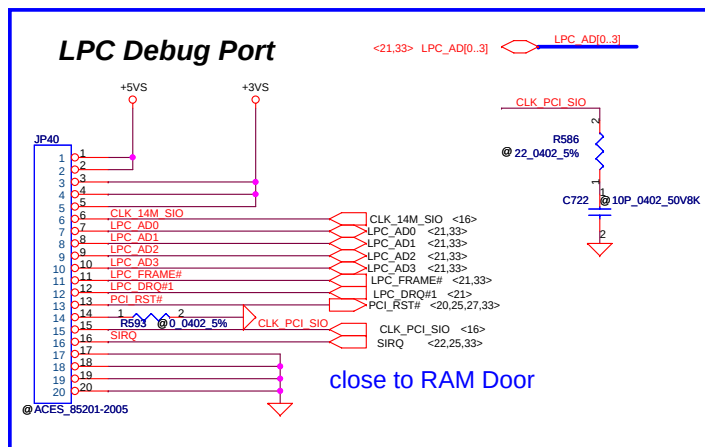
- Pin 1:** ICH_SDOUT_MDC
- Pin 2:** ICH_SYNC_MDC
- Pin 3:** ICH_SYNC_MDC
- Pin 4:** ICH_SYNC_MDC
- Pin 5:** ICH_SYNC_MDC
- Pin 6:** ICH_SYNC_MDC
- Pin 7:** ICH_SYNC_MDC
- Pin 8:** ICH_SYNC_MDC
- Pin 9:** ICH_SYNC_MDC
- Pin 10:** ICH_SYNC_MDC
- Pin 11:** ICH_SYNC_MDC
- Pin 12:** ICH_SYNC_MDC
- Pin 13:** ICH_SYNC_MDC
- Pin 14:** ICH_SYNC_MDC
- Pin 15:** ICH_SYNC_MDC
- Pin 16:** ICH_SYNC_MDC
- Pin 17:** ICH_SYNC_MDC
- Pin 18:** ICH_SYNC_MDC

Additional components and labels:

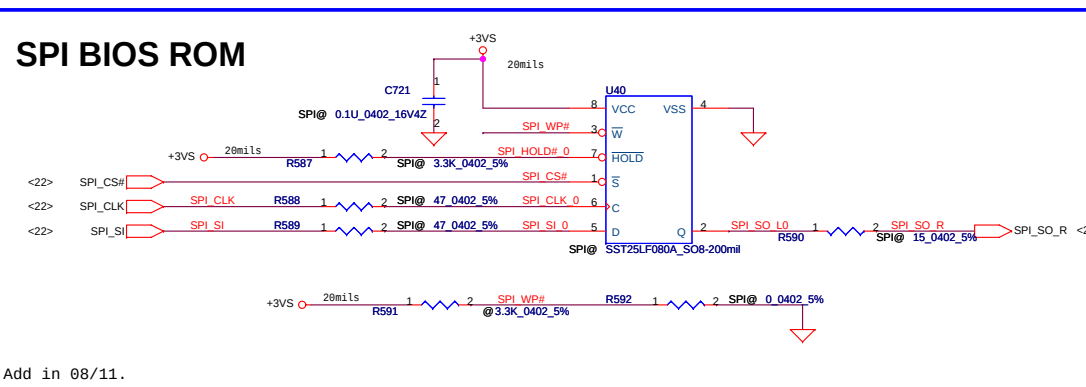
- 20mil:** Dimension indicating the pitch of the connector pins.
- 3.3V:** Voltage level for the ICH_SYNC_MDC signal.
- 33 0402 5%:** Resistor value and tolerance.
- ICH_BITCLK_MDC:** Signal name for the clock line.
- ACES_88018-124G:** Component identifier.
- C714:** Component identifier.
- @10_0402_5%:** Resistor value and tolerance.
- @10P_0402_50V8K:** Resistor value and tolerance.
- Connector for MDC Rev1.5:** Label for the connector.



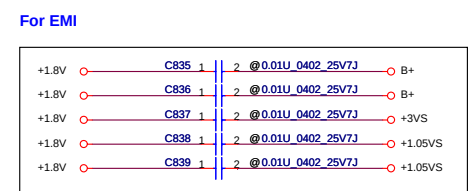
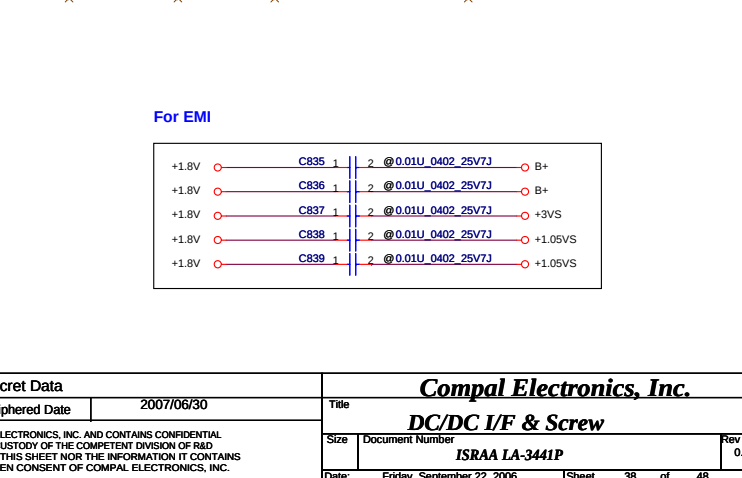
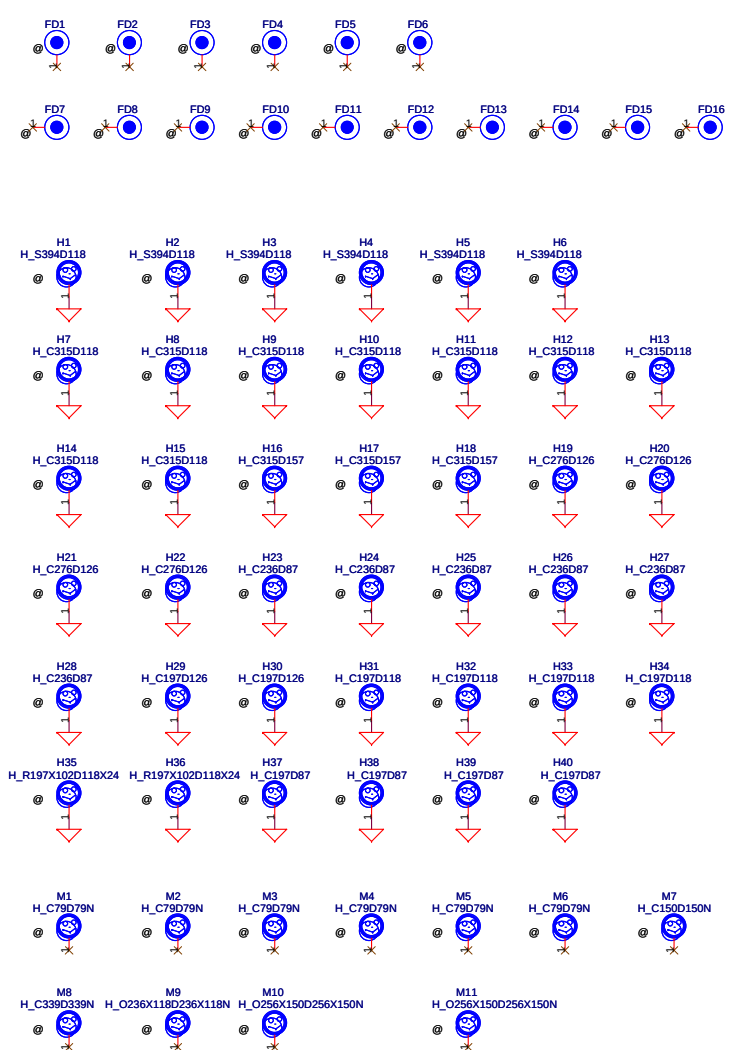
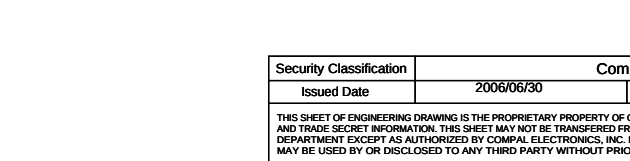
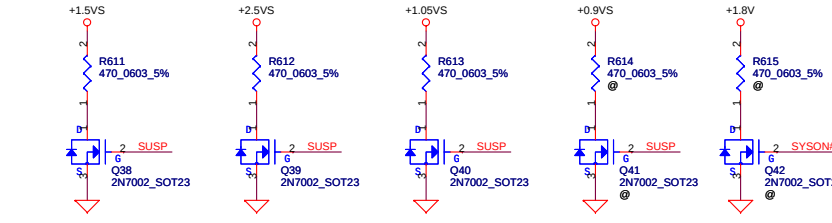
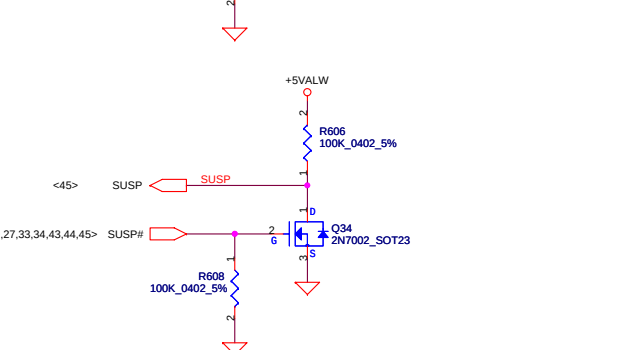
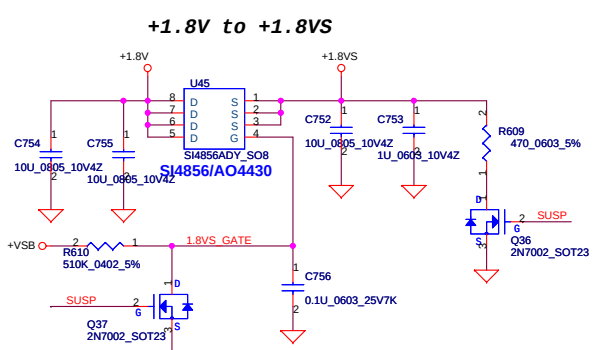
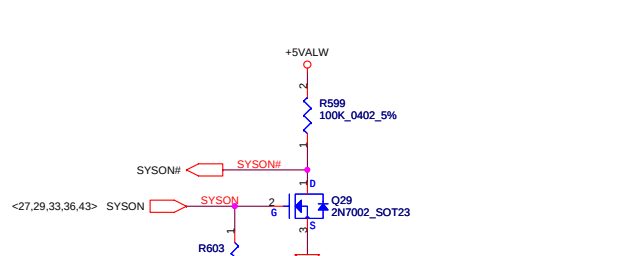
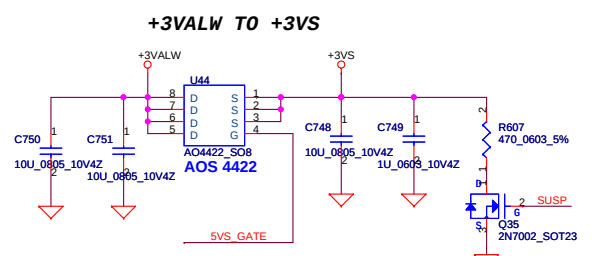
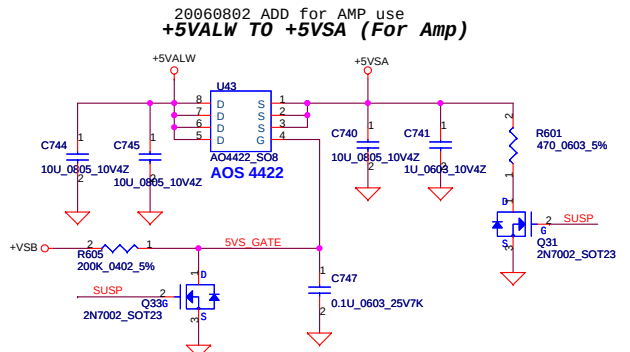
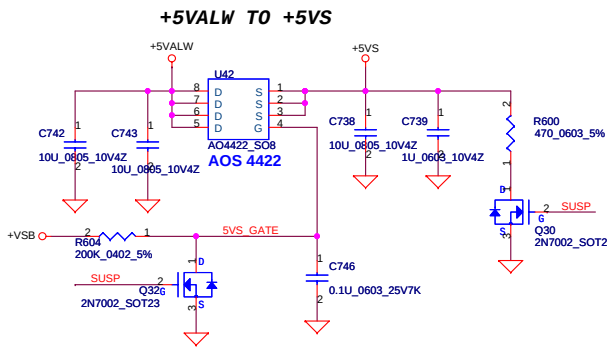
21 22 LPC_AD[0..21] LPC_AD[0..3]



Add in 08/11.

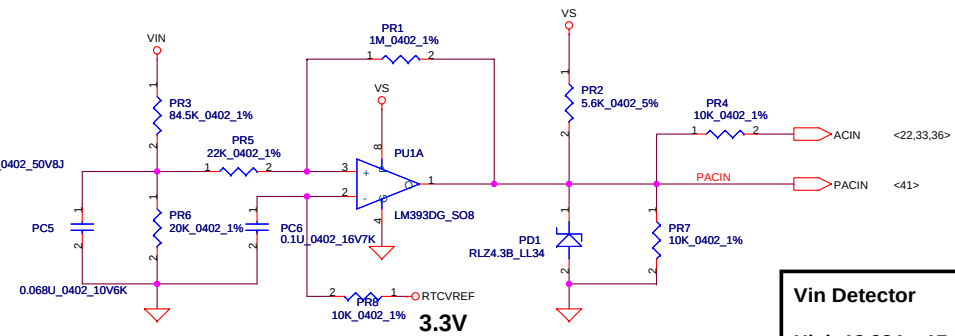
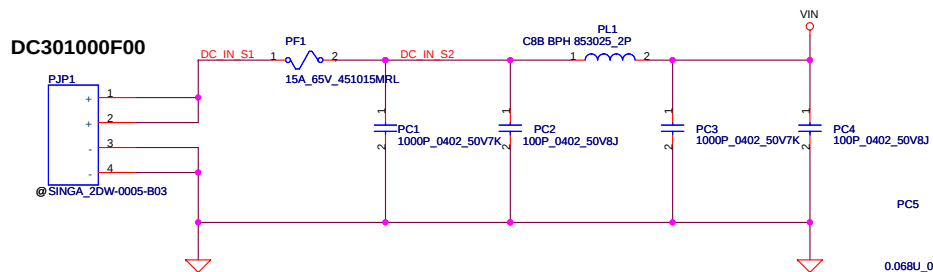


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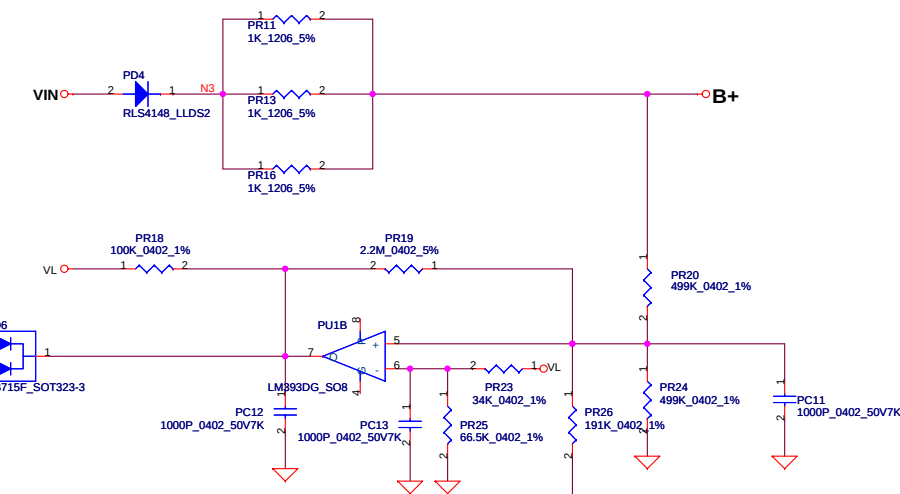
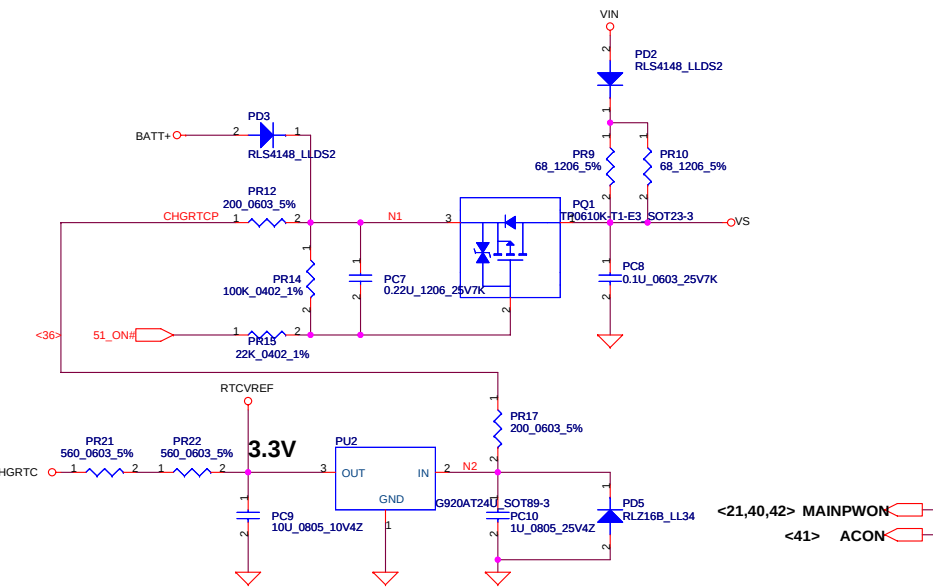


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Issued Date	2006/06/30	Deciphered Date	2007/06/30	DC/DC I/F & Screw	
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				ISRAA LA-3441P	Rev 0.1
				Date:	Friday, September 22, 2006
				Sheet	38 of 48

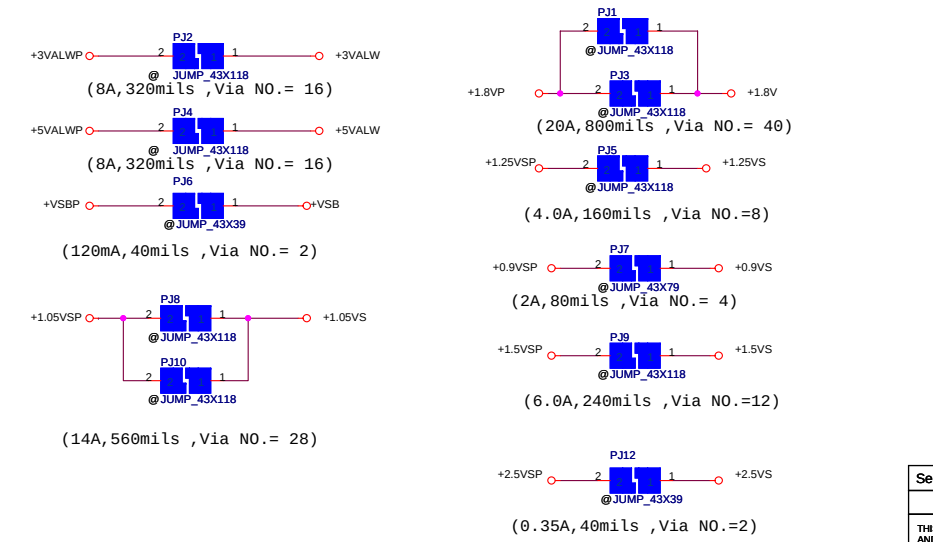
DC301000F00



Vin Detector			
High	18.384	17.901	17.430
Low	17.728	17.257	16.976



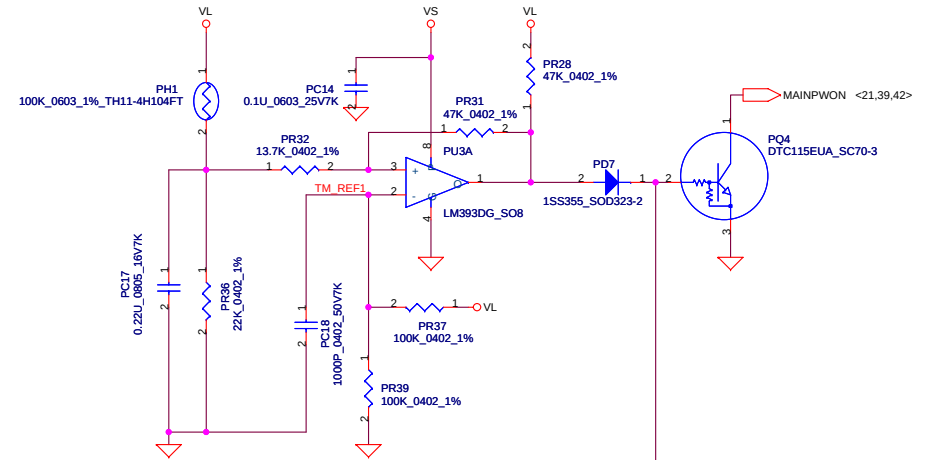
**Precharge detector
15.97V/14.84V FOR
ADAPTOR**



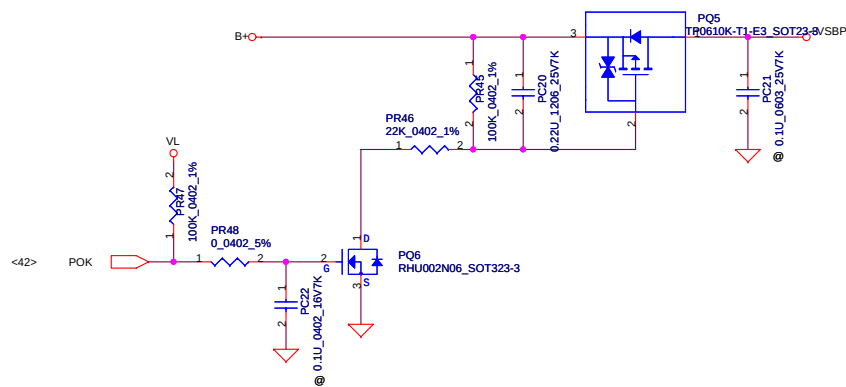
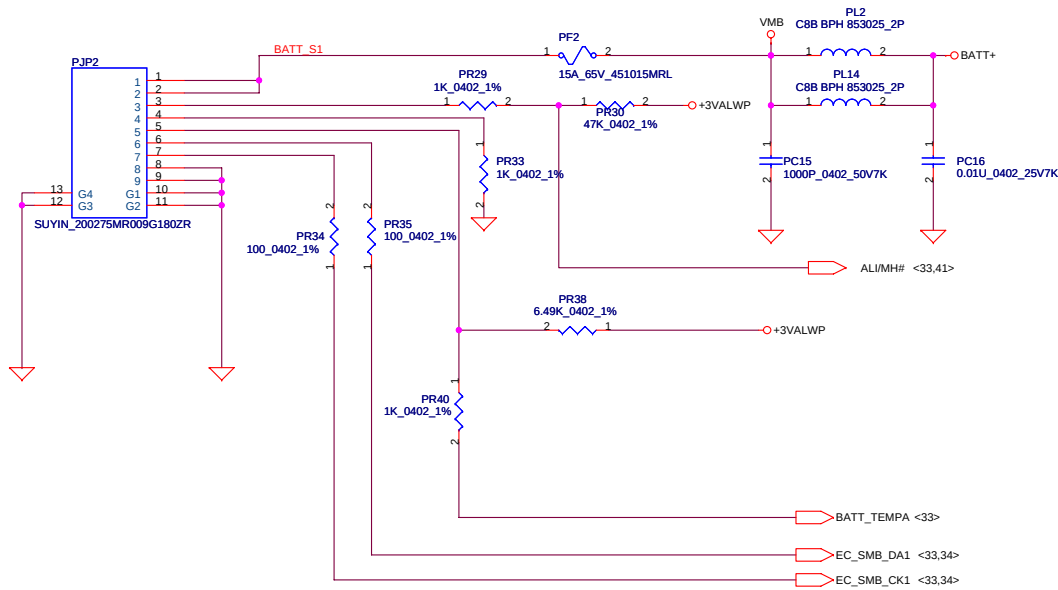
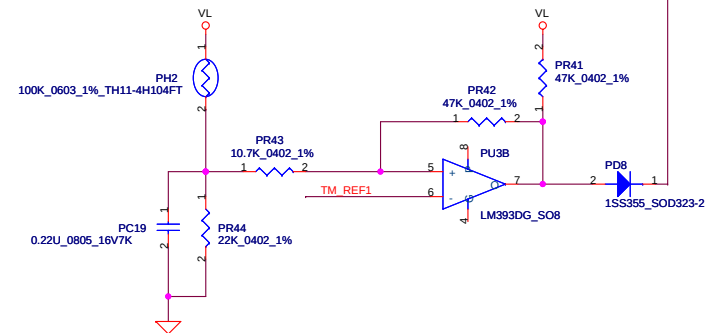
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2006/05/18		Deciphered Date		2007/05/18		Title	
										DCIN & DETECTOR	
										Size Document Number	
										Rev 0.1	
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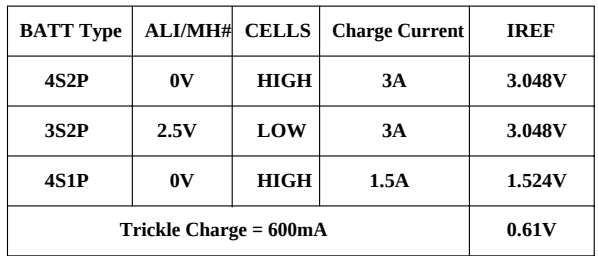
PH1 under CPU botten side :
CPU thermal protection at 84 degree C
Recovery at 45 degree C



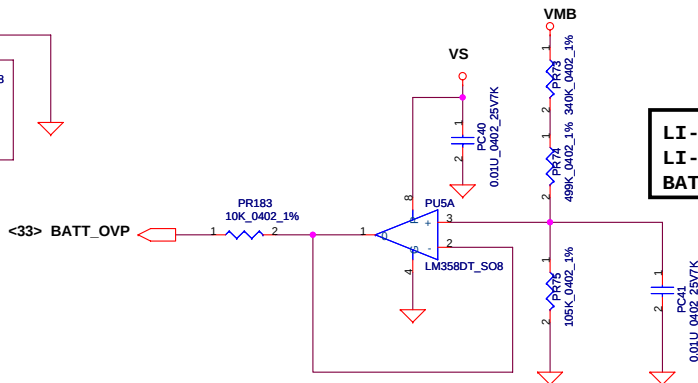
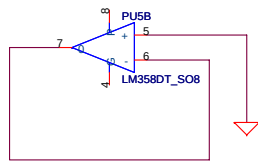
PH2 near main Battery CONN :
BAT. thermal protection at 79 degree C
Recovery at 45 degree C



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Size	Document Number			Rev 0.1	
Date:	Friday, September 22, 2006	Sheet	40	of	48

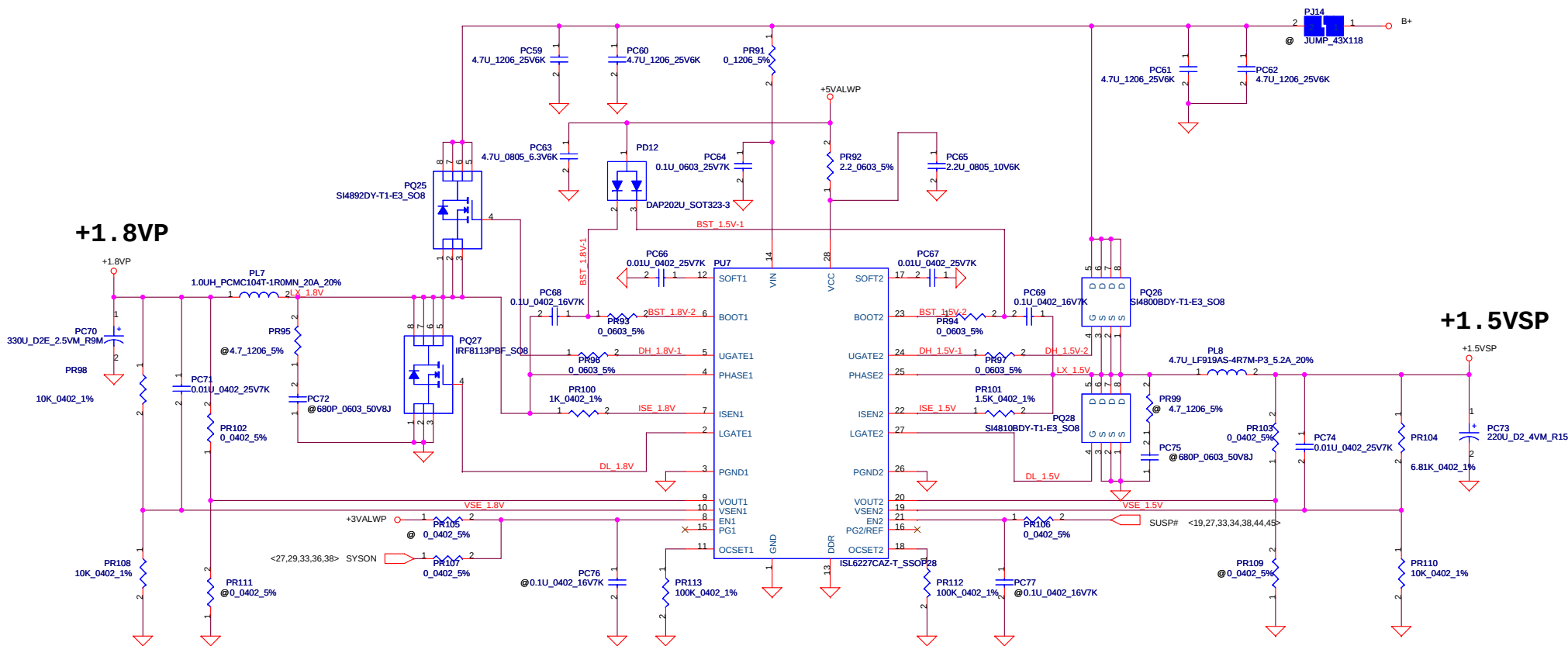


BATT Type	Charging Voltage (0x15)	ALI/MH#	CHGSEL	CV mode
2800mAH 4S pack	17400mV	LOW	LOW	17.20V
2800mAH 3S pack	13050mV	HIGH	LOW	12.90V
Normal 4S LI-ON Cells	16800mV	LOW	HIGH	16.80V
Normal 3S LI-ON Cells	12600mV	HIGH	HIGH	12.60V
Wake up charge while no communication	-	HIGH	HIGH	12.60V

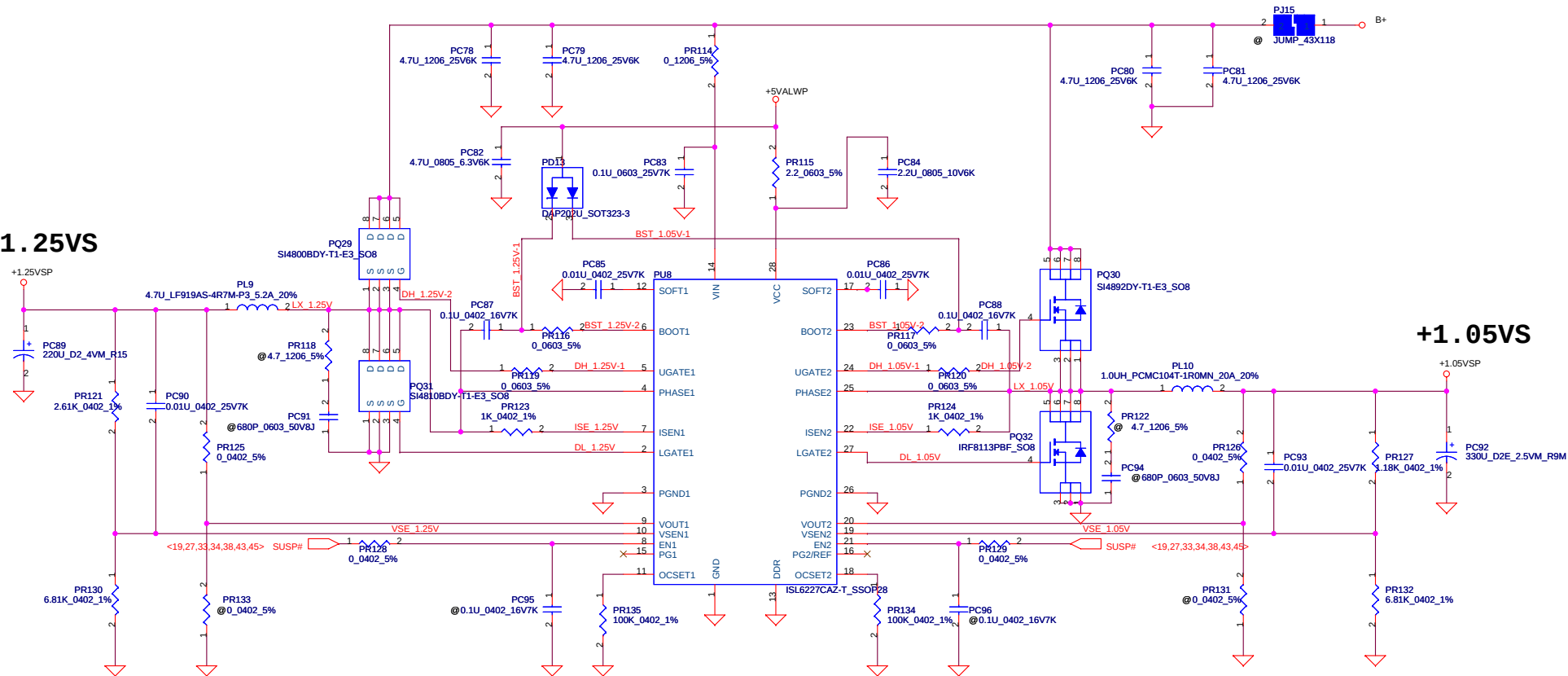


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Compal Electronics, Inc.			
Title		CHARGER	
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HW4 Product Improvement Record (P.I.R.)

Phase: A to B		Date: 2006/01/04		Writer: Timo Teng			
Page #	Action Plan (add; del; change)	Location or Net_List	Before value (Attached file)	After value (Attached file)	Detail Discretion and Root Cause	Rev.	DL/DM Check

LAN

U20

RTL8101E
100M@

TRANSFORMER

U22

TST1284-LF
100M@

PCB

Z771

PCB LA-3441P REV0

NB

U3

965GM
GMR3@

U3

965GM
GMR1@

U3

965PM
PMR1@

SKU ID

R547

8.2K_0402_5%
PM@

DC - JACK

PJP1

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45@

8412

U15

T18412
8412@

8402

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T18402
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