

Compal confidential

Schematics Document
Mobile Yonah uFCPGA with Intel
Calistoga_PM+ICH7-M core logic
2005-06-01
REV:0.1

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	SCHEMATIC, M/B LA-3011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	401395
Date: 星期一, 六月 01, 2005				Sheet	1 of 48
				Rev	A

Compal confidential

Model:HAQAA

File Name : LA-3011

NAPA

Mobile Yonah

uFCPGA-479 CPU

page 4, 5, 6

Thermal Sensor
ADM1032AR

page 4

Clock Generator
ICS9LPRS325AKLFT

page 15

Accelerometer
KXP84-0200

page 28

Intel Calistoga MCH
945PM
PCBGA 1466
page 7, 8, 9, 10, 11, 12

H_A#(3...31)

FSB

533/667MHz

H_D#(0...63)

DDR2 -400/533/667

Dual Channel

DDR2-SO-DIMM X2
BANK 0, 1, 2, 3

page 13, 14

PCI-E x 16

LCD CONN

page 17

nVIDIA G72/G73
ATI M52/M54

page 18

CRT / TV-OUT

page 16

PCI-E BUS

(port 1)

10/100/1000 LAN
82562GX/82573E/82573V

page 26, 27

Mini Express Card

page 28

RJ45/11 CONN

page 26

PCI BUS

CardBus Controller
TI PCI7412

page 22, 23

Slot 0
page 23

1394 port
page 22

5in1 Slot
page 22

DMI

Intel ICH7-M

mBGA-652

page 19, 20, 21, 22

USB2.0

Azalia

Audio CKT
ALC861

page 29

AMP & Audio Jack
TPA0232

page 30

MDC1.5

page 37

USB conn x1
(Docking)

page 34

(port 6)

USB2.0 HUB /
FP Conn

page 38

(port 0)

FingerPrinter UPEK TCS3C/TCD42A
USBx1

page 38

USB conn x2

page 24

(port 4, 5)

BT Conn

page 24

(port 7)

USB conn x2
(Sub Board)

page 24

(port 2, 3)

LPC BUS

TPM1.2
SLB9635TT1.2

page 38

Super I/O
LPC47N217&207

page 33, 38

FIR

page 33

ENE KB910QF

page 25

CIR

page 33

Touch Pad

page 37

Int.KBD

page 37

BIOS

page 35

LED

page 36

RTC CKT.

page 19

Docking

page 34

Power On/Off CKT.

page 37

DC/DC Interface CKT.

page 39

Power Circuit DC/DC

Page, 40, 41, 42, 43, 44, 45, 46

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title
				SCHEMATIC, M/B LA-3011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size
				Custom
				Document Number
				401395
				Rev
				A
				Date
				~P~: 01.2005
				Sheet
				2 of 48

Voltage Rails

Power Plane	Description	S0-S1	S3	S5
VIN	Adapter power supply (18.5V)	NA	NA	NA
B+	AC or battery power rail for power circuit	NA	NA	NA
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+VCCP	1.05V power rail for Processor I/O and MCH/I/CH core power	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDRII Vtt	ON	OFF	OFF
+1.5VS	1.5V switched power rail for PCI-E interface	ON	OFF	OFF
+1.8V	1.8V power rail for DDRII	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail for MCH video PLL	ON	OFF	OFF
+2.5VALW	3.3V always on power rail	ON	ON	ON*
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+RTC_VCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

Internal PCI Devices

DEVICE	Bus	PCI Device ID	IDSEL #
LAN	1	D8	AD24
Azalia	0	D27	AD11
PCI-E	0	D28	AD12
USB1.1/2.0	0	D29	AD13
PCI to PCI (DMI to PCI)	0	D30	AD14
AC97 MODEM	0	D30	AD14
AC97 Audio	0	D30	AD14
PATA/SATA	0	D31	AD15
LPC I/F	0	D31	AD15
SMBUS	0	D31	AD15
CPU I/F	0	D31	AD15
DMA	0	D31	AD15
PMU	0	D31	AD15

External PCI Devices

DEVICE	PCI Device ID	IDSEL #	REQ/GNT #	PIRQ
CARD BUS	D6	AD22	2	ABCD

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 1%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0.100 V
1	8.2K +/- 1%	0.216 V	0.250 V	0.289 V
2	18K +/- 1%	0.436 V	0.503 V	0.538 V
3	33K +/- 1%	0.712 V	0.819 V	0.875 V
4	56K +/- 1%	1.036 V	1.185 V	1.264 V
5	100K +/- 1%	1.453 V	1.650 V	1.759 V
6	200K +/- 1%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

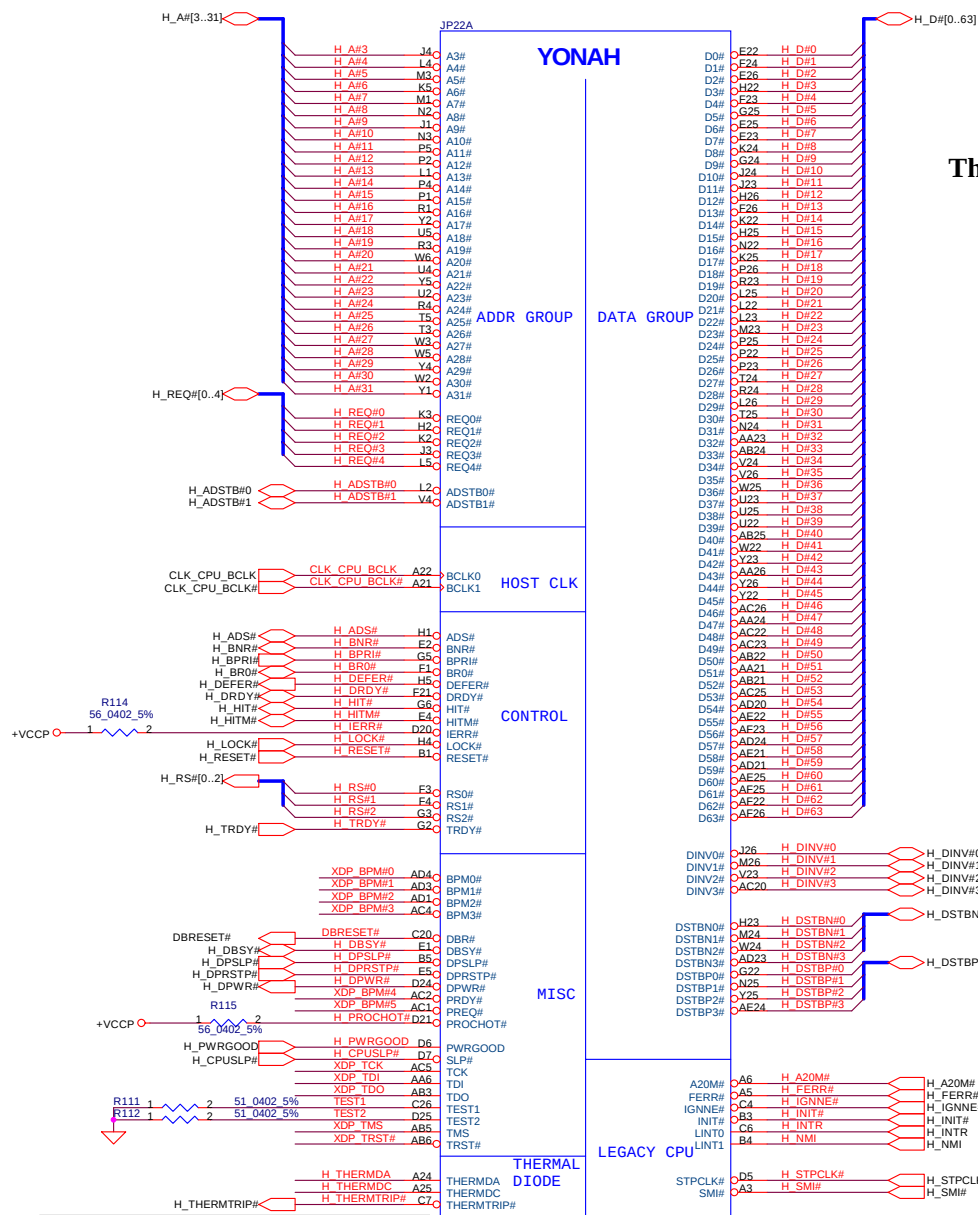
SKU ID Table

SKU ID	SKU
0	
1	
2	
3	
4	
5	
6	
7	

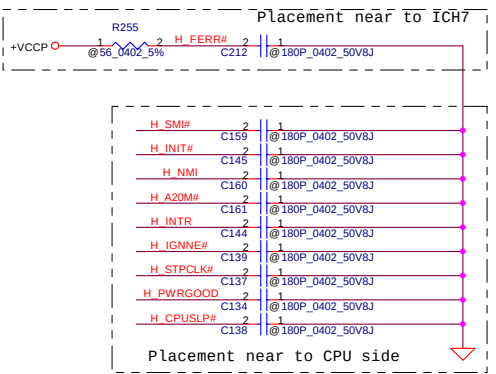
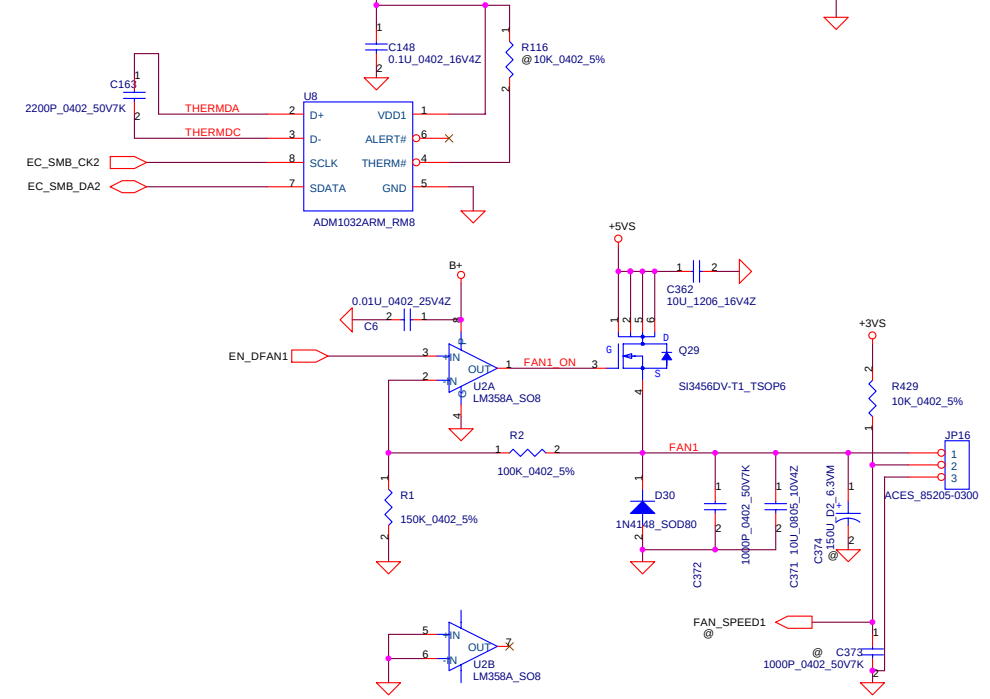
BTO Option Table

BTO Item	BOM Structure
VGA	PM@ GM@
LAN	82573G@ 82573L@ 82573@ 82562@
INT MIC.	45MIC@
DOCKING	WD@ WOD@
CIR	CIR@
FIR	FIR@
SUPER I/O	217@ SI207@
Battery	45@

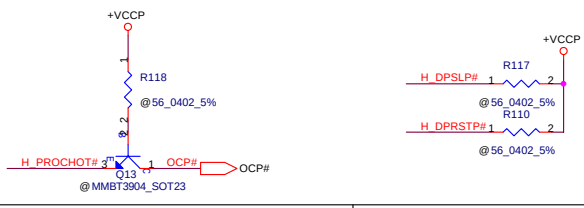
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					401395
				Date:	星期一, 八月 01, 2005
				Sheet	3 of 48
				Rev	A



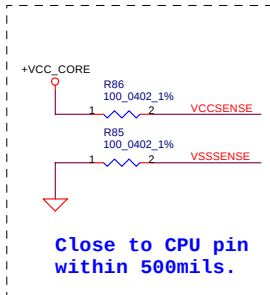
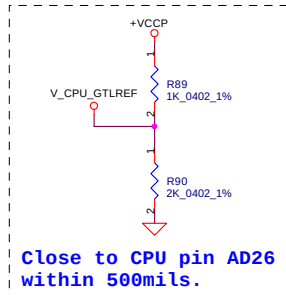
Thermal Sensor ADM1032ARM



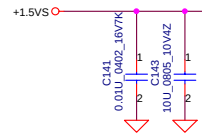
H_THERMDA, H_THERMDC routing together.
Trace width / Spacing = 10 / 10 mil



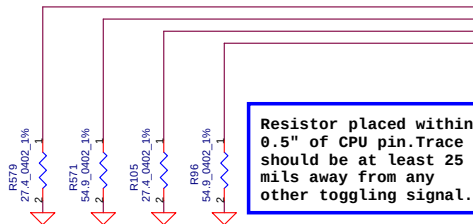
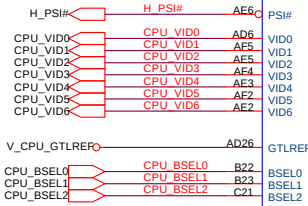
Security Classification		Compal Secret Data		Title	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					401395
				Date	Rev
				星期一, 六月 01, 2005	A
				Sheet	4 of 48



Length match within 25 mils
The trace width 18 mils space 7 mils



CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
133	0	0	1
166	0	1	1

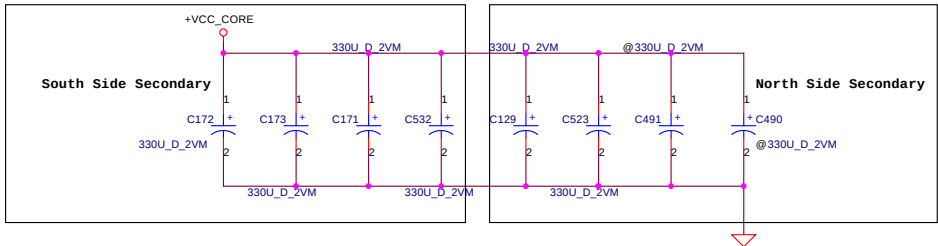
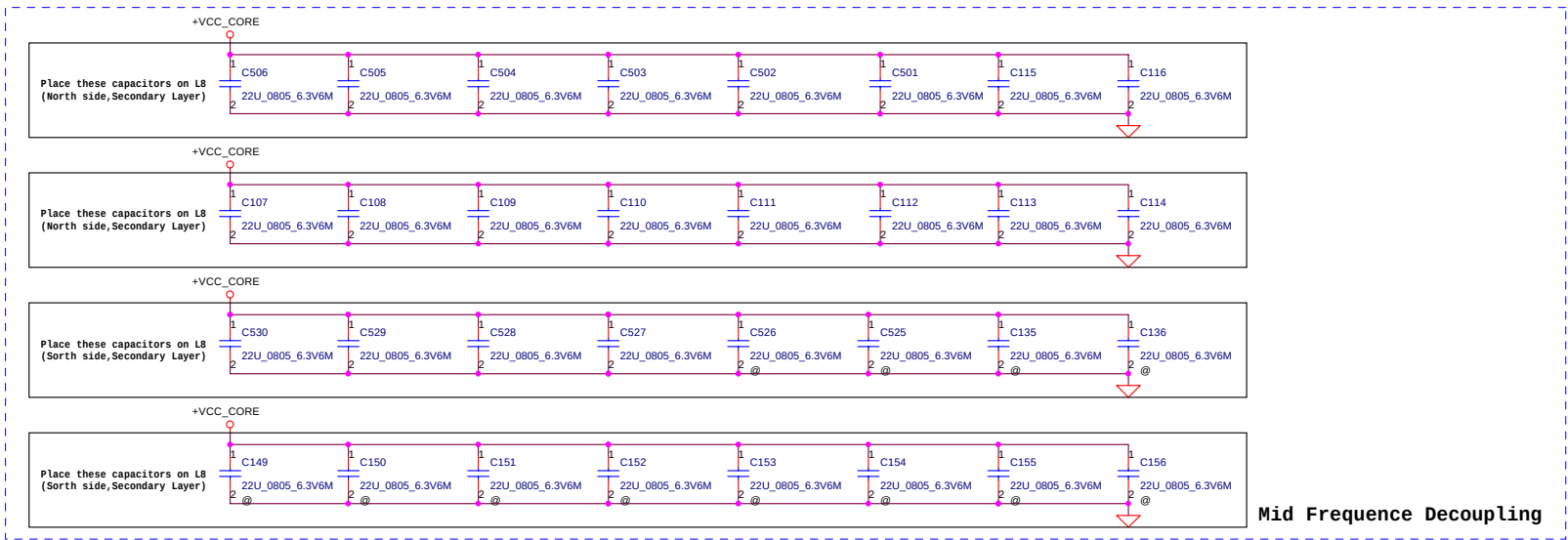


YONAH

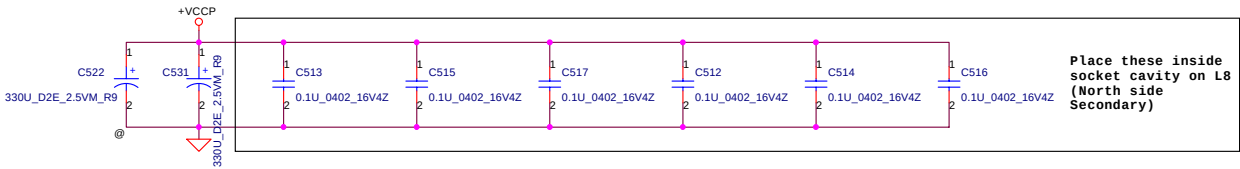
POWER, GROUND, RESERVED SIGNALS AND NC

YONAH

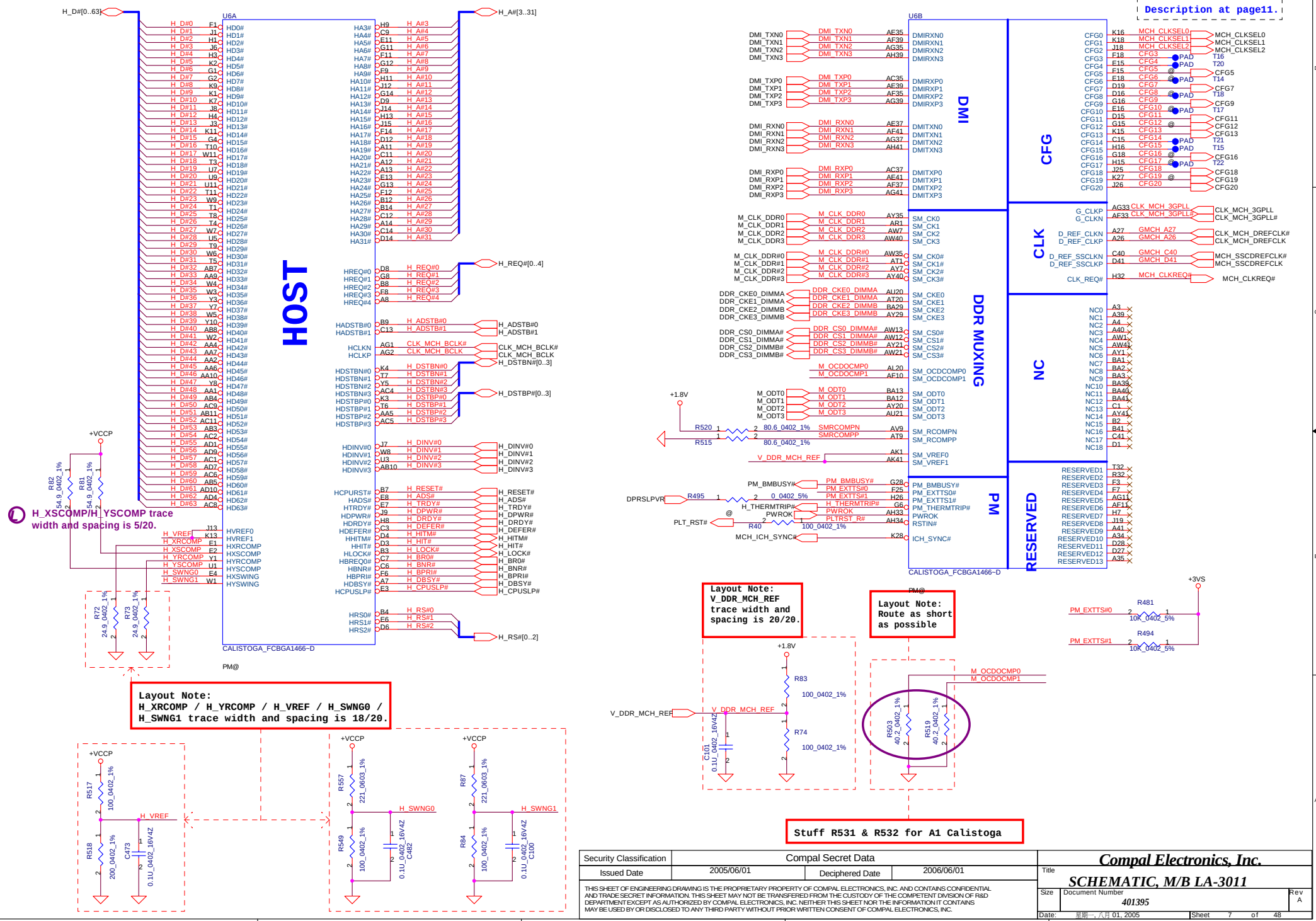
POWER, GROUND

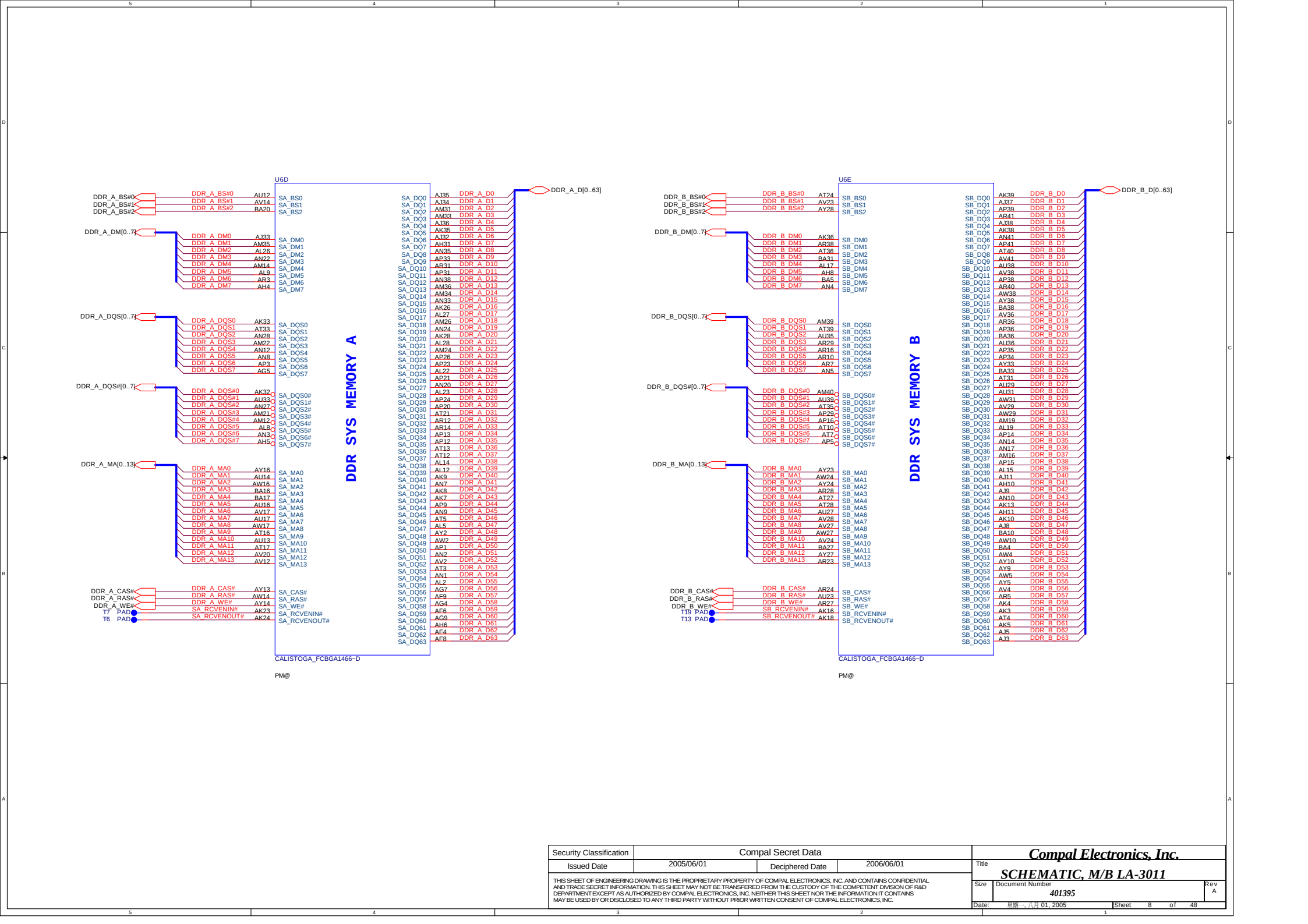


ESR <= 1.5m ohm
Capacitor > 1980uF

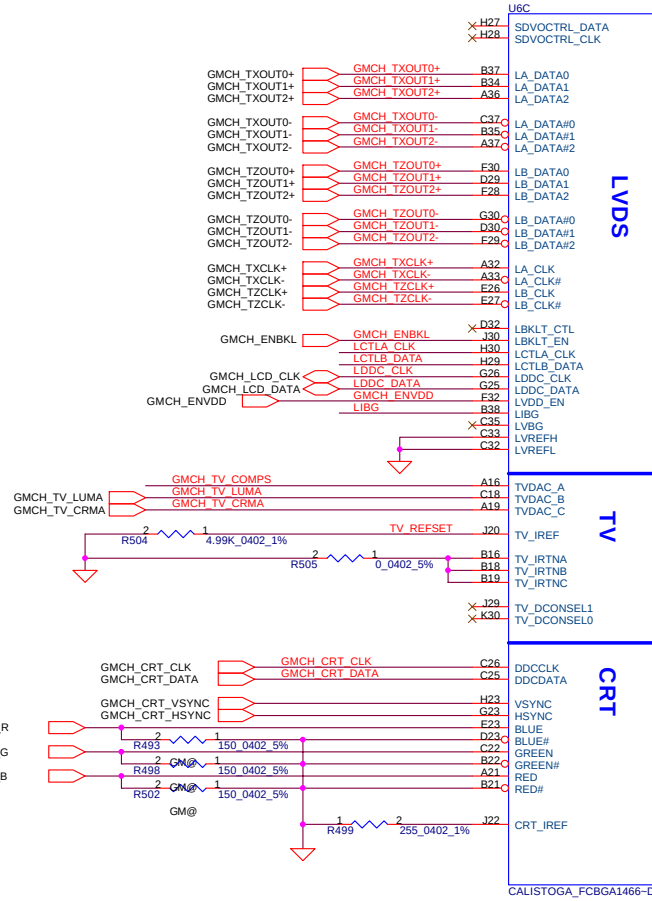
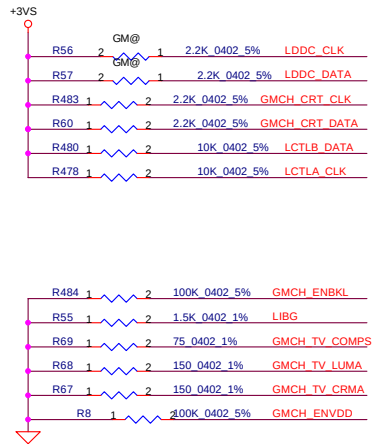


Security Classification		Compal Secret Data		Title	
Issued Date		Deciphered Date		Size	
2005/06/01		2006/06/01		Document Number	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		401395		Rev A	
Date: 星期一, 八月 01, 2005		Sheet 6 of 48			





Security Classification		Compal Secret Data		Title	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	SCHEMATIC, M/B LA-3011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				401395	Rev A
Date		星期一, 六月 01, 2005		Sheet	8 of 48



PEGCOMP trace width and spacing is 18/25 mils.

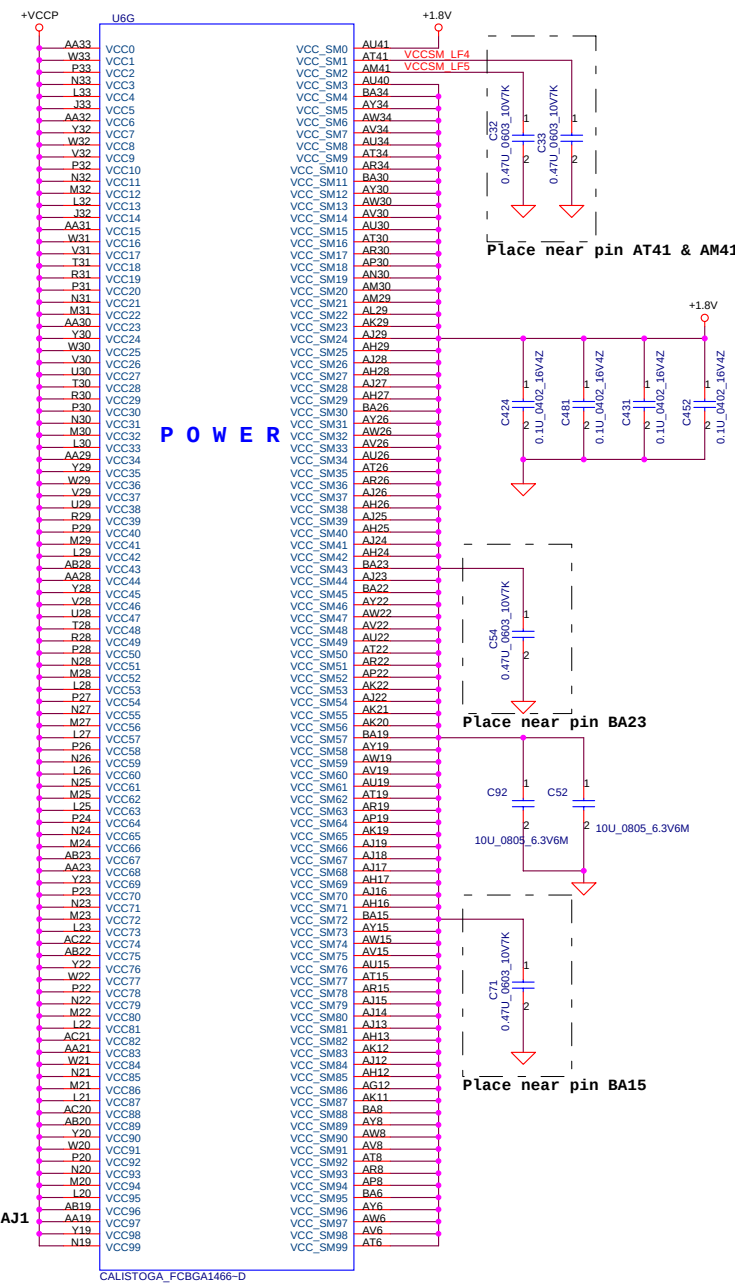
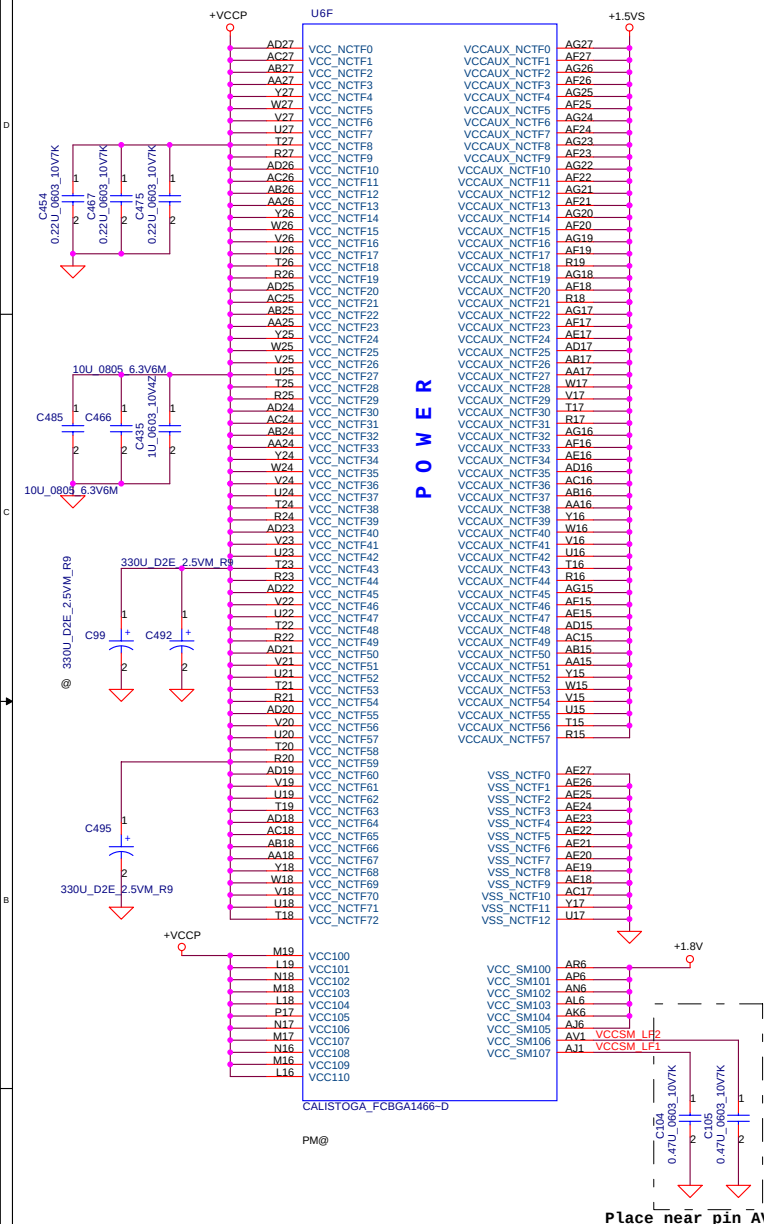
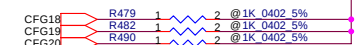
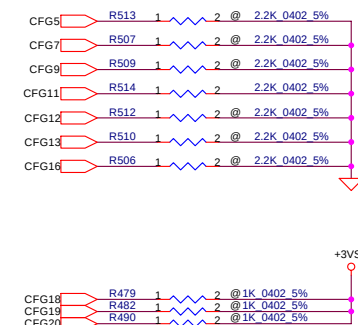
EXP_COMP1	D40	PEGCOMP	1	24.9_0402 1%	R472	+1.5VS_PCIE
EXP_COMP0	D38					
EXP_RXN0	F34	PCIE GTX C MRX N0				
EXP_RXN1	G38	PCIE GTX C MRX N1				
EXP_RXN2	H34	PCIE GTX C MRX N2				
EXP_RXN3	J38	PCIE GTX C MRX N3				
EXP_RXN4	L34	PCIE GTX C MRX N4				
EXP_RXN5	M38	PCIE GTX C MRX N5				
EXP_RXN6	N34	PCIE GTX C MRX N6				
EXP_RXN7	P38	PCIE GTX C MRX N7				
EXP_RXN8	R34	PCIE GTX C MRX N8				
EXP_RXN9	T38	PCIE GTX C MRX N9				
EXP_RXN10	V34	PCIE GTX C MRX N10				
EXP_RXN11	W38	PCIE GTX C MRX N11				
EXP_RXN12	Y34	PCIE GTX C MRX N12				
EXP_RXN13	AA38	PCIE GTX C MRX N13				
EXP_RXN14	AB34	PCIE GTX C MRX N14				
EXP_RXN15	AC38	PCIE GTX C MRX N15				
EXP_RXP0	D34	PCIE GTX C MRX P0				
EXP_RXP1	E38	PCIE GTX C MRX P1				
EXP_RXP2	F34	PCIE GTX C MRX P2				
EXP_RXP3	G38	PCIE GTX C MRX P3				
EXP_RXP4	H34	PCIE GTX C MRX P4				
EXP_RXP5	J38	PCIE GTX C MRX P5				
EXP_RXP6	M34	PCIE GTX C MRX P6				
EXP_RXP7	N38	PCIE GTX C MRX P7				
EXP_RXP8	P34	PCIE GTX C MRX P8				
EXP_RXP9	R38	PCIE GTX C MRX P9				
EXP_RXP10	T34	PCIE GTX C MRX P10				
EXP_RXP11	V38	PCIE GTX C MRX P11				
EXP_RXP12	W34	PCIE GTX C MRX P12				
EXP_RXP13	Y38	PCIE GTX C MRX P13				
EXP_RXP14	AA34	PCIE GTX C MRX P14				
EXP_RXP15	AB38	PCIE GTX C MRX P15				

EXP_TXN0	F36	C400	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N0
EXP_TXN1	G40	C36	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N1
EXP_TXN2	H36	C384	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N2
EXP_TXN3	J40	C25	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N3
EXP_TXN4	L36	C398	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N4
EXP_TXN5	M40	C38	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N5
EXP_TXN6	N36	C382	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N6
EXP_TXN7	P40	C27	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N7
EXP_TXN8	R36	C386	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N8
EXP_TXN9	T40	C40	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N9
EXP_TXN10	V36	C380	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N10
EXP_TXN11	W40	C29	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N11
EXP_TXN12	Y36	C384	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N12
EXP_TXN13	AA40	C42	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N13
EXP_TXN14	AB36	C387	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N14
EXP_TXN15	AC40	C31	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX N15
EXP_TXP0	D36	C401	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P0
EXP_TXP1	E40	C35	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P1
EXP_TXP2	G36	C385	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P2
EXP_TXP3	H40	C24	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P3
EXP_TXP4	J36	C399	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P4
EXP_TXP5	L40	C37	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P5
EXP_TXP6	M36	C383	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P6
EXP_TXP7	N40	C26	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P7
EXP_TXP8	P36	C387	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P8
EXP_TXP9	R40	C39	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P9
EXP_TXP10	T36	C381	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P10
EXP_TXP11	V40	C28	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P11
EXP_TXP12	W36	C385	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P12
EXP_TXP13	Y40	C41	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P13
EXP_TXP14	AA36	C388	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P14
EXP_TXP15	AB40	C30	PM@ 0.1U 0402 16V7K	PCIE MTX C GRX P15

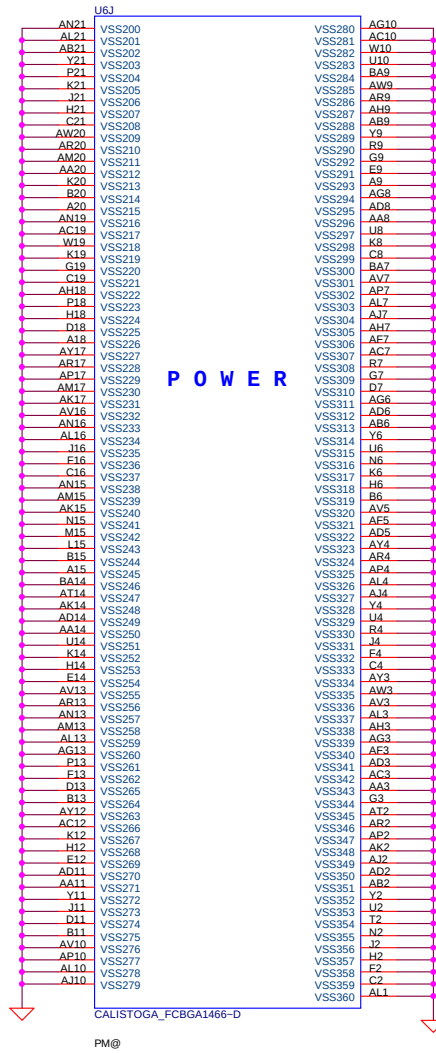
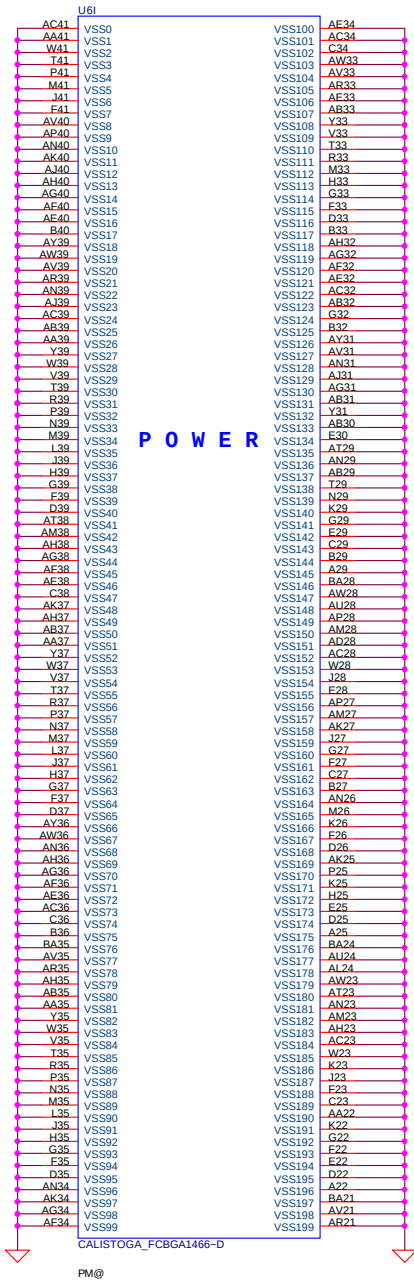
Strap Pin Table

CFG[3:17] have internal pull up
CFG[19:18] have internal pull down

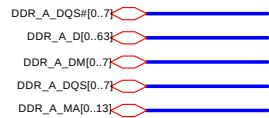
CFG[2:0]	011 = 667MT/s FSB 001 = 533MT/s FSB
CFG5	0 = DMI x 2 1 = DMI x 4 *(Default)
CFG7	0 = Reserved 1 = Mobile Yonah CPU *(Default)
CFG9	0 = Lane Reversal Enable 1 = Normal operation (Default) *
CFG11	0 = Calistoga * (According to Intel Napa Schematic Checklist & CRB Rev1.301 document 2.2Kohm pull-downs resistor is required) 1 = Reserved
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation *(Default)
CFG16	0 = Dynamic ODT Disabled 1 = Dynamic ODT Enabled *(Default)
CFG18	0 = 1.95V *(Default) 1 = 1.5V
CFG19	0 = Normal Operation *(Default) 1 = DMI Lane Reversal Enable
SDVO_CTRLDATA	0 = No SDVO Device Present *(Default) 1 = SDVO Device Present
CFG20 (PCIE/SDVO select)	0 = Only PCIE or SDVO is operational. *(Default) 1 = PCIE/SDVO are operating simu.



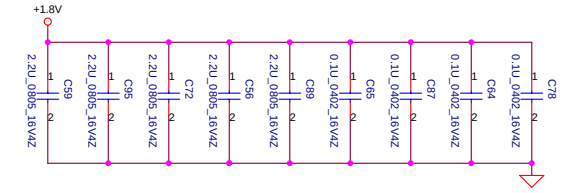
Security Classification				Compal Secret Data				Title			
Issued Date				2005/06/01				Deciphered Date			
2005/06/01				2006/06/01				Size			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				401395				Rev A			
Date: 星期一, 六月 01, 2005				Sheet 11 of 48							



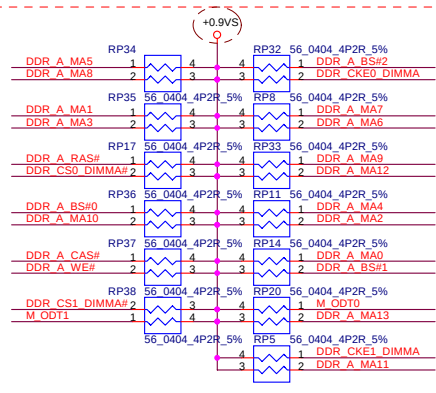
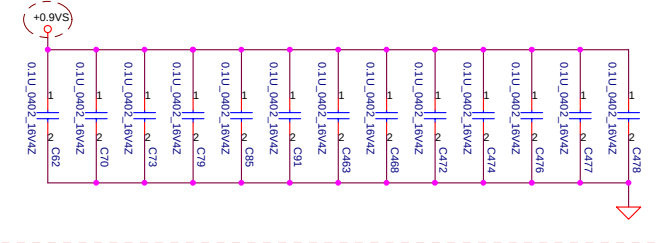
Security Classification		Compal Secret Data		Title	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Compal Electronics, Inc.	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				401395	Rev A
Date: 星期一, 八月 01, 2005				Sheet	12 of 48



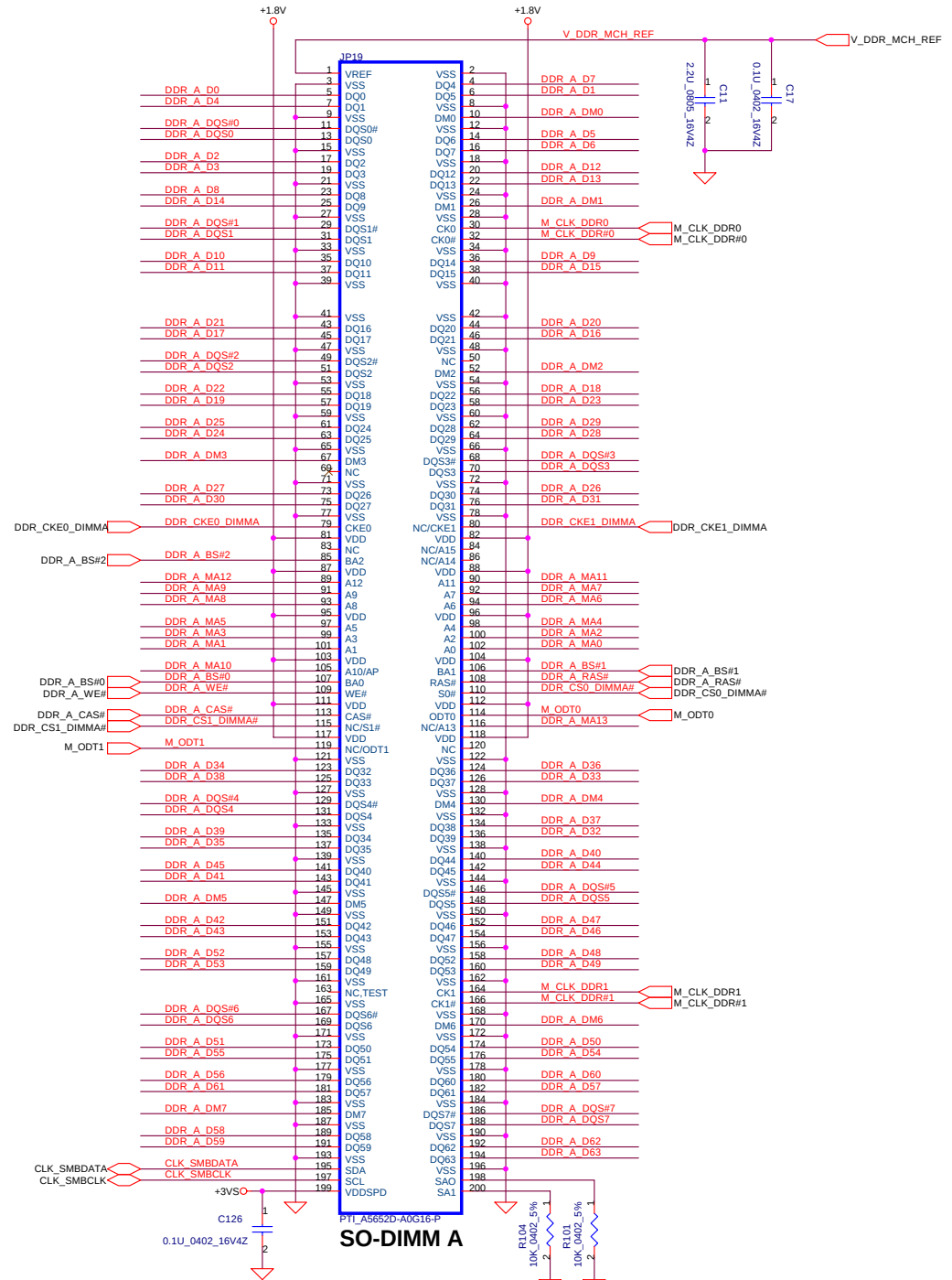
Layout Note:
Place near JP27



Layout Note:
Place one cap close to every 2 pullup
resistors terminated to +0.9VS

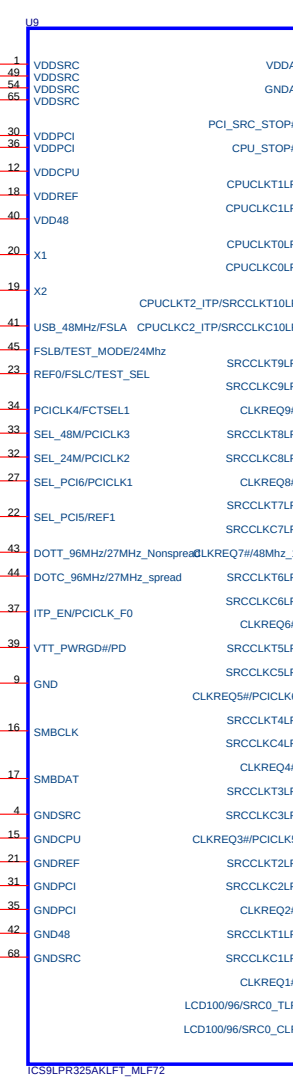


Layout Note:
Place these resistor
closely JP27,all
trace length Max=1.5"



Security Classification			Compal Secret Data		Title	
Issued Date	2005/06/01	Deciphered Date	2006/06/01		SCHEMATIC, M/B LA-3011	Rev A
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPANY OR DIVISION OR REPRODUCED OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					401395	Rev A
Date: 星期一, 八月 01, 2005					Sheet 13	of 48

Security Classification		Compal Secret Data		Compal Electronics, Inc. <u>SCHEMATIC, M/B LA-3011</u>	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					401395
					REV A
				Date	星期一, 八月 01, 2005 Sheet 14 of 48

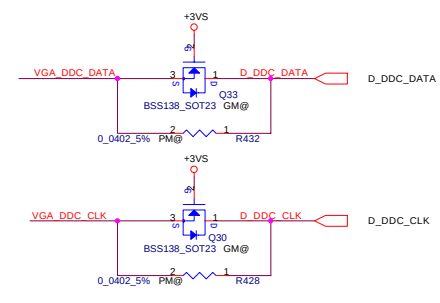
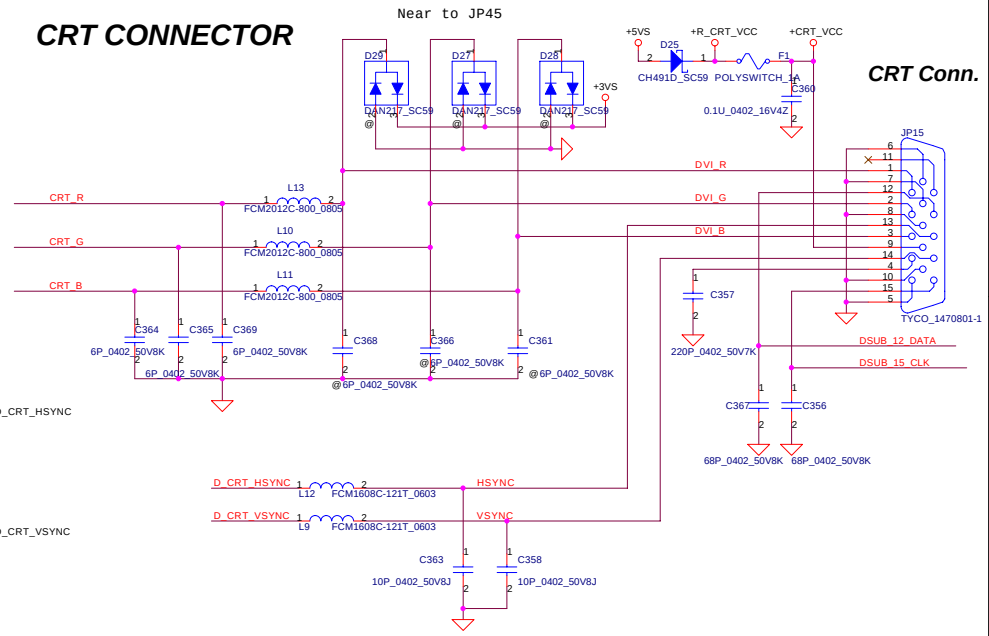
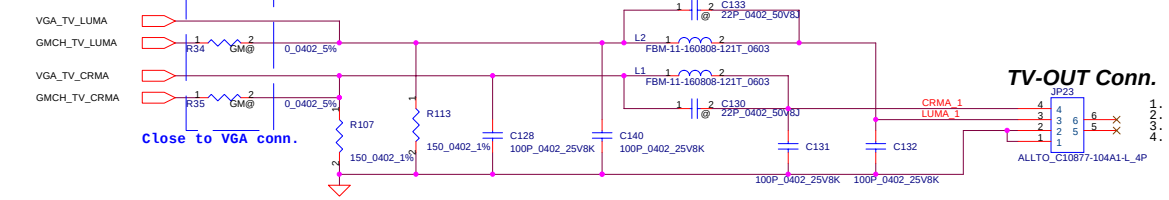
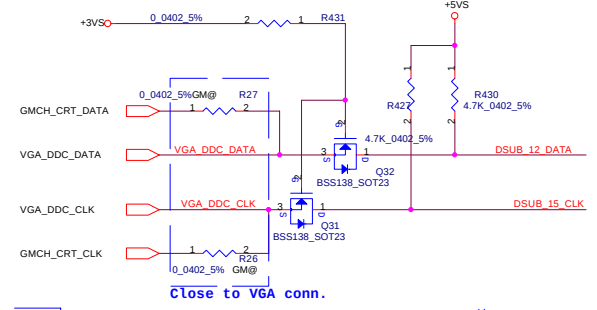
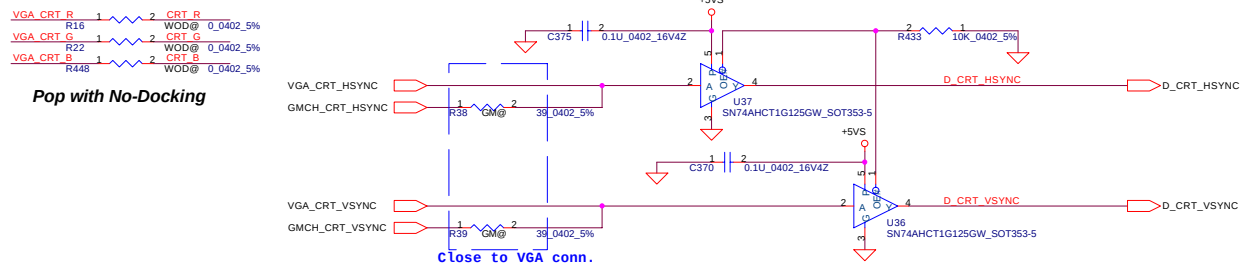
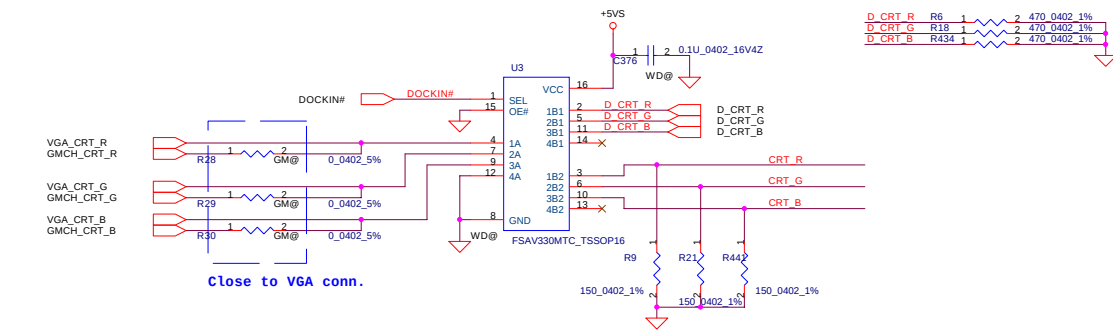


Reserve for ICS954305

Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, M/B LA-3011		
Issued Date	2005/06/01	Deciphered Date	2006/06/01			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev A
					401395	
				Date:	星期一, 八 201, 2005	Sheet

CRT Connector

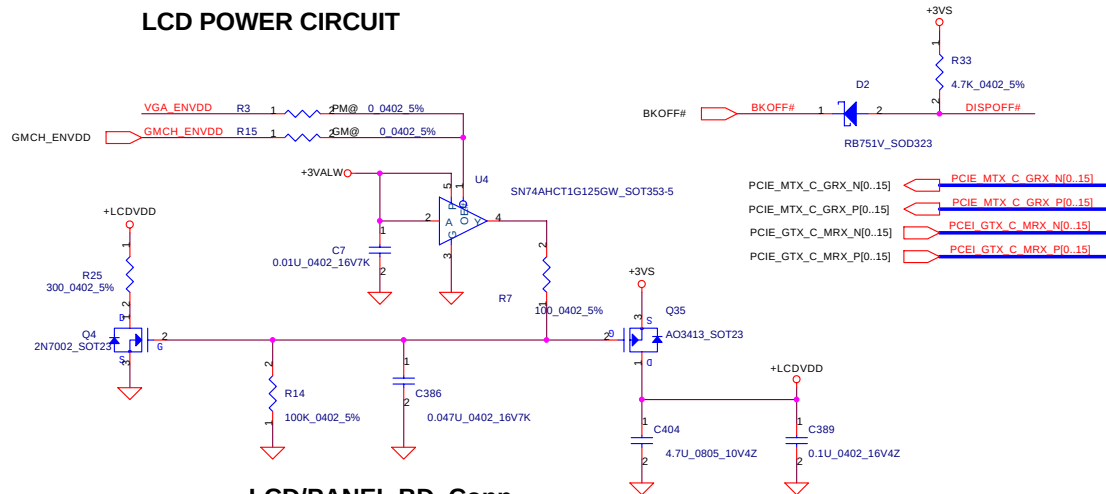
Match with Docking resistor to 150ohm



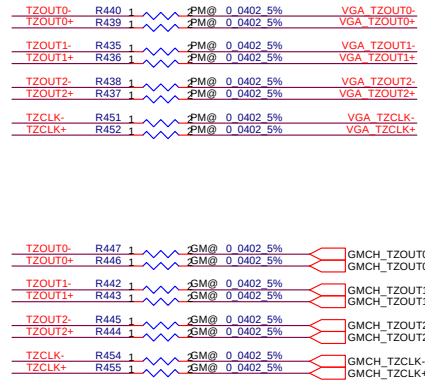
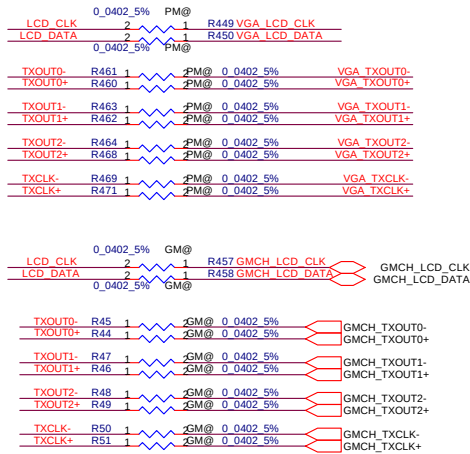
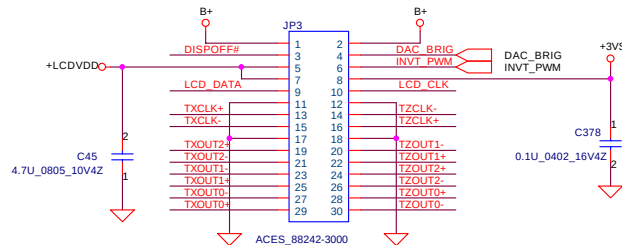
1. Y ground
2. C ground
3. Y (luminance+sync)
4. C (chrominance)

Security Classification	Compal Secret Data			Compal Electronics, Inc. SCHEMATIC, M/B LA-3011		
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev A
					401395	
				Date	星期五, 六月 01, 2005	Sheet 16 of 48

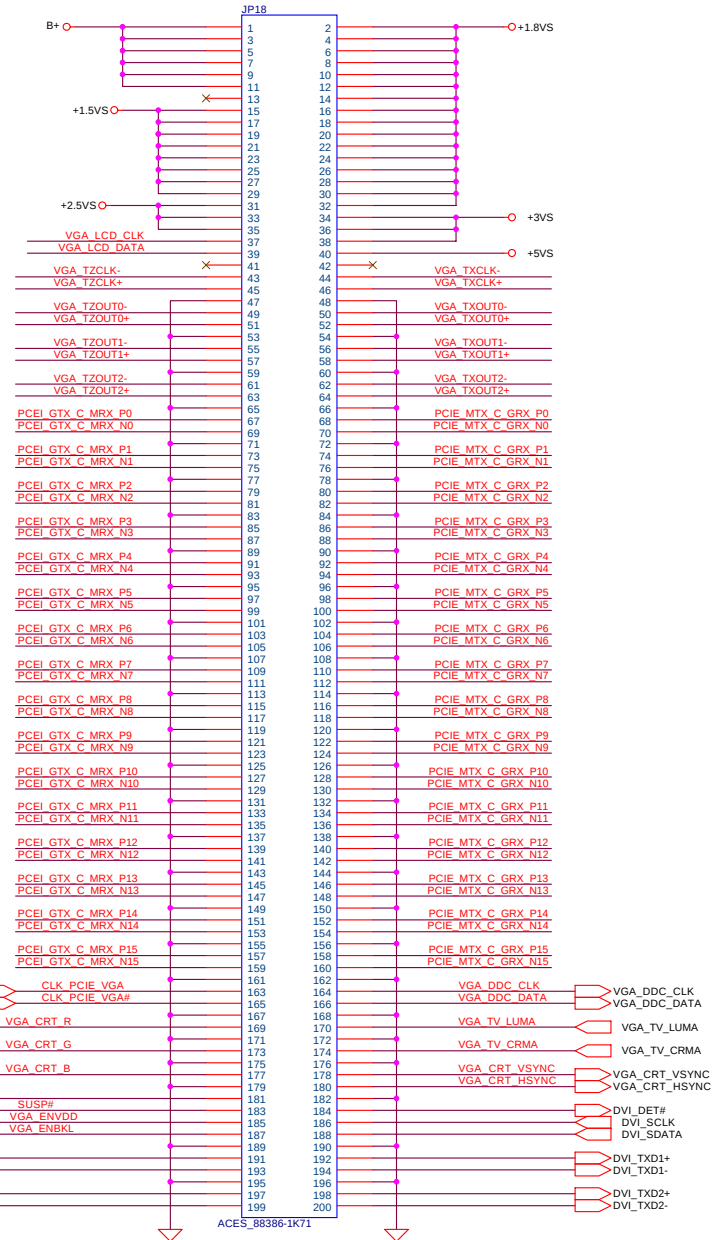
LCD POWER CIRCUIT



LCD/PANEL BD. Conn.



VGA BOARD Conn.



Security Classification			Compal Secret Data			Title		
Issued Date	2005/06/01	Deciphered Date	2006/06/01			Size	Document Number	Rev
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.						401395		A
						Date:	日期: 八月 01, 2005	Sheet 17 of 48

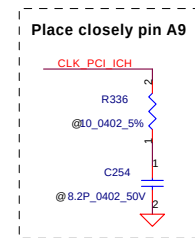
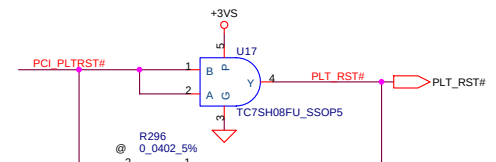
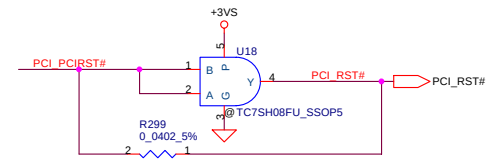
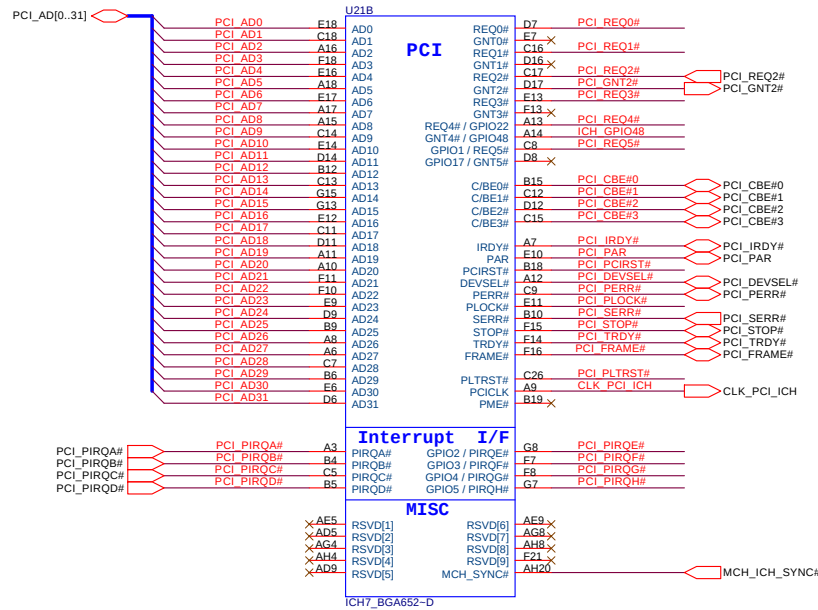
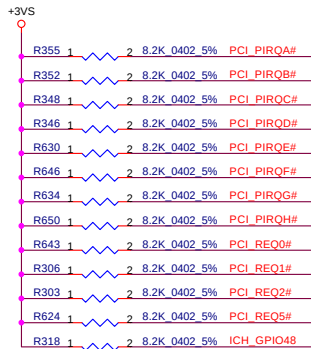
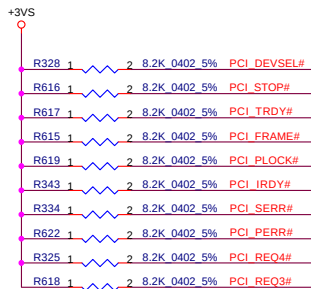
Compal Electronics, Inc.

SCHEMATIC, M/B LA-3011

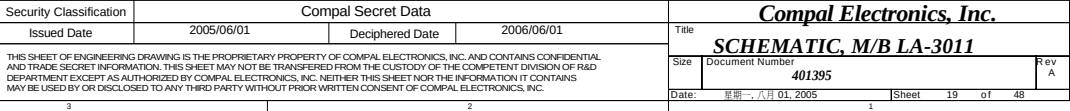
401395

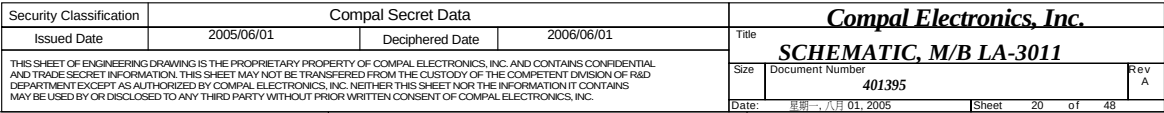
日期: 八月 01, 2005

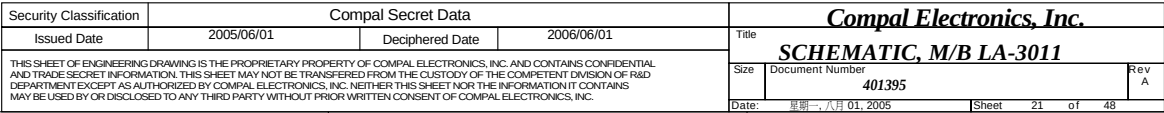
Sheet 17 of 48

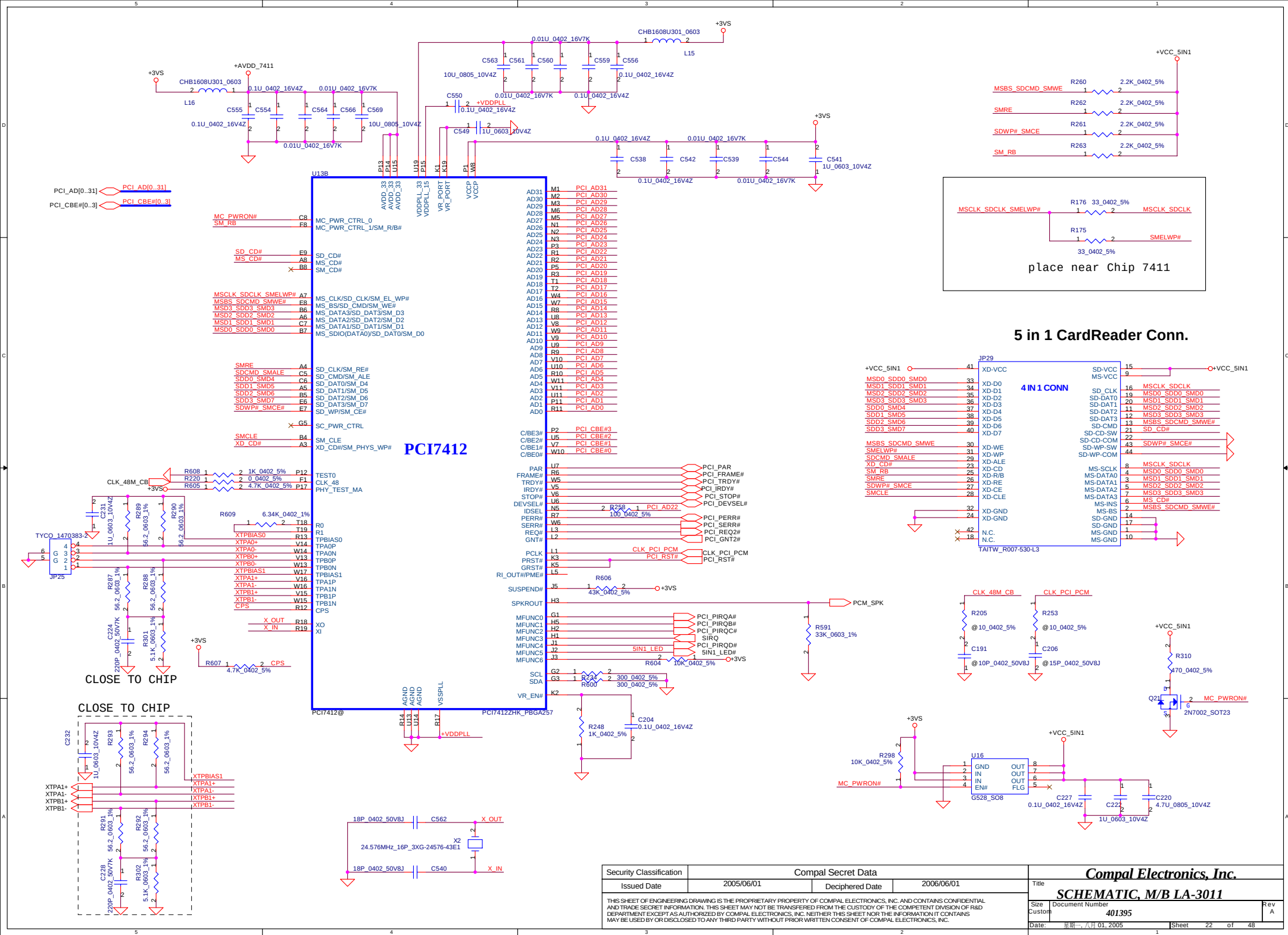


Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				401395	Rev A
Date:	星期一, 八月 01, 2005	Sheet	18	of	48

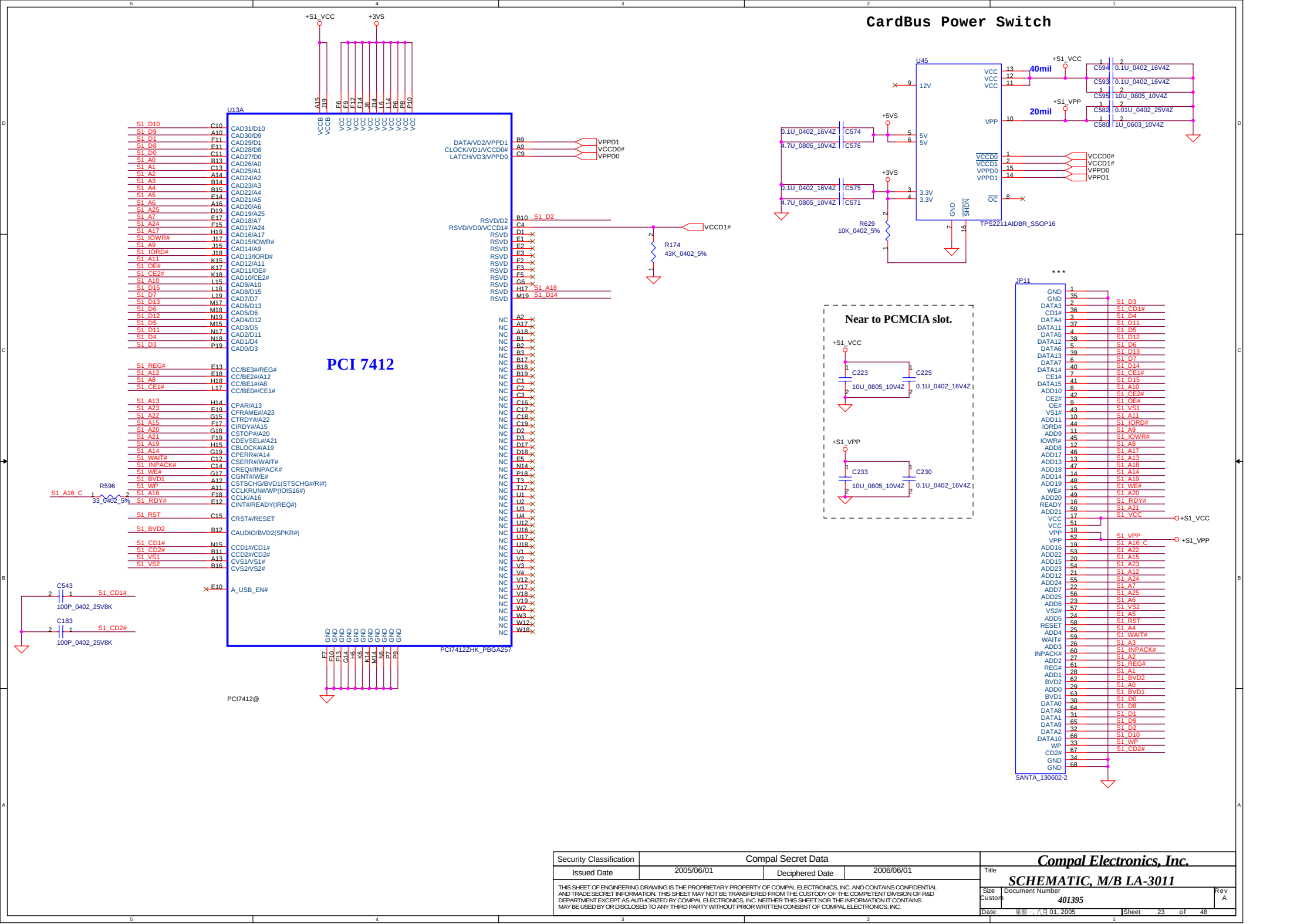


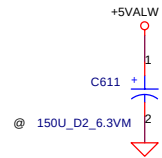




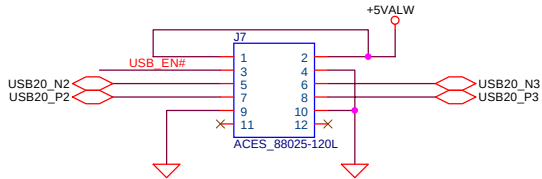


CardBus Power Switch

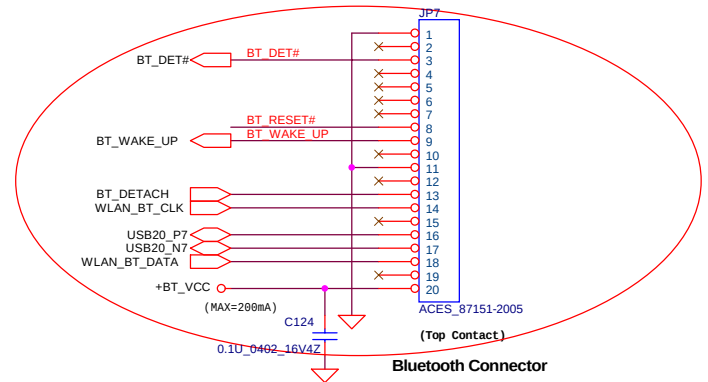
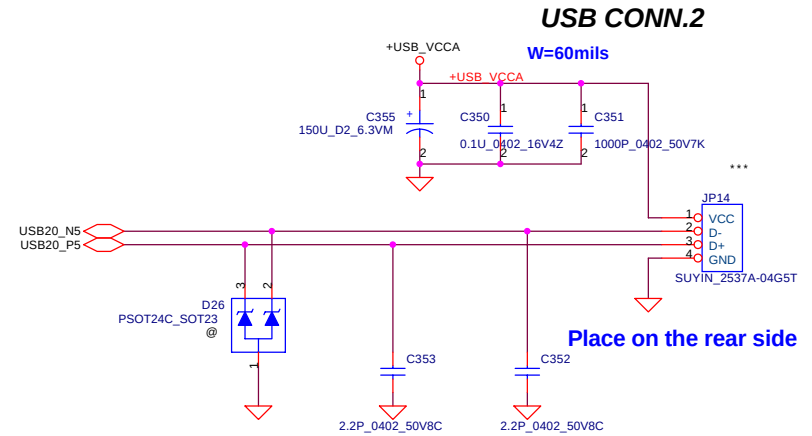
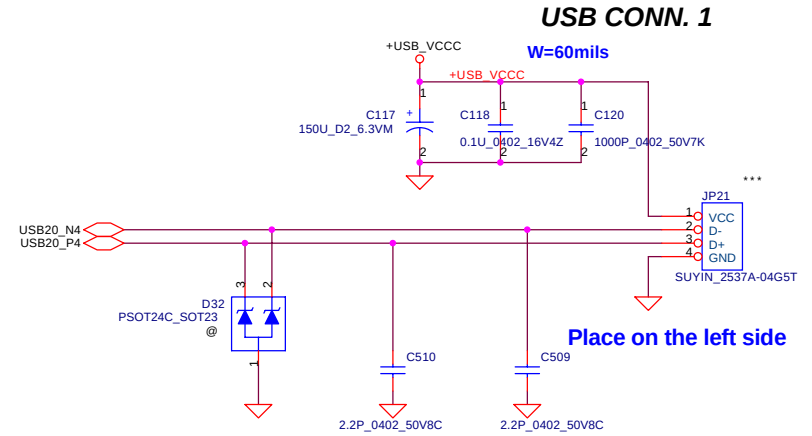
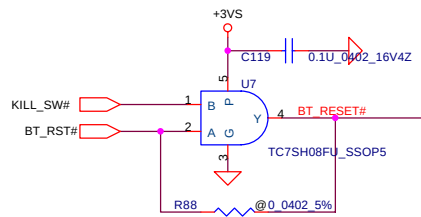
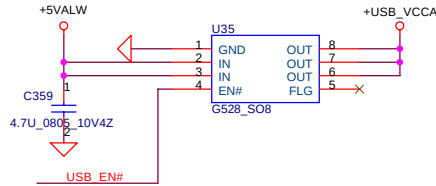
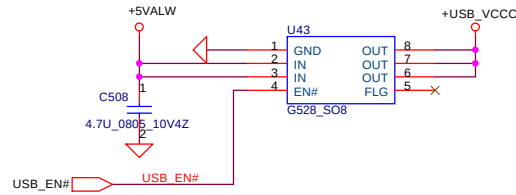
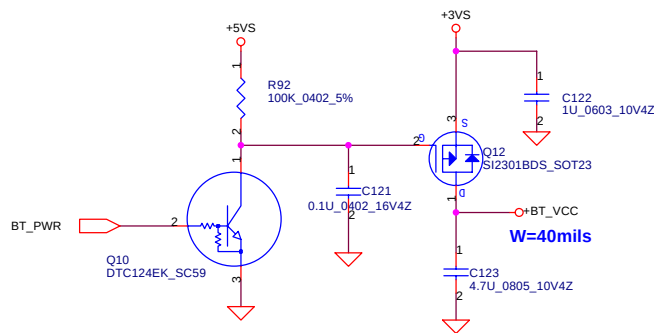




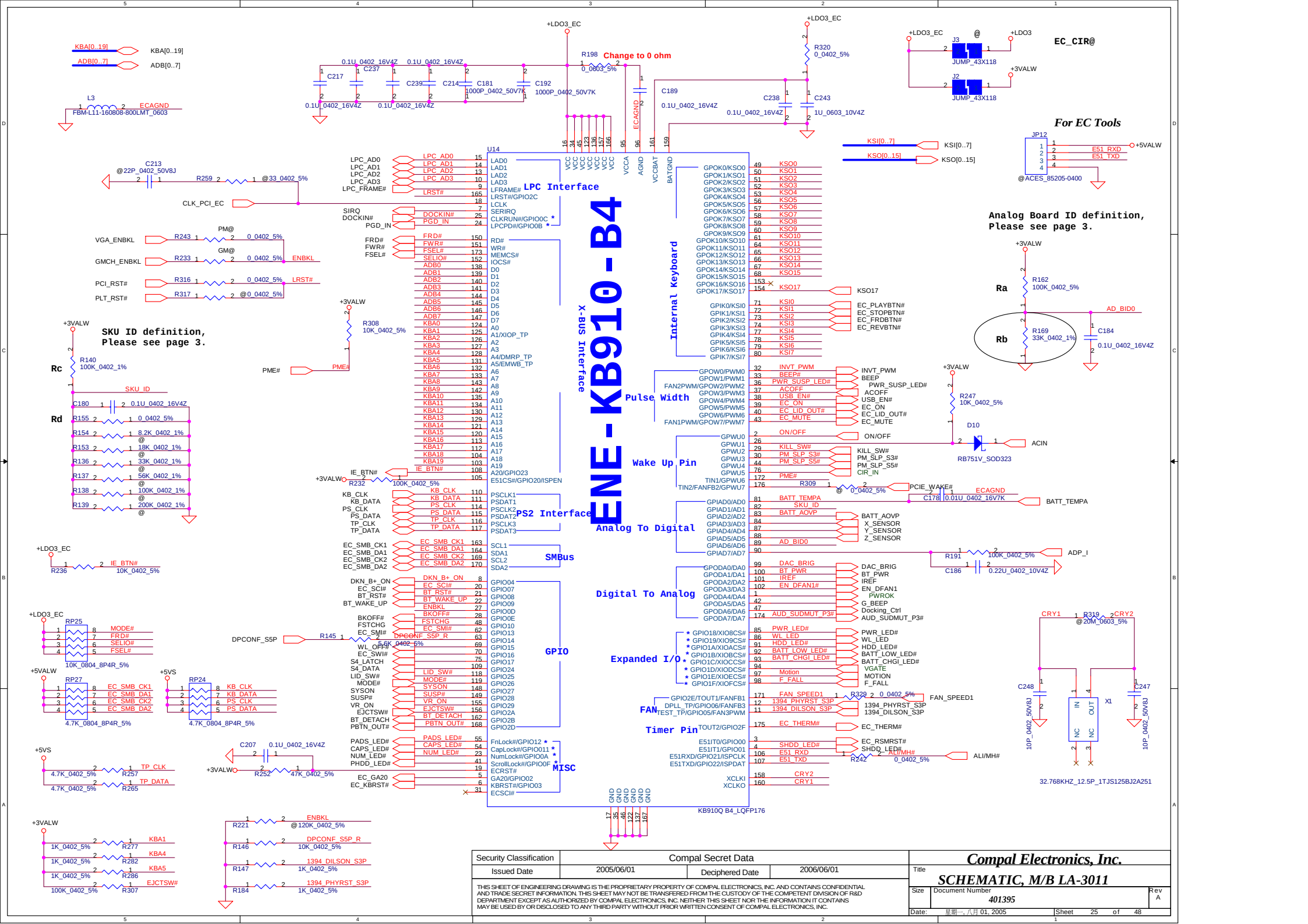
2 port in right side



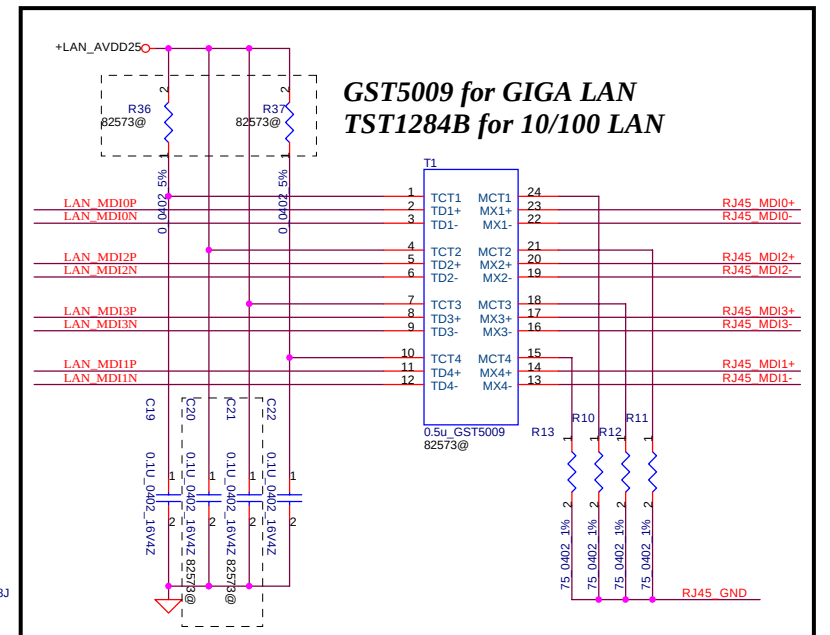
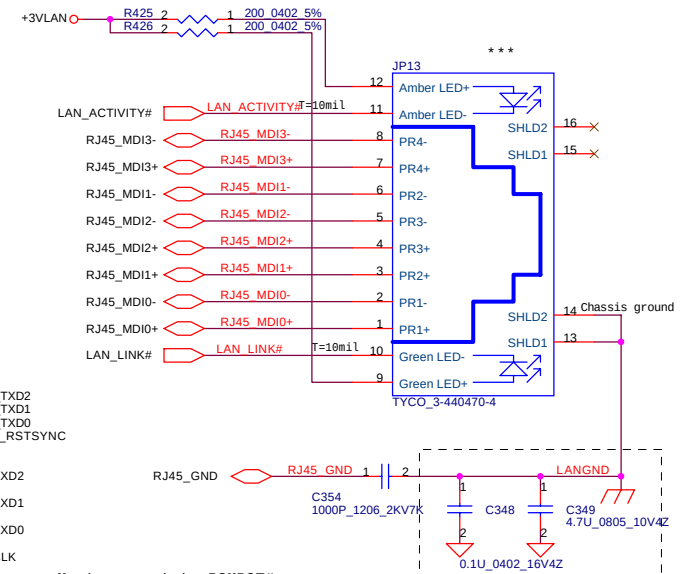
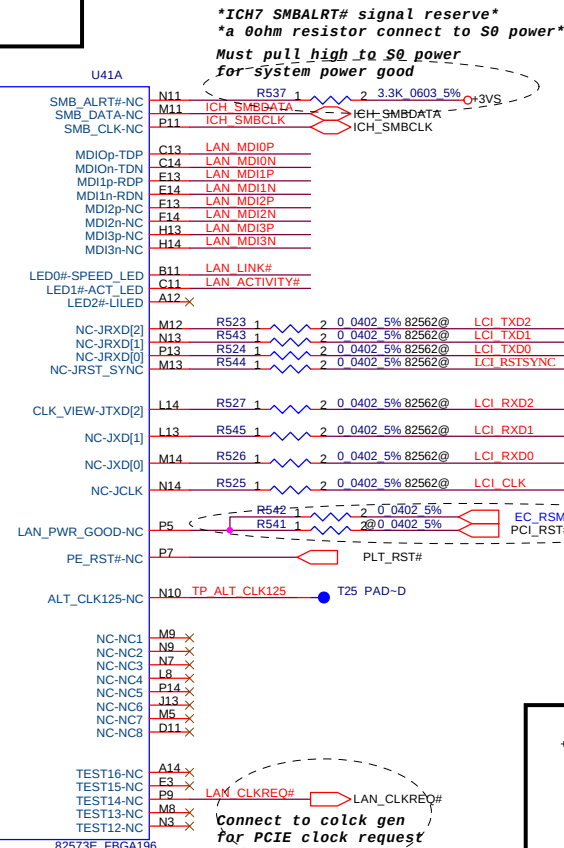
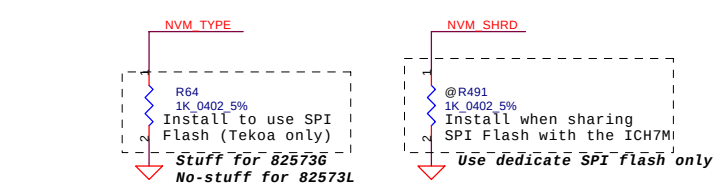
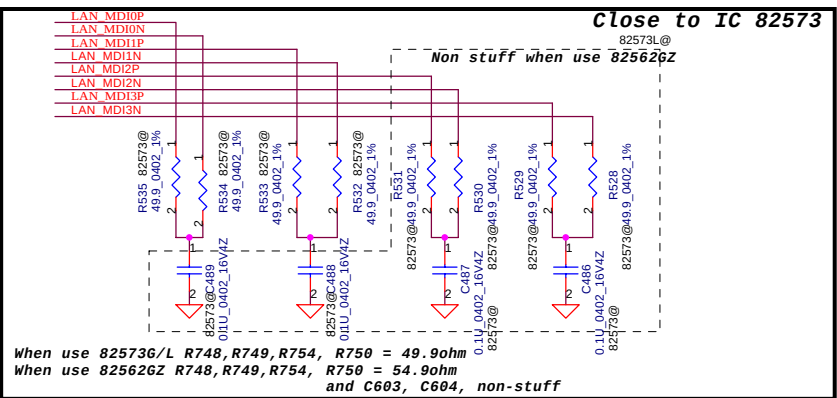
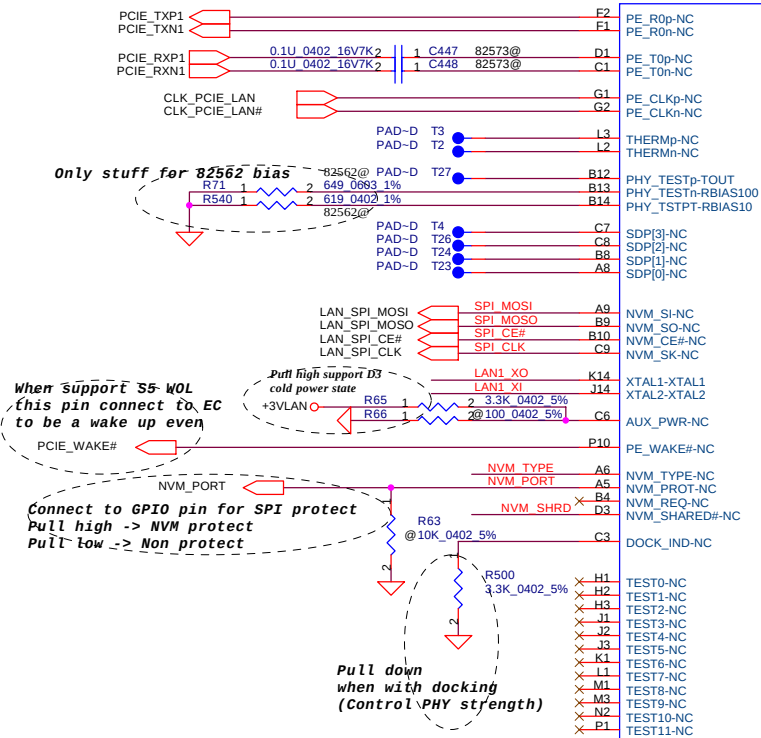
Bluetooth Conn.



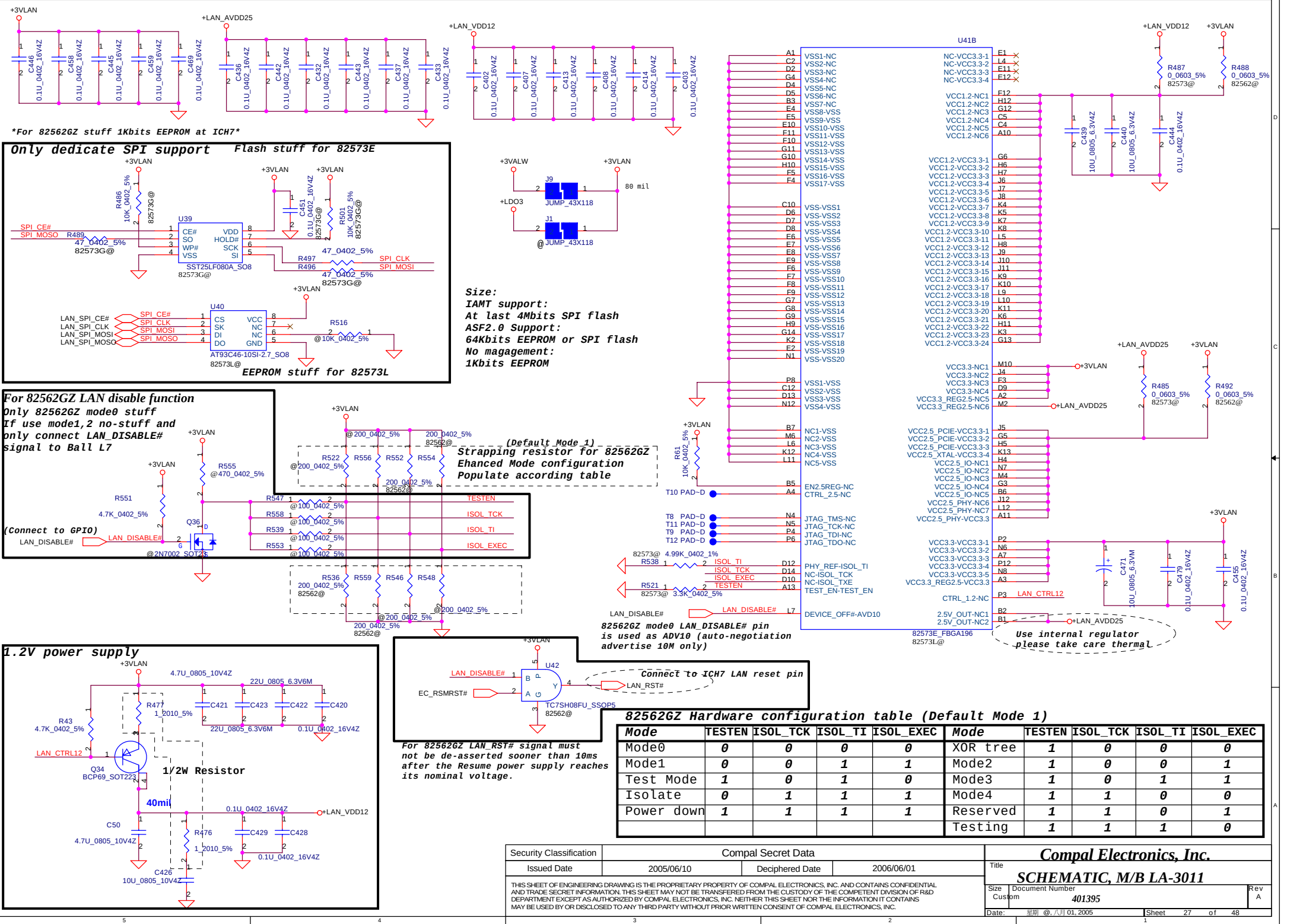
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	SCHEMATIC, M/B LA-3011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
					401395
				Date	Rev A
				2005 八月 01	24 of 48

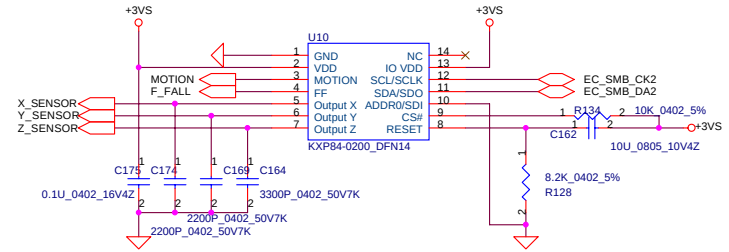
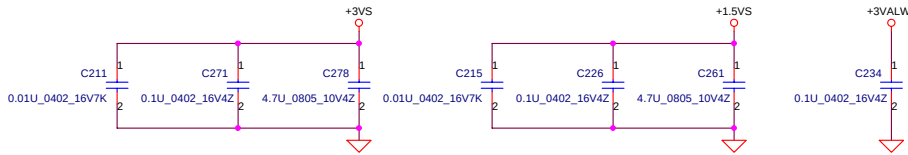


Tekoa 82573E -> IAMT support only (WoL Support)
Vidalia 82573L -> No management
82562GZ -> 10/100 support

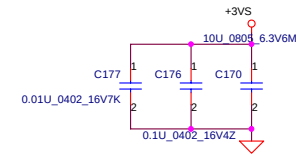
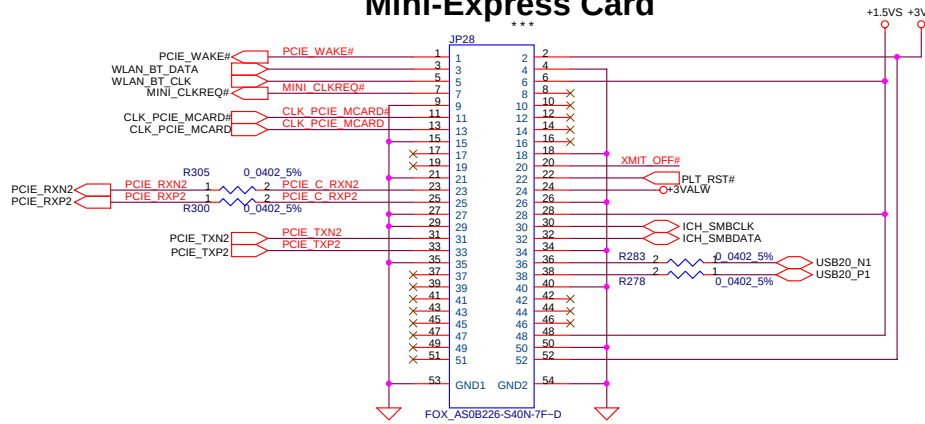


Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, M/B LA-3011	
Issued Date	2005/06/10	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Rev
				Custom	A
				Document Number 401395	
Date: 星期日 @ 八月 01, 2005				Sheet 26 of 48	

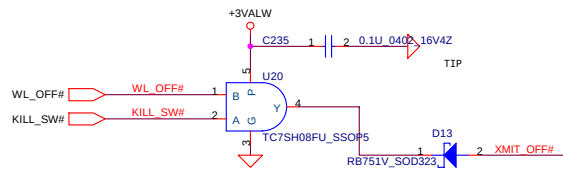
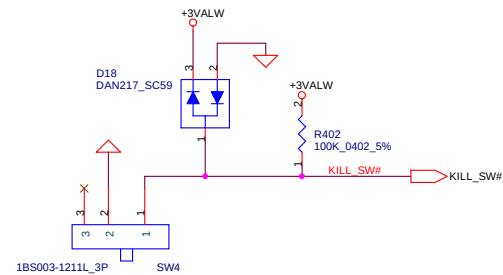




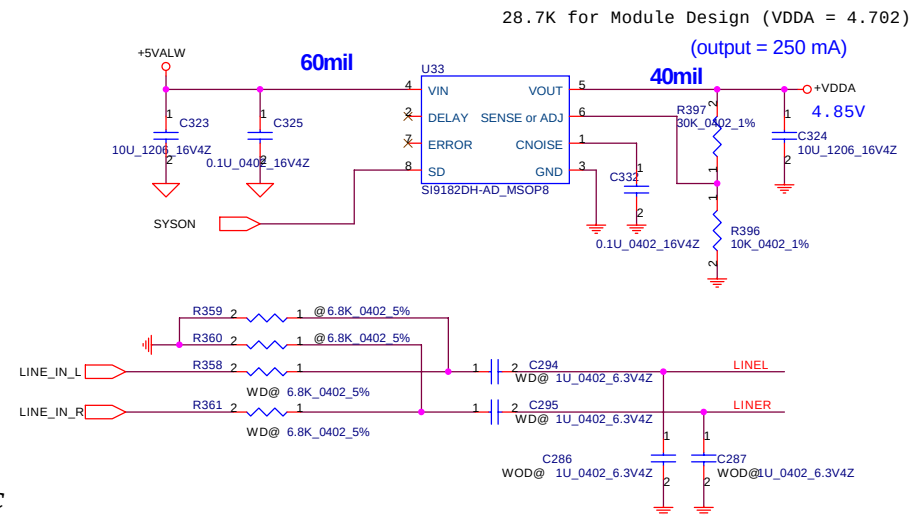
Mini-Express Card



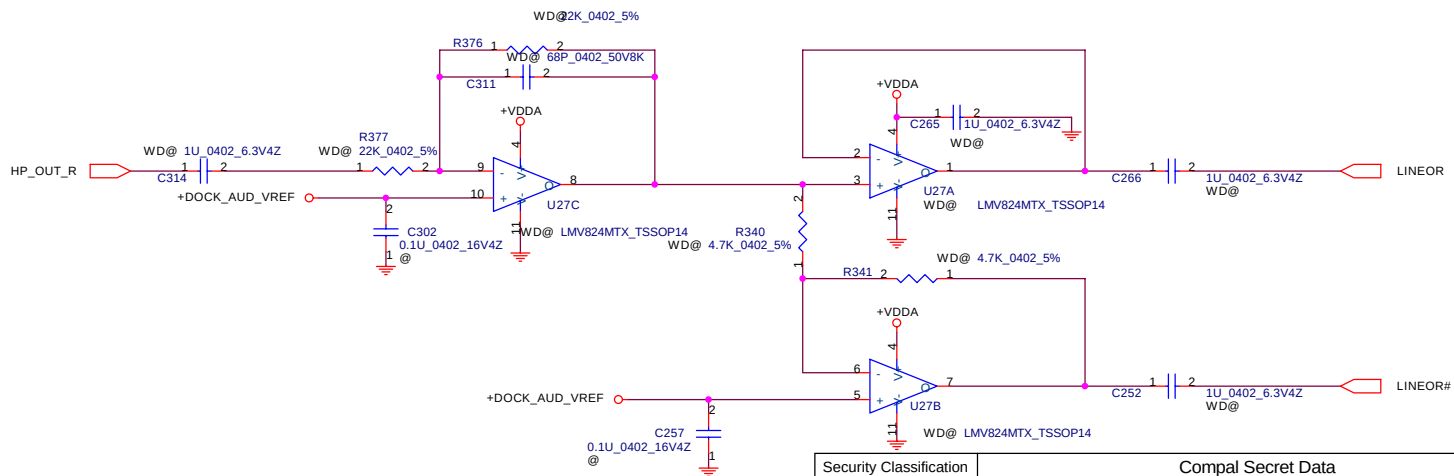
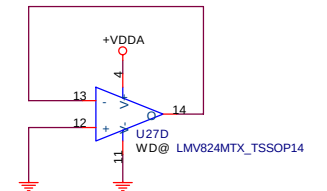
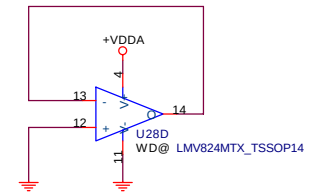
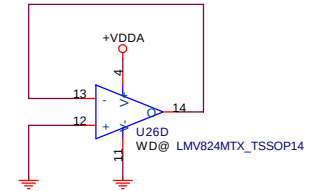
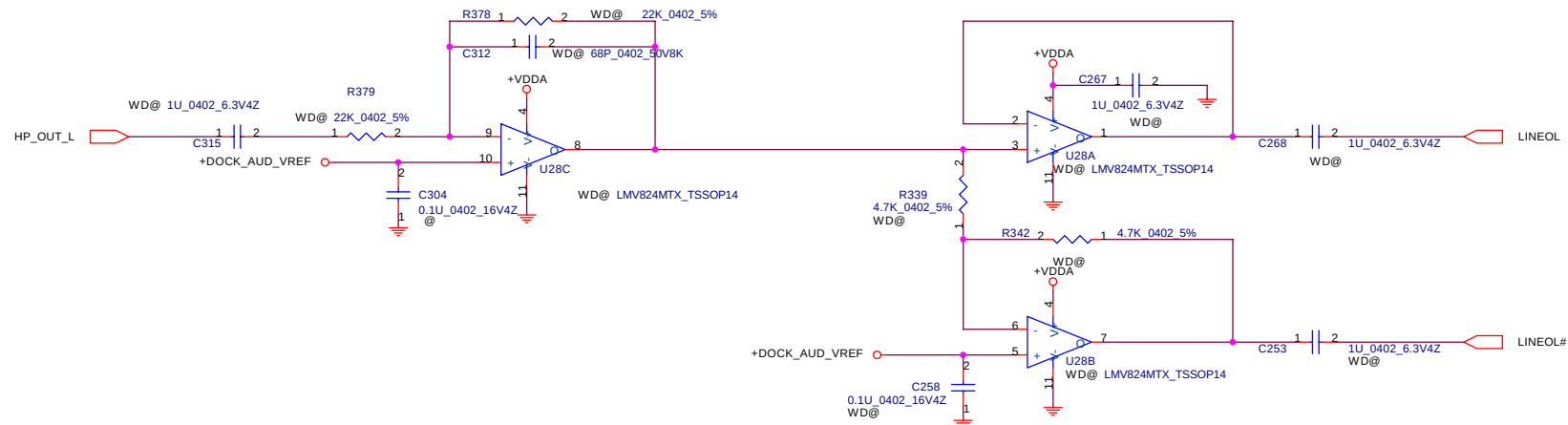
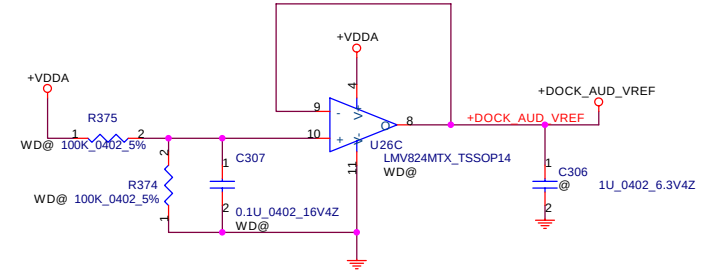
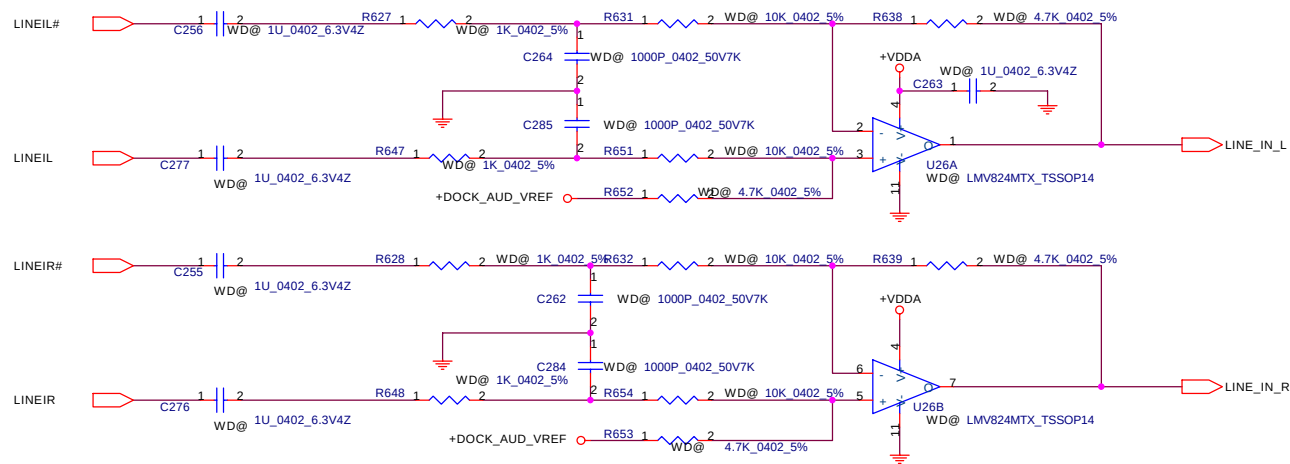
Kill Switch



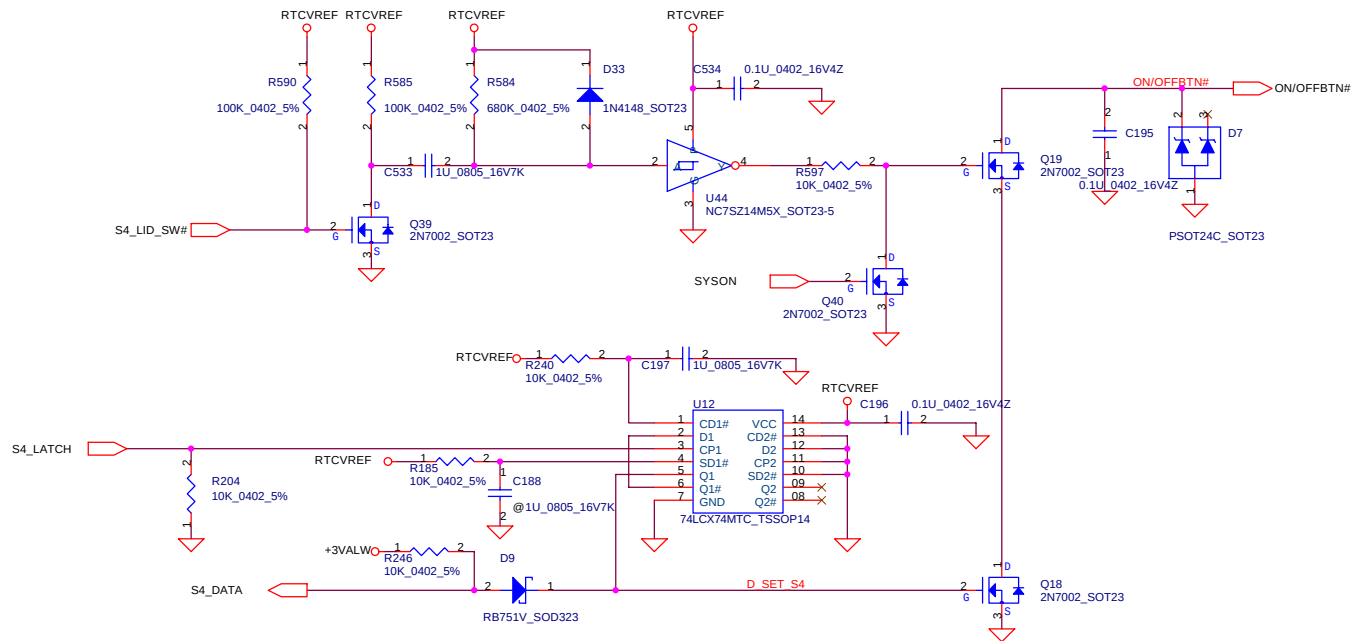
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	SCHEMATIC, M/B LA-3011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				401395	Rev A
				Date:	星期一, 八月 01, 2005
				Sheet	28 of 48



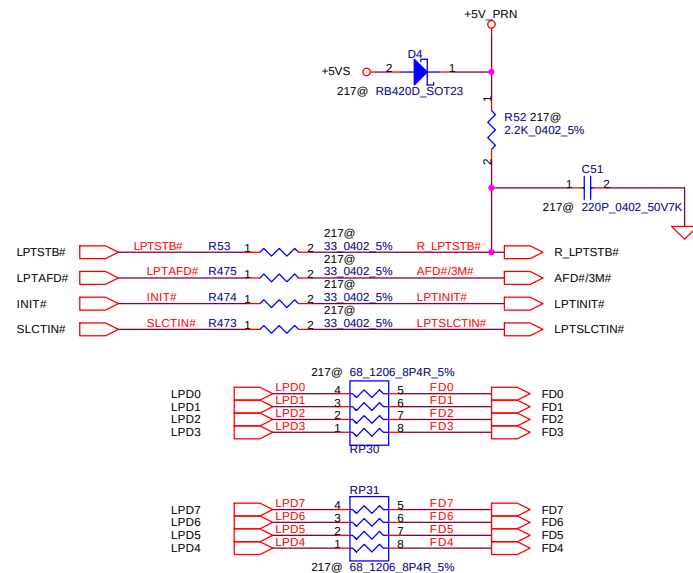
Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-3011			
Size B	Document Number 401395		Rev A
Date	星期一, 八月 01, 2005	Sheet 29	of 48



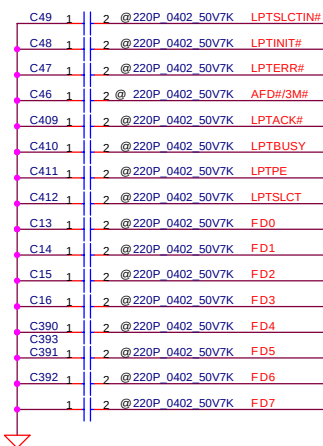
Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, M/B LA-3011		
Issued Date	2005/04/25	Deciphered Date	2006/06/01			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev A
					401395	
3		2		Date:	星期一, 八月 01, 2005	Sheet 31 of 48



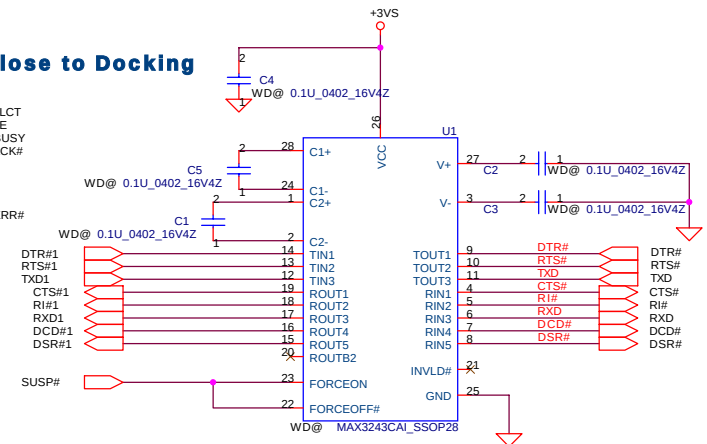
Docking Parallel Port



Close to Docking

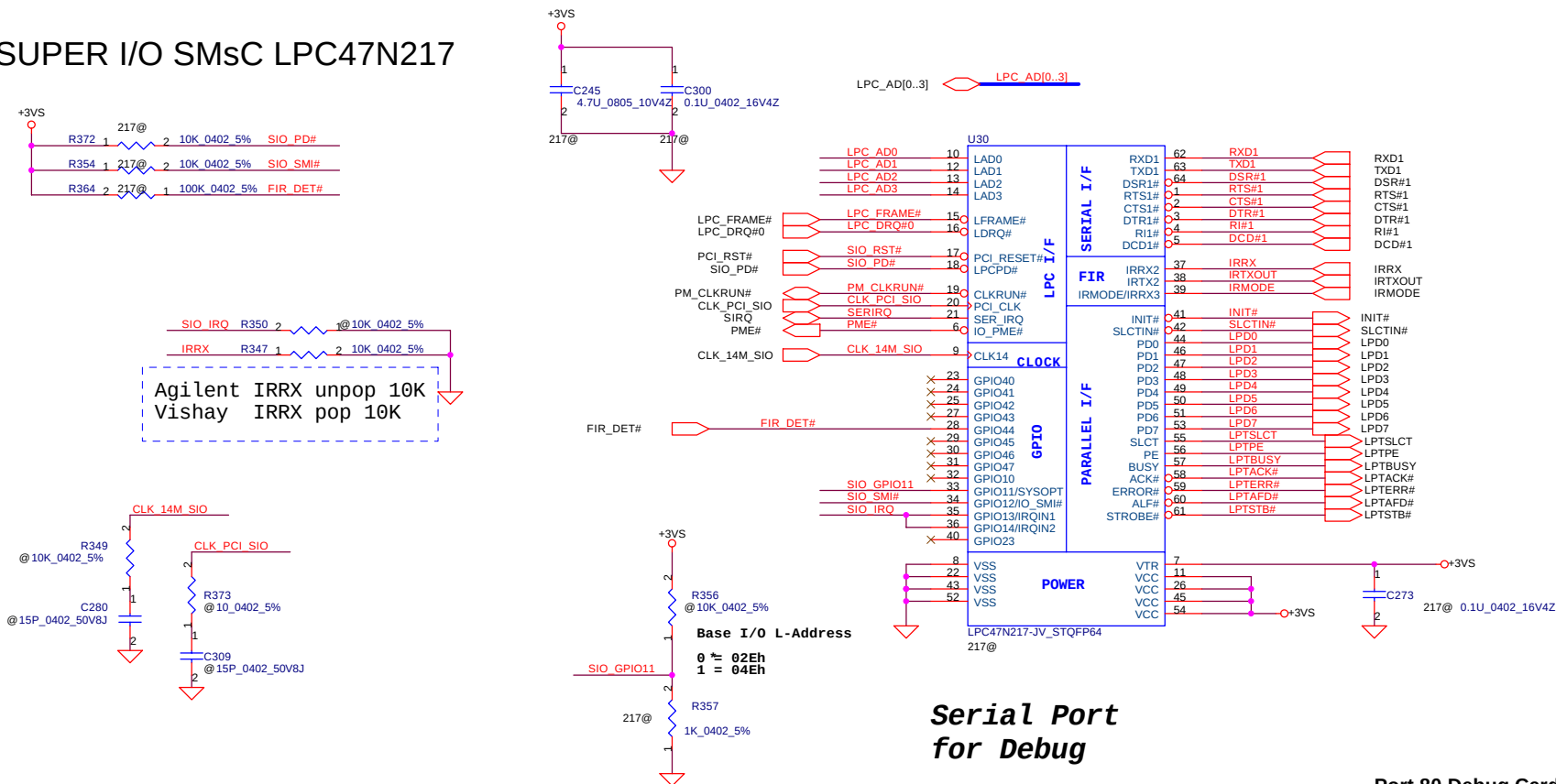


Close to Docking



Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/04/25	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, M/B LA-3011	
Size		Document Number		Rev	
		401395		A	
Date		2005/08/01		Sheet 32 of 48	

SUPER I/O SMC LPC47N217

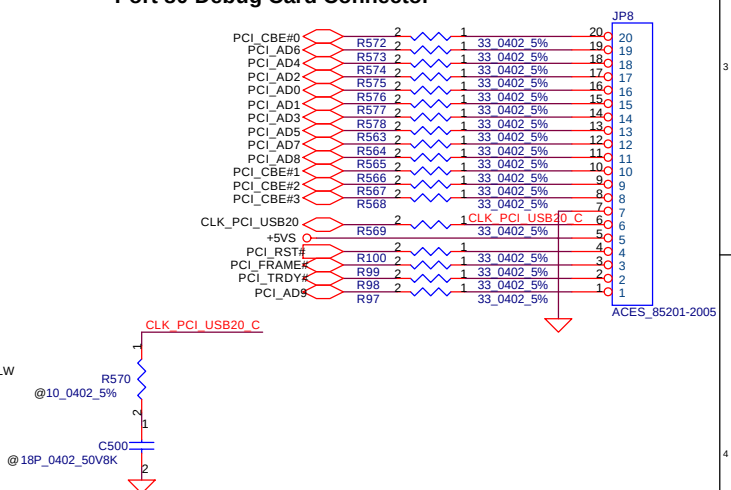
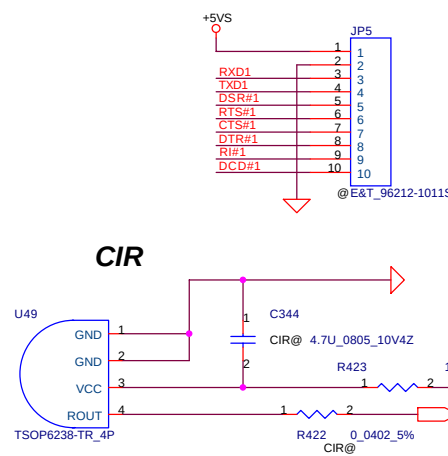
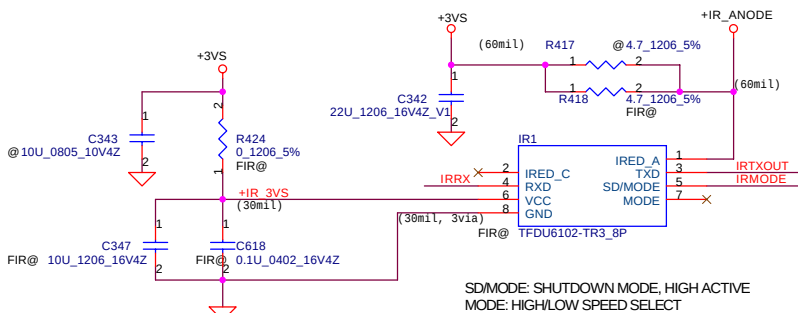
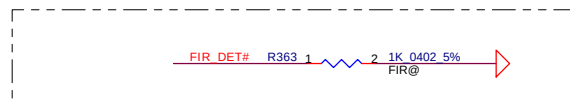


Serial Port for Debug

Port 80 Debug Card Connector

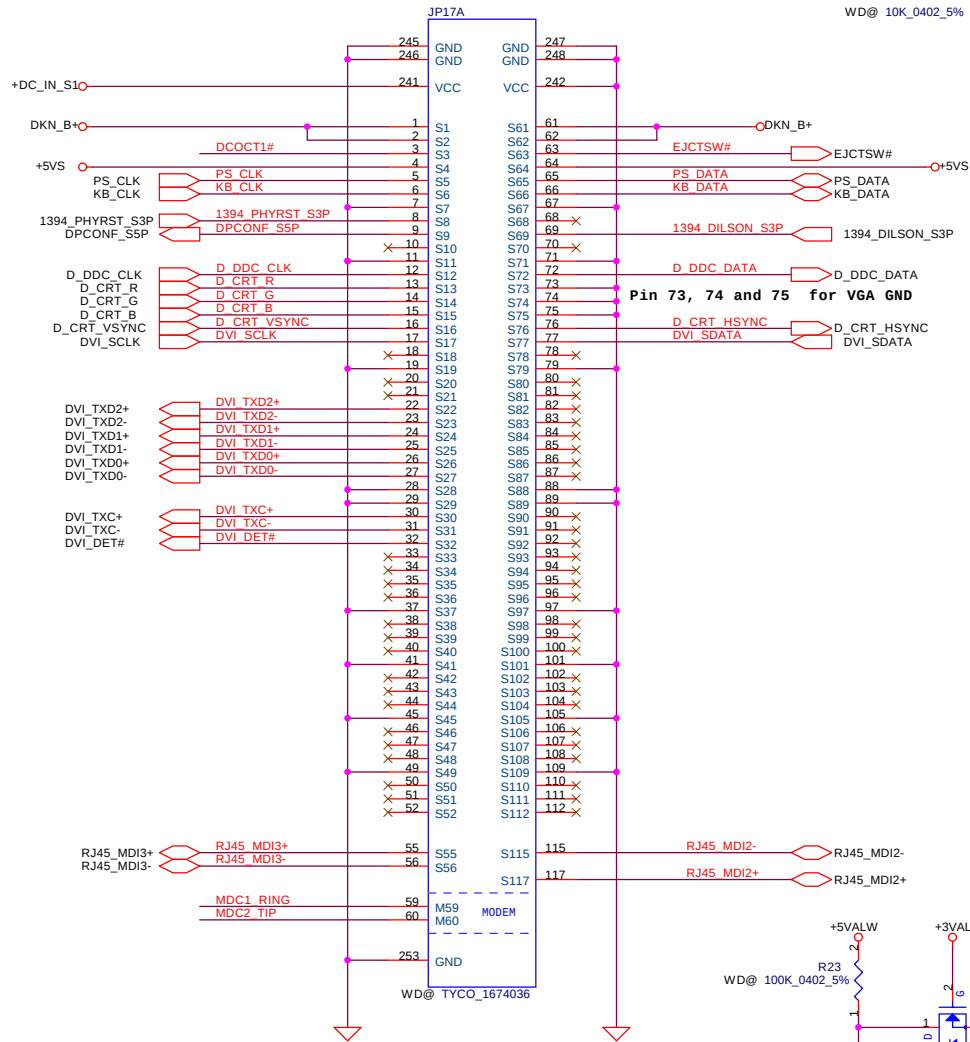


L: R POP; FIR Enable
H: R De-POP/FIR Disable

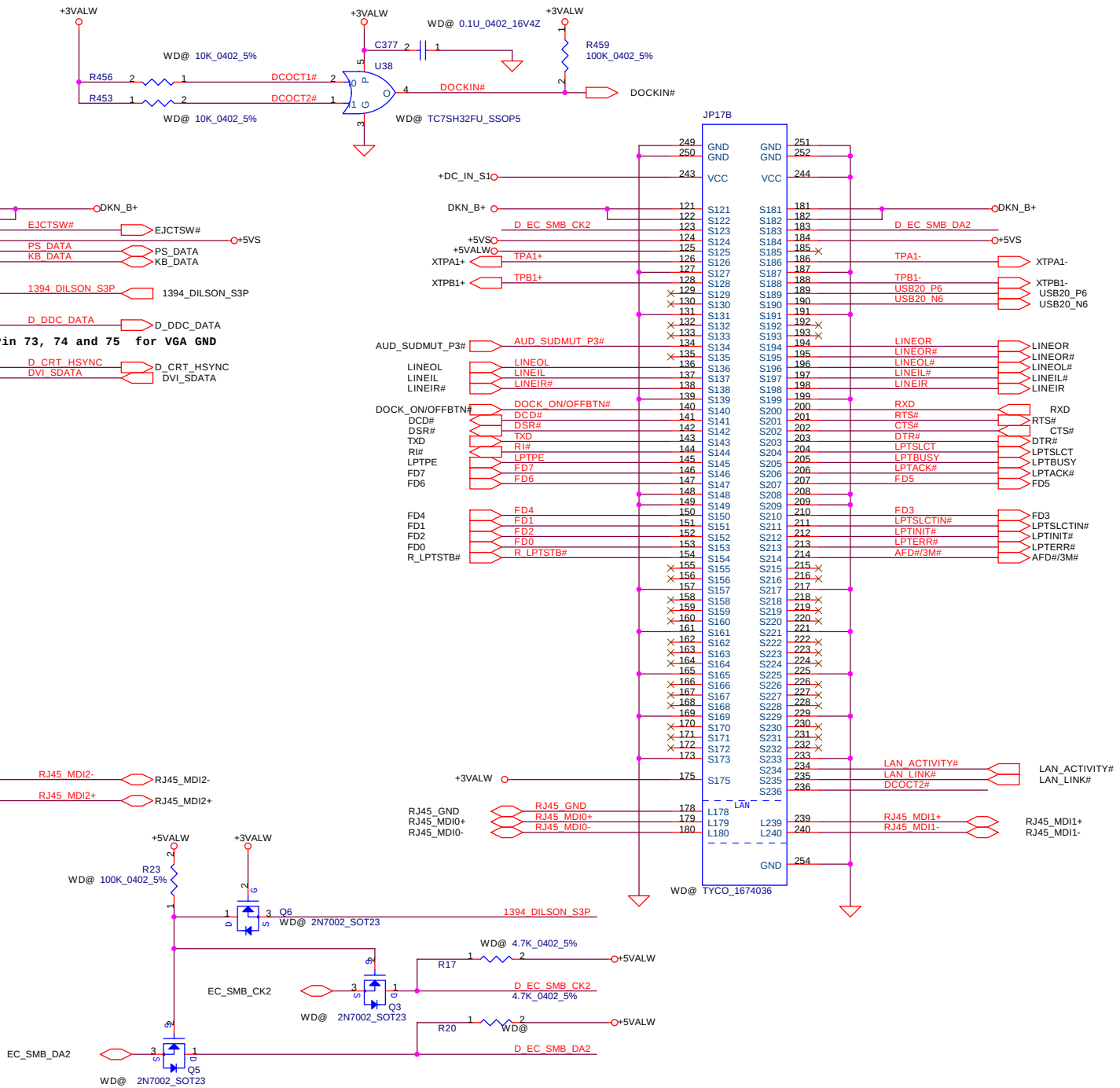
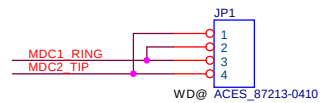


Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, M/B LA-3011		
Issued Date	2005/04/25	Deciphered Date	2006/06/01			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Title		Rev A
				Size	Document Number 401395	
				Date:		星期二, 六月 01, 2005

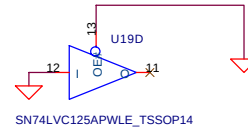
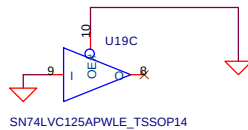
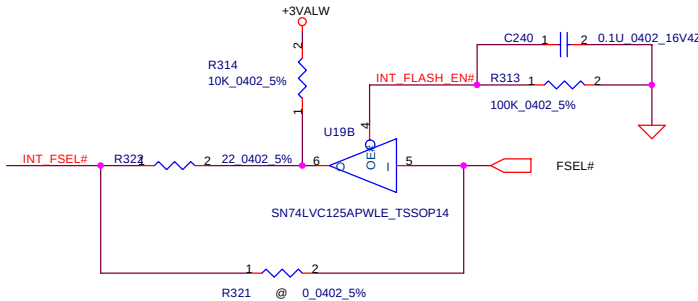
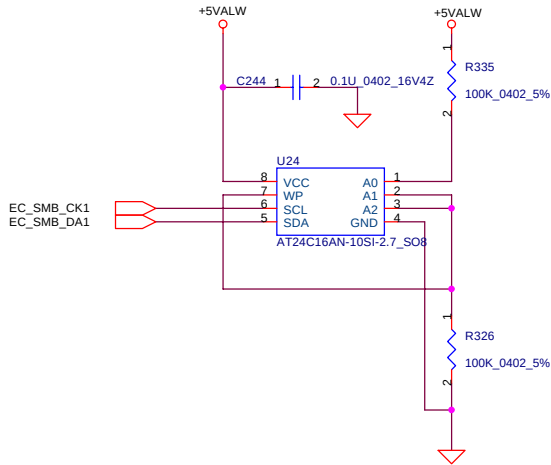
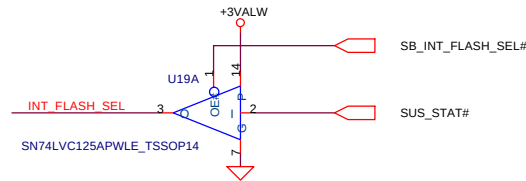
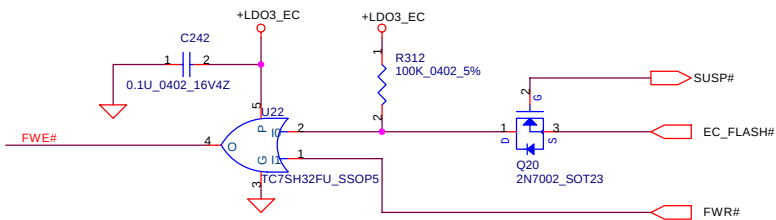
Docking Conn.



MDC to Docking Conn.



Security Classification		Compal Secret Data		Compal Electronics, Inc. SCHEMATIC, M/B LA-3011			
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	401395	Rev A
				Date:	星期五, 六月 01, 2005	Sheet	34 of 48

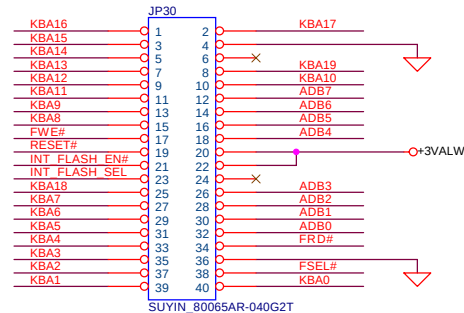
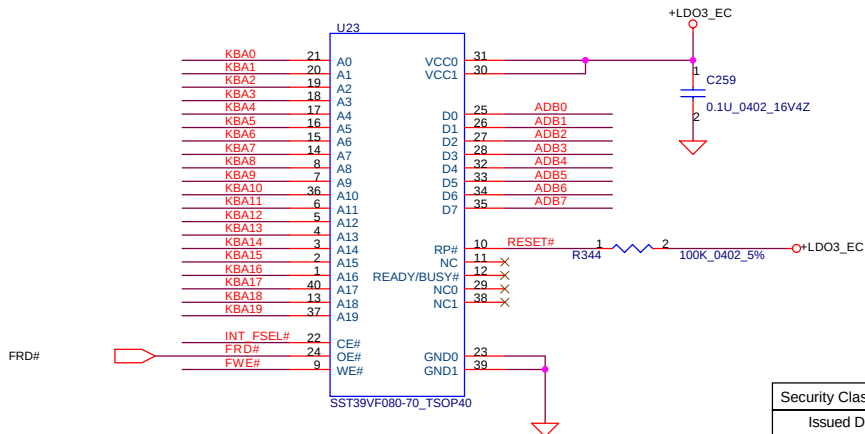


KBA[0..19] KBA[0..19]

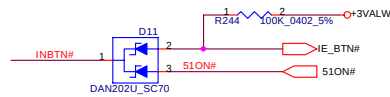
ADB[0..7] ADB[0..7]

1MB ROM Socket

1MB Flash ROM

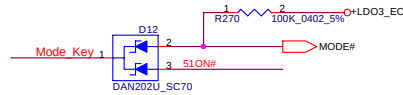


Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, M/B LA-3011
Size		Document Number	401395	Rev A
Date:		星期一, 八月 01, 2005	Sheet	35 of 48

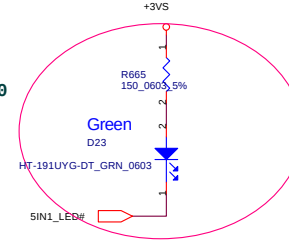


10C/106C: Green: SC591UYG000
10/10C: Blue: SC5191NB000

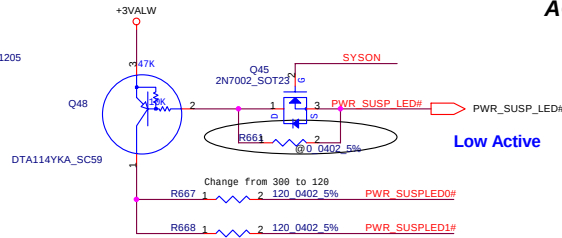
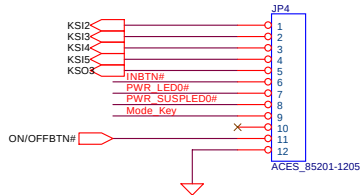
Vivace(SW-DJ) Button



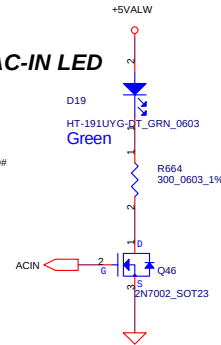
5 In 1 Card LED (Close to Socket)



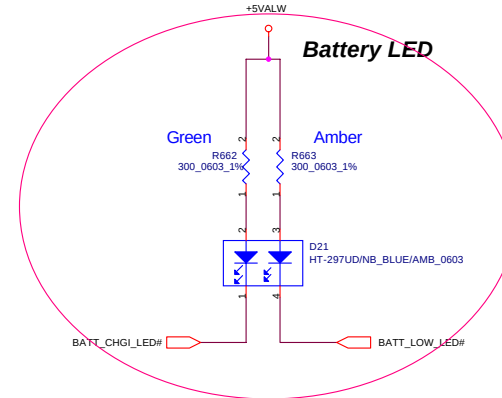
PWR BTN/B



AC-IN LED

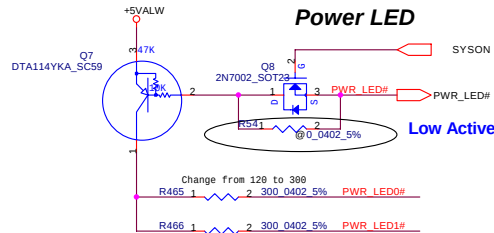
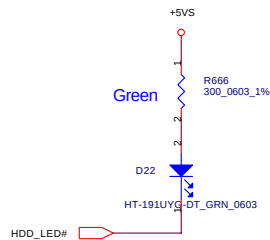


Battery LED



10/10C: Blue/Amber: SC597UDB000
10C/106C: Green/Amber: SC500001900

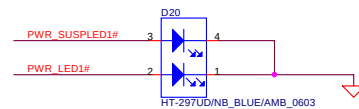
HDD LED



Power LED

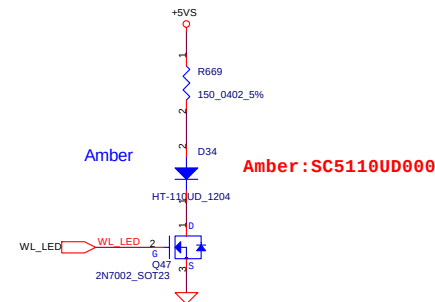
Low Active

Amber



Green

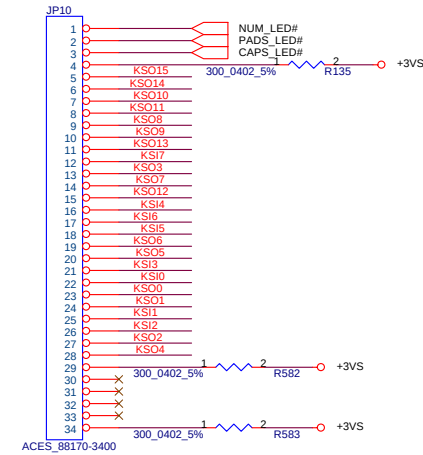
Wireless Lan LED



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	SCHEMATIC, M/B LA-3011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev
					401395	A
				Date:	星期一, 八月 01, 2005	Sheet 36 of 48

KSO[0..17] KSO[0..17]

KSI[0..7] KSI[0..7]

[illegible][illegible]

Lid Switch

The schematic diagram shows the Lid Switch circuit. It features a 47K resistor (R419) connected to RTCVREF and a 100K resistor (R416) connected to +LDO3_EC. The U34 A3212EEH_MLP6 IC is configured with VDD OUTPUT, NC, and GND pins. A 0.1uF capacitor (C345) is connected to the VDD OUTPUT pin, and a 10pF capacitor (C346) is connected to the GND pin. The output of the IC is connected to the LID pin of the DAN202U_SC70 diode. The diode is connected to the LID_SW# pin, which is also connected to the S4_LID_SW pin.

TP CONN.

TP_DATA
TP_CLK

TP_DATA
TP_CLK

SW L
SW R

R102 2

0.1uF_0402_16V4Z

+5VS

C125

0.1uF_0402_16V4Z

JP9

12
11
10
9
8
7
6
5
4
3
2
1

SW L
SW R

R102 2

0.1uF_0402_16V4Z

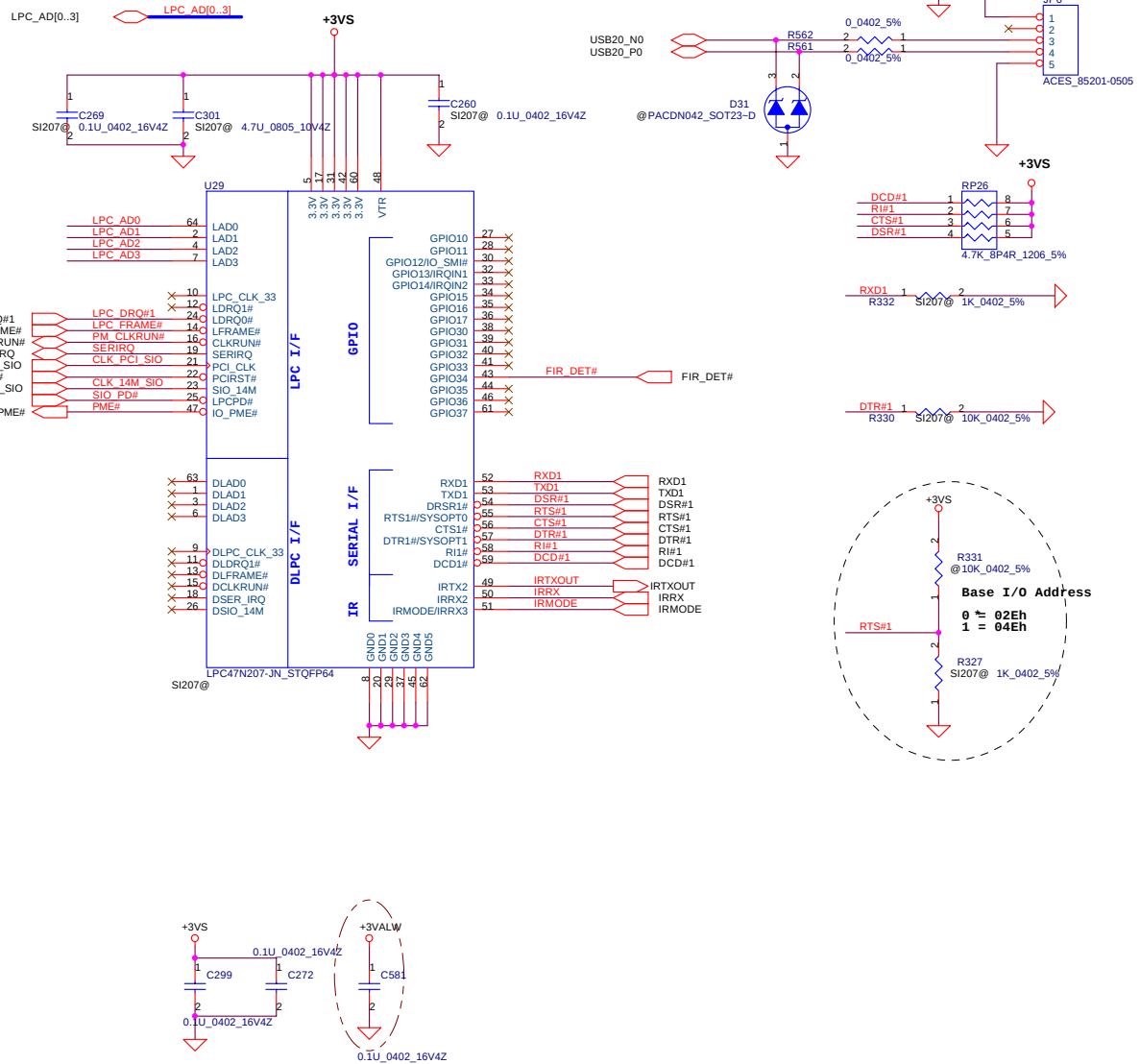
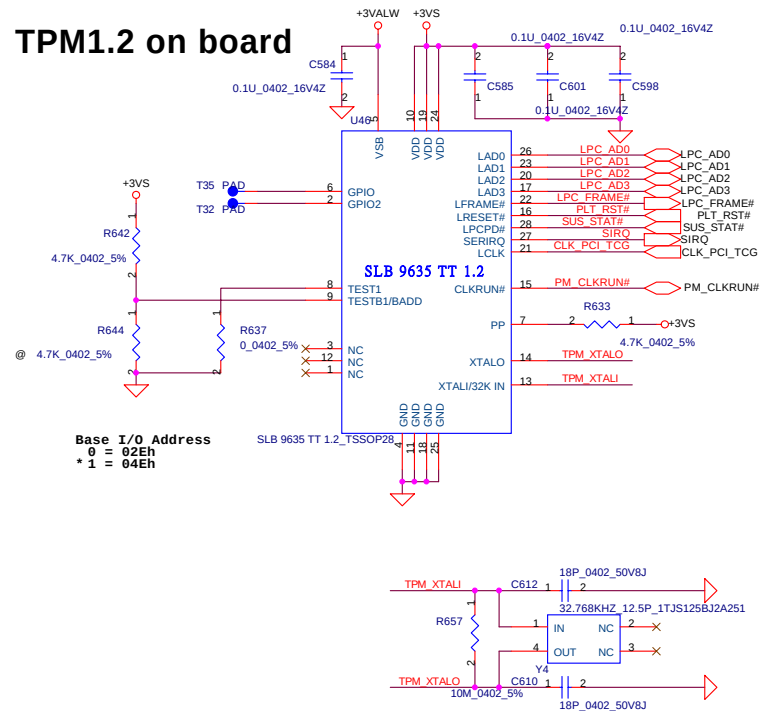
ACES_87151-1207

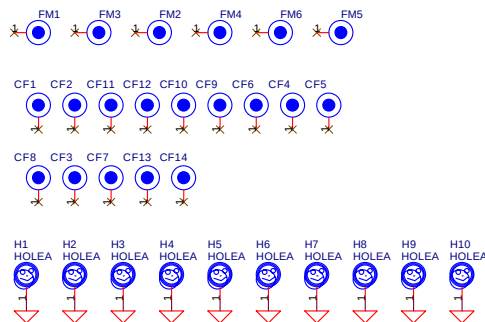
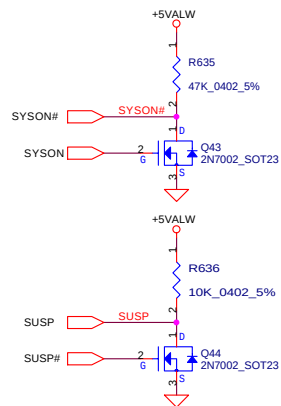
Touchpad mount direction:
Standard: N/A, Reverse: Stuff

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	SCHEMATIC, M/B LA-3011	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number	Rev A
					401395	
				Date:	星期一, 八月 01, 2005	Sheet 37 of 48

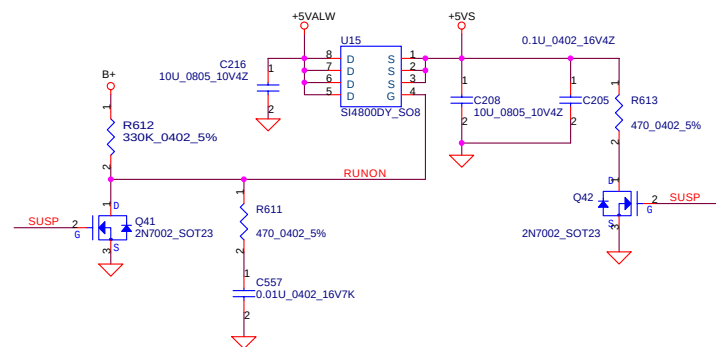
Finger printer

TPM1.2 on board

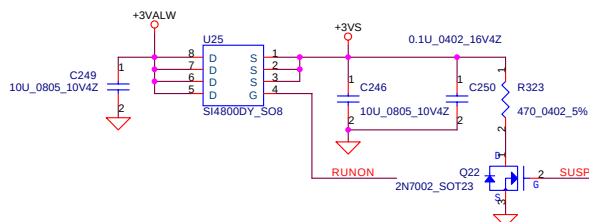




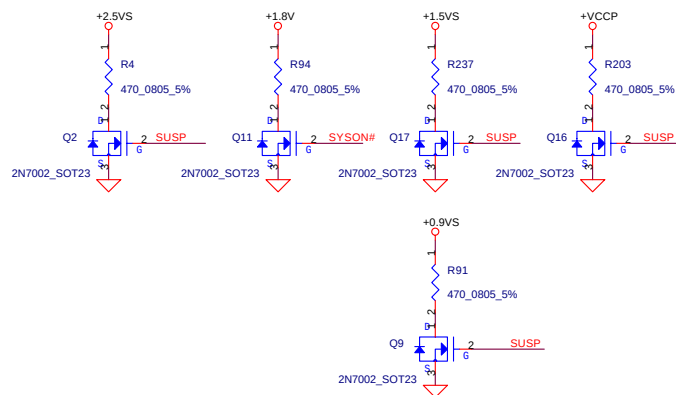
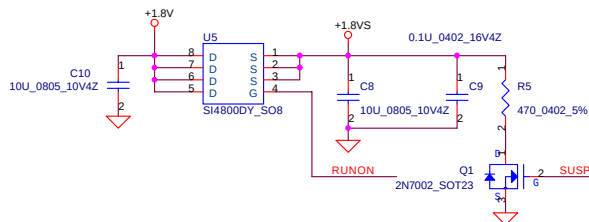
+5VALW to +5VS Transfer



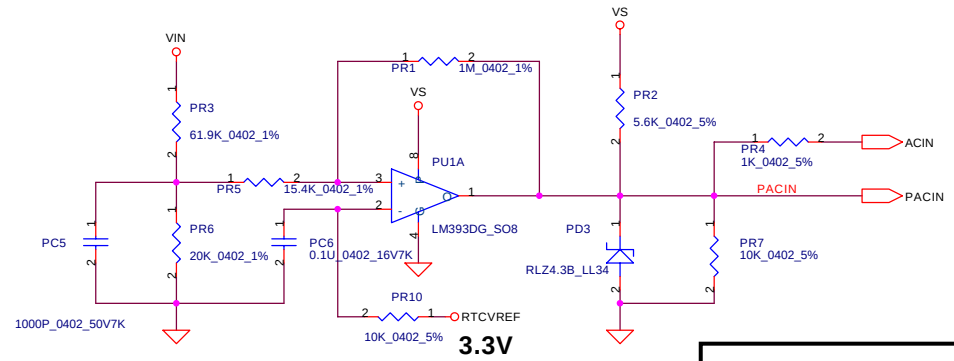
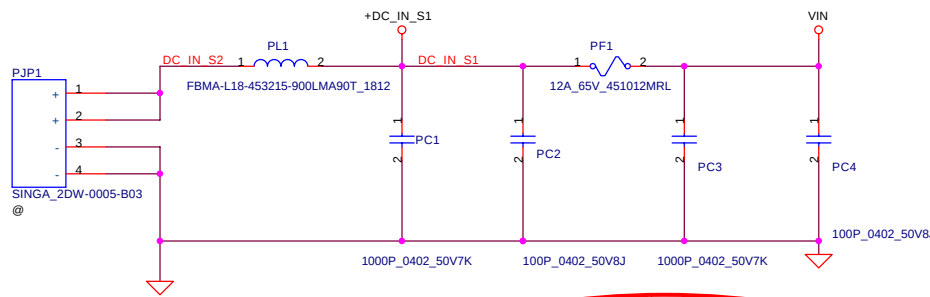
+3VALW to +3VS Transfer



+1.8V to +1.8VS Transfer

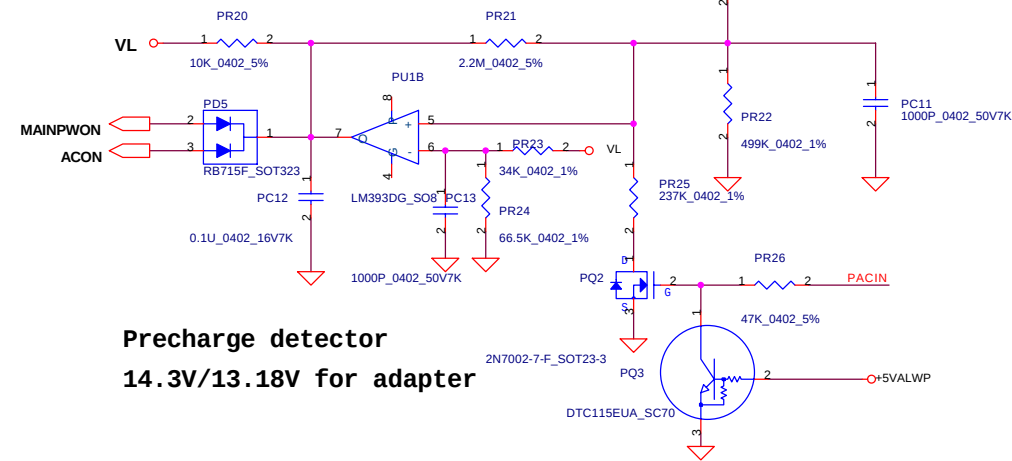
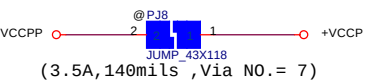
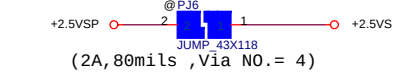
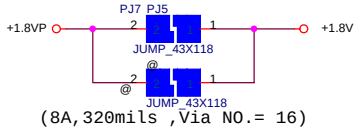
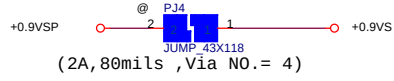
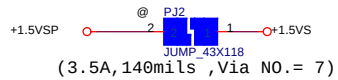
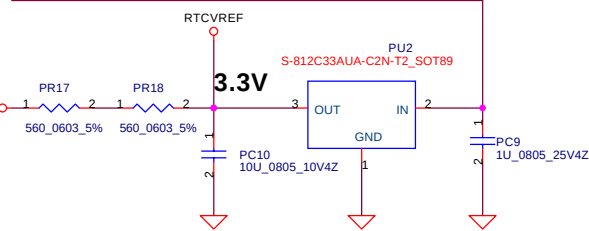
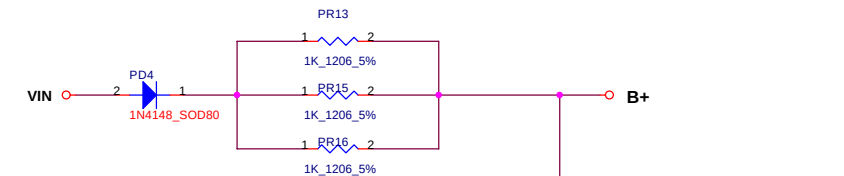
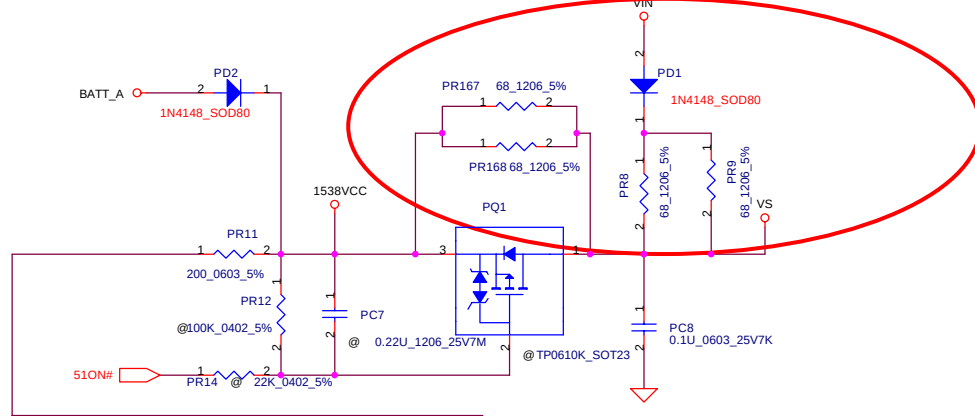


Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2005/06/01	Deciphered Date	2006/06/01	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, M/B LA-3011	
				Size	Document Number
				401395	Rev A
Date: 星期一, 八月 01, 2005		Sheet 39 of 48		E	



Vin Detector

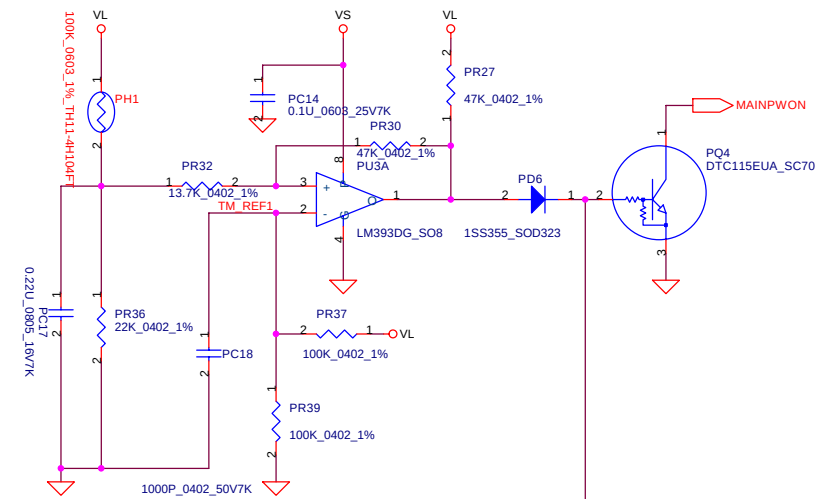
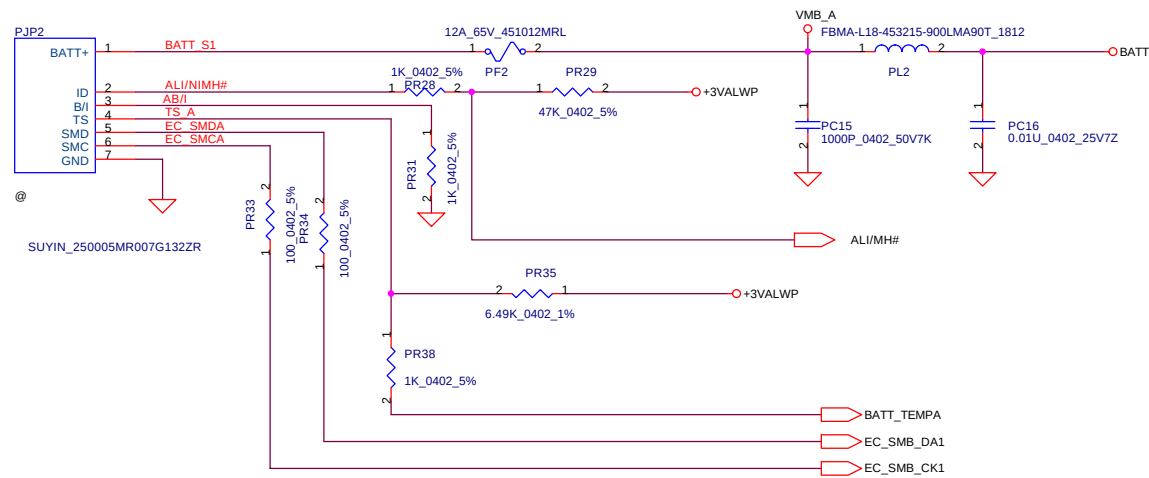
High 14.57 14.01 13.46
Low 14.06 13.51 12.98



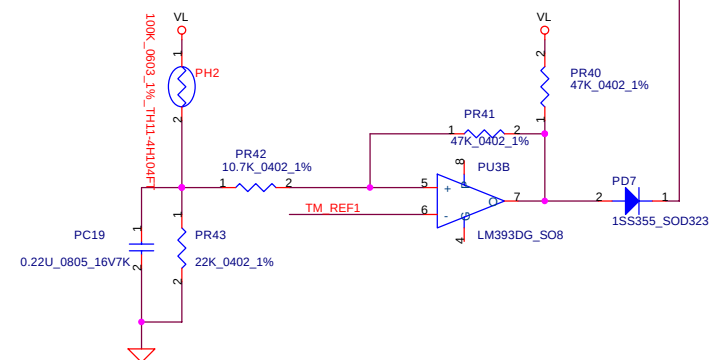
Precharge detector 14.3V/13.18V for adapter

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/23	Deciphered Date	2006/06/23	Title	SCHEMATIC, M/B LA-3011
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B	Document Number 401395
				Date: 星期一, 八月 01, 2005	Sheet 40 of 48

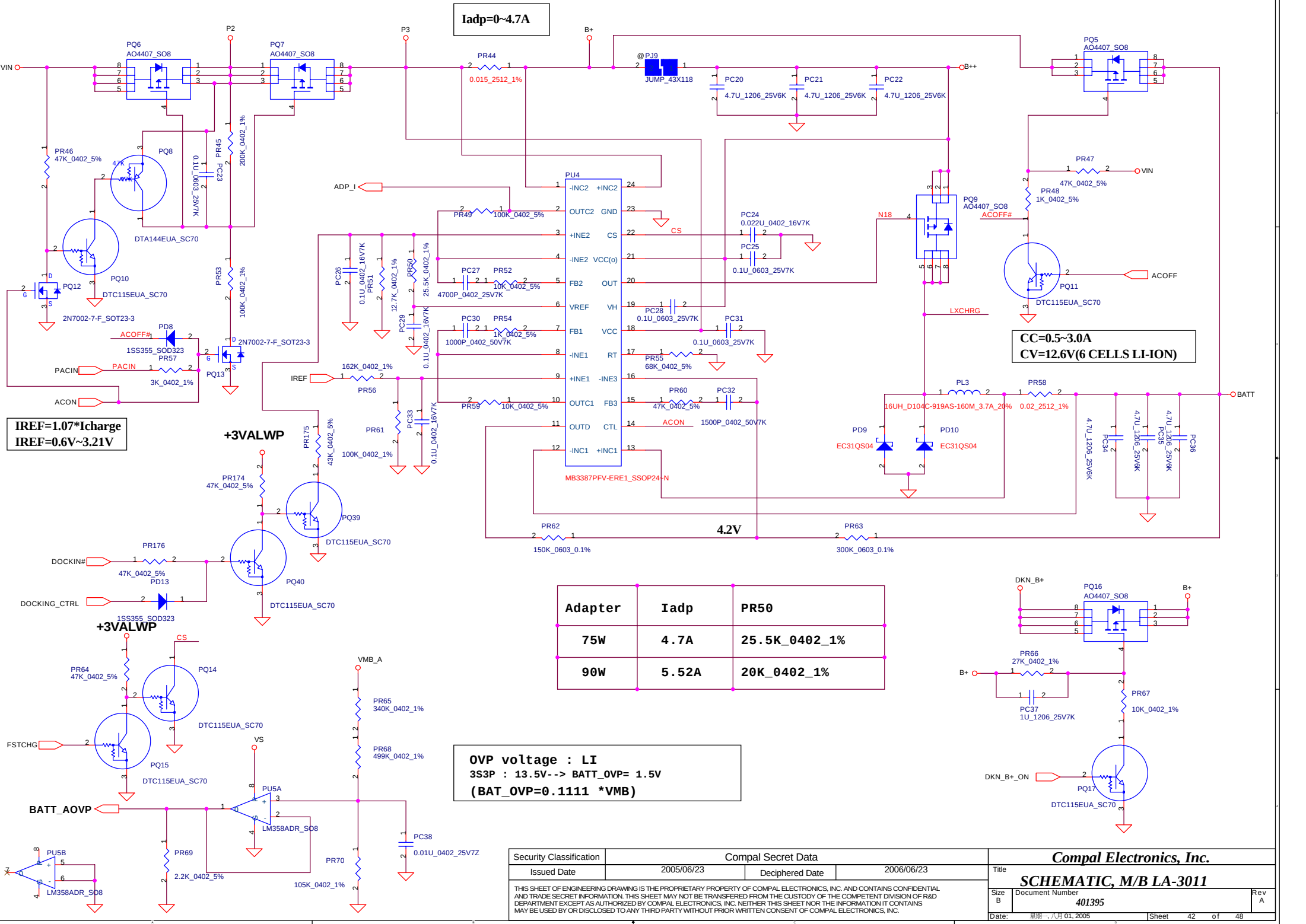
PH1 under CPU botten side :
CPU thermal protection at 84 degree C
Recovery at 45 degree C



PH2 near main Battery CONN :
BAT. thermal protection at 79 degree C
Recovery at 45 degree C



Security Classification	Compal Secret Data			Compal Electronics, Inc.		
Issued Date	2005/06/23	Deciphered Date	2006/06/23	Title SCHEMATIC, M/B LA-3011		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size B	Document Number	Rev A
					401395	
Date:				星期四, 八月 01, 2005	Sheet 41 of 48	



Iadp=0~4.7A

IREF=1.07*Icharge
IREF=0.6V~3.21V

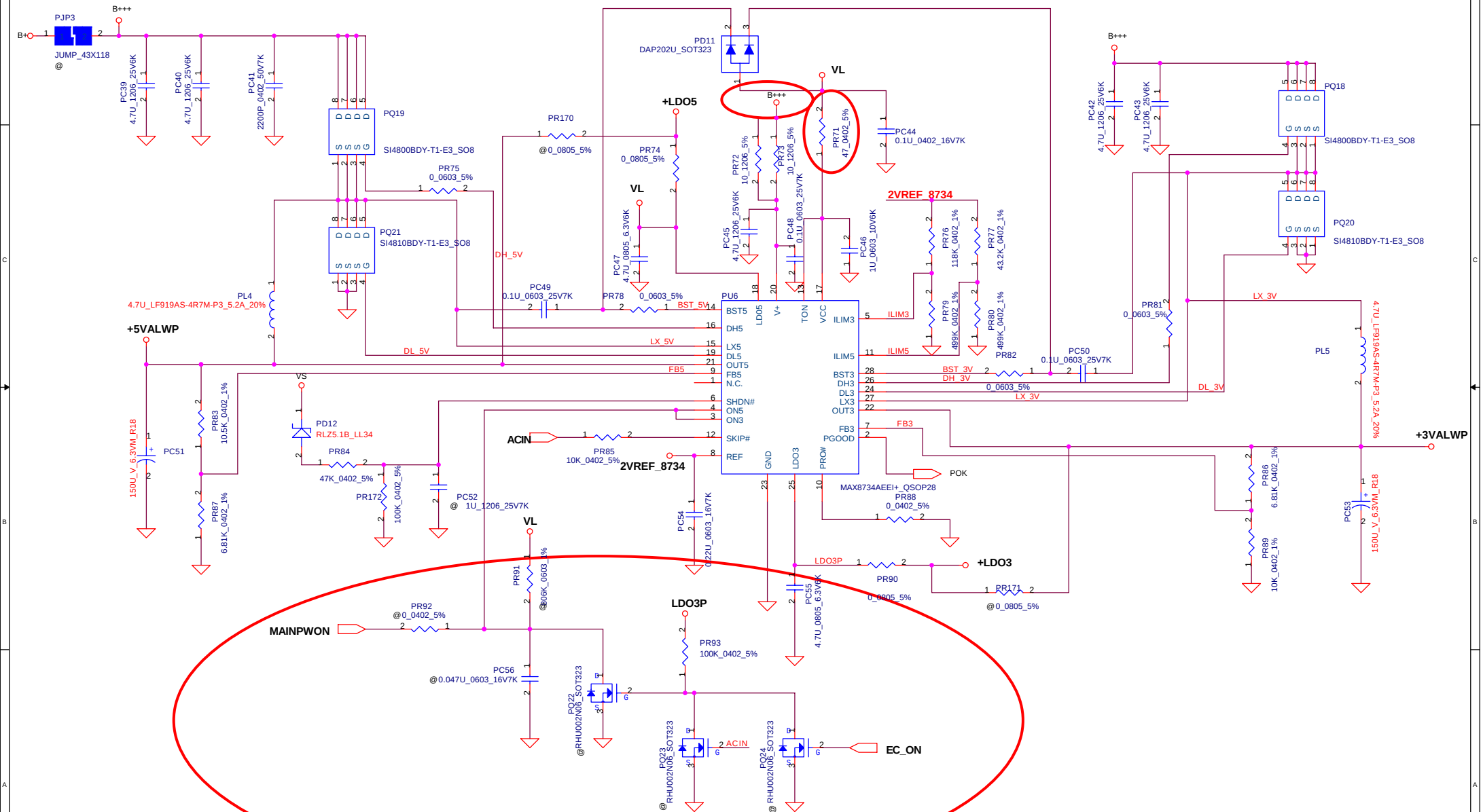
+3VALWP

CC=0.5~3.0A
CV=12.6V(6 CELLS LI-ION)

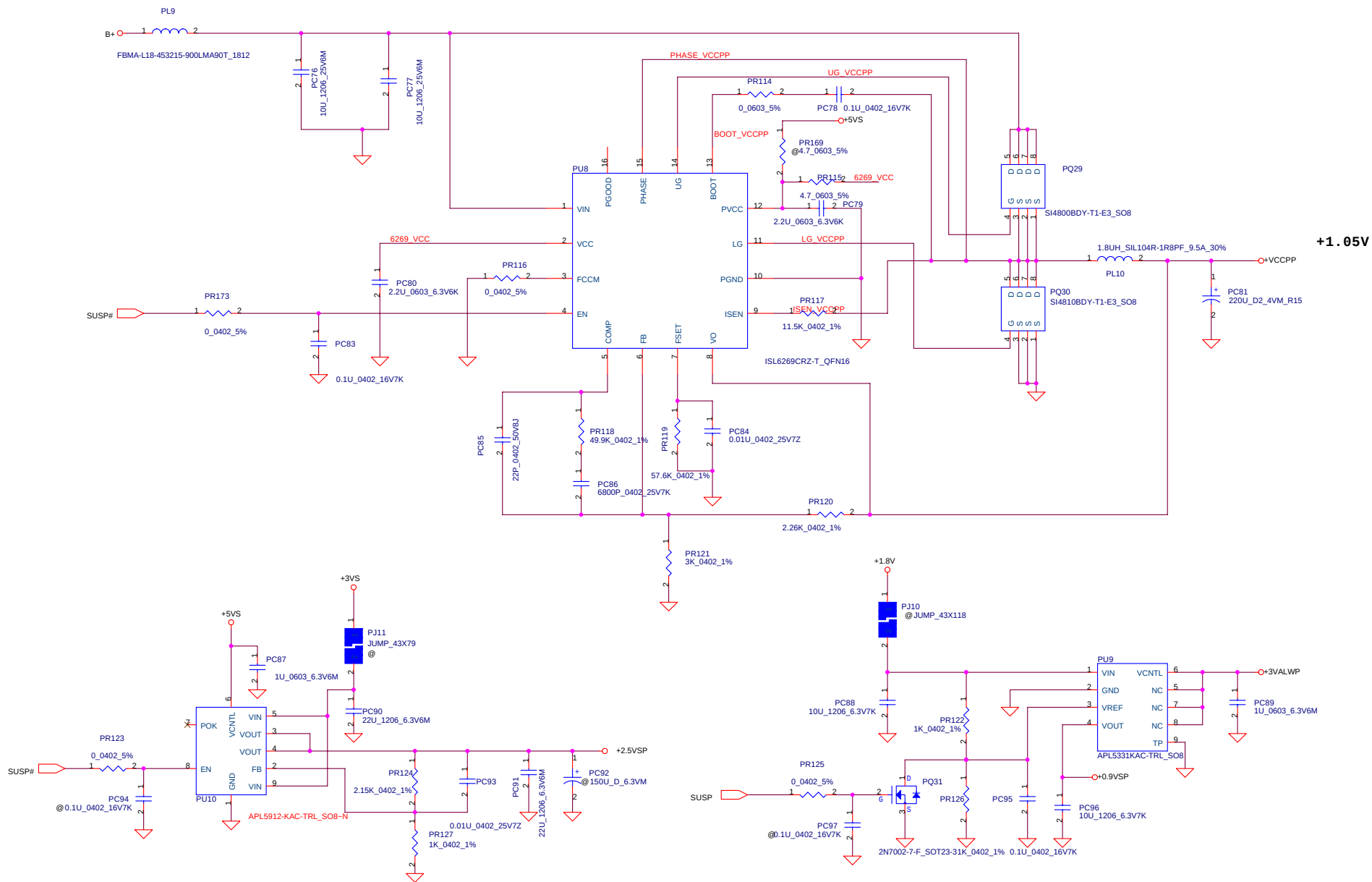
Adapter	Iadp	PR50
75W	4.7A	25.5K_0402_1%
90W	5.52A	20K_0402_1%

OVP voltage : LI
3S3P : 13.5V--> BATT_OVP= 1.5V
(BATT_OVP=0.1111 *VMB)

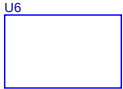
+3.3VALWP/+5VALWP



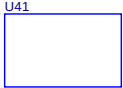
Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2005/06/23	Deciphered Date	2006/06/23	Title		
THIS SHEET OF ENGINEERING DRAWINGS IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, M/B LA-3011		
				Size	Document Number	Rev
				Custom	401395	A
Date: 日期: 六月 01, 2005				Sheet	43 of 48	



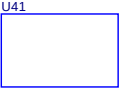
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/23	Deciphered Date	2006/06/23	Title	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SCHEMATIC, M/B LA-3011	
Size	Custom	Document Number	401395	Rev	A
Date:	2005/06/01	Sheet	45	of	48



945GM
GM@



82573E
82573E@



82562
82562@



PCB ZKU LA-3011 REV0



PCI8412
PCI8412@

THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.

Compal Electronics, Inc.			
Title			
SCHEMATIC, M/B LA-3011			
Size	Document Number		Rev
B	401395		A
Date:	星期一, 八月 01, 2005		Sheet 48 of 48
		1	