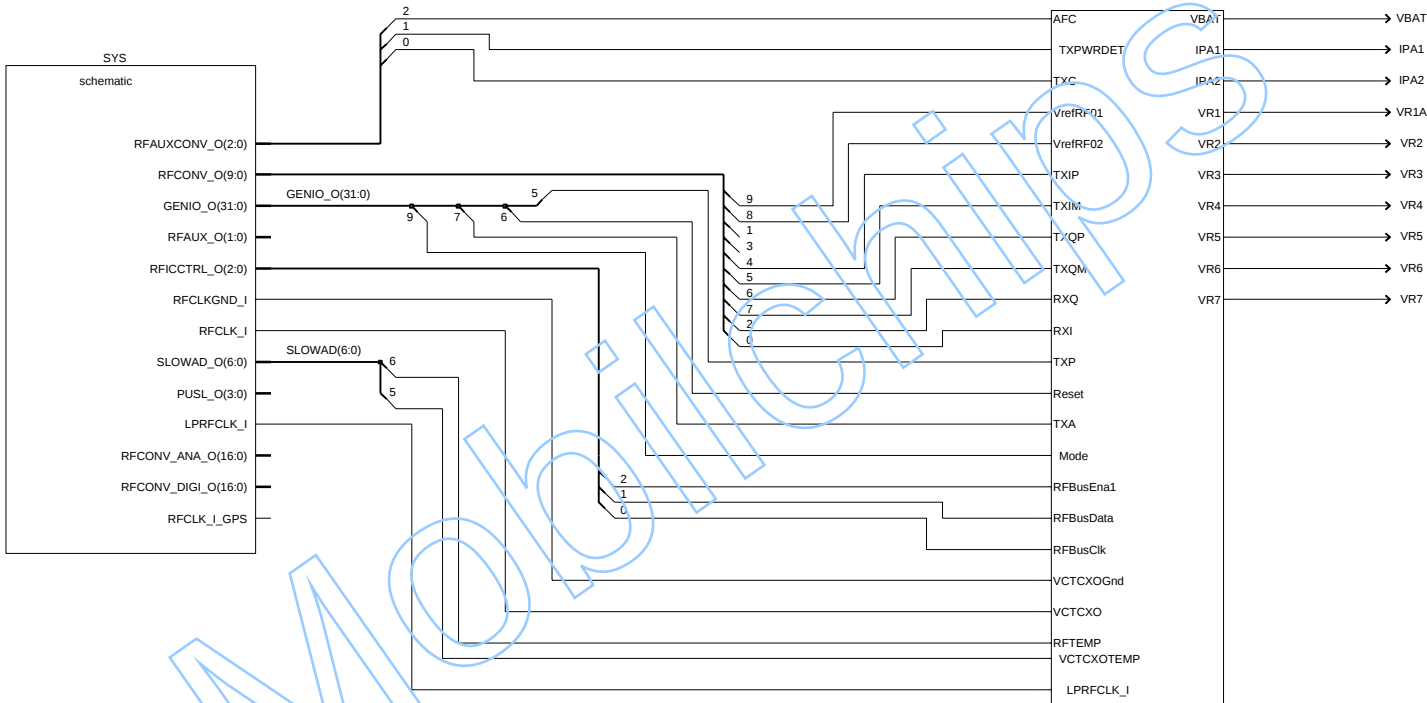
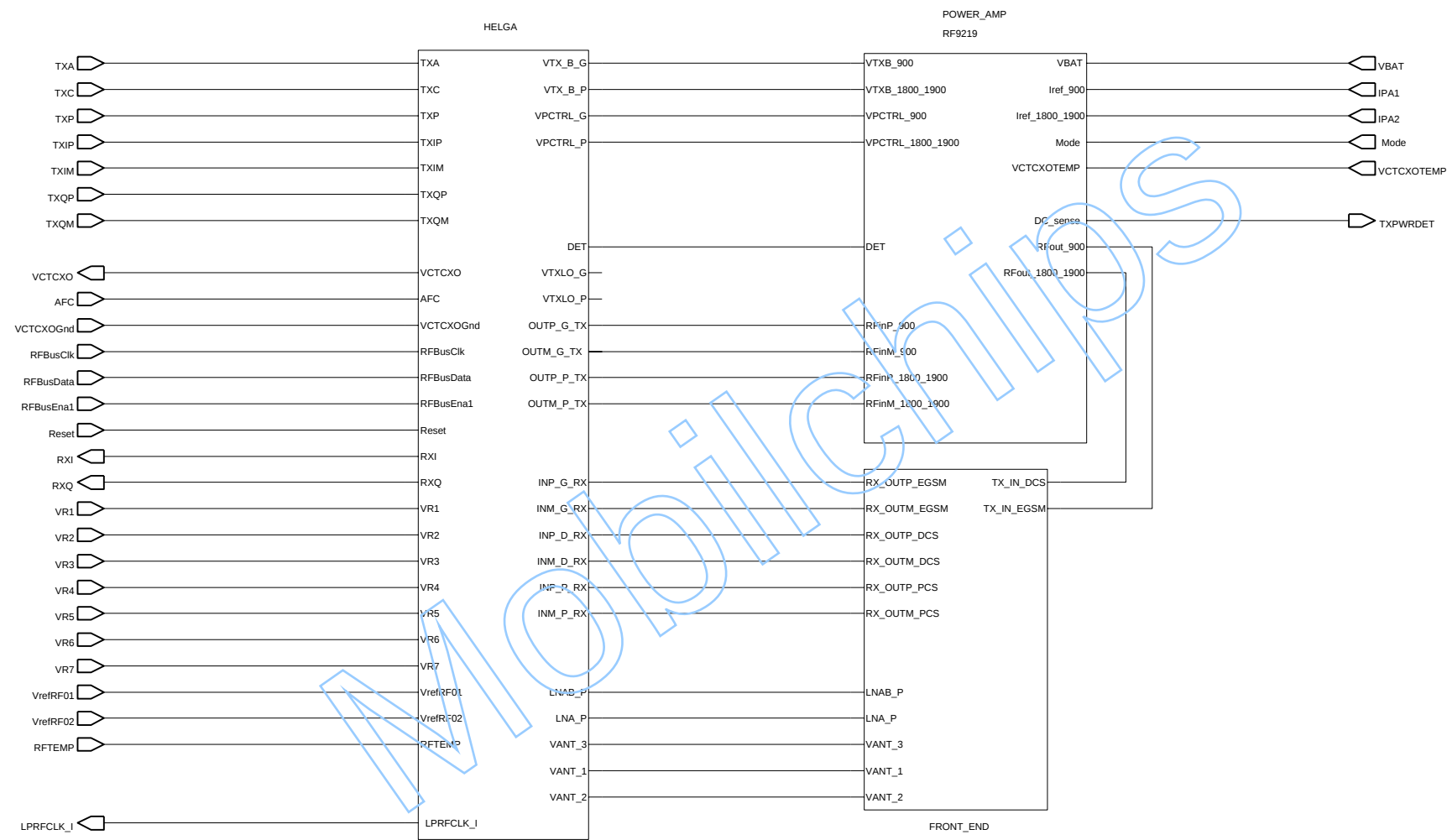


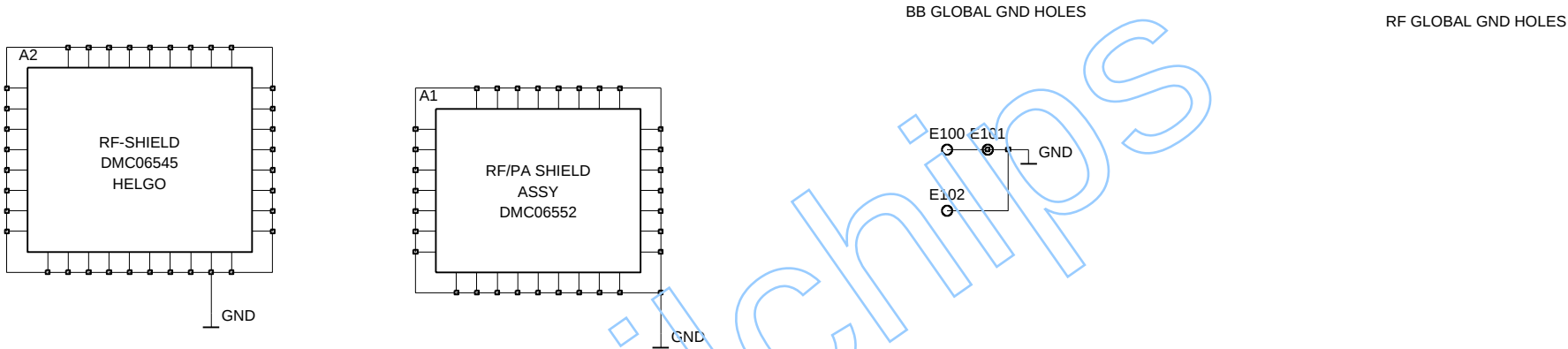
Top level



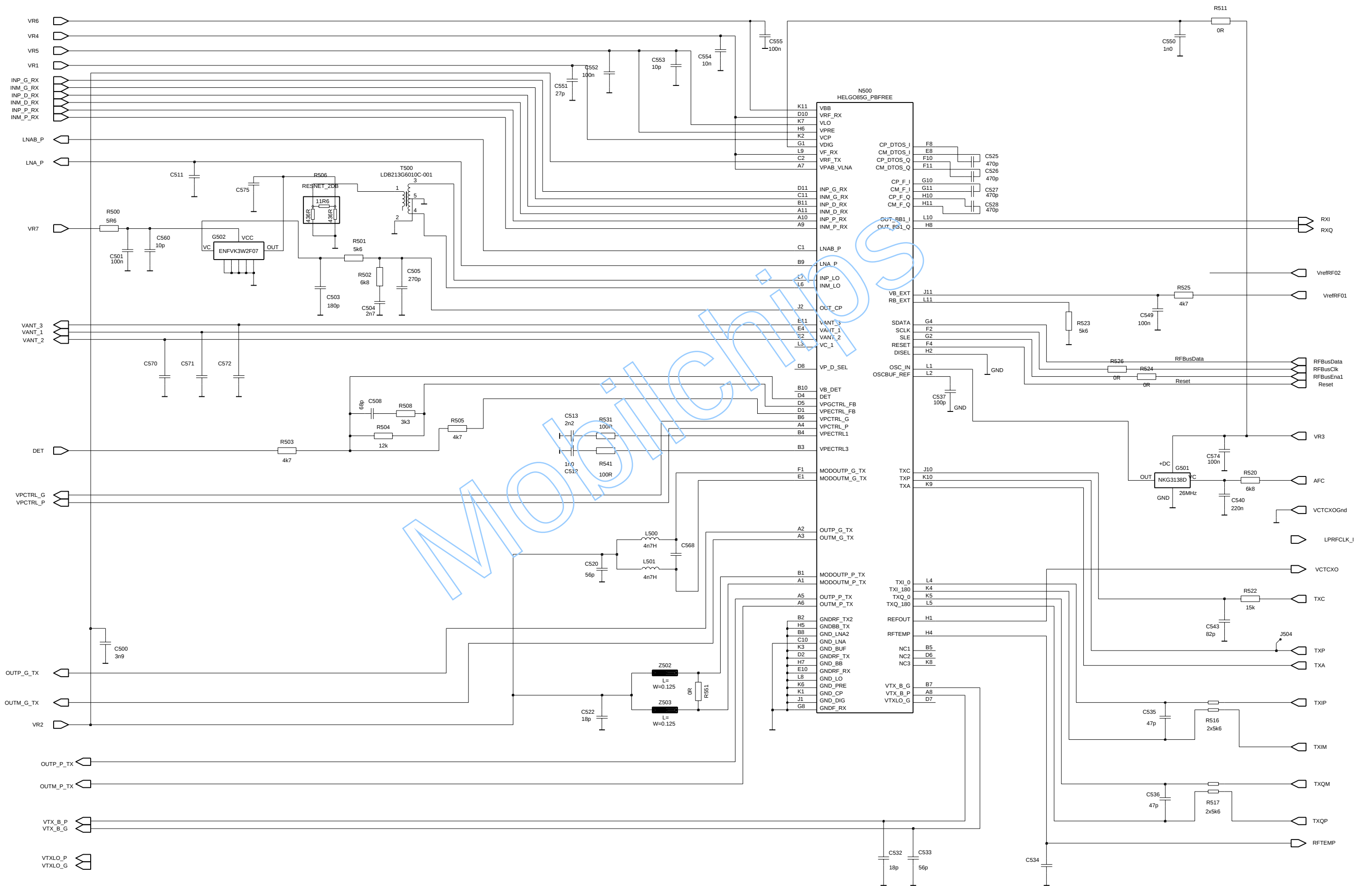
RF top level



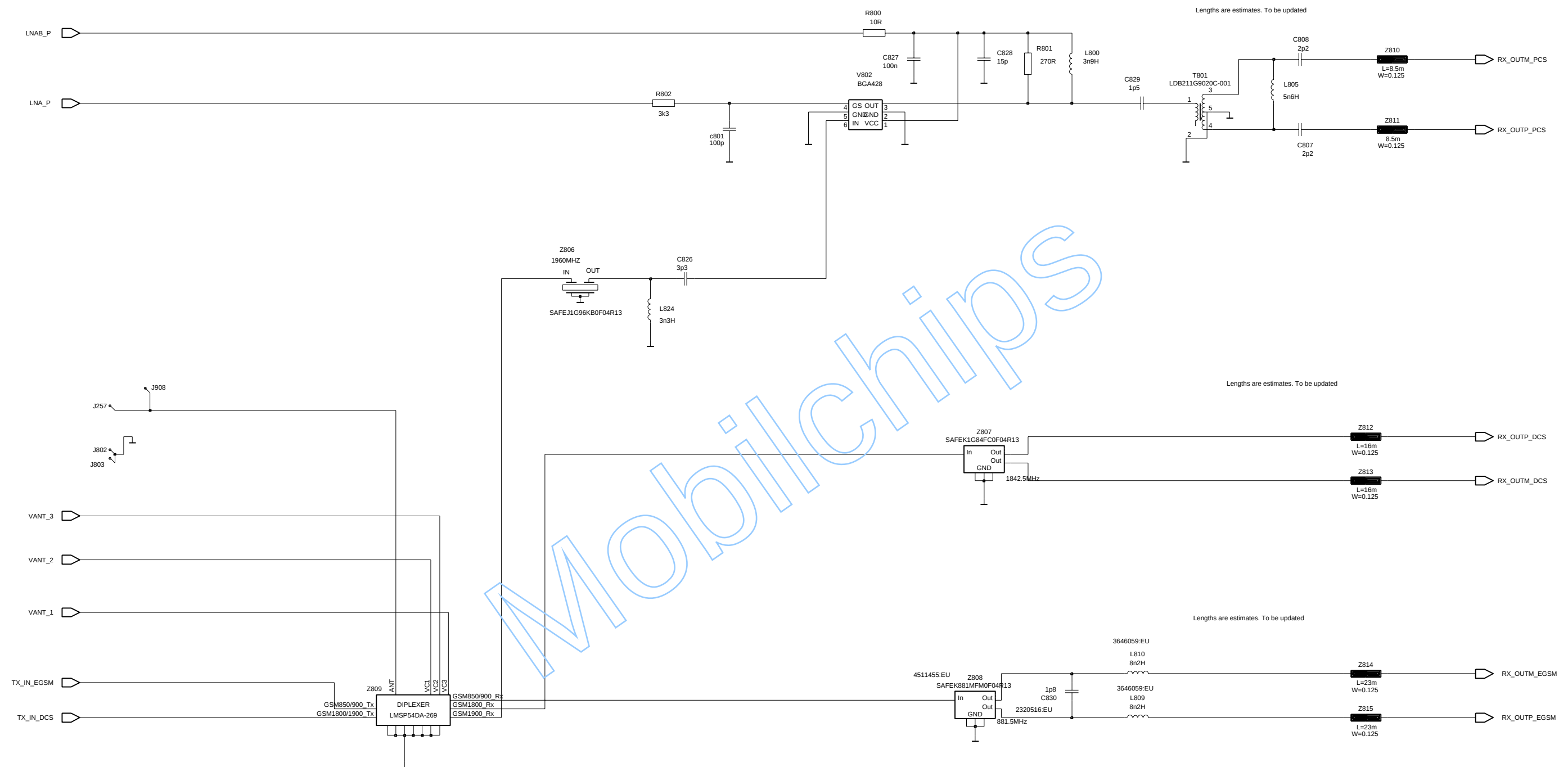
RF shields and vias



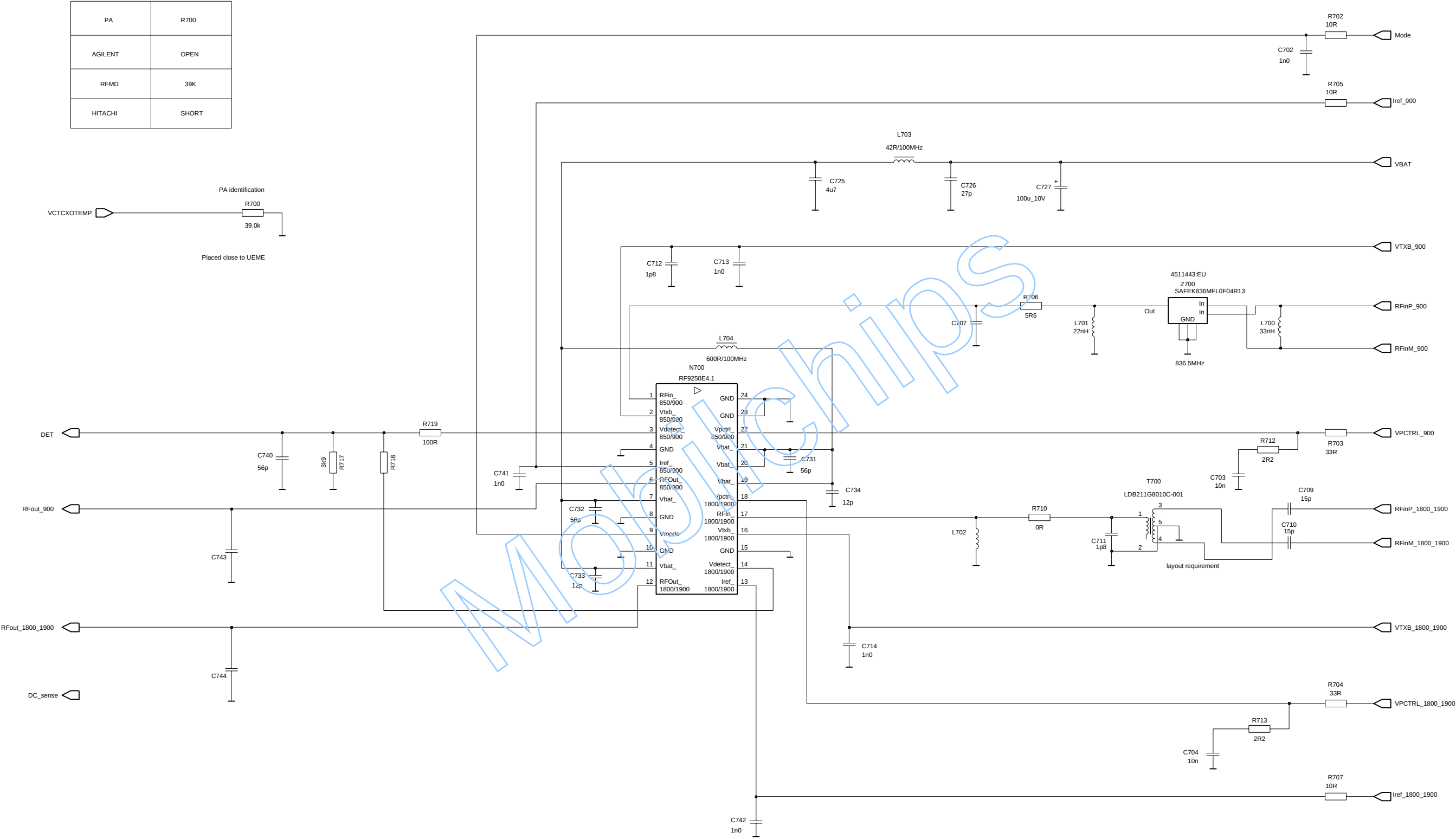
Helgo



Page A-5



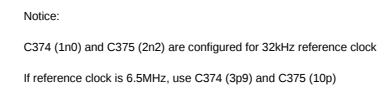
Power amplifier and power detection



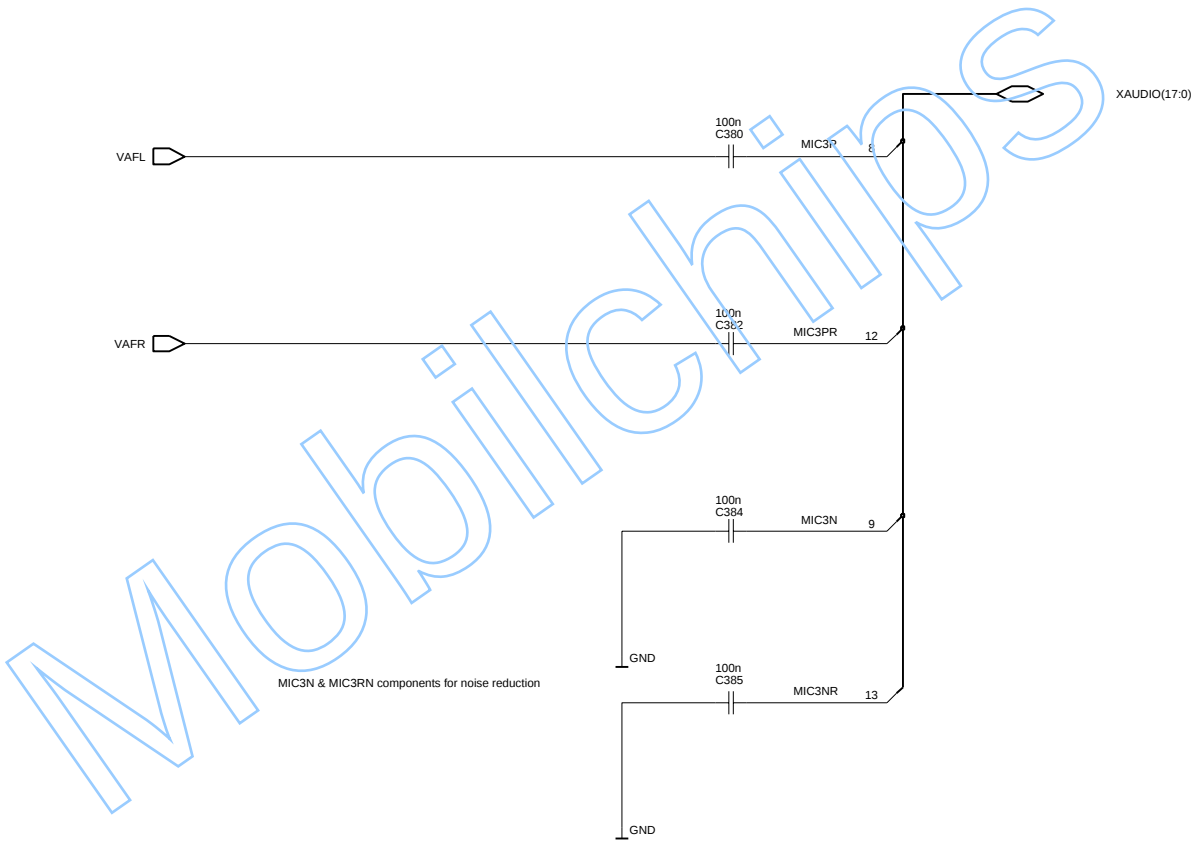
GENIO(31:0)

AUDUEMCTRL(3:0)

PUSL(3:0)

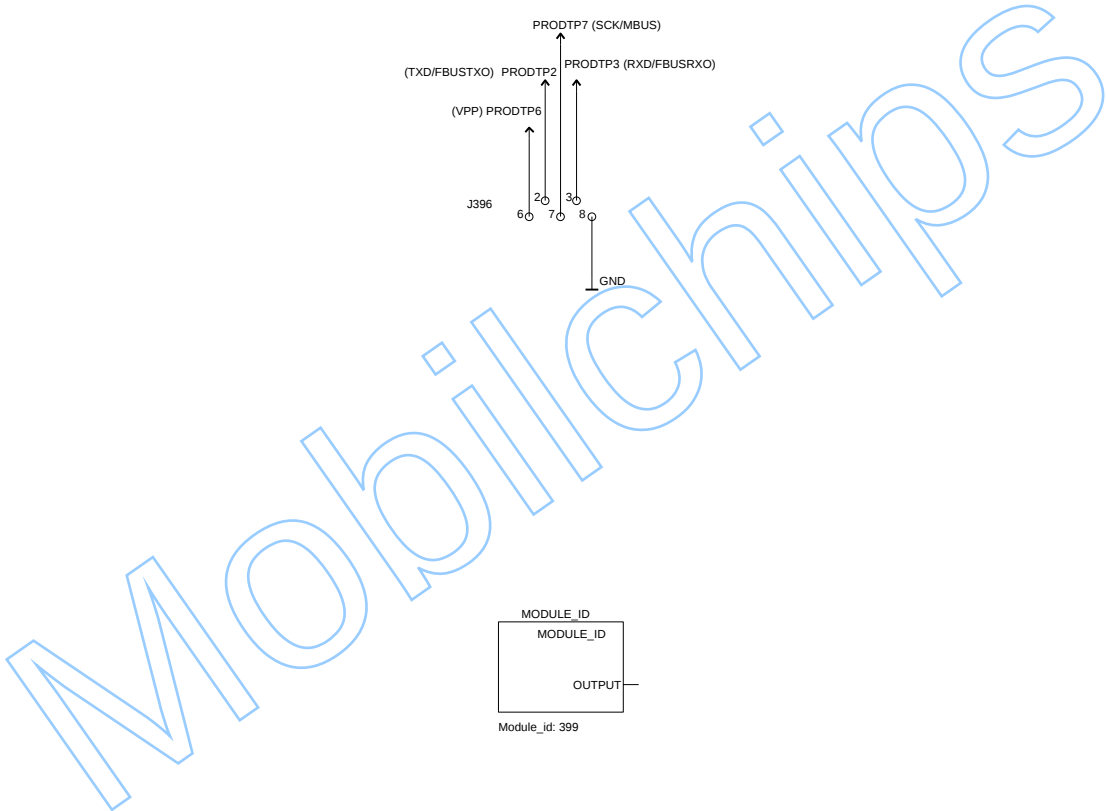


Differential stereo



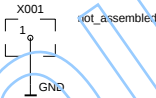
5 pin production test pattern

OUT



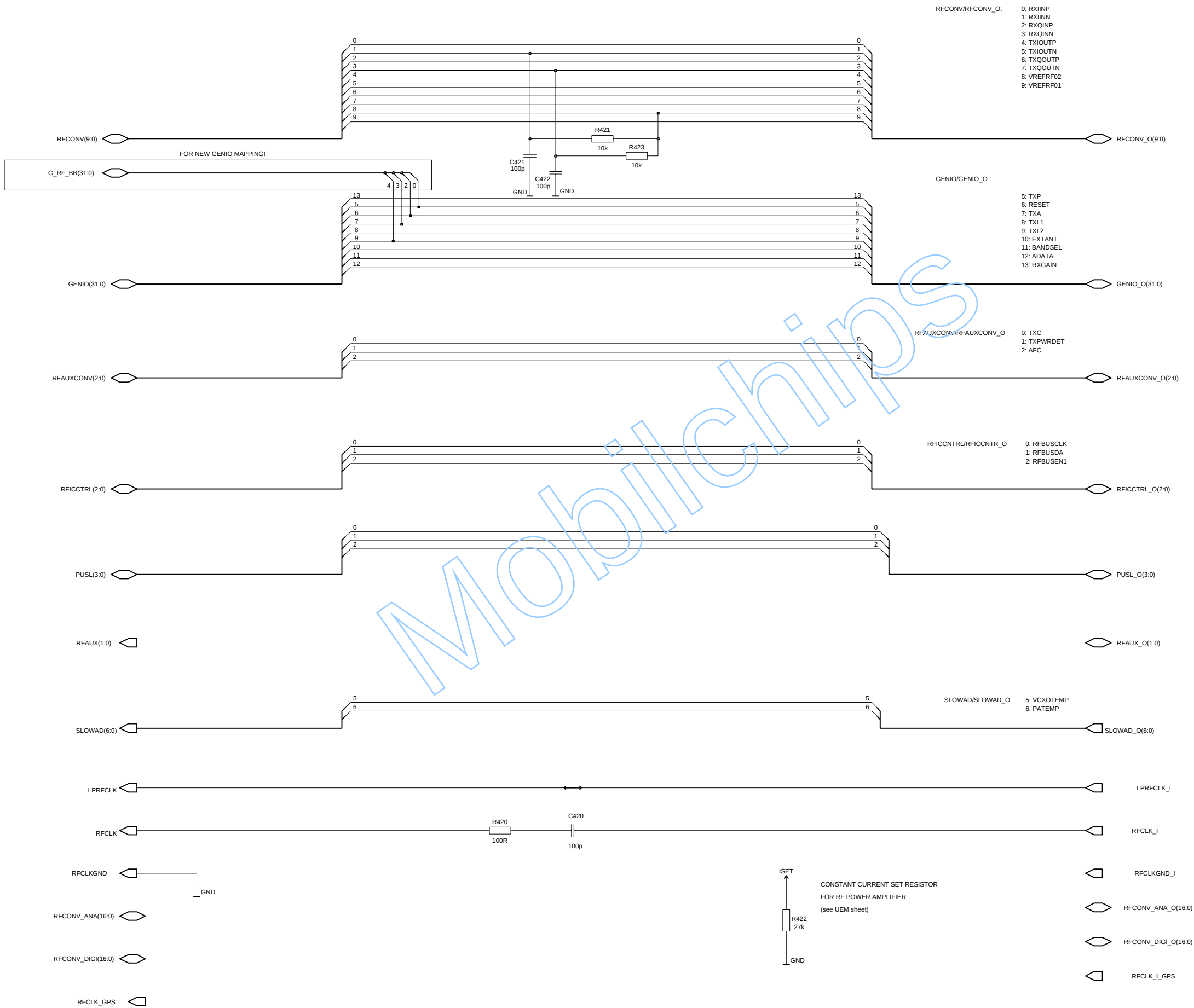
Module ID

Mobilchips

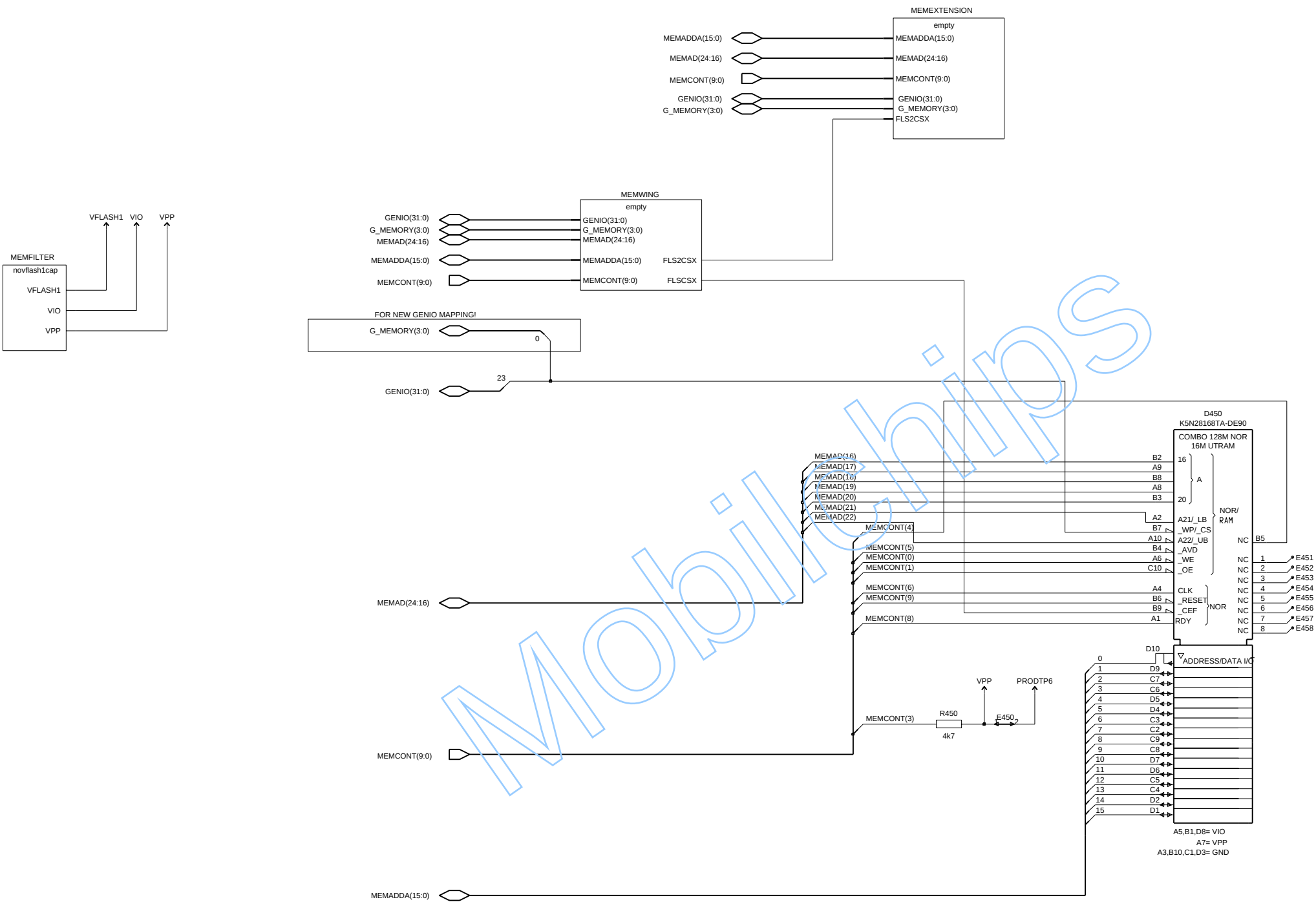


OUTPUT ➤

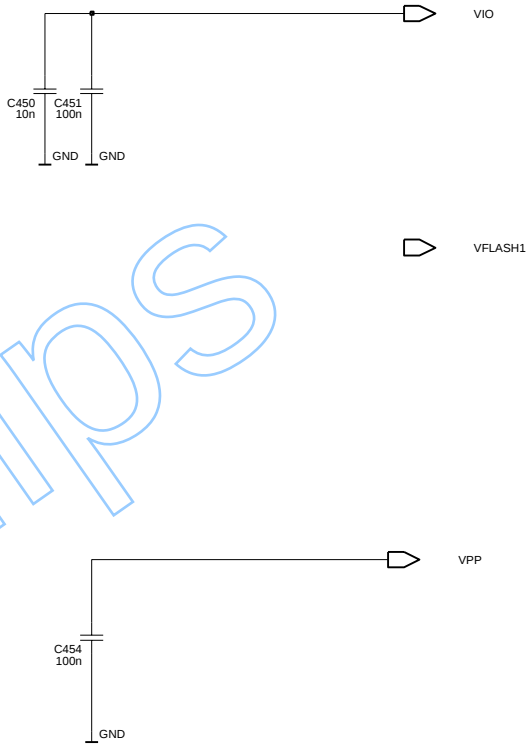
GSM RF-baseband interface



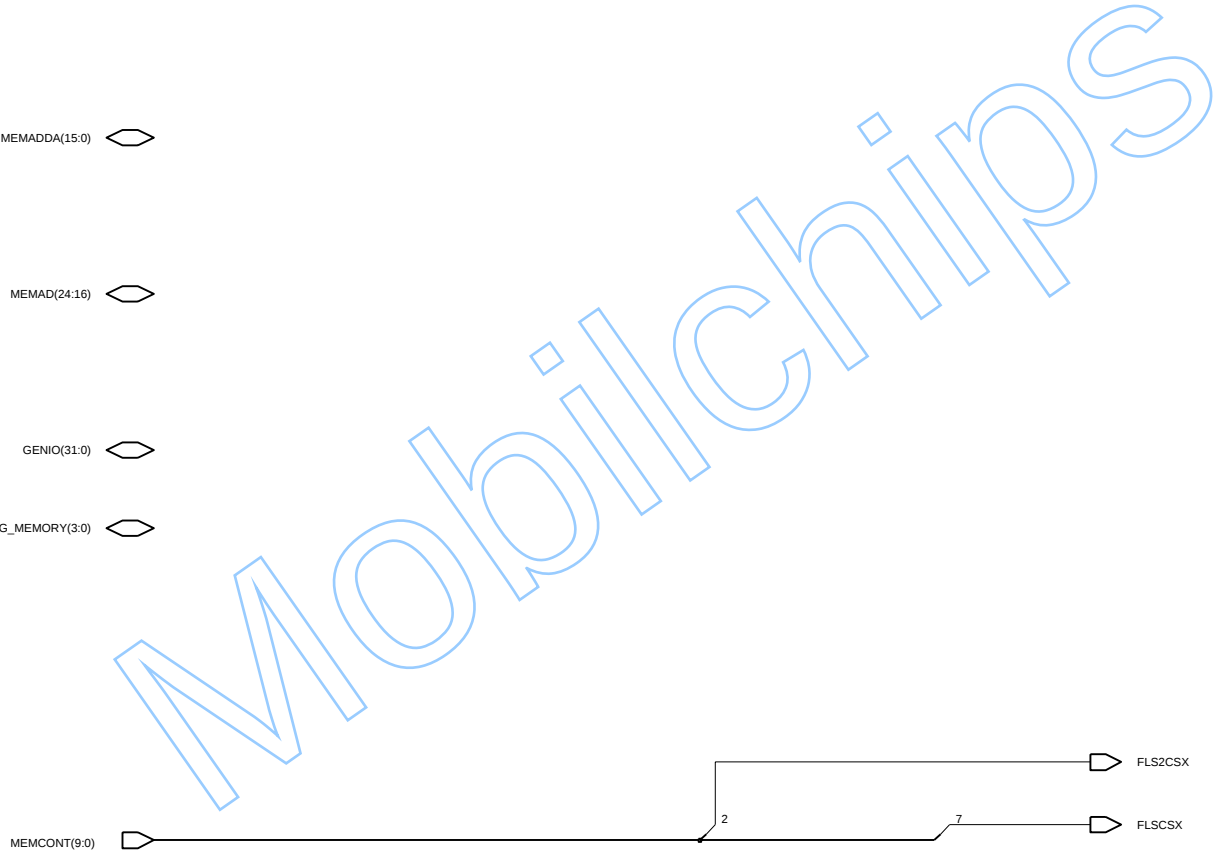
Combo memory 128+16 Mbt pb free



Discrete capacitors

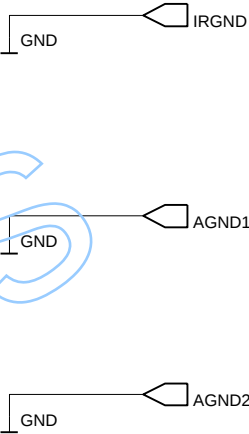


Empty wing sheets

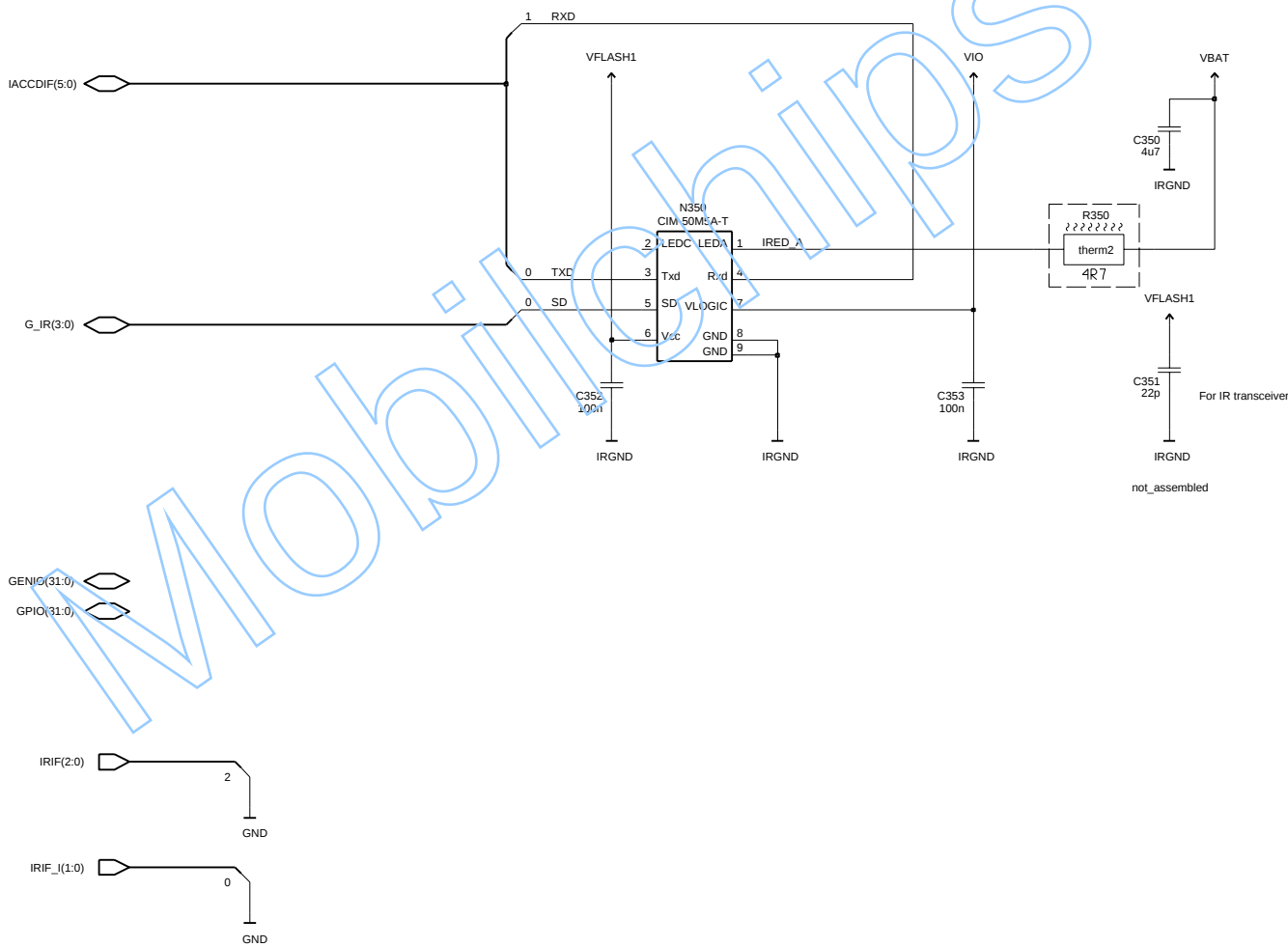


Multi GND

Mobilchips



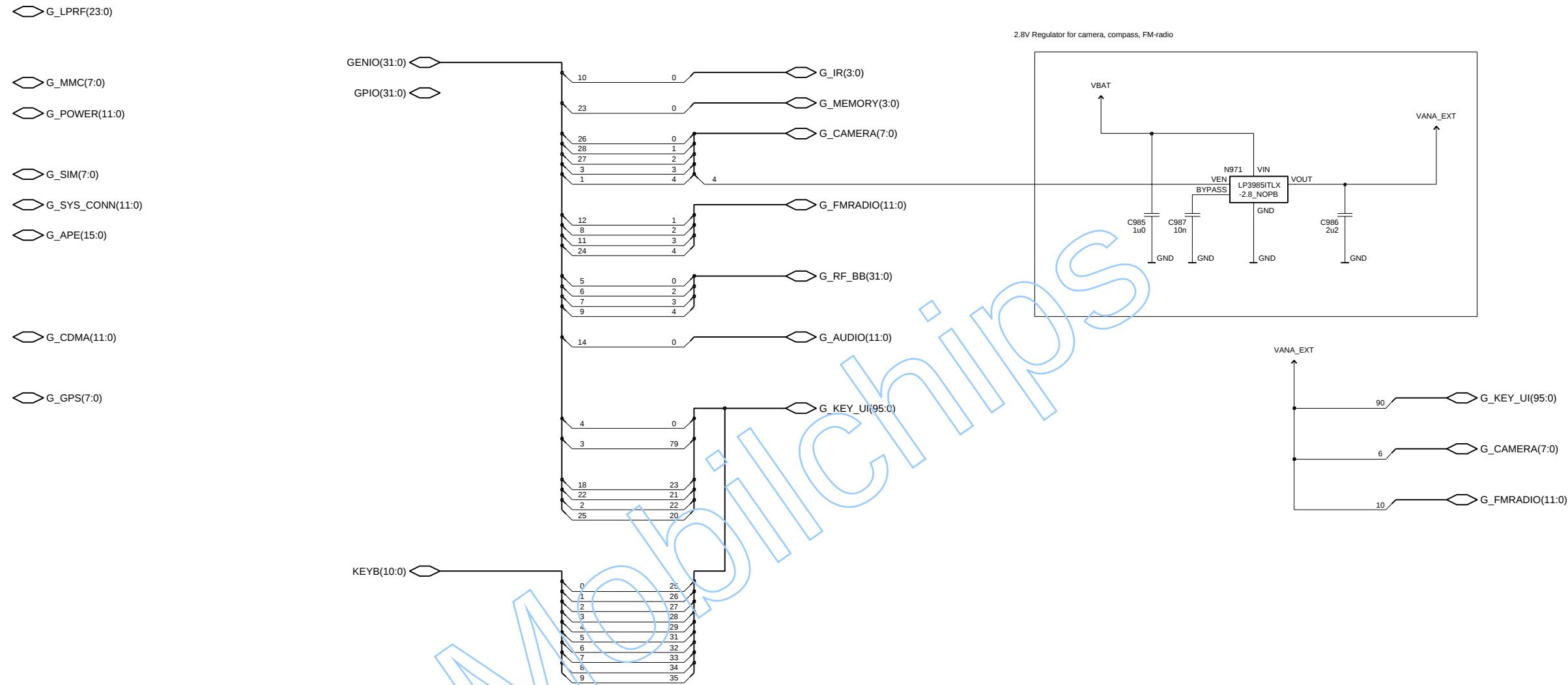
IR Module 1.8V



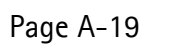
Used references

C	350 - 353
N	350
R	350

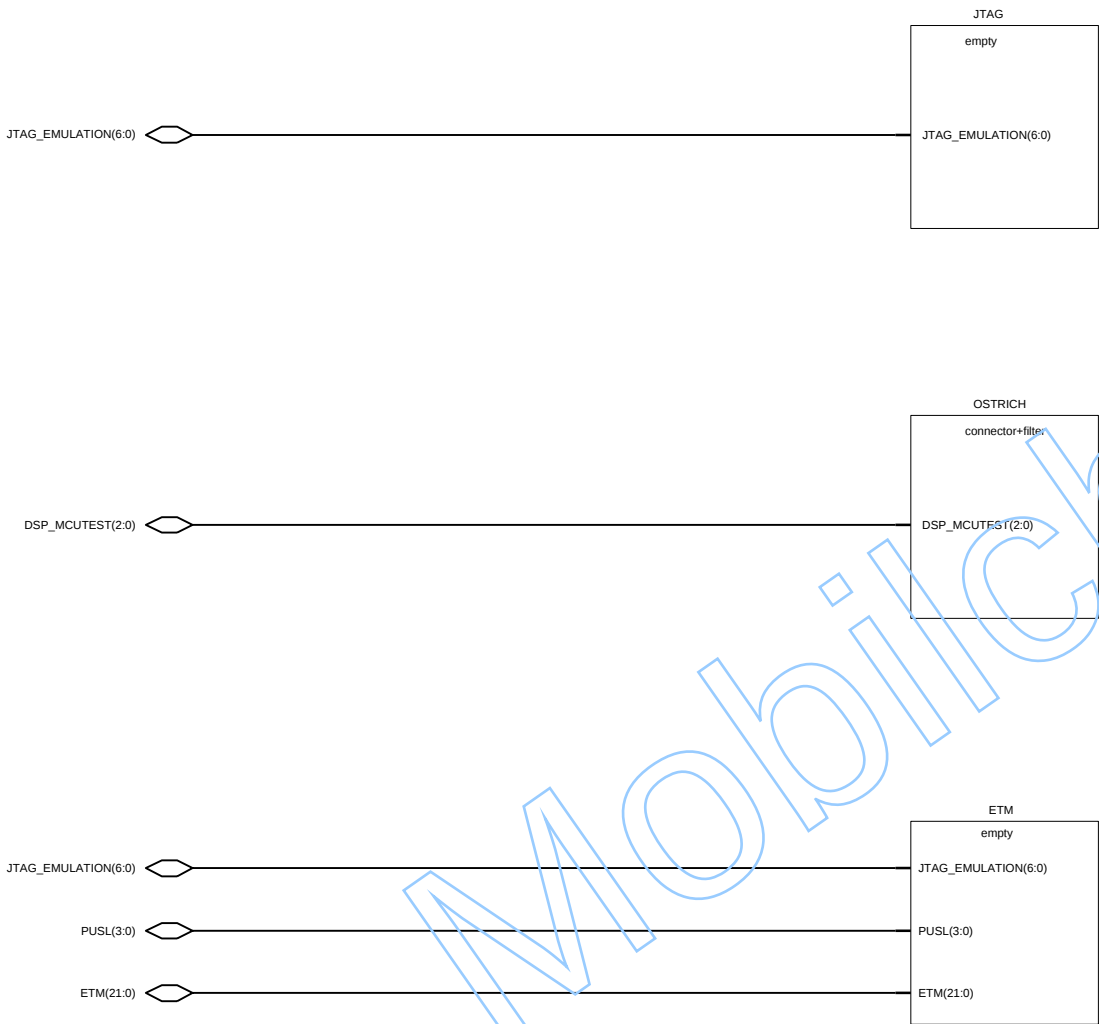
G-map



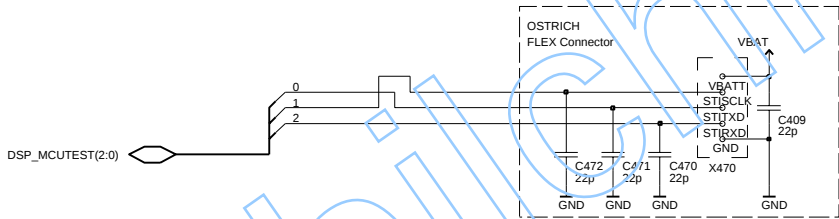
- RFAUX(1.0)
- IHF(1.0)
- USB(2.0)
- PWRONX
- HOOKINT
- APE_GPIO(15.0)
- UIDRV(5.0)
- IACCDIF(5.0)
- SIMIF(3.0)
- AUDUEMCTRL(3.0)
- PUSL(3.0)
- GPIO(31.0)
- G_SYS_CONN(11.0)



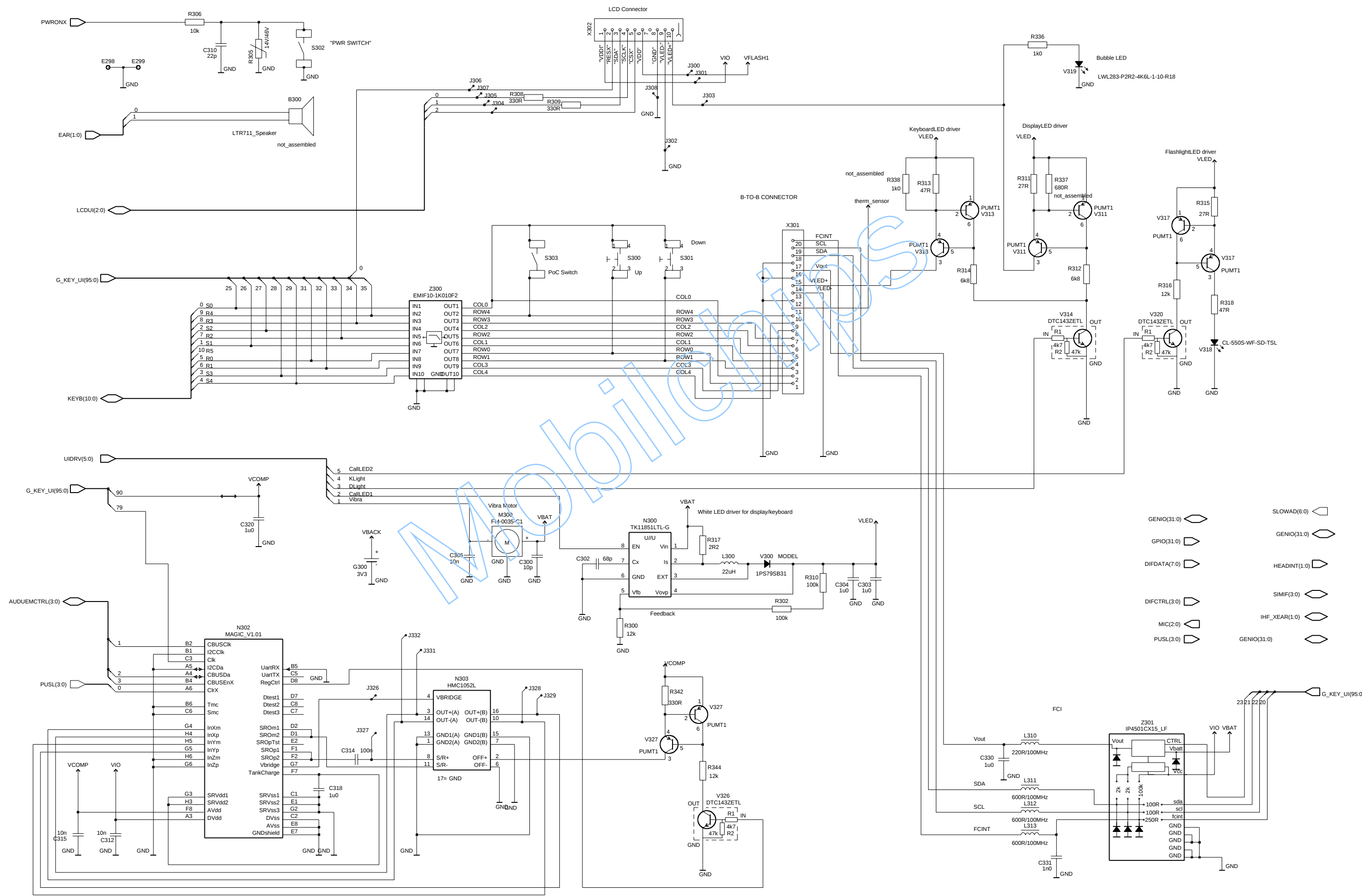
Test and emulator interface



Connector based Ostrich test interface

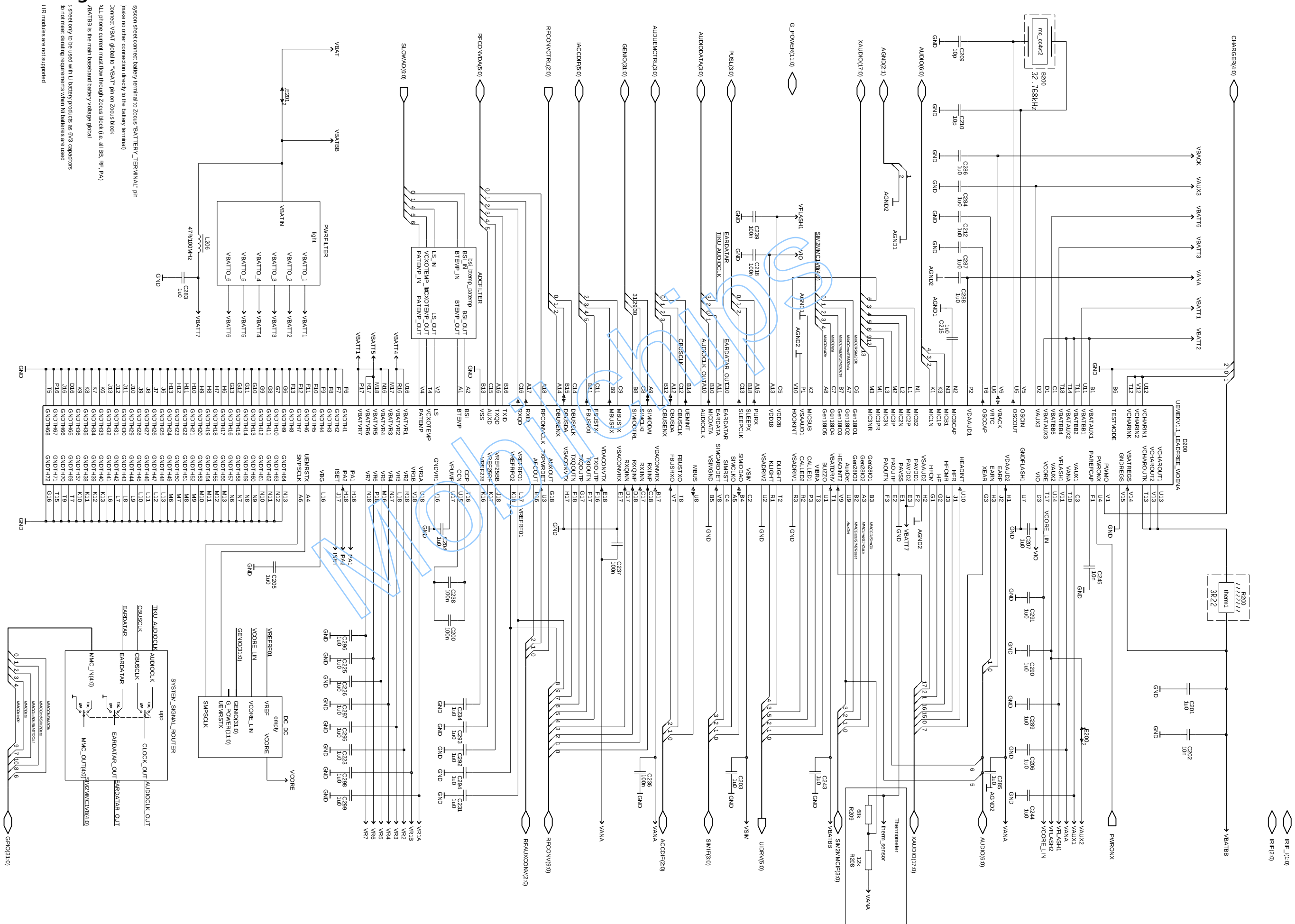


Key UI

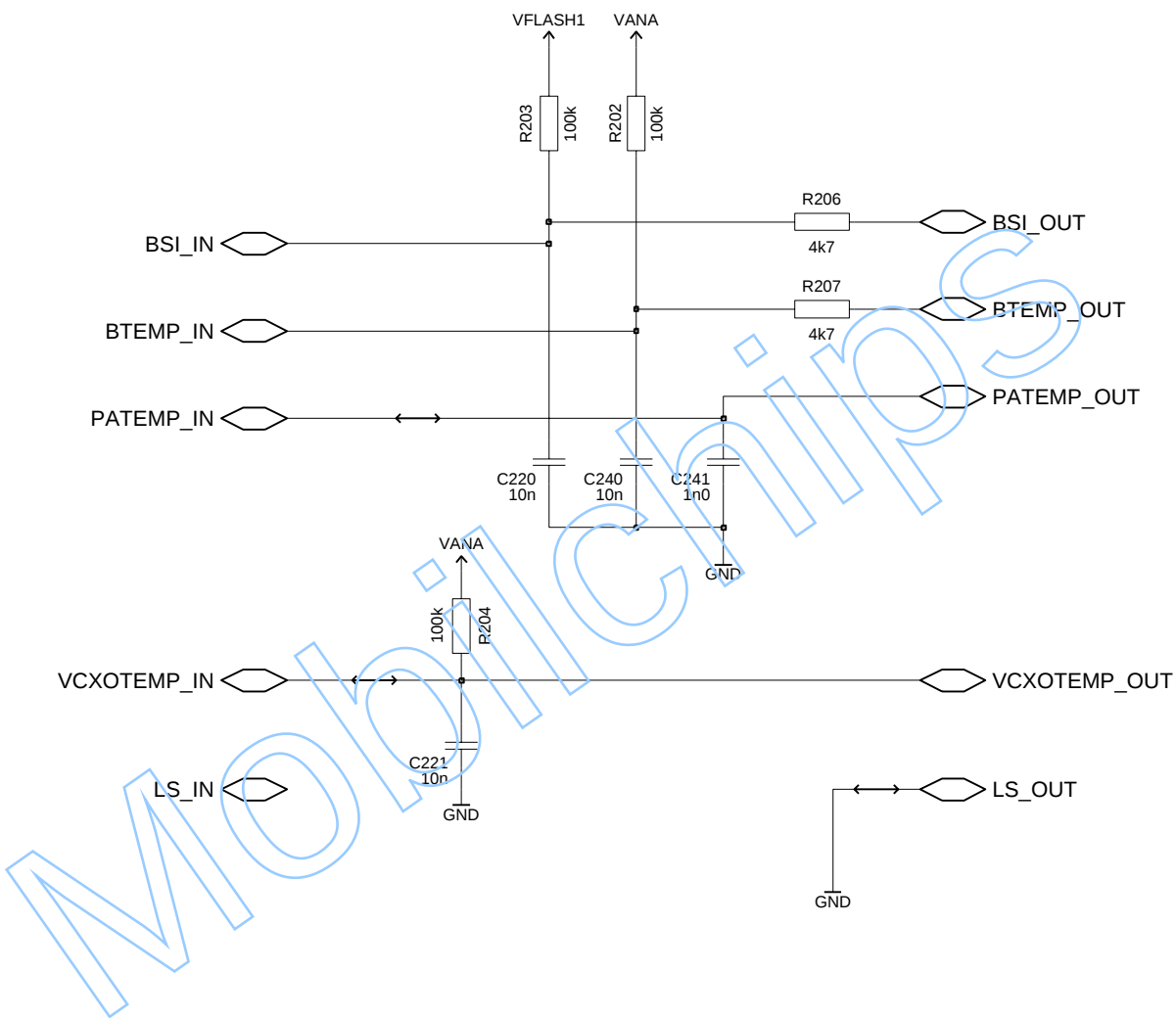


[illegible]

Page A-24



UEME ADC filter block



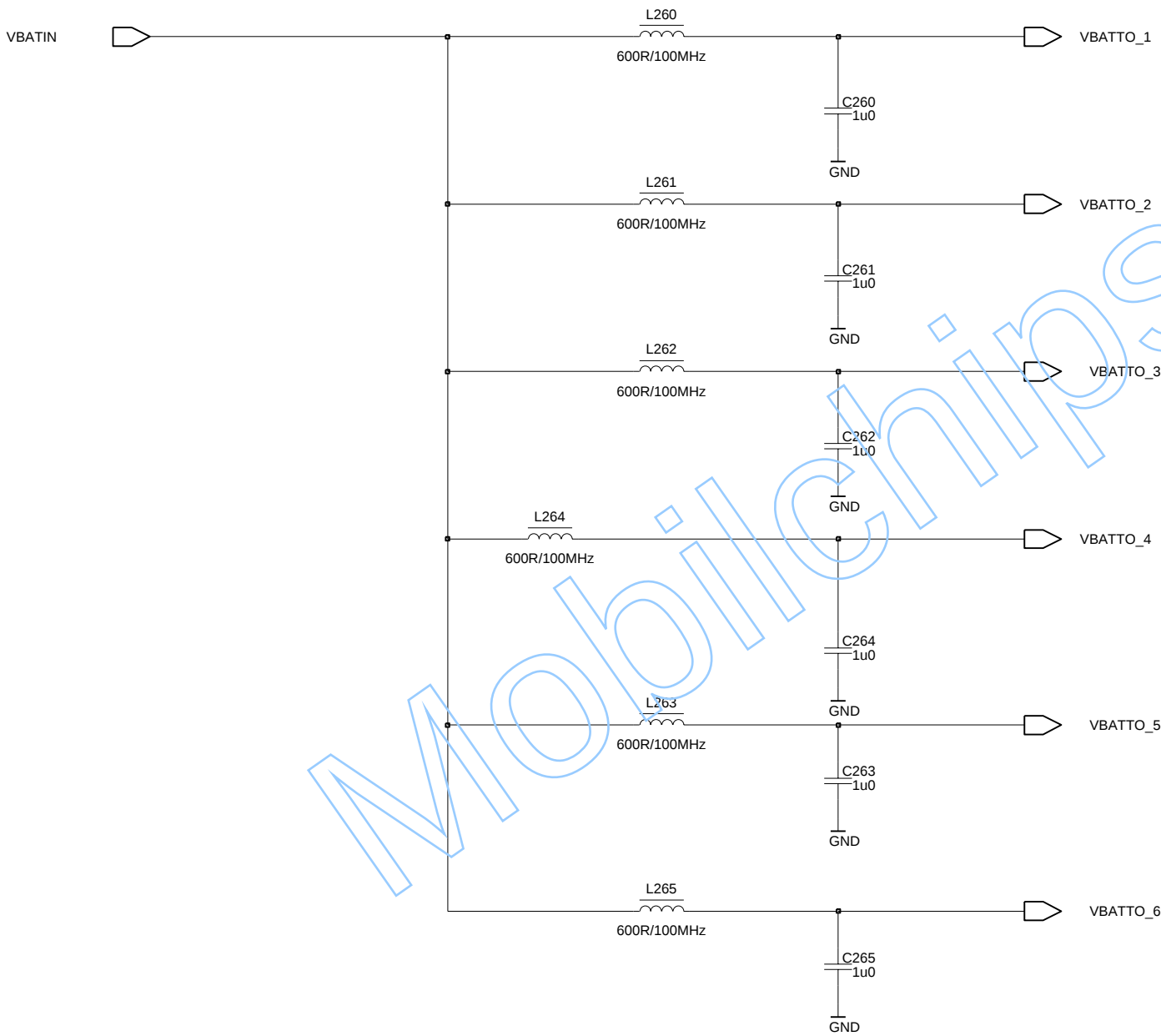
NOTE: Helga RF drives PATEMP directly
so PATEMP does not need a pullup

The diagram illustrates the external interface of the ADXL345 module. It shows the following connections:

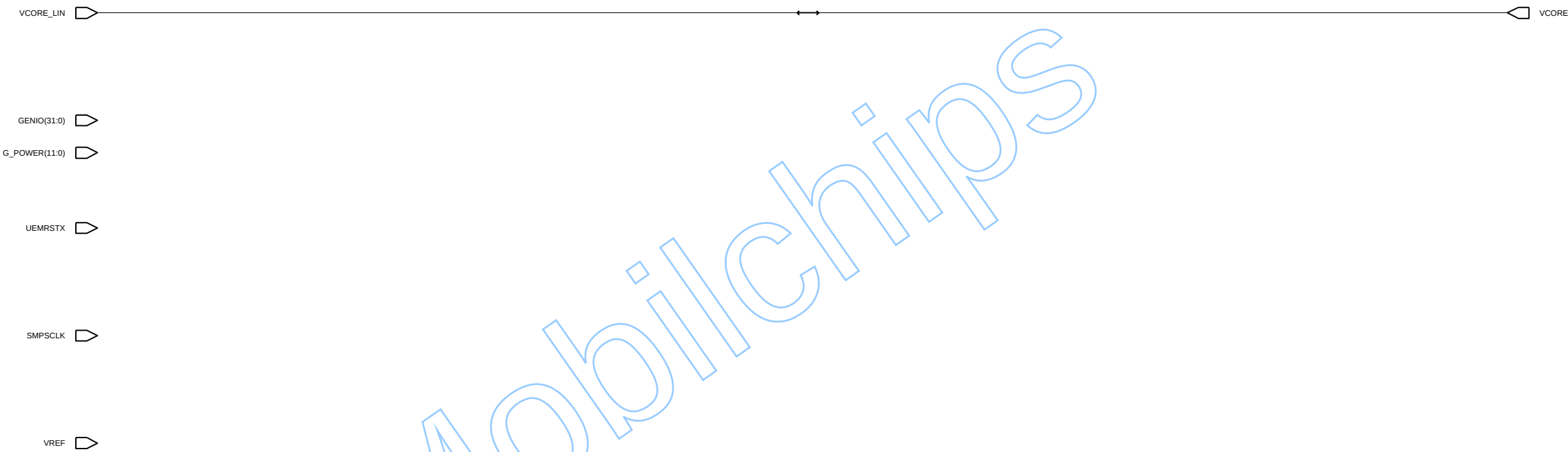
- AUDIOCLK**: A single-line connection.
- CBUSCLK**: A single-line connection.
- EARDATAR**: A single-line connection.
- MMC_IN(4:0)**: A 5-bit bus connection, with lines numbered 0, 1, 2, 3, and 4.
- GND**: Ground connection.
- MMC_OUT(4:0)**: A 5-bit bus connection, with lines numbered 0, 1, 2, 3, and 4.

The diagram also shows a 5-bit bus labeled **MMC_IN(4:0)** connected to a 5-bit bus labeled **MMC_OUT(4:0)**. The bus lines are numbered 0, 1, 2, 3, and 4. The GND connection is shown at the bottom.

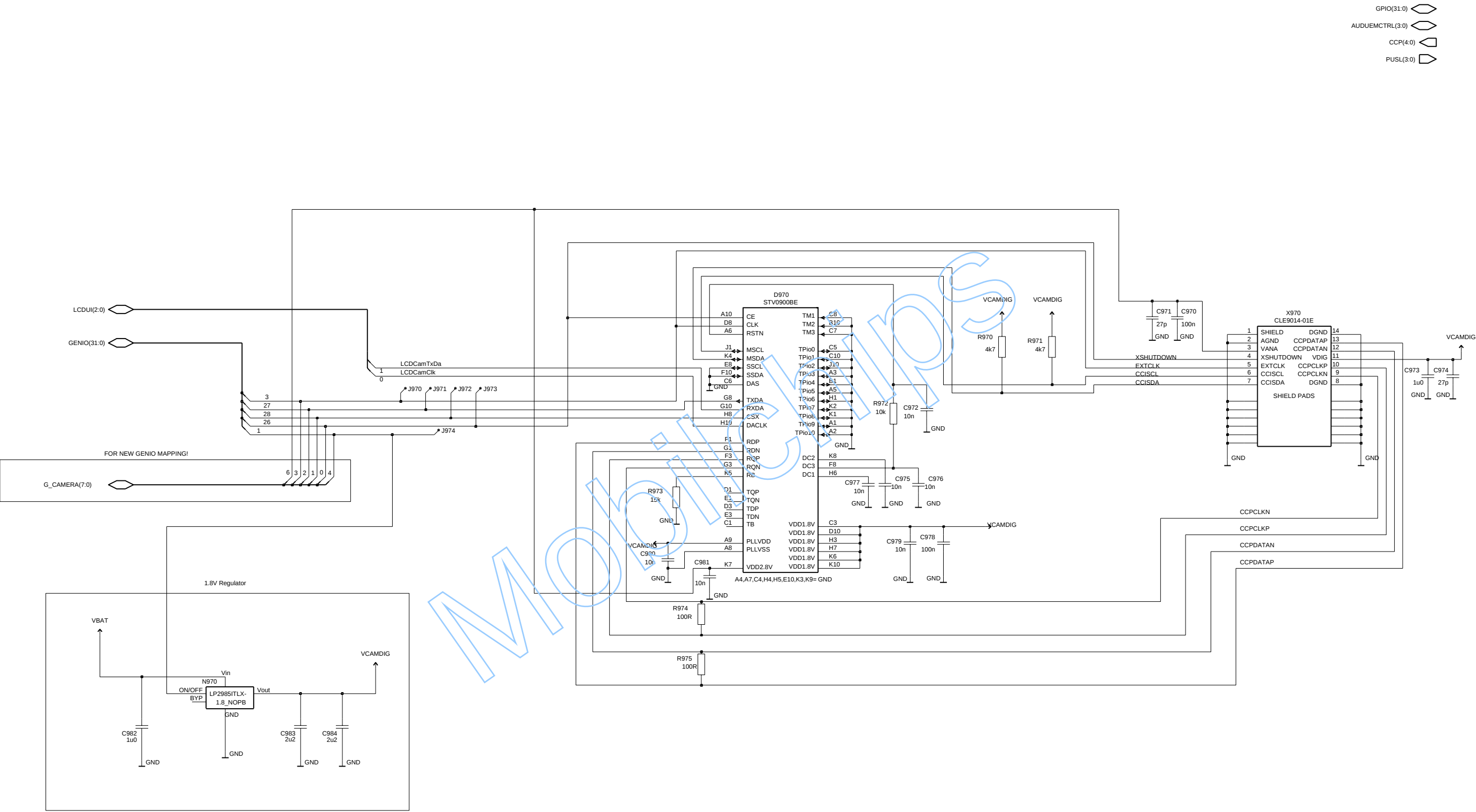
Light filtering



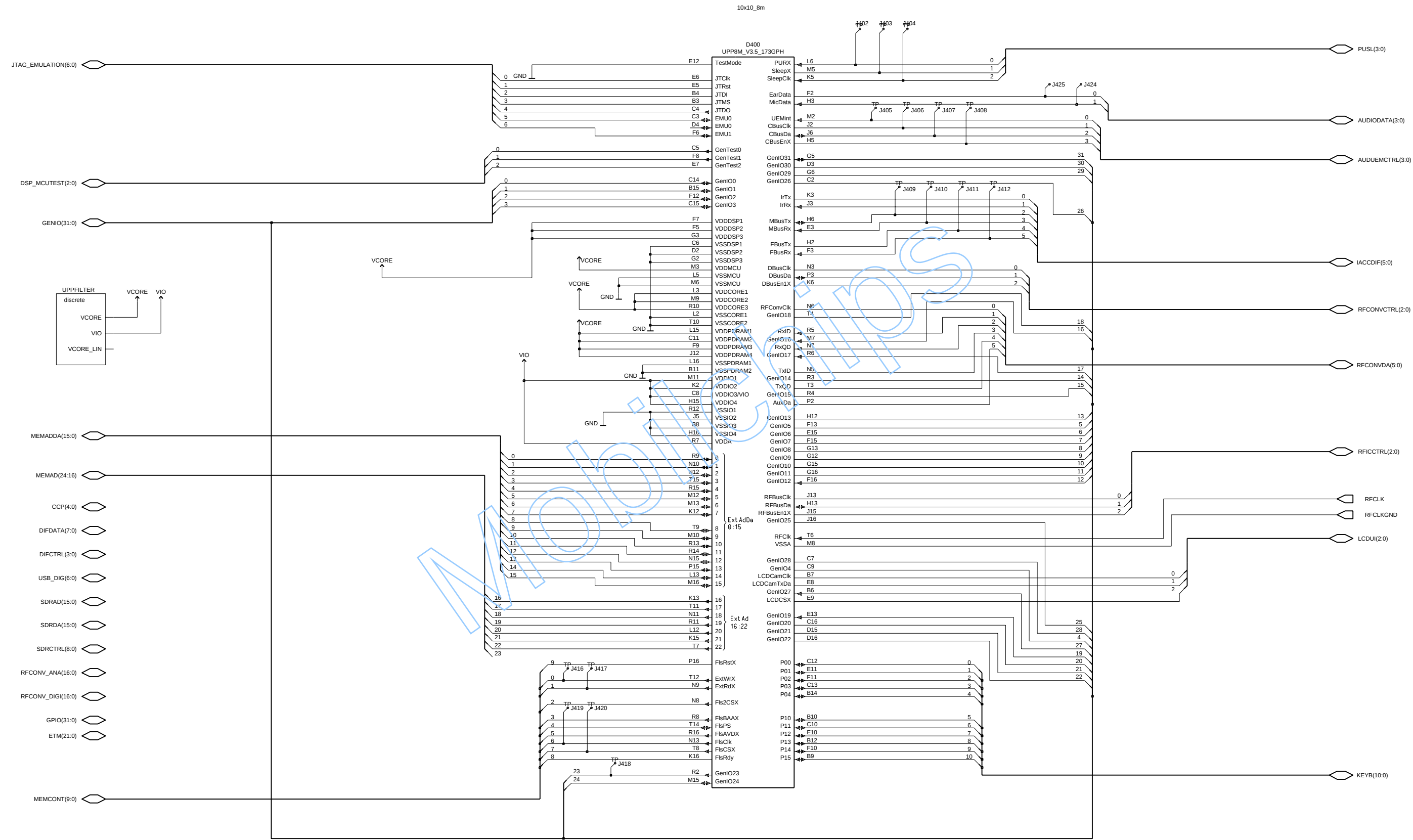
DC/DC converter



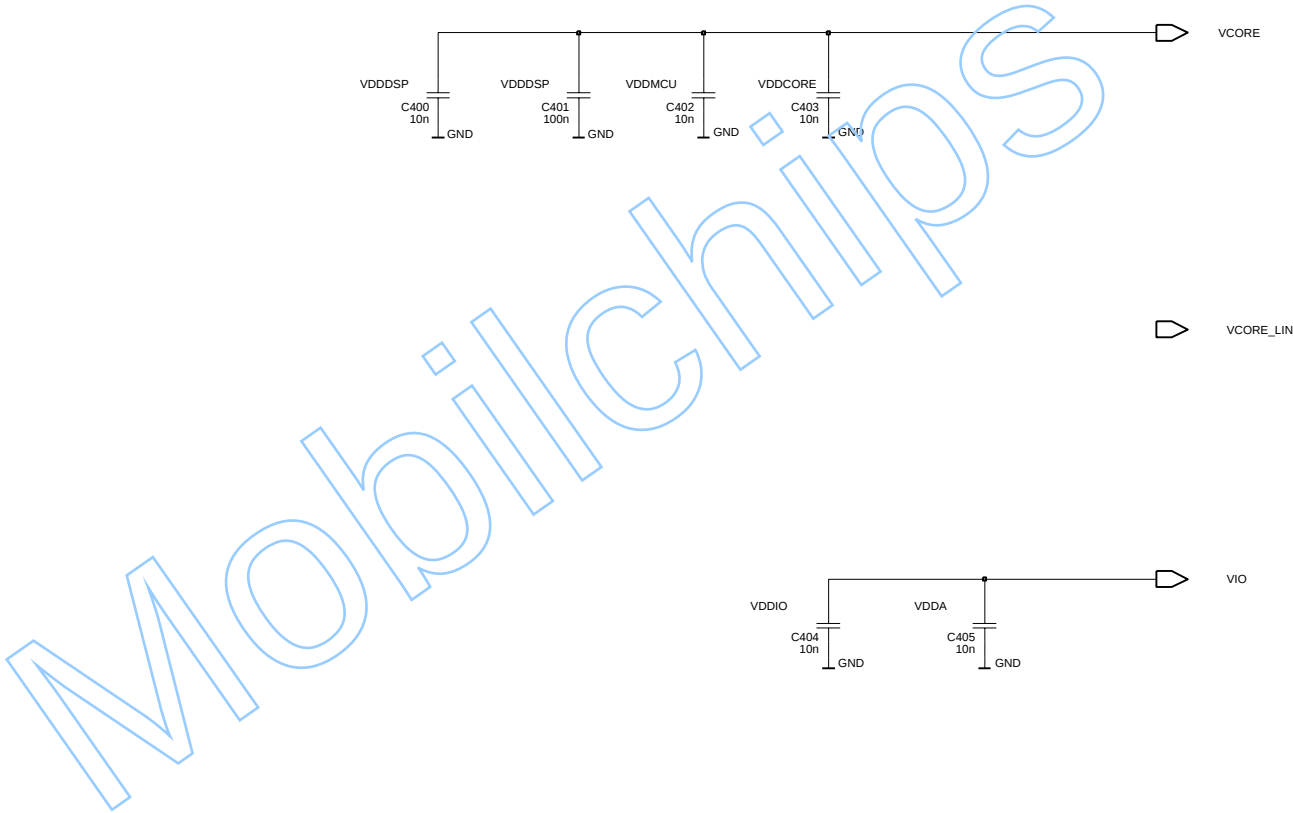
Camera



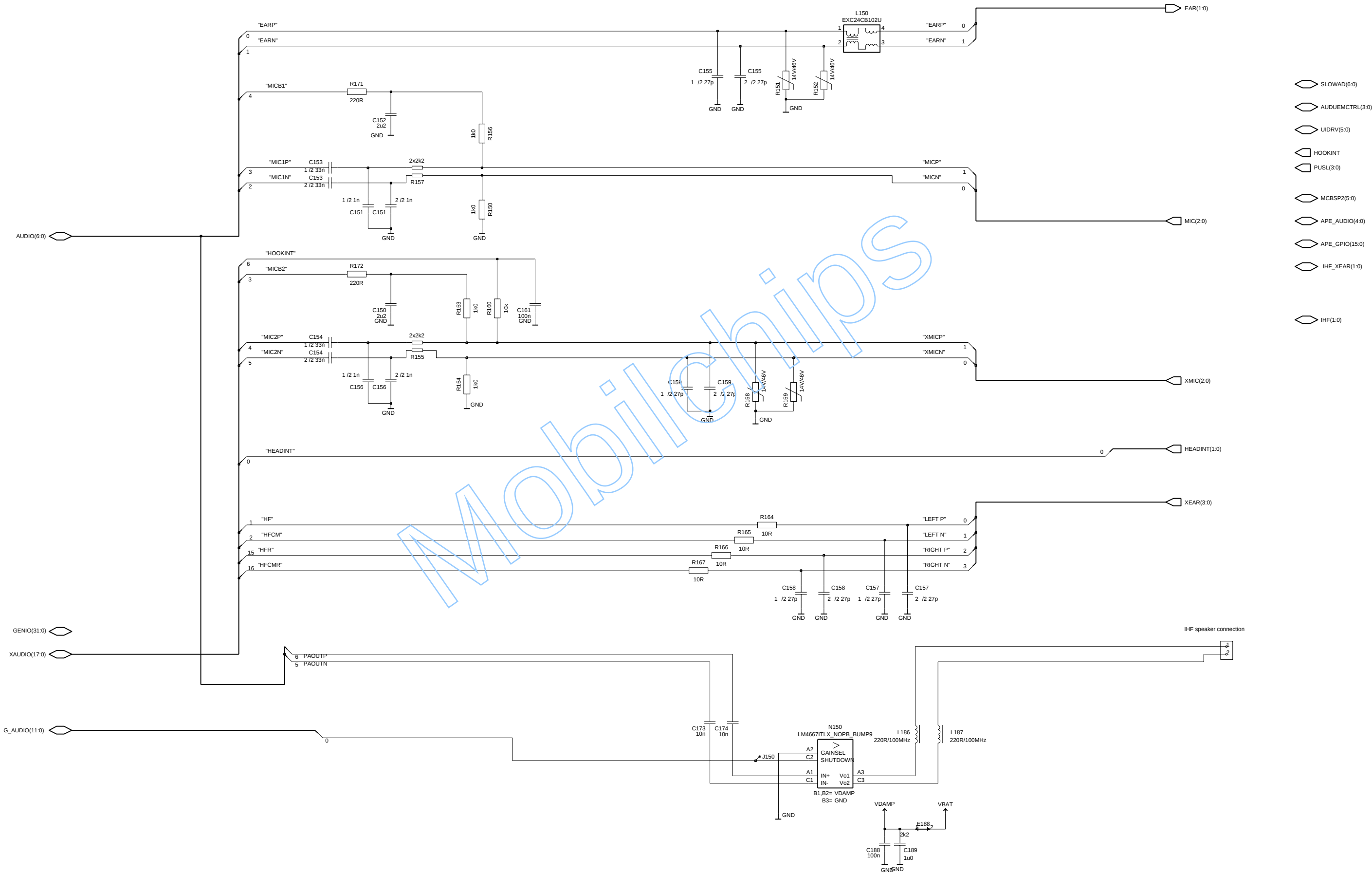
UPP 8M implementation



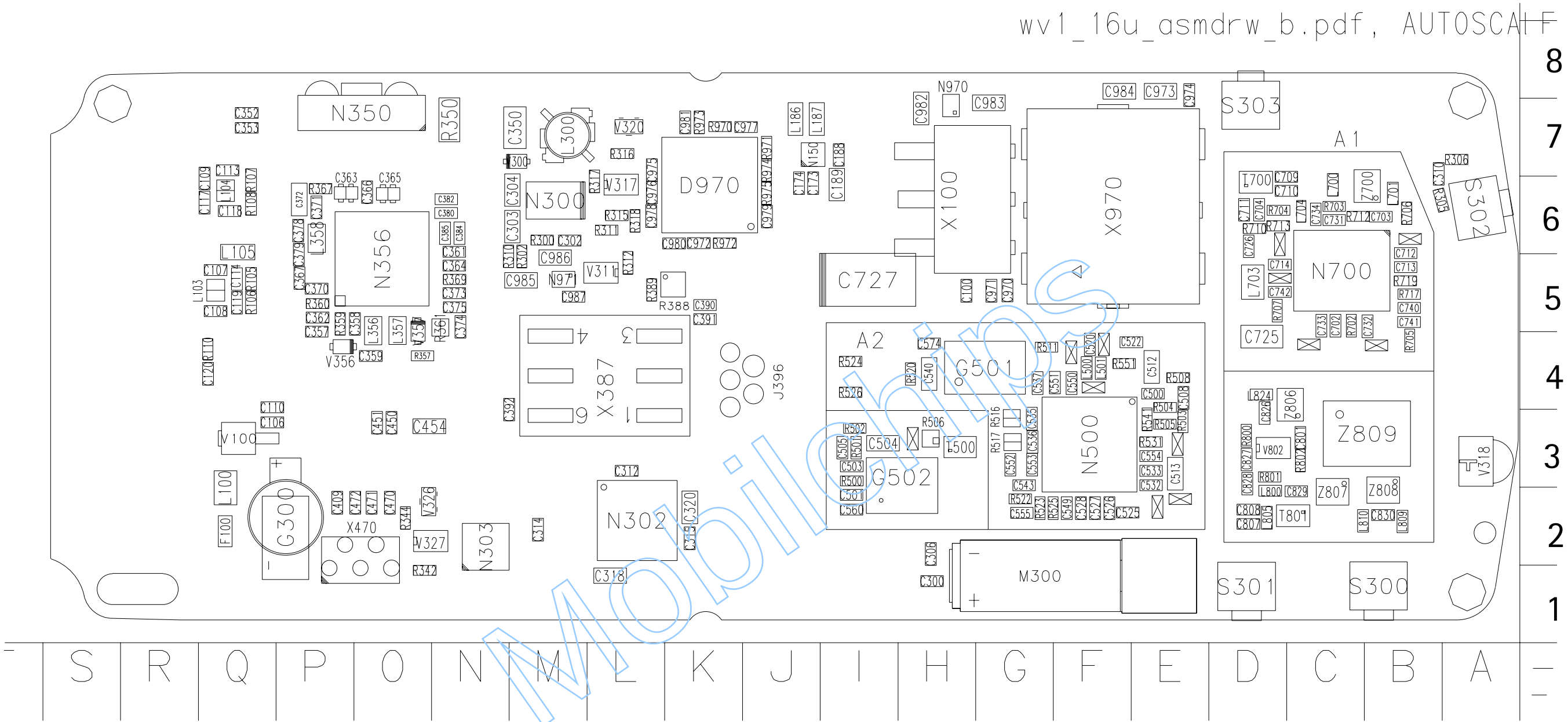
Discrete decoupling capacitors for UPP



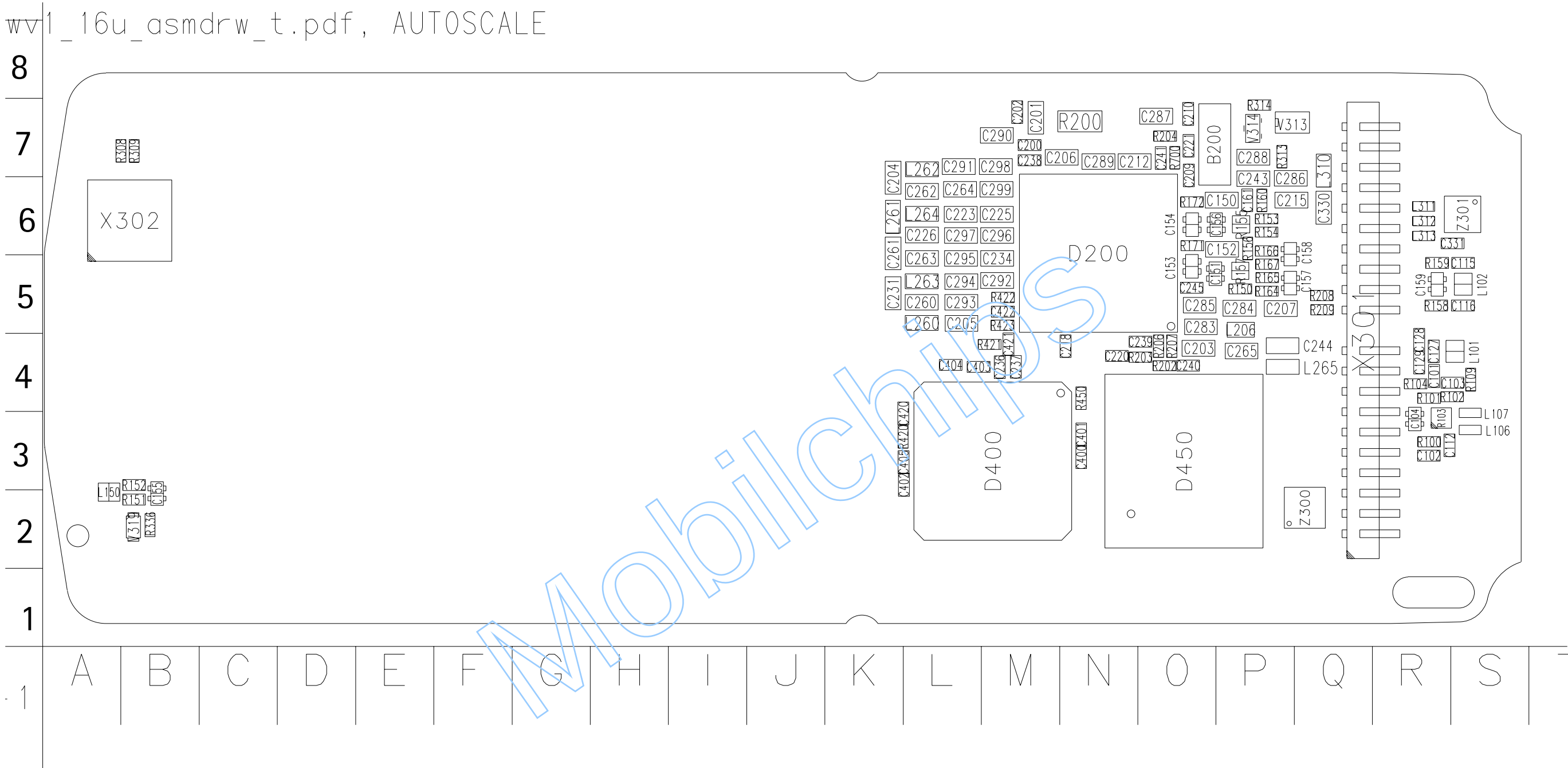
Audio



NPL-4, component layout, bottom

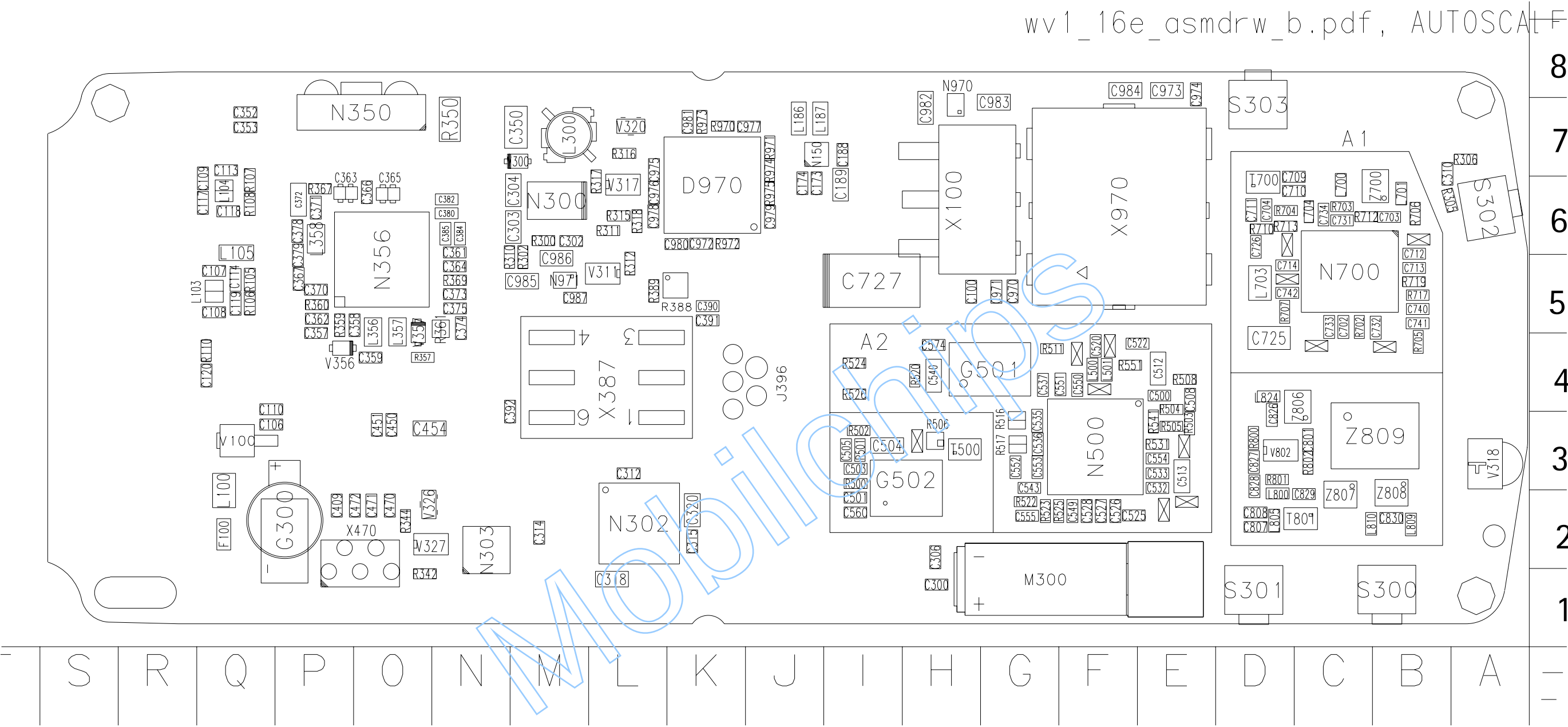


NPL-4, component layout, top



NPL-5, component layout, bottom

wv1_16e_asmdrw_b.pdf, AUTOSCAT



www.1_16e_asmdrw_t.pdf, AUTOSCALE

