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HSF Property:ROHS or Halogen-Free(5L3?)

RALPH1.1

SI-2 BUILD

2012.1.3

12-12-2011		
DATE	CHANGE NO.	REV

	EE	DATE	POWER	DATE	INVENTEC			
DRAWER								
DESIGN								
CHECK								
RESPONSIBLE								
SIZE					VER	SIZE	CODE	DOC NUMBER
FILE NAME						C	CS	1310A24893-0
PIN	David Chang					SHEET	#	67

LV1.1_Lauren
Everest Main Board

REV
AX1

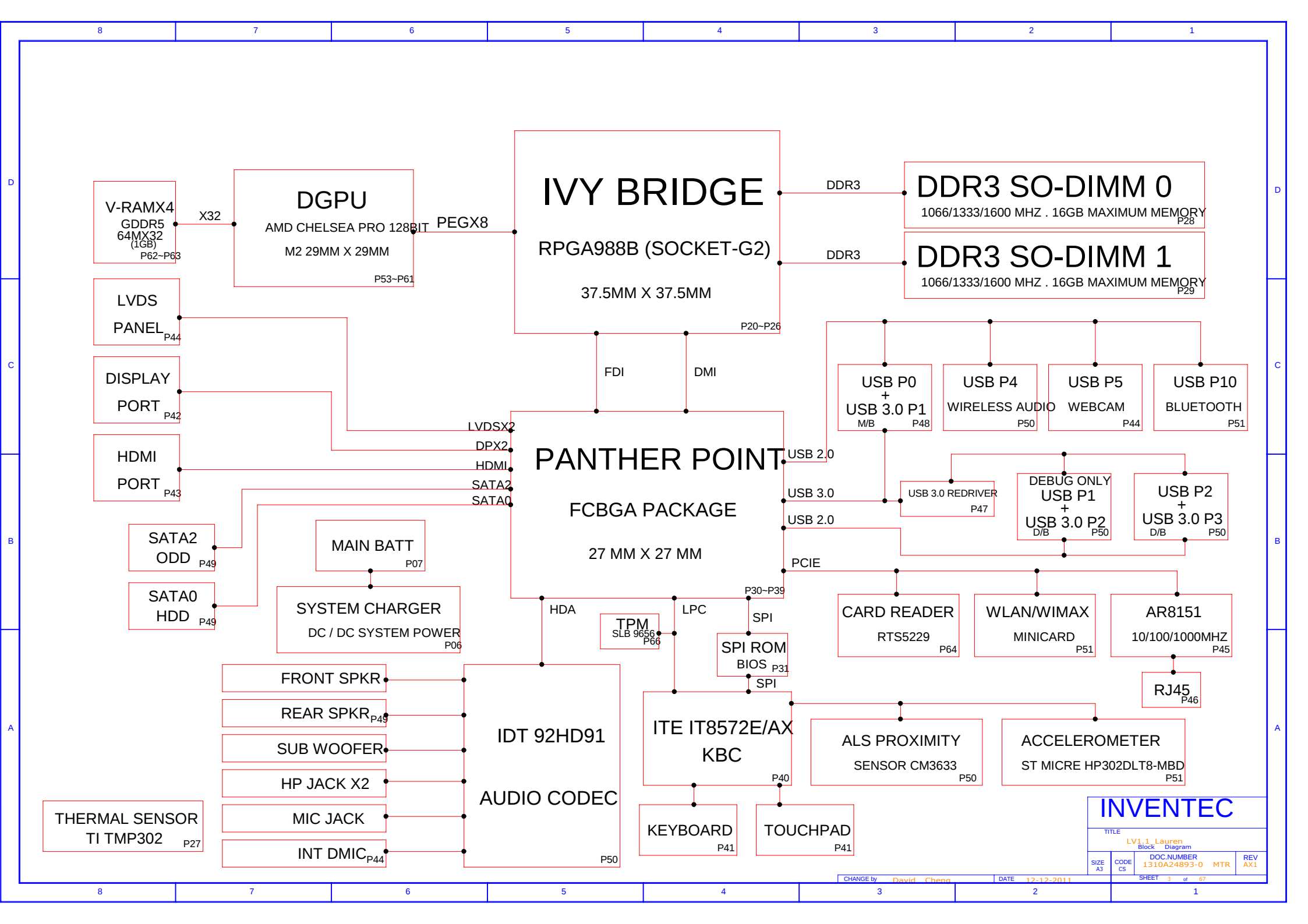
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07. BATTERY CONN	40. KBC
08. VRP5V0A & VRP3V3A	41. KEYBOARD CONN & DC-JACK LED & TOUCHPAD CONN
09. VRP1V5 & P0V75S	42. DISPLAY PORT
10. VRP1V05S_VCCP	43. HDMI
11. VRP1V8S	44. LVDS CONN
12. VRPVSA	45. LAN
13. PVCORE & PVAXG	46. RJ45
14. PVCORE & PVAXG	47. USB30 REDRIVER
15. VRPVCORE_DGPU	48. USB30 CONN1 & REAR SPEAKER CONN
16. PVDDCI	49. AU/B CONN & HDD CONN & HD CONN & PBN CONN & ODD REDRIVER
17. PVPCIE	50. WIRELESS AUDIO/B CONN & MUTE BUTTON & LDPS/B CONN CR/B CONN & JOG DIAL CONN & AU-USB30 CONN
18. POWER PAD	51. WLAN CONN & BLUETOOTH CONN & HARDDRIVE PROTECTION
19. P5V0S & P3V3S & P1V5S	52. SCREWS
20. CPU	53. GPU
21. CPU-1	54. GPU-1-PCIE INTERFACE
22. CPU-2-POWER	55. GPU-2-BACO
23. CPU-3-PCIE-GRAPHICS & DMI & FDI & EDP	56. GPU-3-GPIO & THERMAL
24. CPU-4-MEMORY BUS	57. GPU-4-MEMORY CHANNEL A
25. CPU-5-POWER-GRAPHICS	58. GPU-5-MEMORY CHANNEL B
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28. SO-DIMM0	61. GPU-8-POWER & GPU LVDS
29. SO-DIMM1	62. GPU-9-CHANNEL A MEMORY (GDDR5)
30. PCH	63. GPU-10-CHANNEL B MEMORY (GDDR5)
31. PCH-1-RTC & IHDA & SPI & LPC & SATA & EEPROM	64. CARD READER BOARD
32. PCH-2-PCIE & CLOCKS & SMBUS	65. ODD/B & WIRELESS AUDIO/B & POWER BUTTON/B
33. PCH-3-DMI & FDI & POWER MANAGEMENT	66. TPM
	67. EMI

INVENTEC

TITLE			
LV1.1 Lauren Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0	AX1
CHANGE by		DATE	SHEET
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V-RAMX4
GDDR5
64MX32
(1GB)
P62~P63

LVDS
PANEL
P44

DISPLAY
PORT
P42

HDMI
PORT
P43

SATA2
ODD
P49

SATA0
HDD
P49

DGPU

AMD CHELSEA PRO 128BIT
M2 29MM X 29MM
P53~P61

MAIN BATT
P07

SYSTEM CHARGER
DC / DC SYSTEM POWER
P06

FRONT SPKR
REAR SPKR
SUB WOOFER
HP JACK X2
MIC JACK
INT DMIC

THERMAL SENSOR
TI TMP302
P27

IDT 92HD91
AUDIO CODEC
P50

IVY BRIDGE
RPGA988B (SOCKET-G2)
37.5MM X 37.5MM
P20~P26

PANTHER POINT
FCBGA PACKAGE
27 MM X 27 MM
P30~P39

TPM
SLB 9656
P66

SPI ROM
BIOS
P31

ITE IT8572E/AX
KBC
P40

KEYBOARD
P41

TOUCHPAD
P41

ALS PROXIMITY
SENSOR CM3633
P50

ACCELEROMETER
ST MICRE HP302DLT8-MBD
P51

CARD READER
RTS5229
P64

WLAN/WIMAX
MINICARD
P51

AR8151
10/100/1000MHZ
P45

RJ45
P46

USB P0
+
USB 3.0 P1
M/B
P48

USB P4
WIRELESS AUDIO
P50

USB P5
WEBCAM
P44

USB P10
BLUETOOTH
P51

USB 3.0 REDRIVER
P47

DEBUG ONLY
USB P1
+
USB 3.0 P2
D/B
P50

USB P2
+
USB 3.0 P3
D/B
P50

DDR3 SO-DIMM 0
1066/1333/1600 MHZ . 16GB MAXIMUM MEMORY
P28

DDR3 SO-DIMM 1
1066/1333/1600 MHZ . 16GB MAXIMUM MEMORY
P29

INVENTEC				
TITLE				
LV1.1 Lauren Block Diagram				
SIZE	CODE	DOC NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1
SHEET		3	of	67

D

C

B

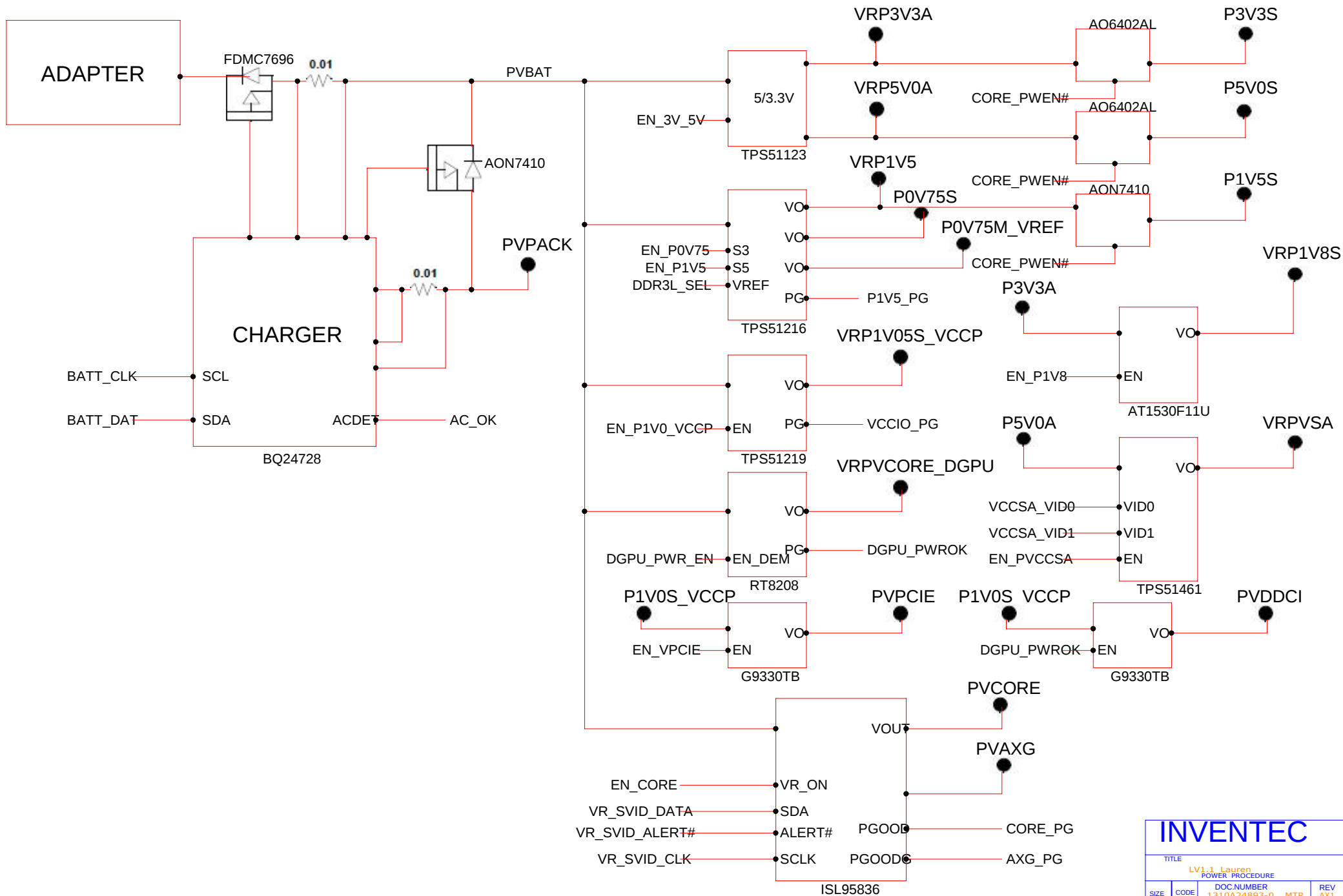
A

D

C

B

A



INVENTEC				
TITLE				
LV101 Lauren POWER PROCEDURE				
SIZE	CODE	DOC NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1
SHEET 4 of 67				

Power sequence

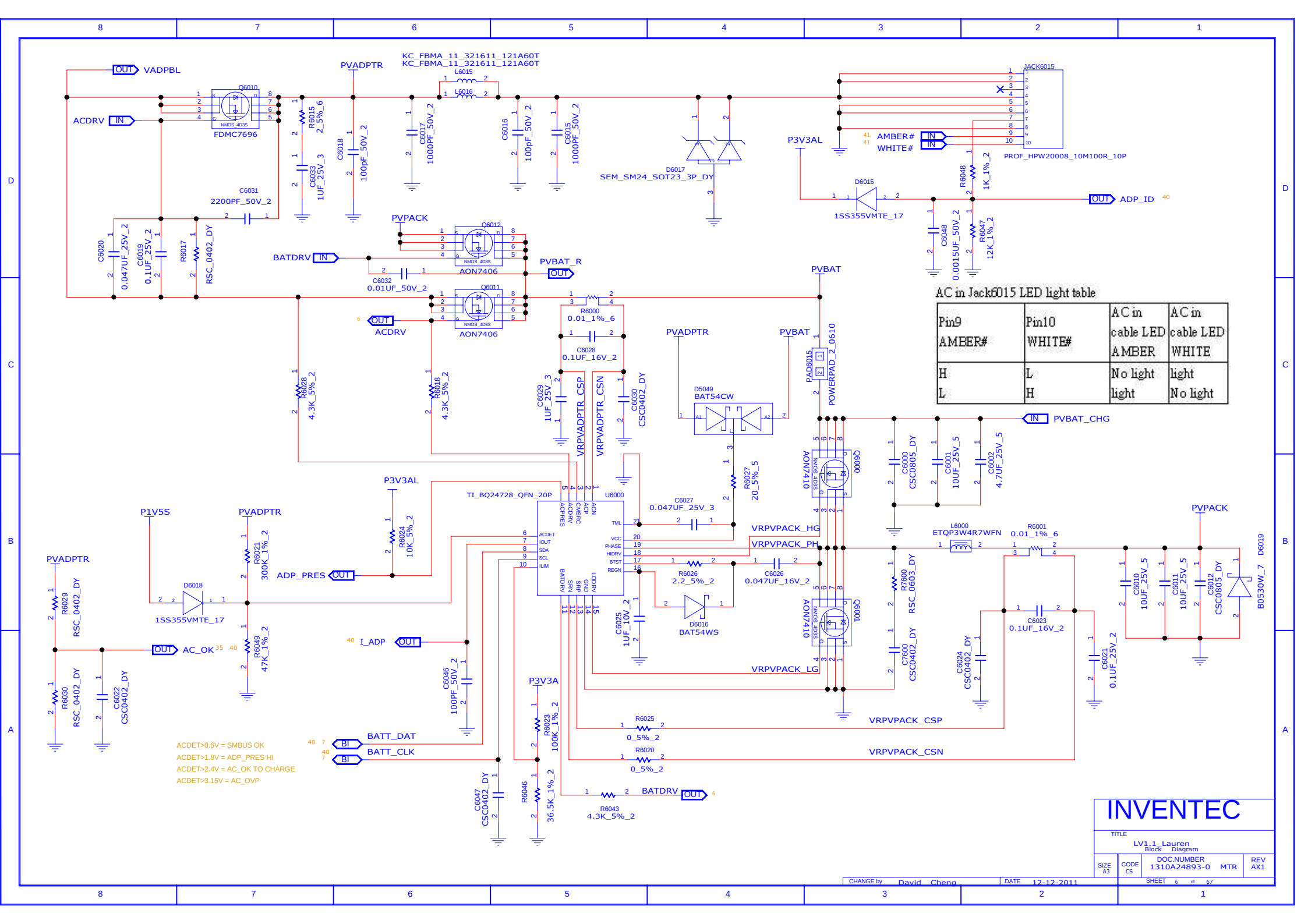


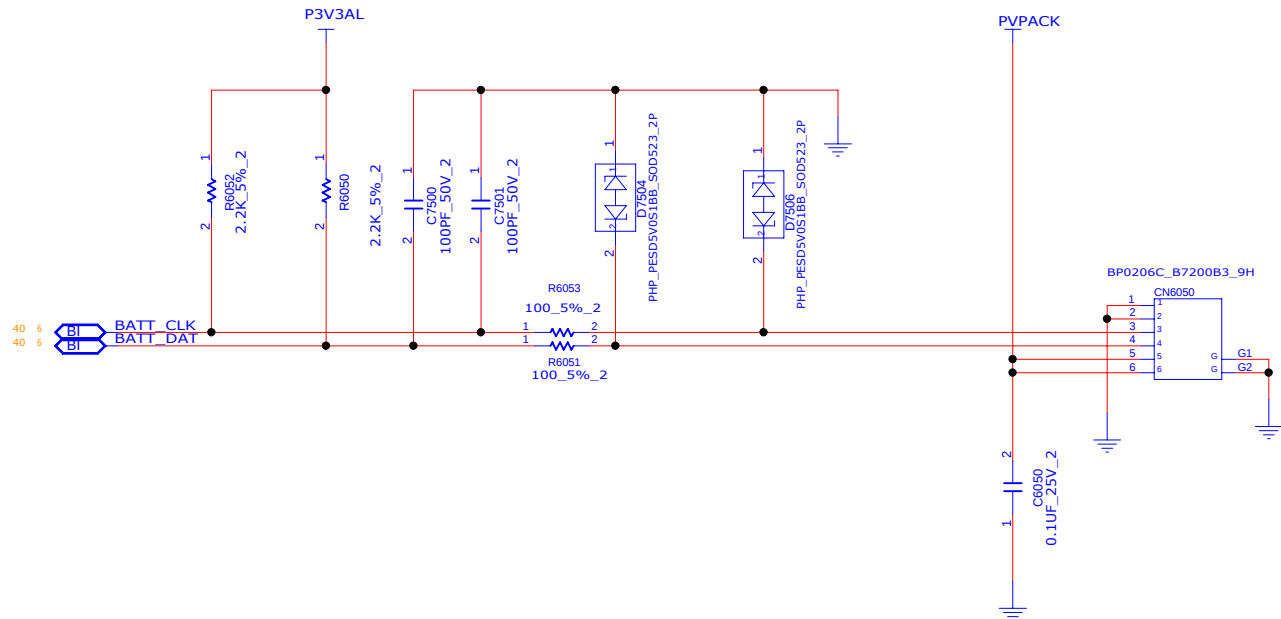
INVENTEC

TITLE
LV1.1: Lauren
Block Diagram
DOC NUMBER
1310A24893-0 MTR REV
AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 5 of 67







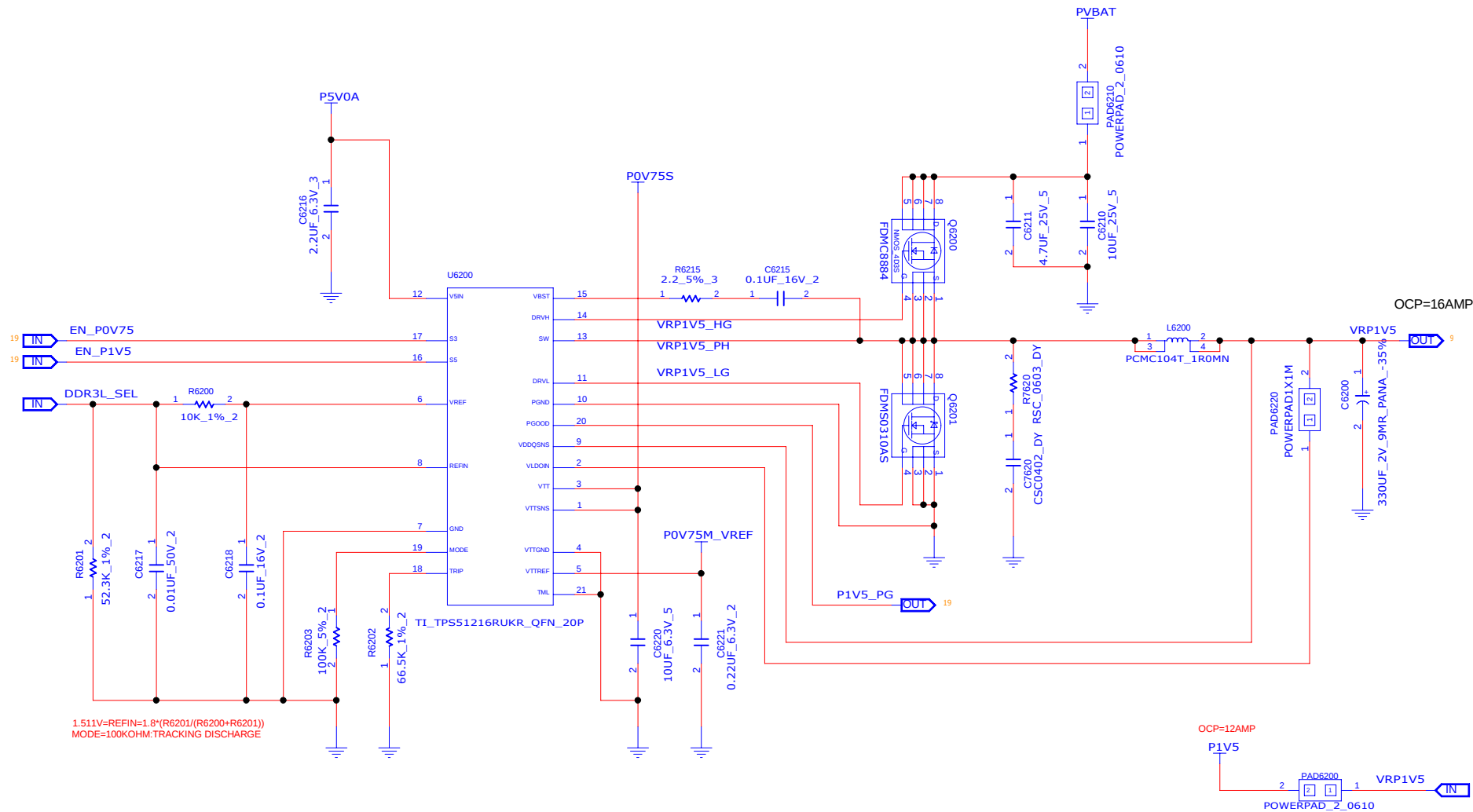


Table 1. S3/S5 Power State Control

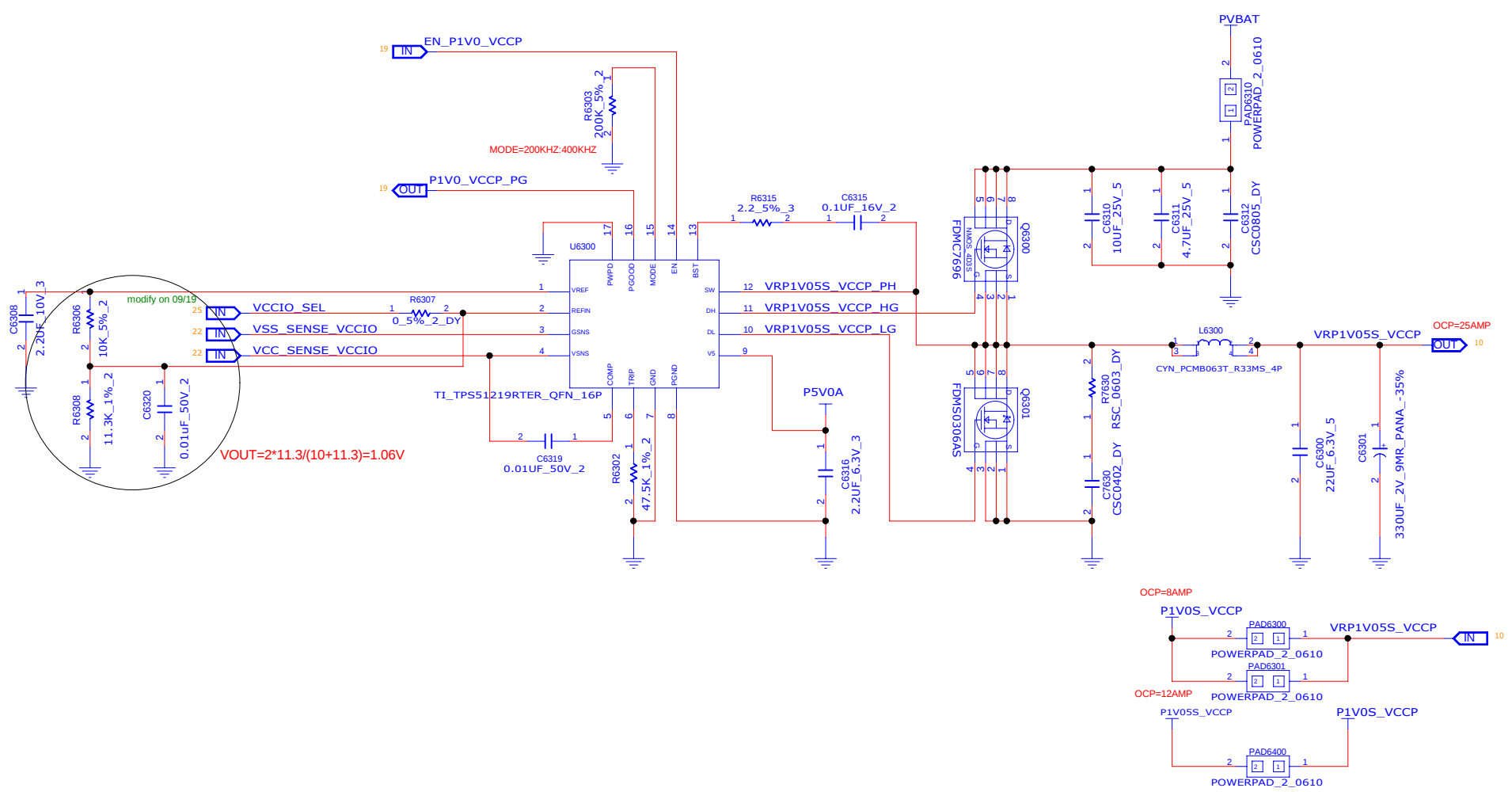
STATE	S3	S5	VREF	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF(High-Z)
S4/S5	LO	LO	OFF	OFF(Discharge)	OFF(Discharge)	OFF(Discharge)

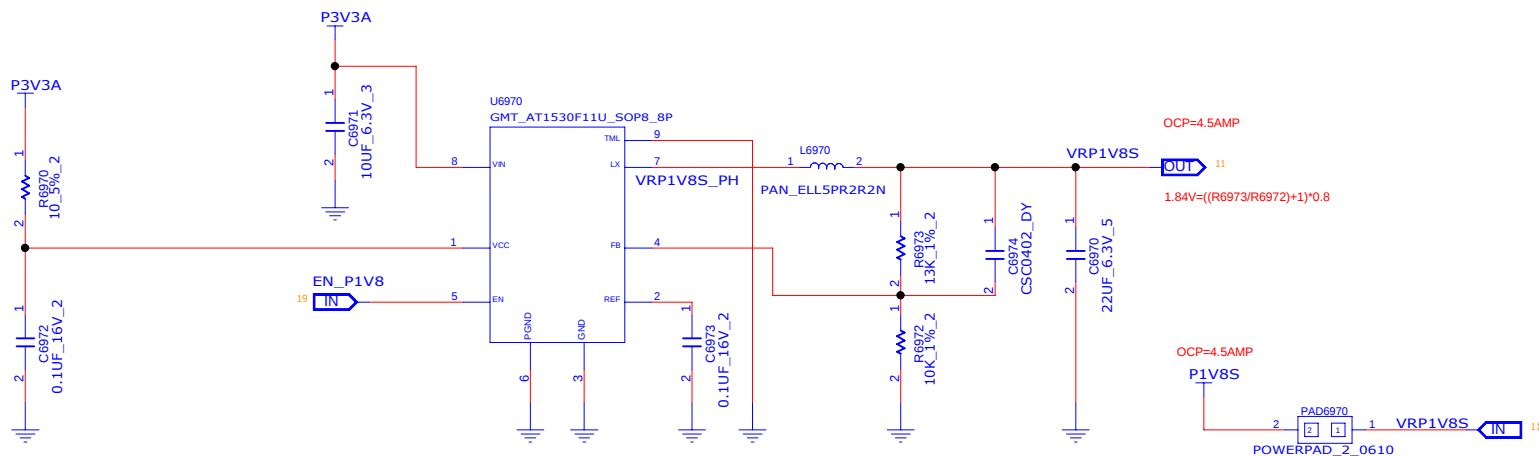
INVENTEC

TITLE
LV1.1 Lauren
Block Diagram
DOC NUMBER
1310A24893-0 MTR REV
AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 8 of 67



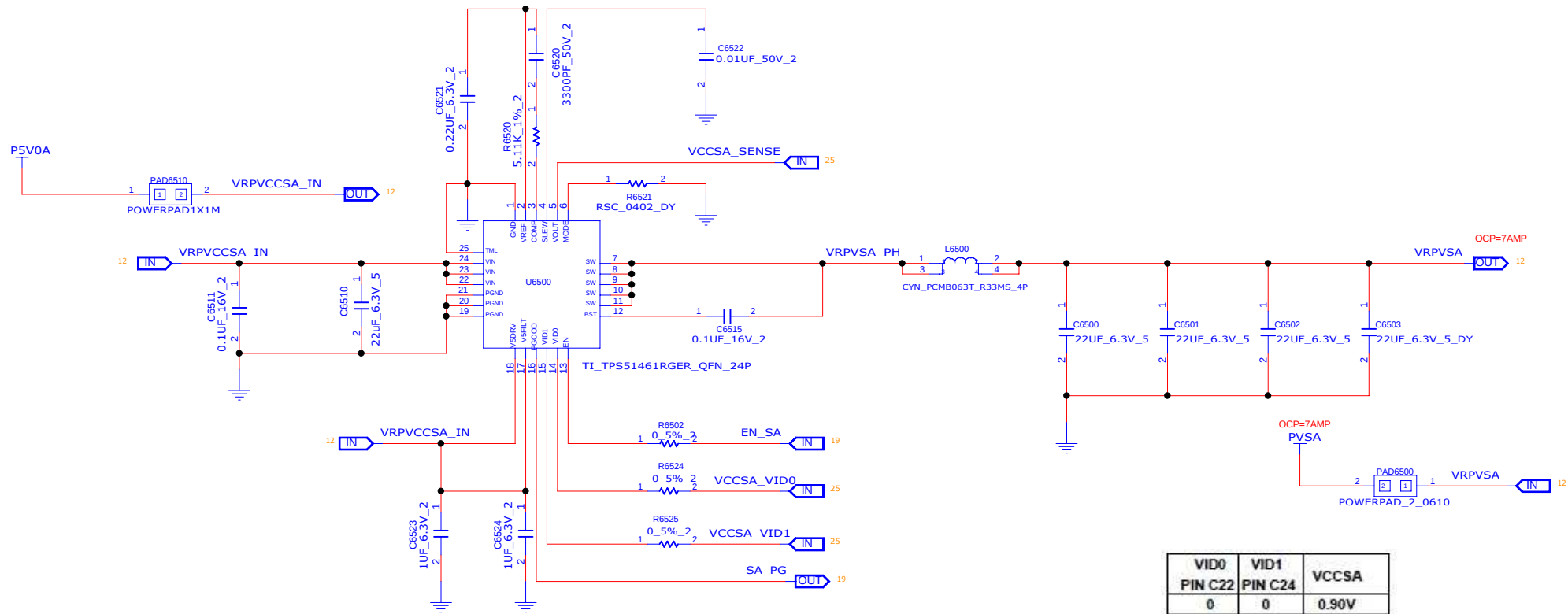


INVENTEC

TITLE			
LV1:1_Lauren Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 11 of 67



INVENTEC

TITLE
LV1.1_Lauren
Block Diagram

SIZE CODE DOC NUMBER
A3 CS 1310A24893-0 MTR REV
AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 12 of 67

D

C

B

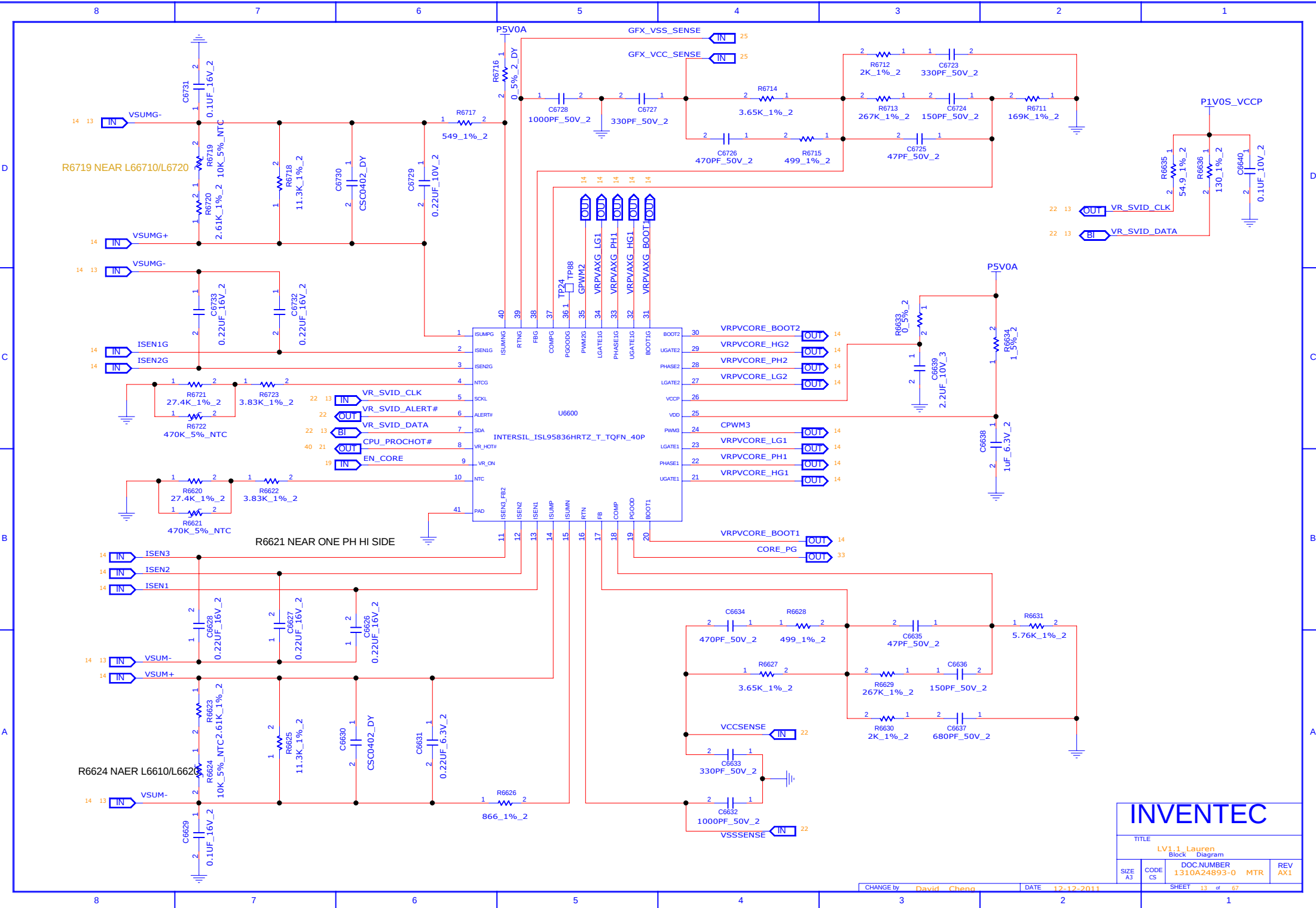
A

D

C

B

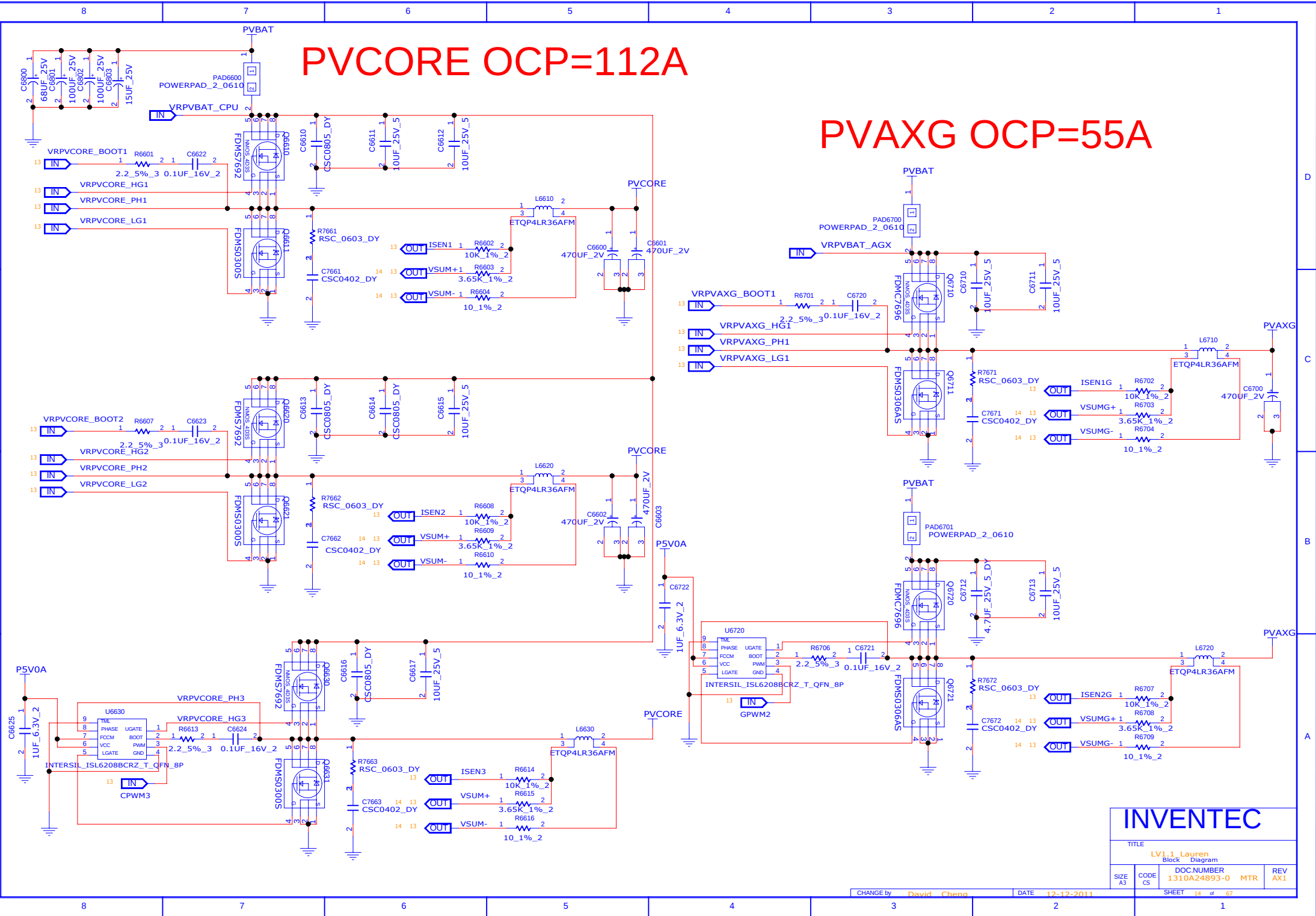
A



INVENTEC				
TITLE				
LV1:1-Lauren Block Diagram				
DOC NUMBER				
1310A24893-0 MTR				
REV				
AX1				
SIZE	CODE			
A3	CS			

PVCORE OCP=112A

PVAXG OCP=55A

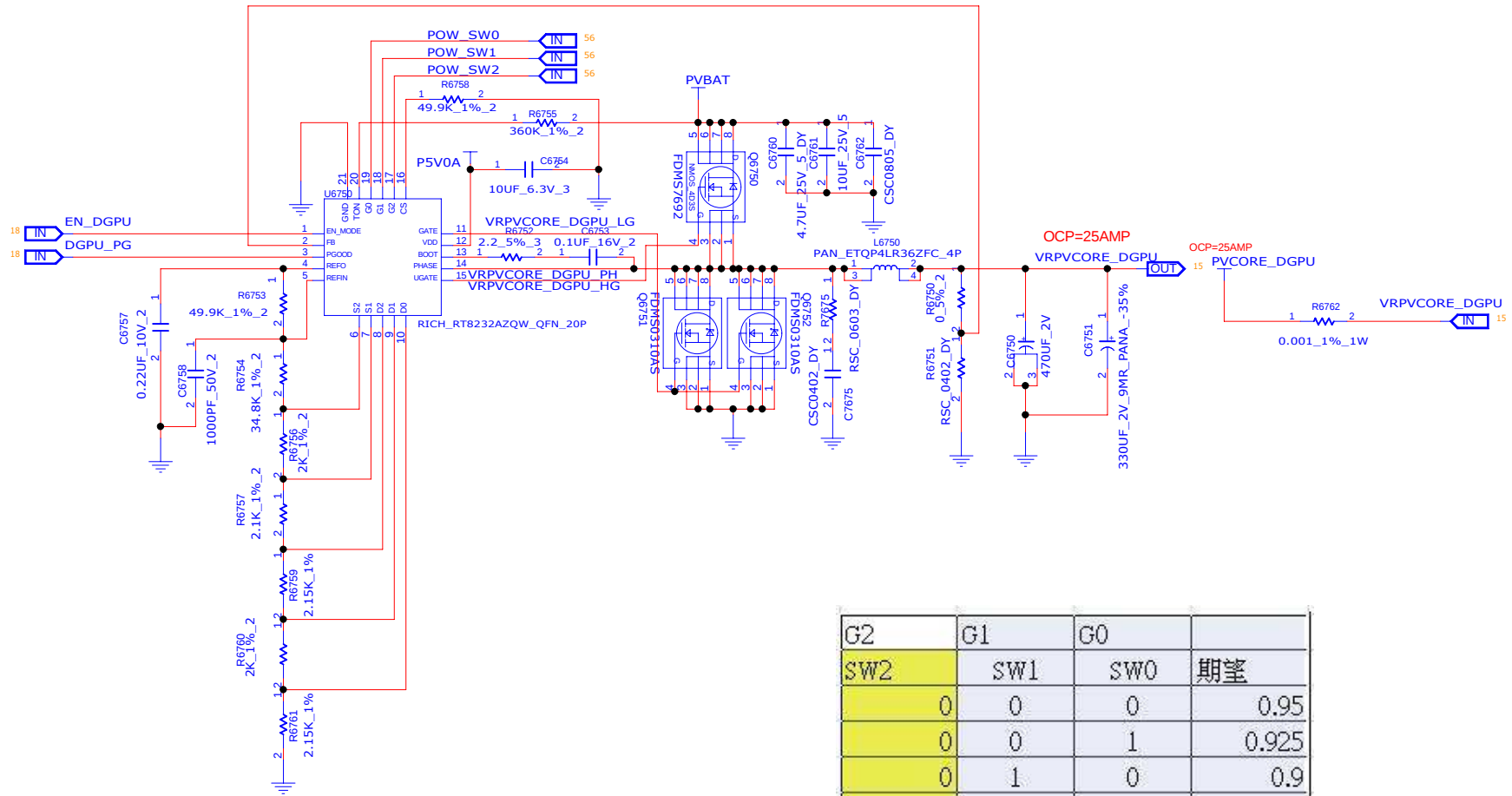


INVENTEC

TITLE
LV1.1: Lauren Block Diagram
DOC NUMBER
1310A24893-0 MTR
REV
AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 14 of 67



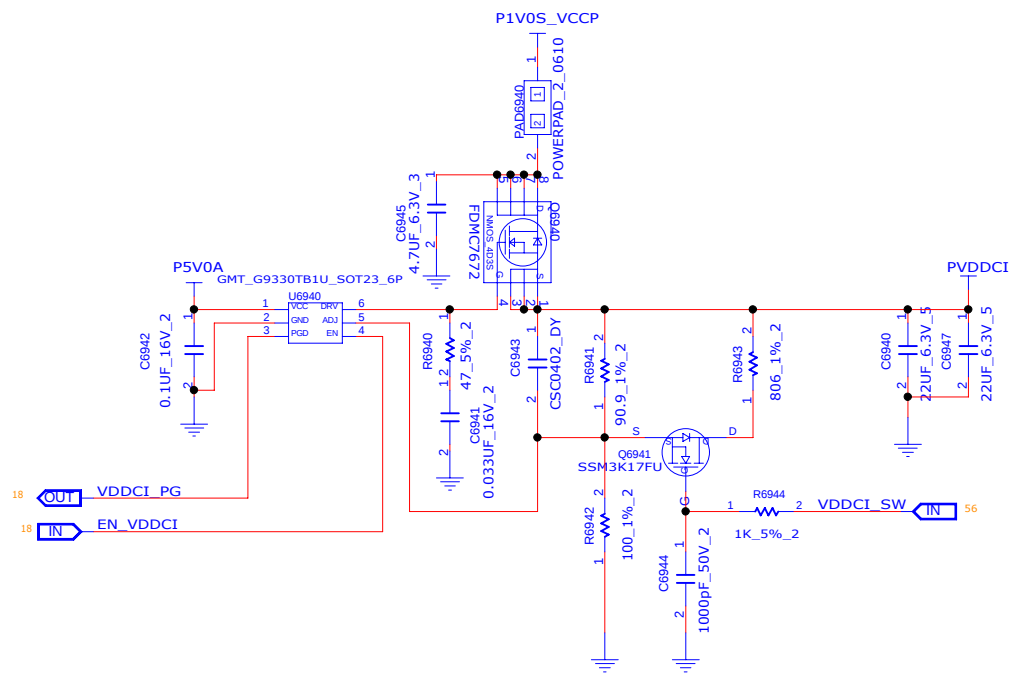
G2	G1	G0	期望
SW2	SW1	SW0	期望
0	0	0	0.95
0	0	1	0.925
0	1	0	0.9
0	1	1	0.875
1	0	0	0.85
1	0	1	0.825
1	1	0	x
1	1	1	x

INVENTEC

TITLE			
LV1.1_Lauren Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1

CHANGE by David Cheng DATE 12-12-2011

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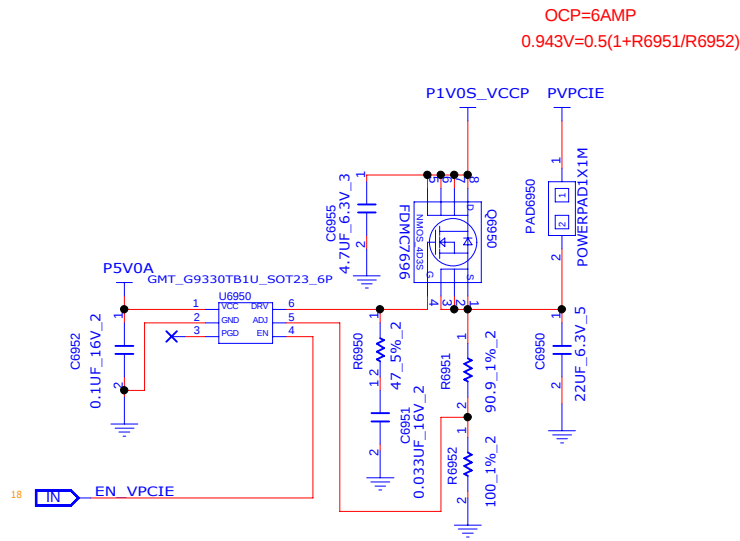
OCP=6AMP
HIGH : 0.908V = 0.5 [1 + ((R6941//R6943) / R6942)]
LOW : 0.954V=0.5 (1+ R6941/R6942)

INVENTEC

TITLE			
LV1.1_Lauren Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0	MTR
		REV AX1	

CHANGE by David Cheng DATE 12-12-2011

SHEET 16 of 67

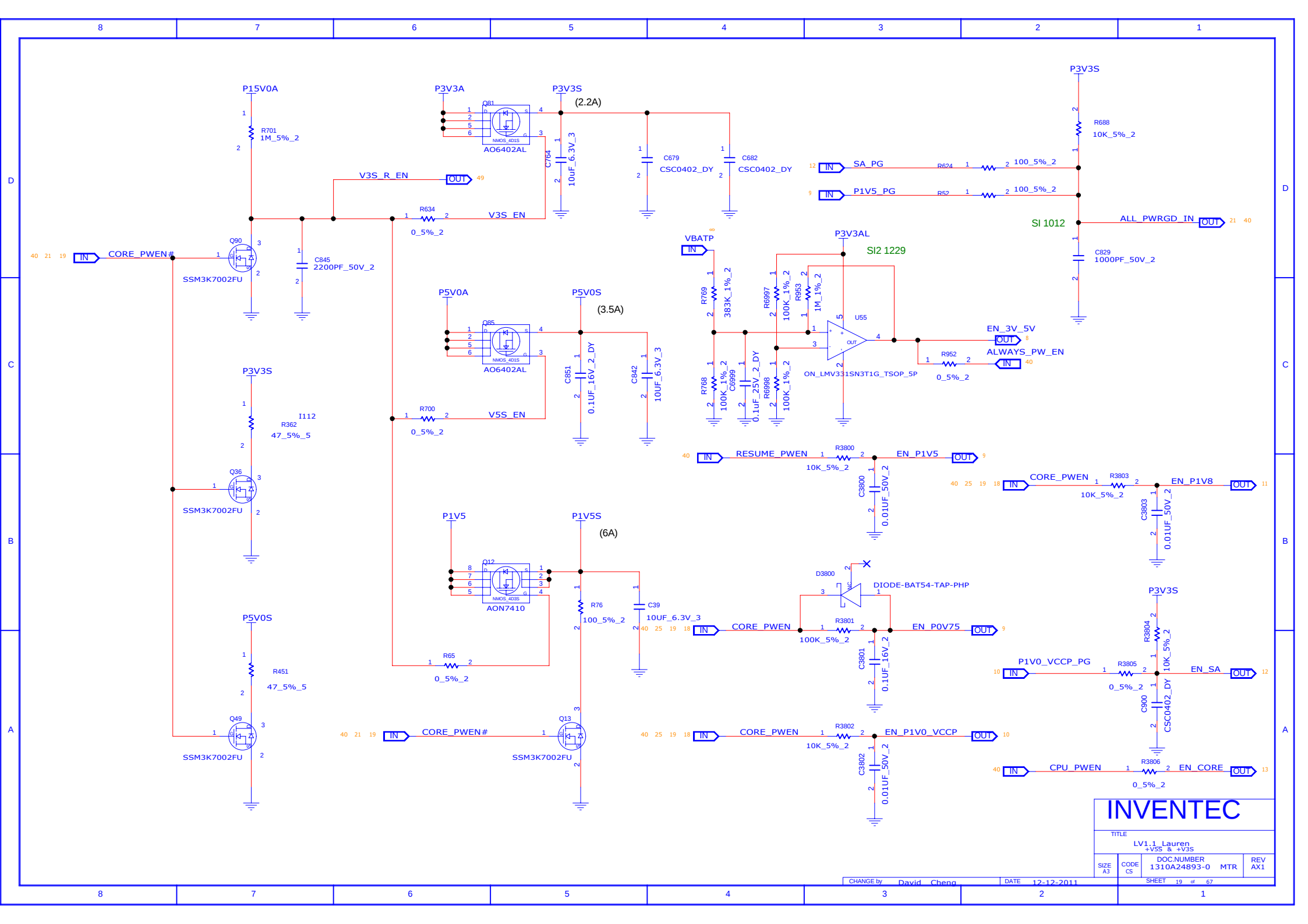


INVENTEC

TITLE			
LV1.1_Lauren Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0 MTR	REV AX1

CHANGE by David Cheng DATE 12-12-2011

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INVENTEC

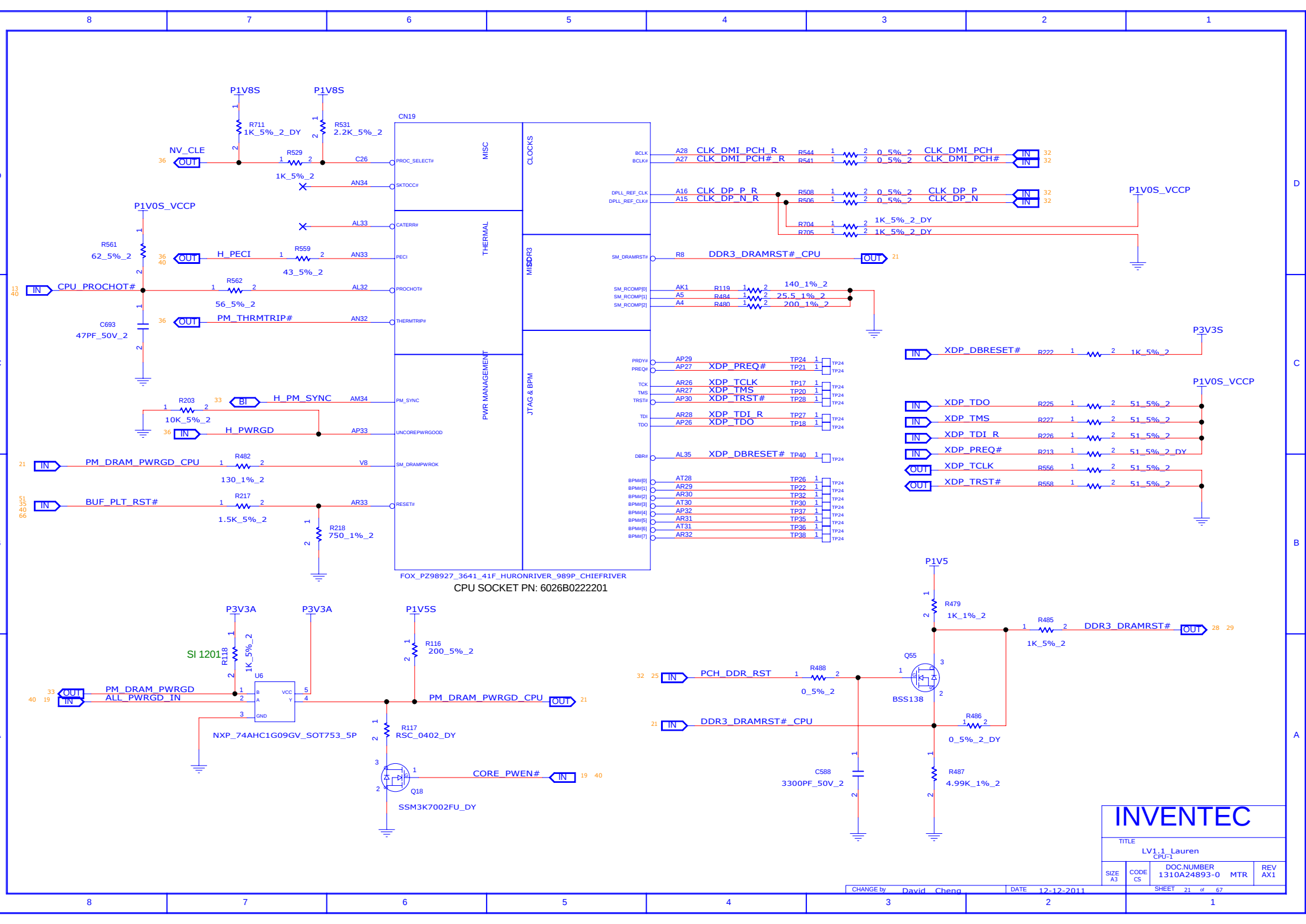
TITLE			
LV1.1_Lauren +V5S & +V3S			
SIZE A3	CODE CS	DOCNUMBER 1310A24893-0	REV AX1
CHANGE by David Cheng		DATE 12-12-2011	SHEET 19 of 67

8	7	6	5	4	3	2	1
D							D
C							C
B							B
A							A
8	7	6	5	4	3	2	1

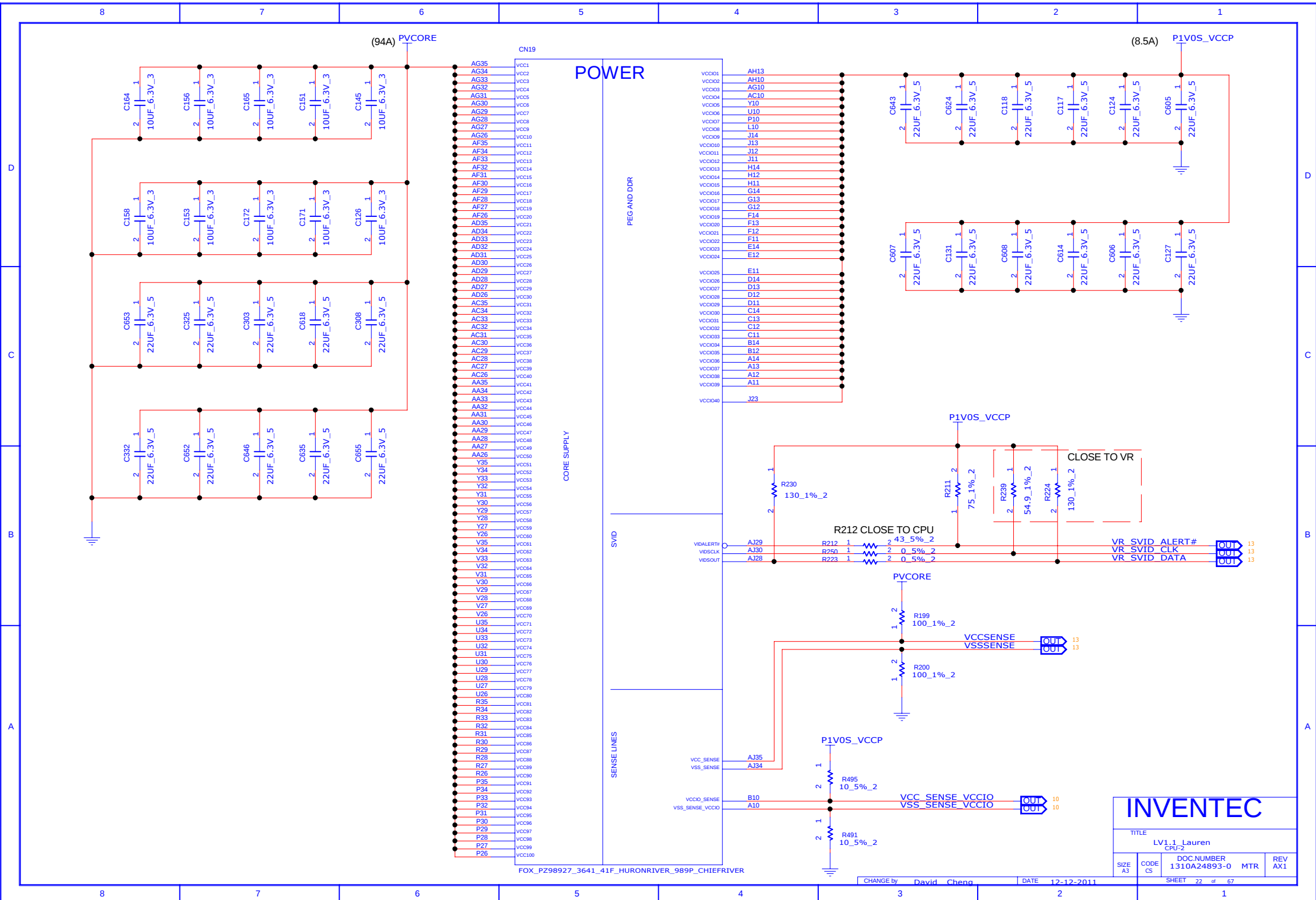
CPU

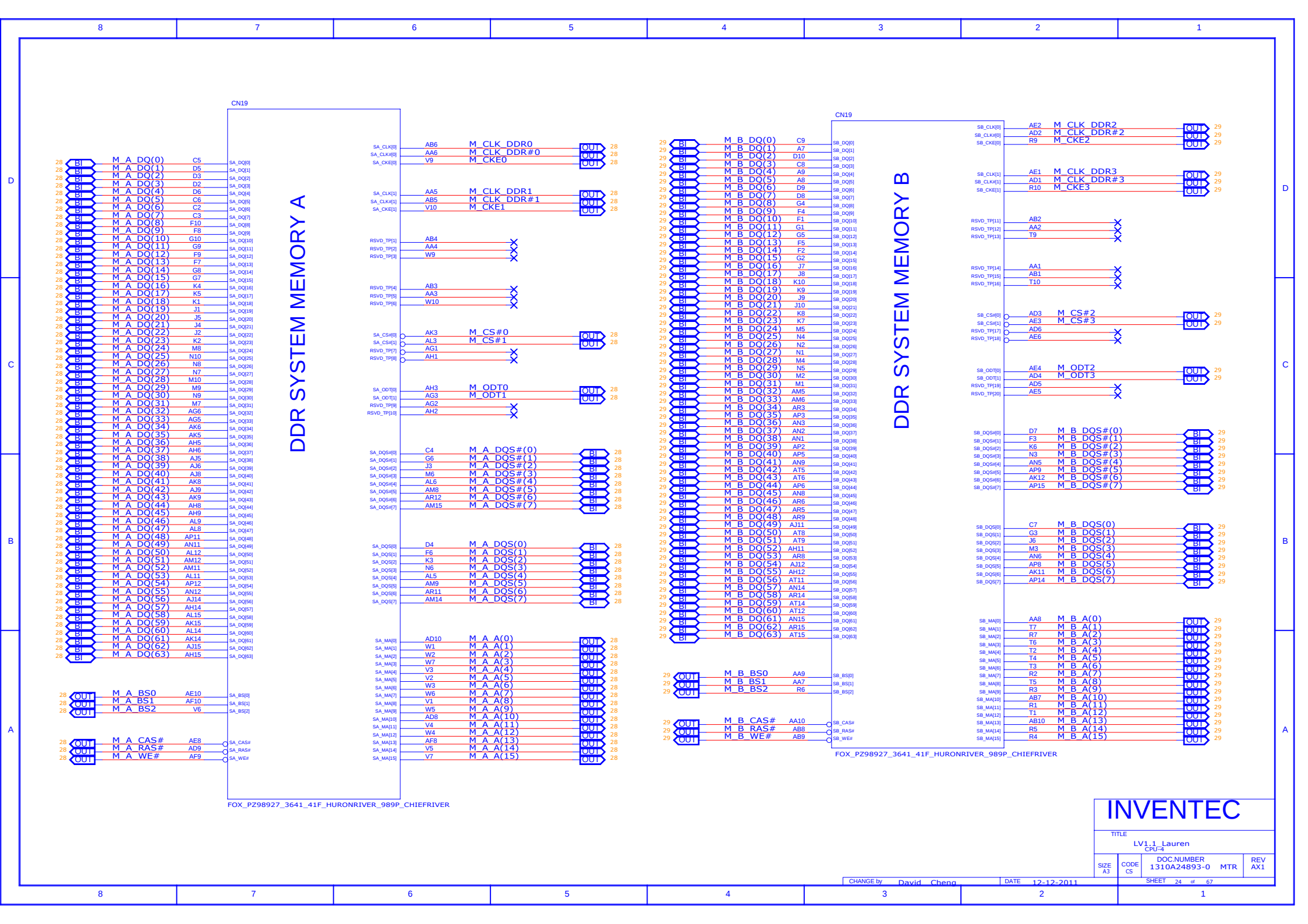
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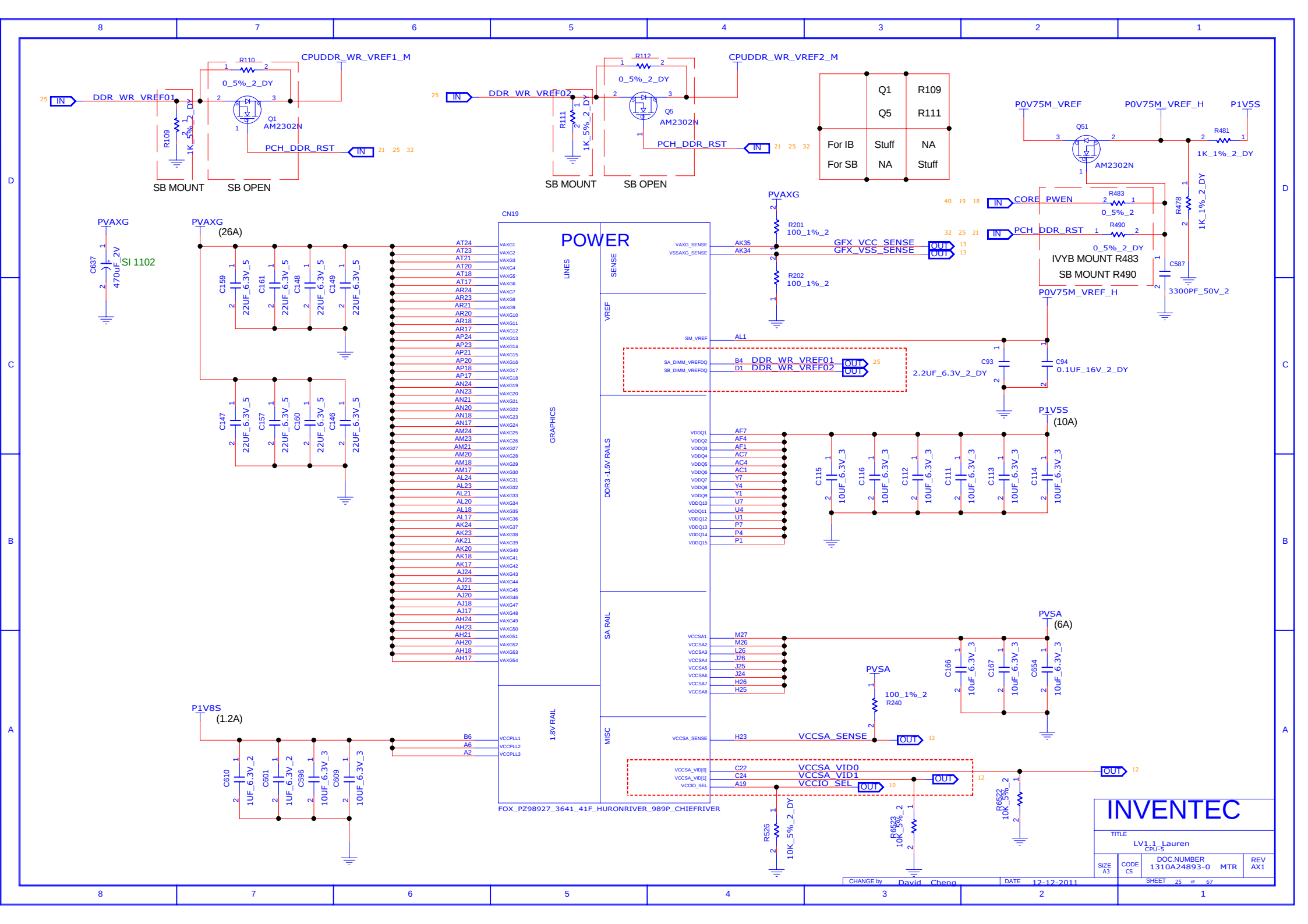
TITLE			
LV1: J. Lauren BLANK			
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0 MTR	REV AX1



INVENTEC				
TITLE				
LV1.1_Lauren				
CPU1				
SIZE	CODE	DOC NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1



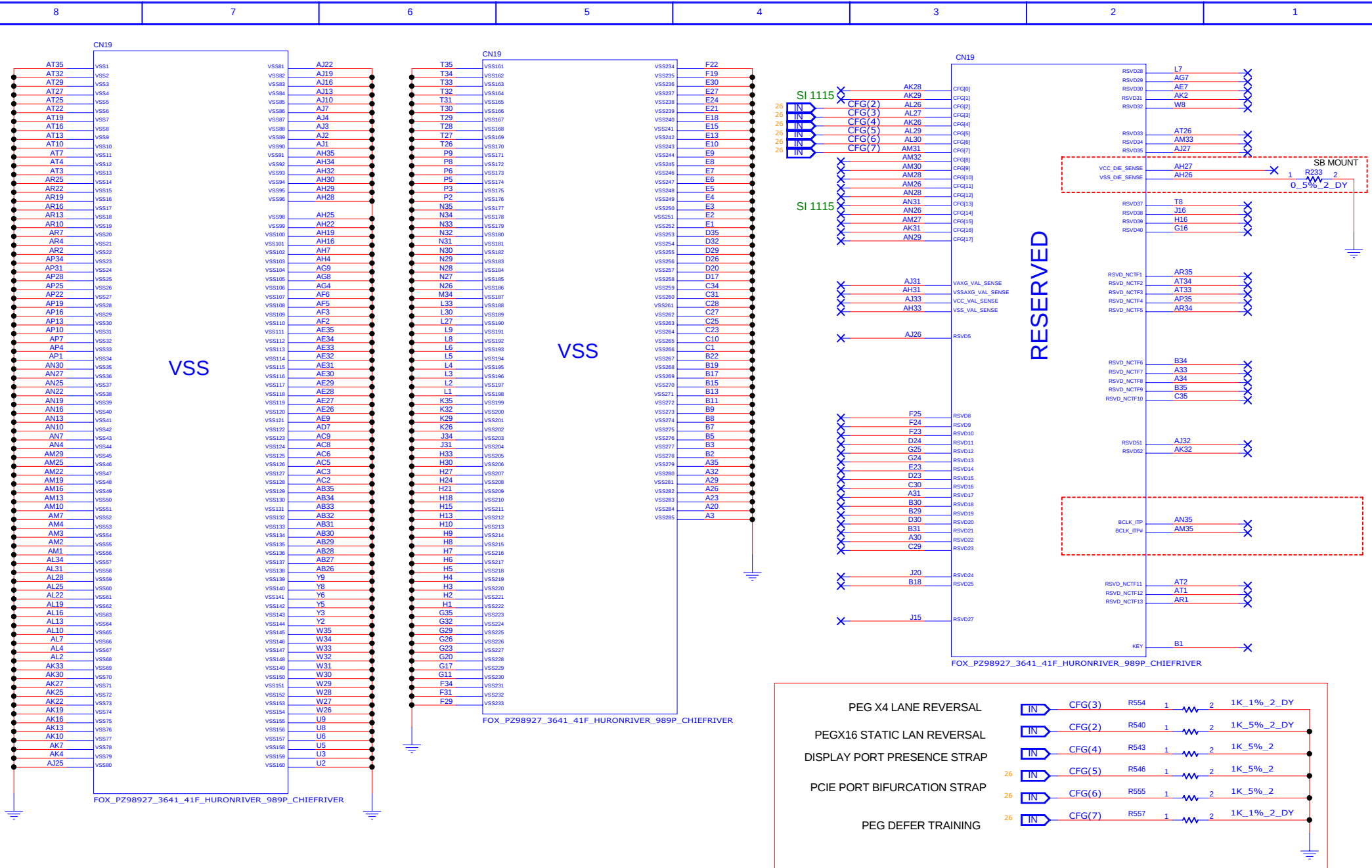




INVENTEC				
TITLE				
LV1.1_Lauren				
CPU5				
SIZE	CODE	DOCNUMBER	MTR	REV
A3	CS	1310A24893-0		AX1

A

A



STRAP PIN

INVENTEC

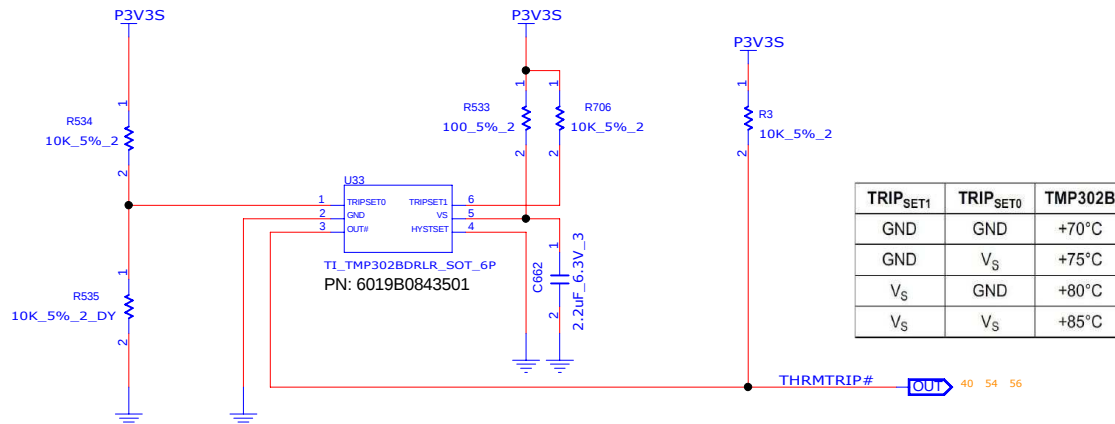
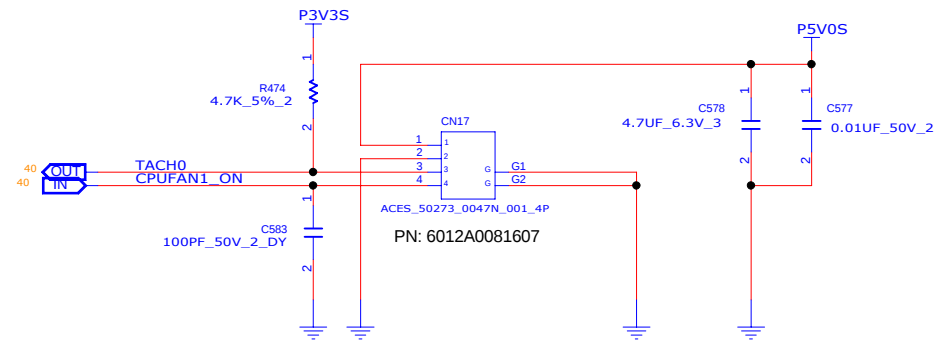
TITLE	LV1.1 Lauren
DATE	CPI 6

SIZE A3	CODE CS	DOC.NUMBER 1310A24893-0	MTR	F A
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SHEET 26 of 67

CHANGE by	David Cheng	DATE	12-12-2011
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FAN CONN



TRIP _{SET1}	TRIP _{SET0}	TMP302B
GND	GND	+70°C
GND	V _S	+75°C
V _S	GND	+80°C
V _S	V _S	+85°C

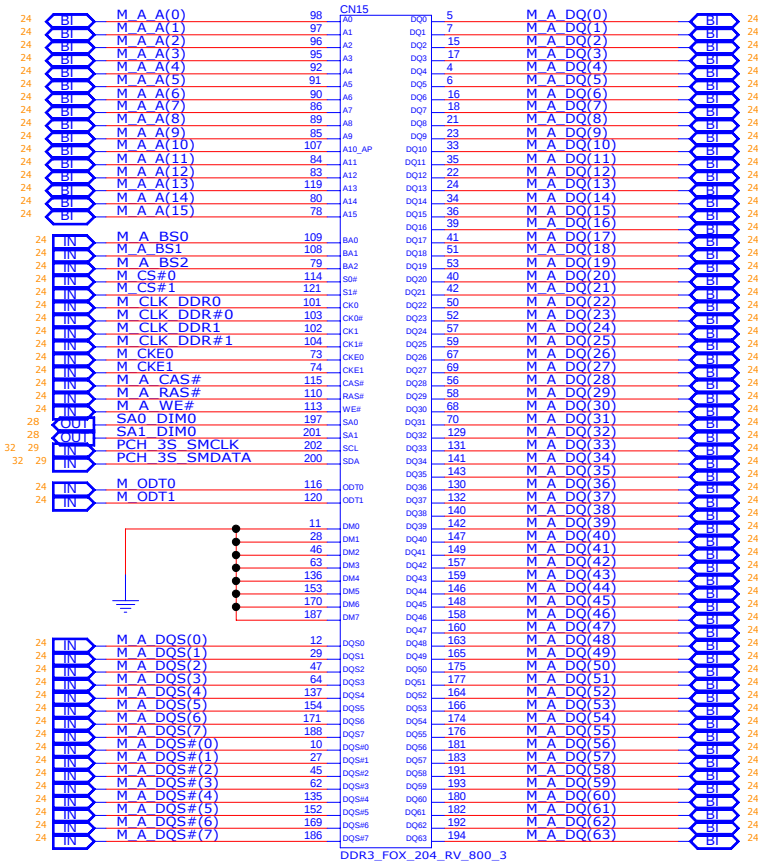
INVENTEC

TITLE			
LV1.1_Lauren			
FAN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1

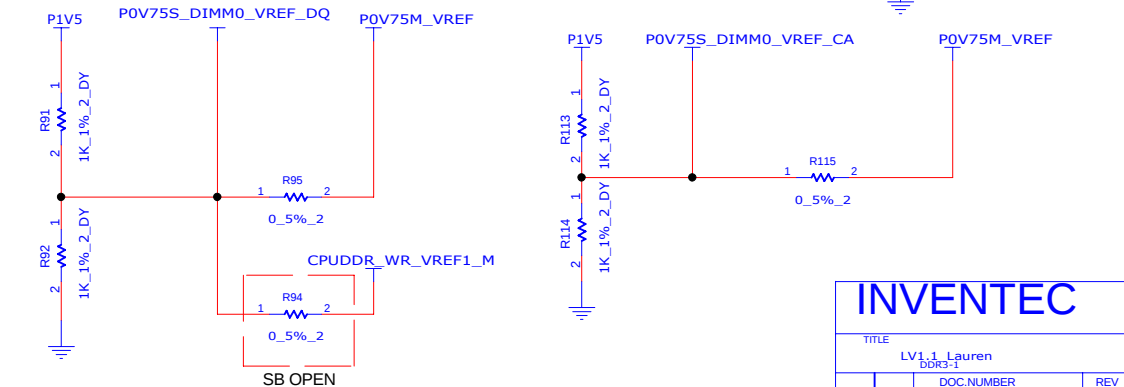
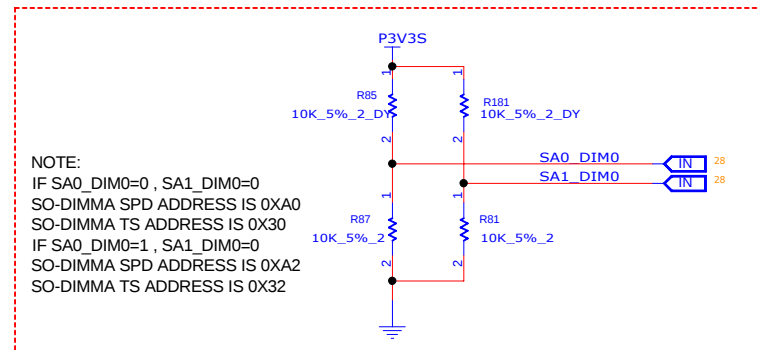
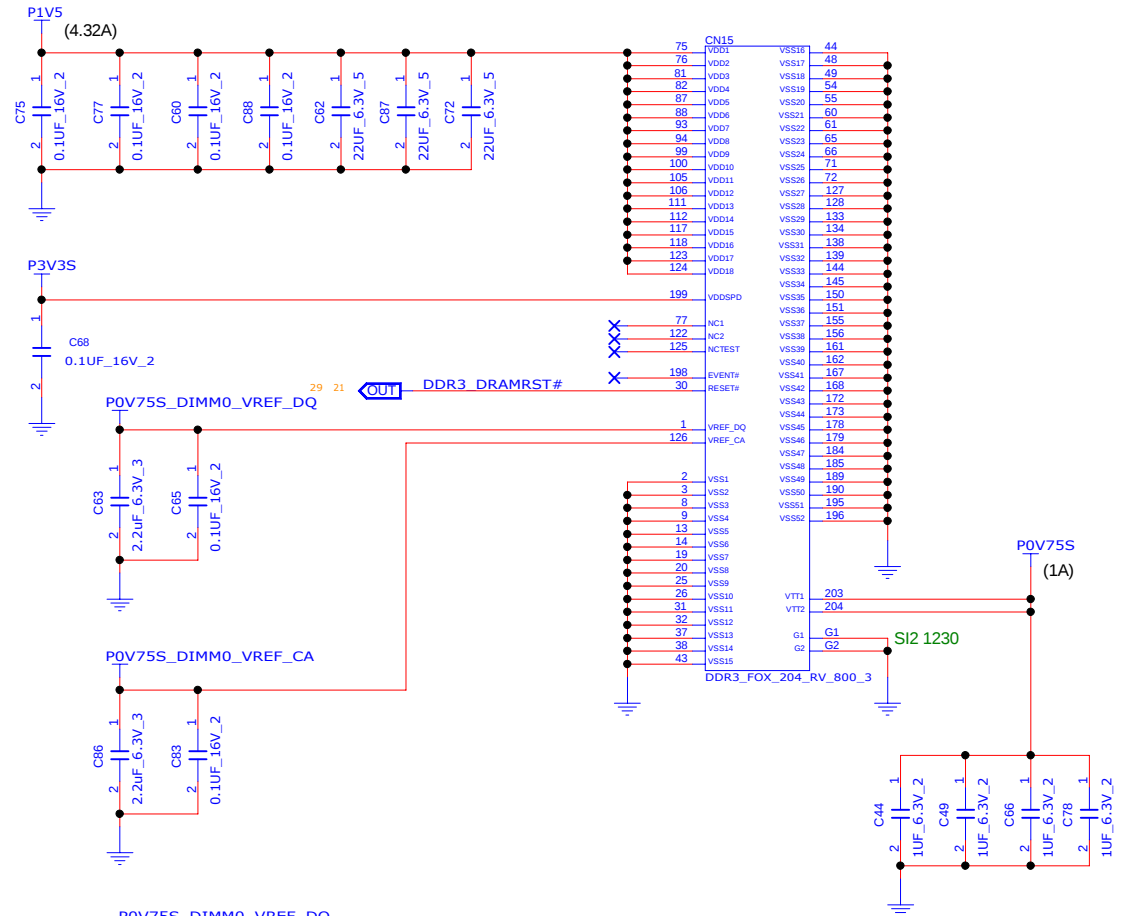
CHANGE by David Cheng DATE 12-12-2011

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DDR3 SO-DIMM 0



DDR3 SOCKET (RV_800) PN: 6026B0221101

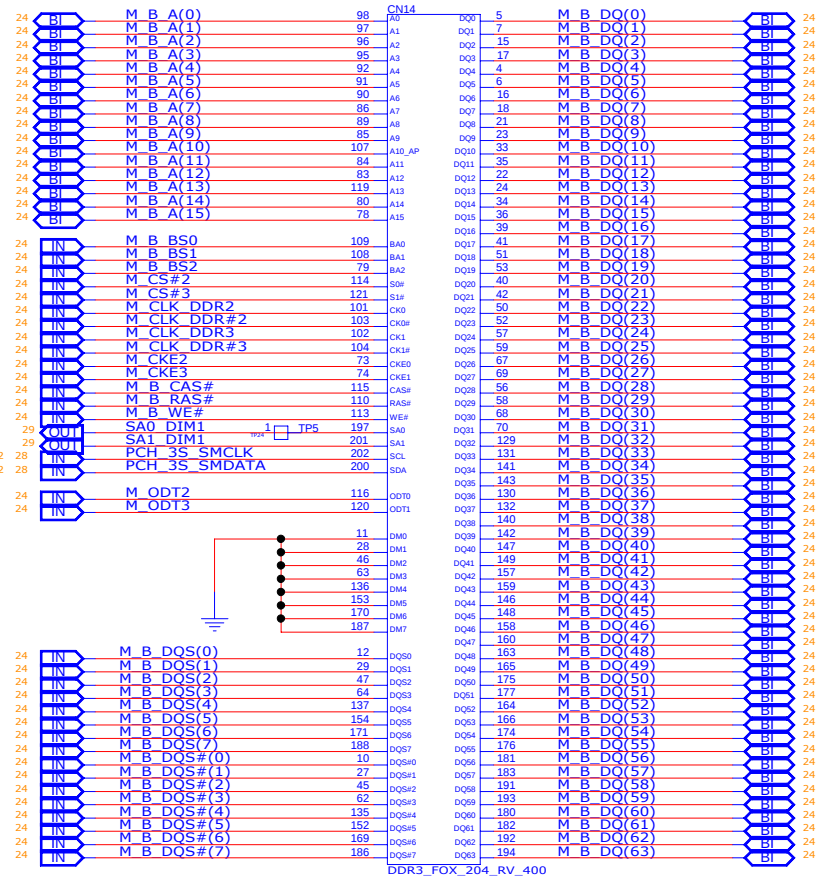


INVENTEC

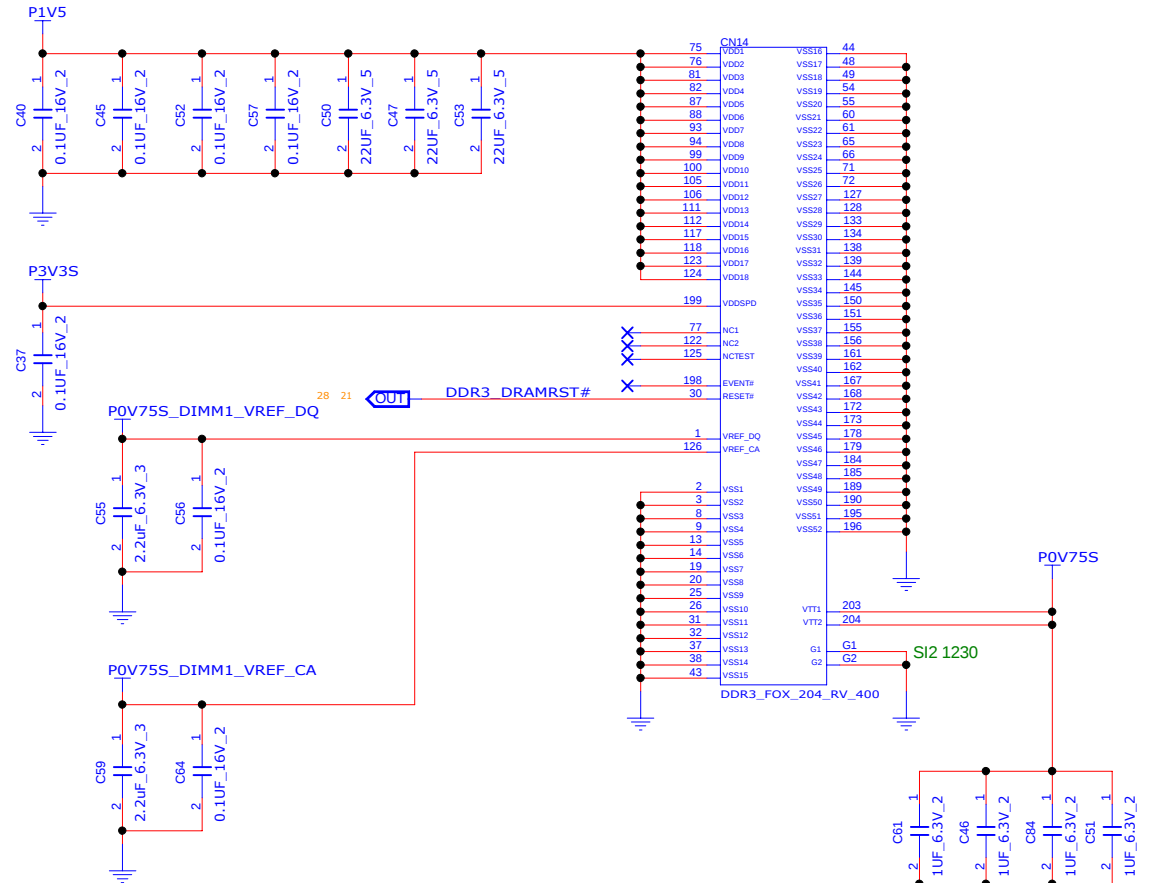
TITLE
LV1.1 Lauren
DDR3-1

SIZE A3 CODE CS DOC NUMBER 1310A24893-0 MTR REV AX1

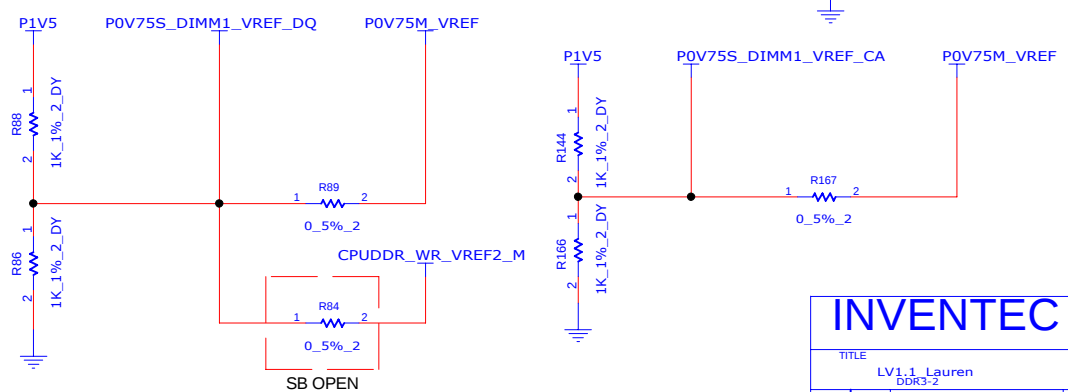
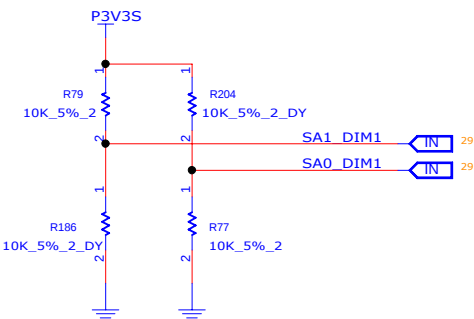
DDR3 SO-DIMM 1



DDR3 SOCKET (RV_400) PN: 6026B0221601



NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

TITLE				
LV1.1 Lauren				
DDR3-2				
SIZE	CODE	DOC.NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1
SHEET		29	of	67

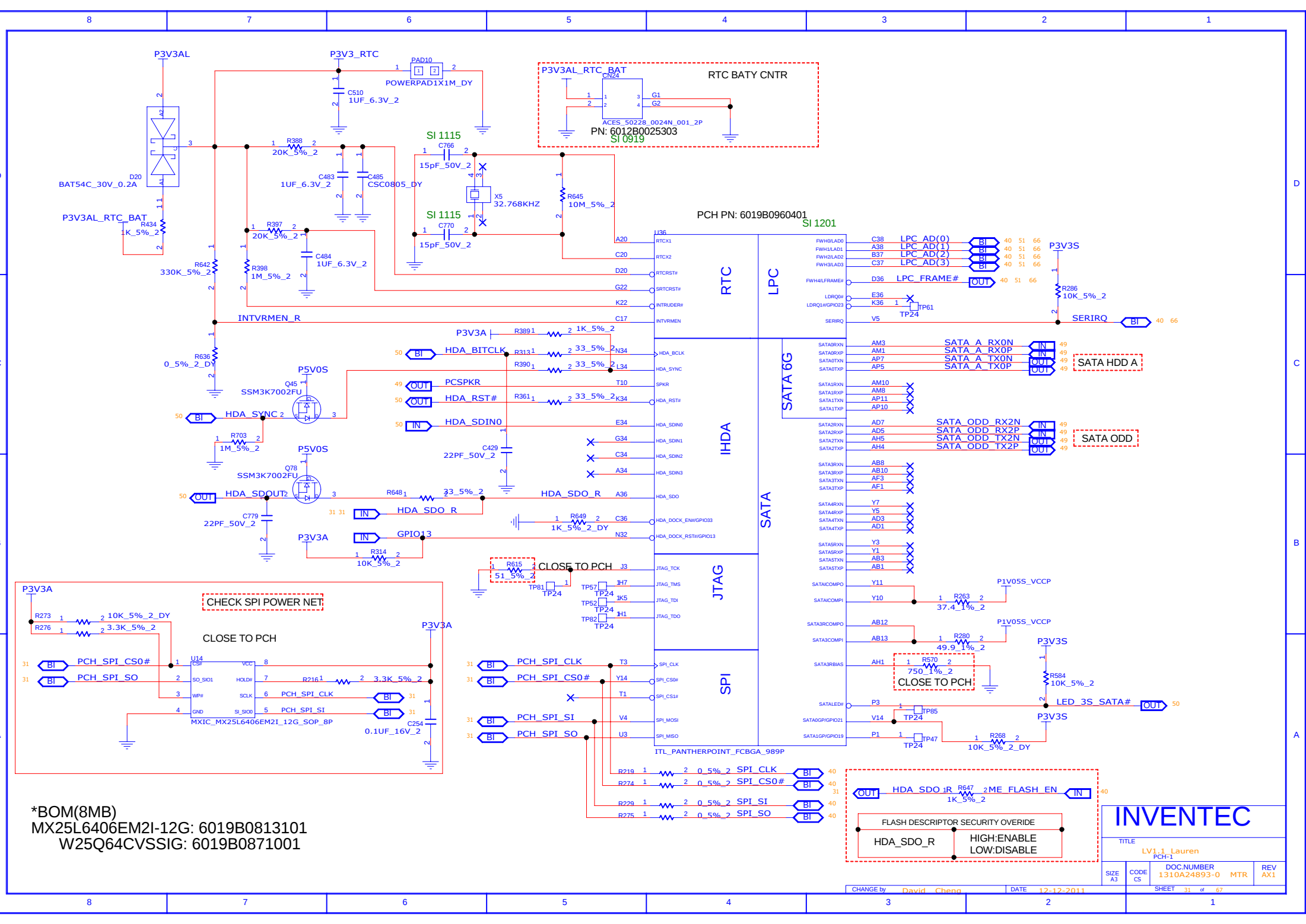
8	7	6	5	4	3	2	1
D							
C							
B							
A							
8	7	6	5	4	3	2	1

PCH

INVENTEC

TITLE			
LV1: J. Lauren BLANK			
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0 MTR	REV AX1

CHANGE by	David. Cheng	DATE	12-12-2011	SHEET	30	of	67
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*BOM(8MB)
MX25L6406EM2I-12G: 6019B0813101
W25Q64CVSSIG: 6019B0871001

INVENTEC			
TITLE			
LV1-L-Lauren			
PCH1			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0	AX1
MTR			
SHEET 31 of 67			



NOTE:1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED

D

D

C

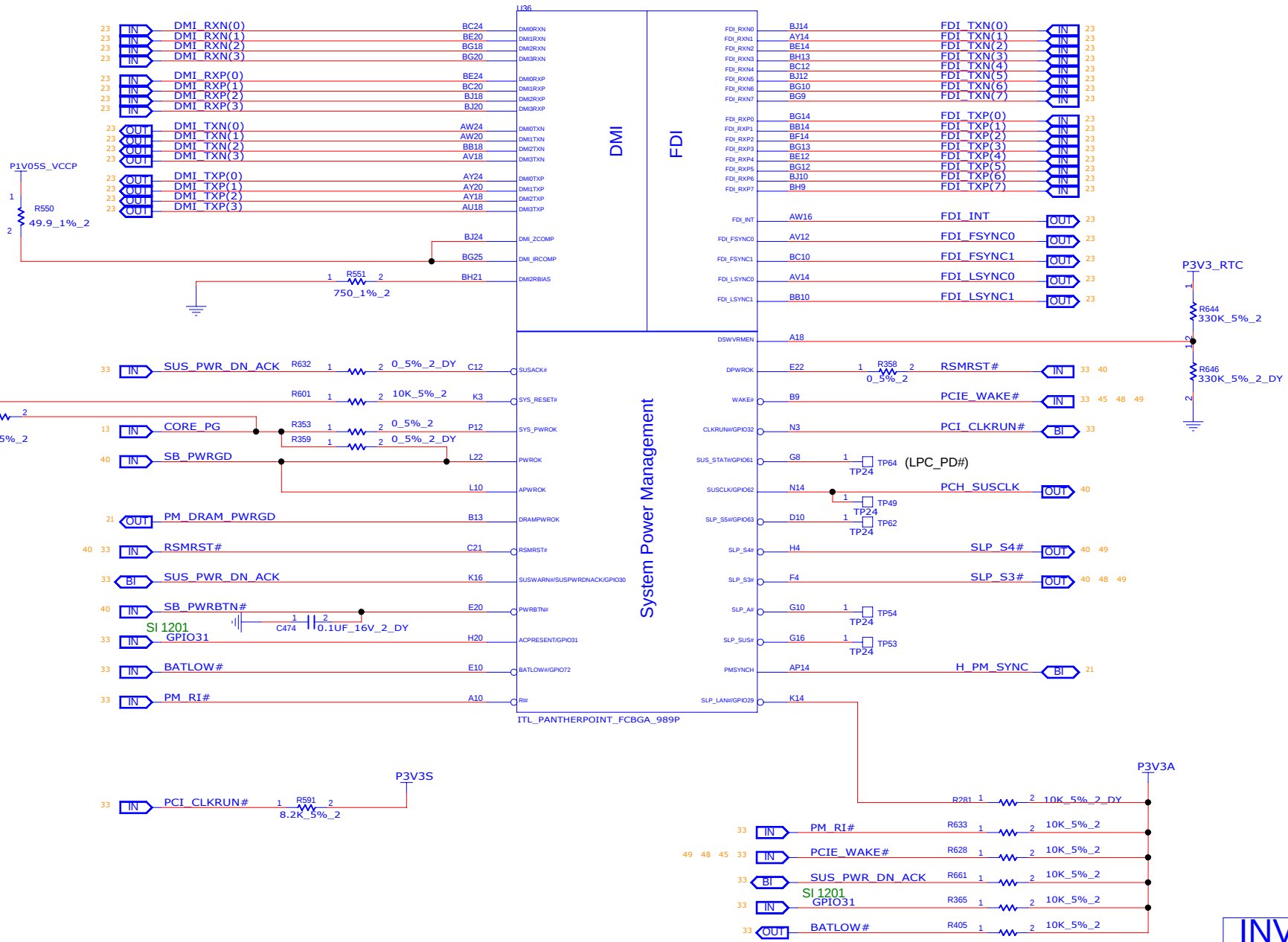
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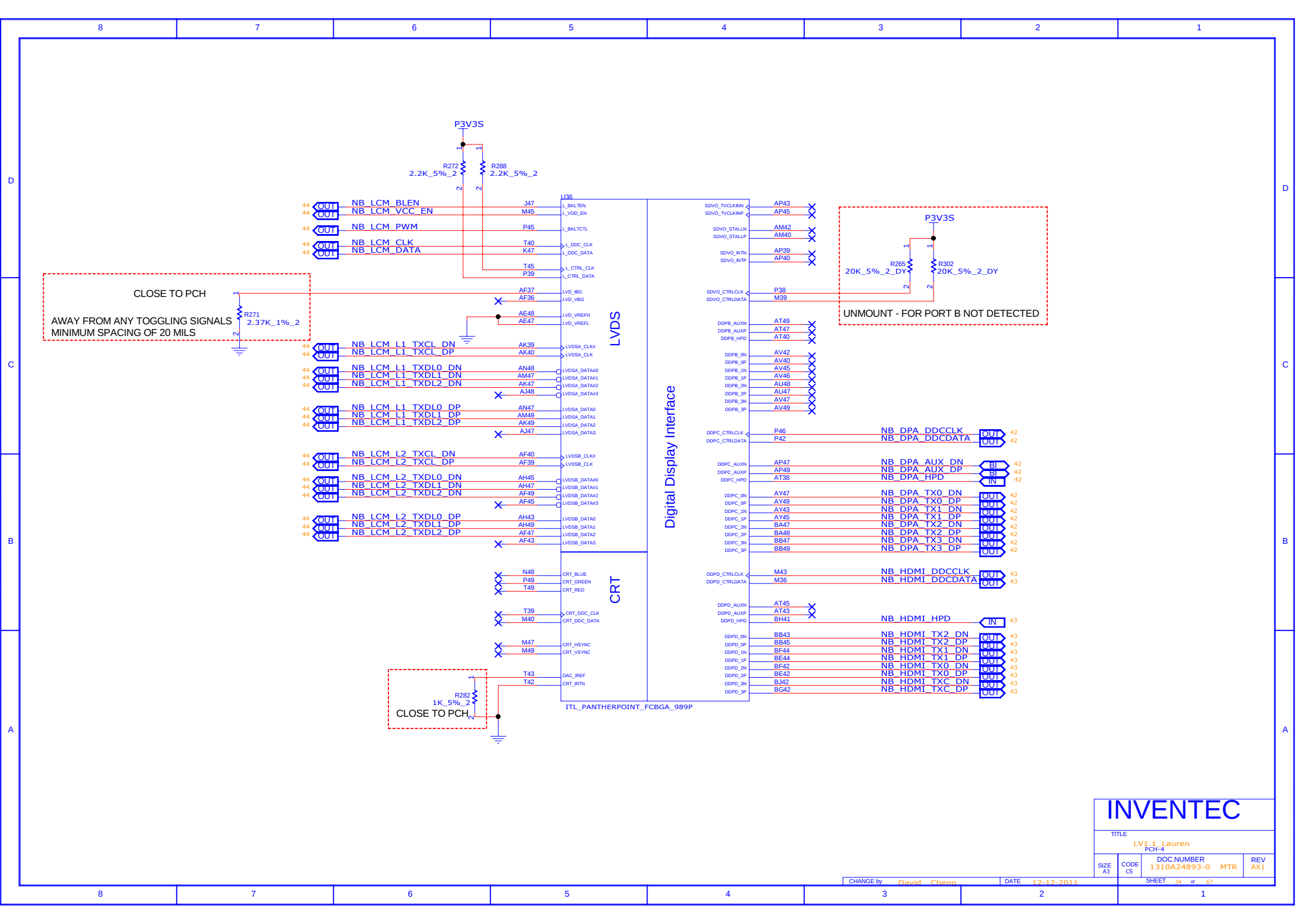
B

A

A



INVENTEC				
TITLE				
LV1.1_Lauren				
PCH3				
SIZE	CODE	DOC NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1



INVENTEC

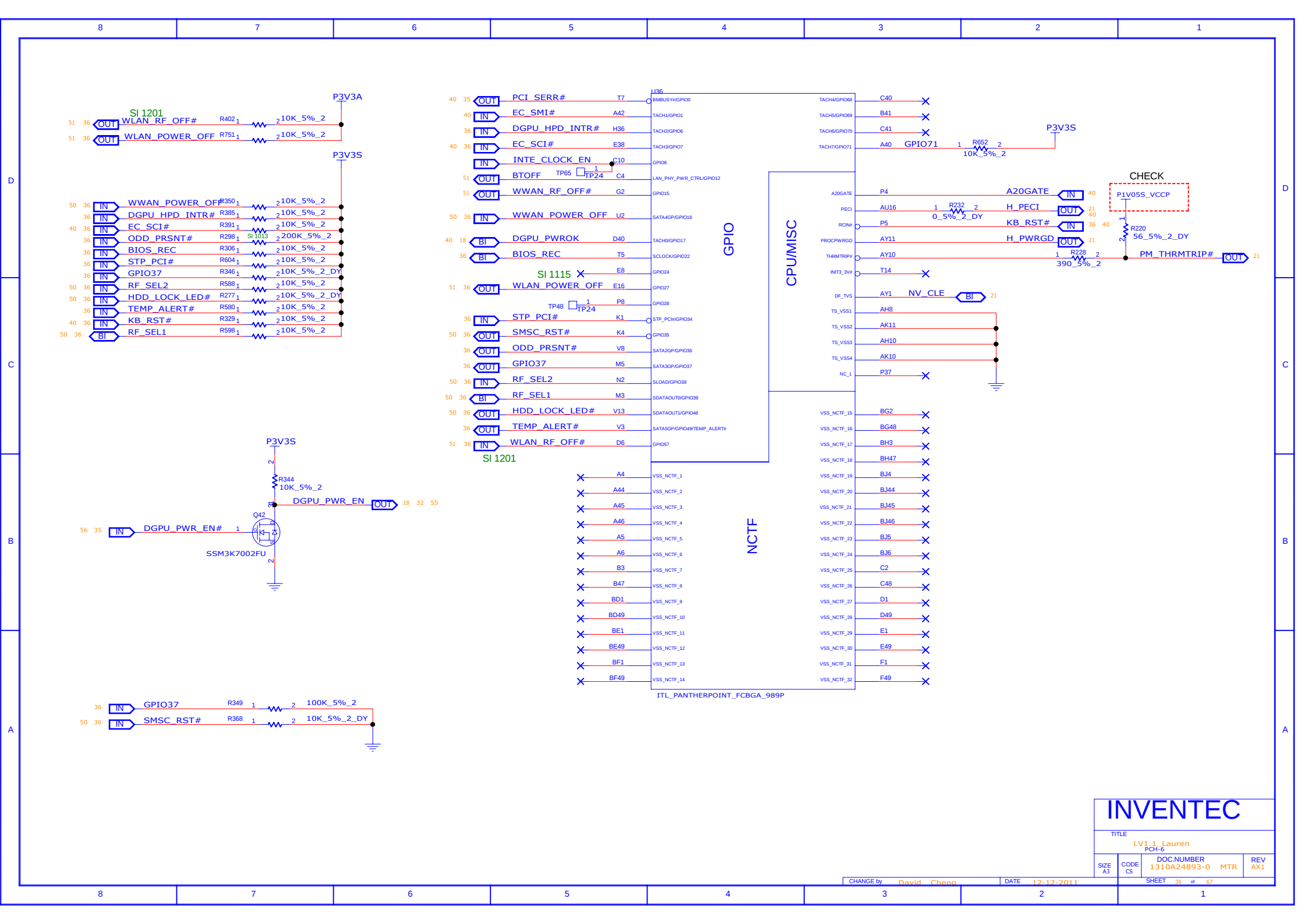
TITLE
LV1-1-Lauren
PCH4

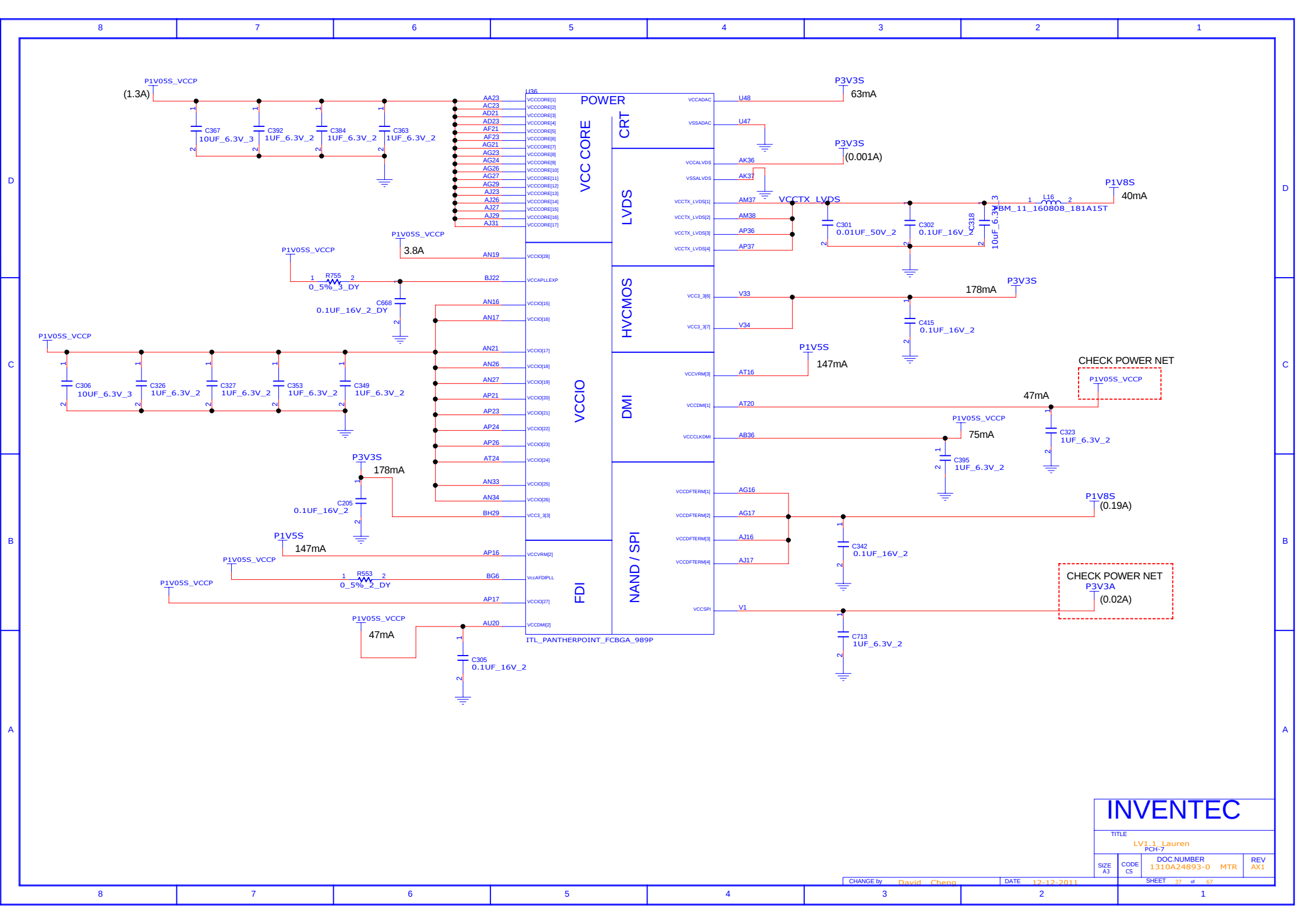
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CHANGE by David Cheng DATE 12-12-2011

SHEET 34 of 67





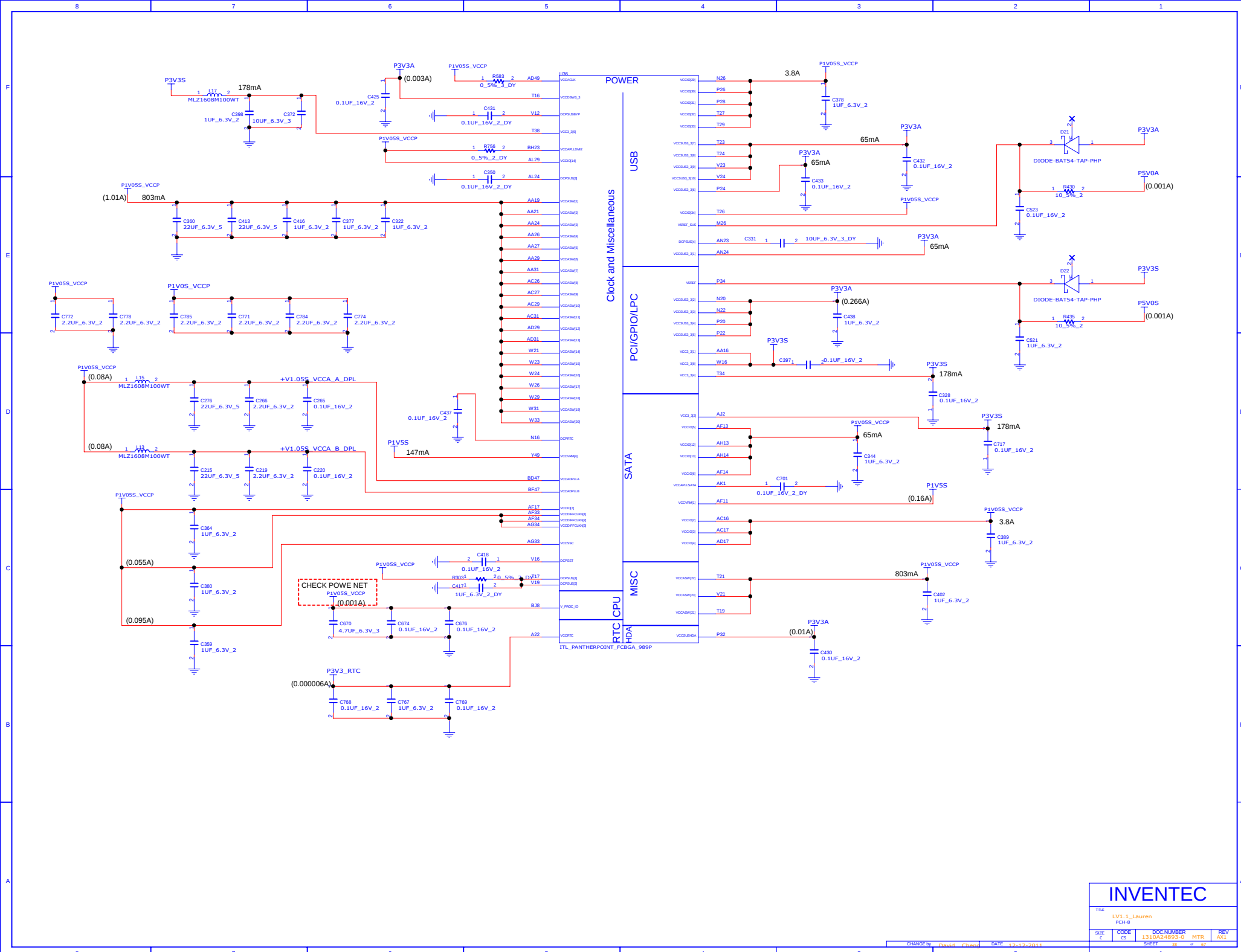


INVENTEC

TITLE				
LV111_Lauren				
PCH7				
SIZE	CODE	DOC NUMBER	MTR	REV
A3	CS	1310A24893-0		AX1

CHANGE by David Cheng DATE 12-12-2011

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D

C

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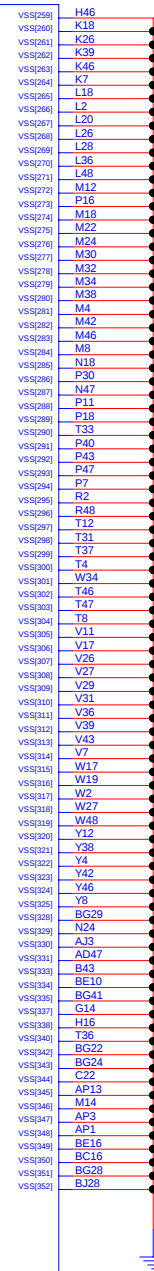
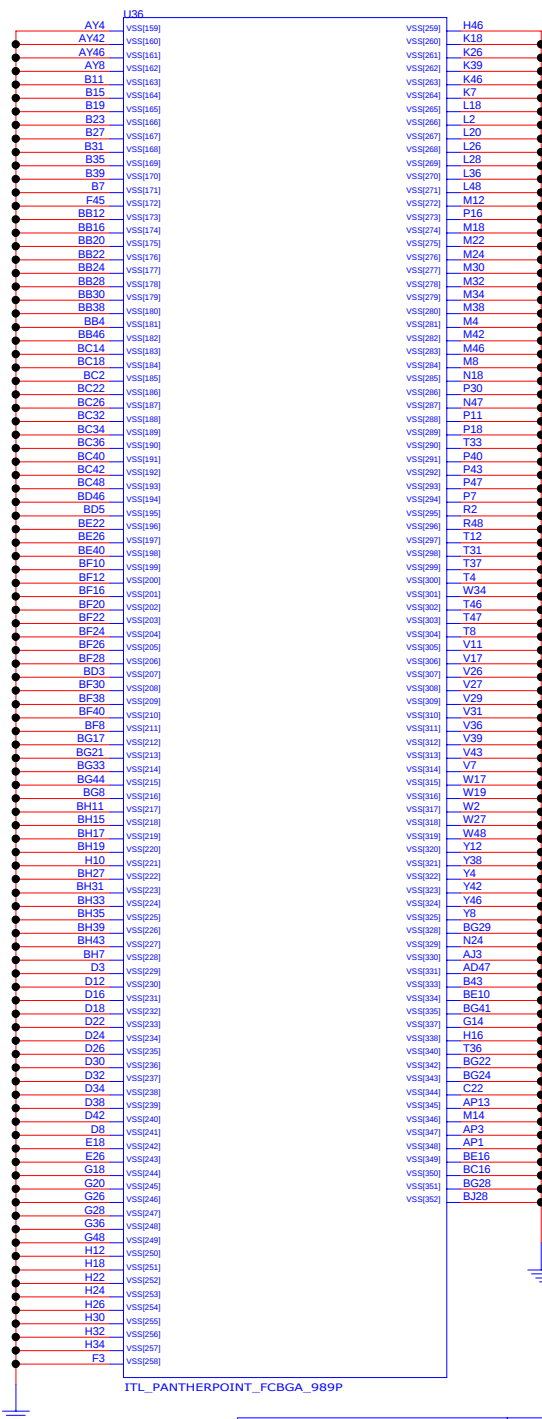
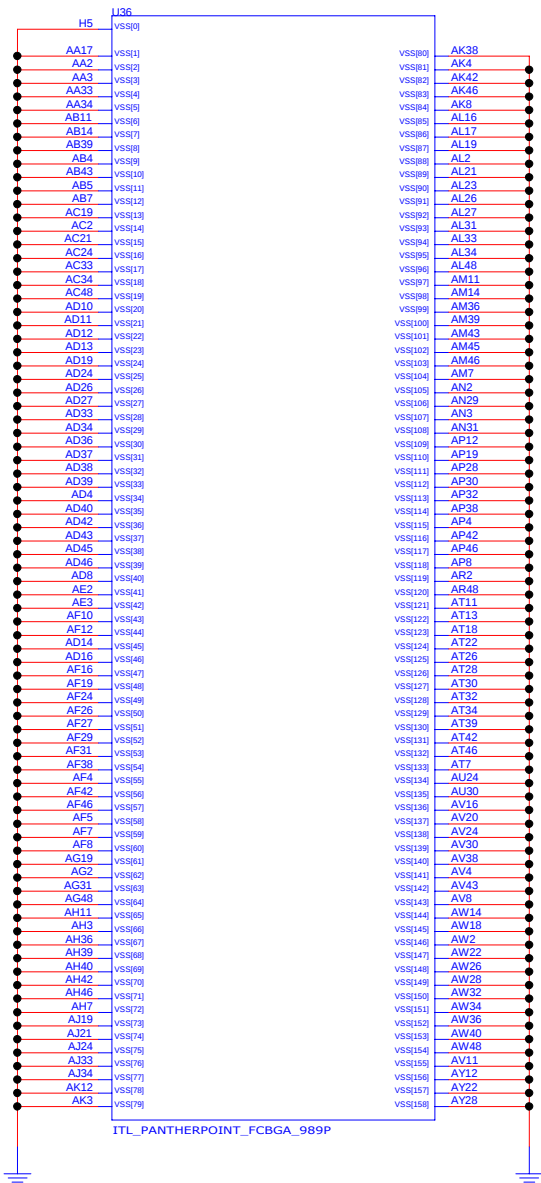
A

D

C

B

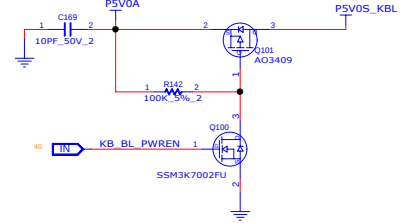
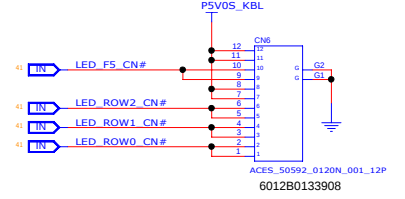
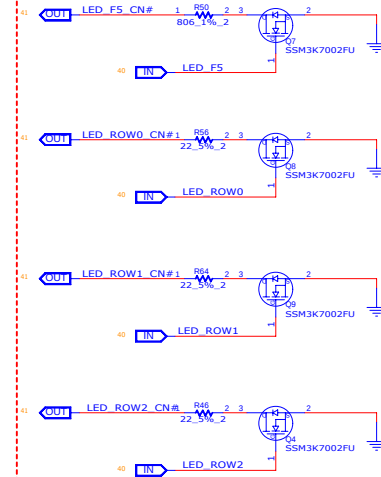
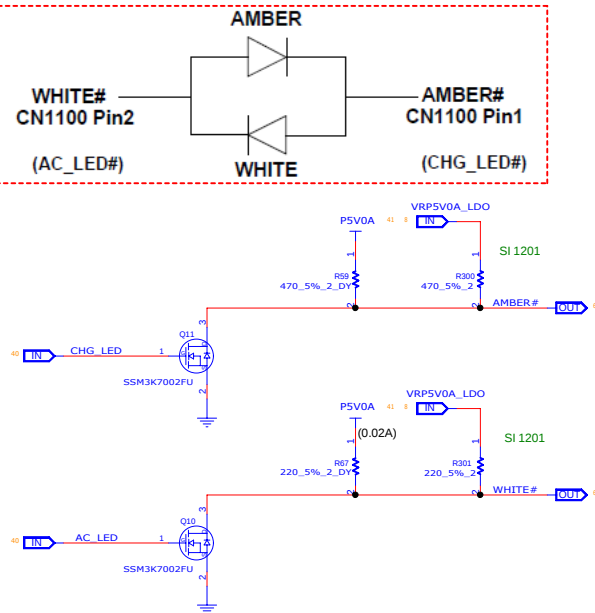
A



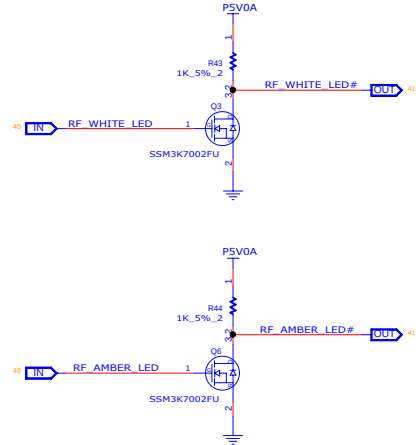
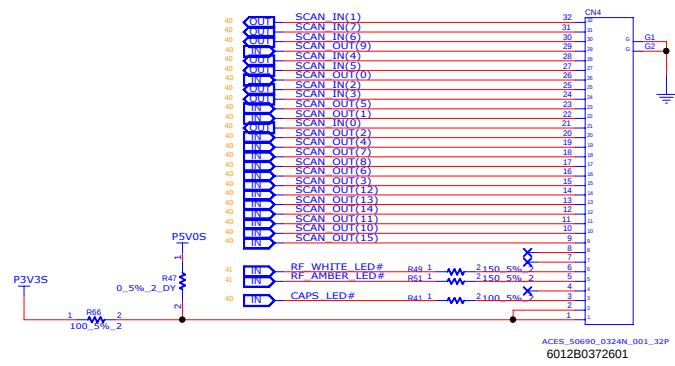
INVENTEC				
TITLE				
LV1.1, Lauren PCH-9				
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0	MTR	REV AX1
SHEET 39 of 67				



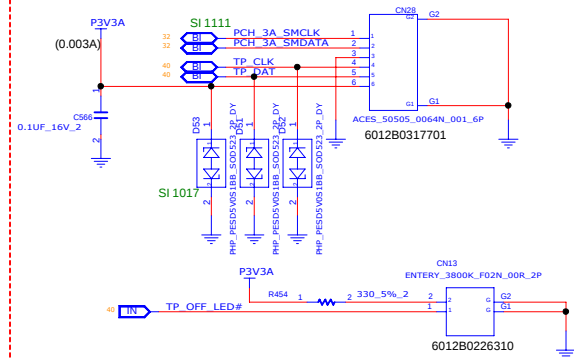
DC-JACK LED



KEYBOARD CONN



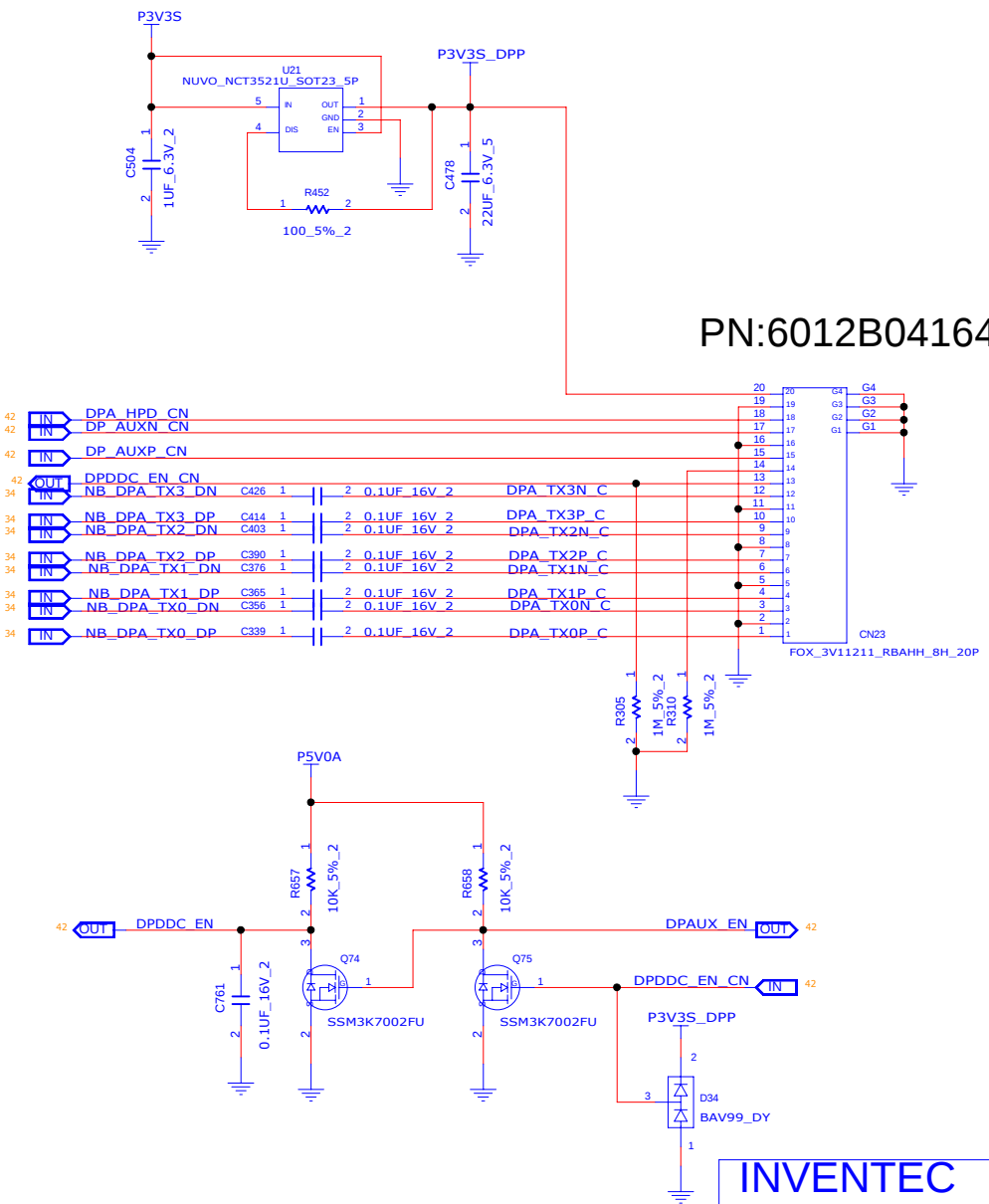
TOUCHPAD MODULE CONN



INVENTEC

Title			
LV1.1_Lauren			
KEYBOARD & TOUCHPAD			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A24853-5	MTR
SHEET		SI	67

1

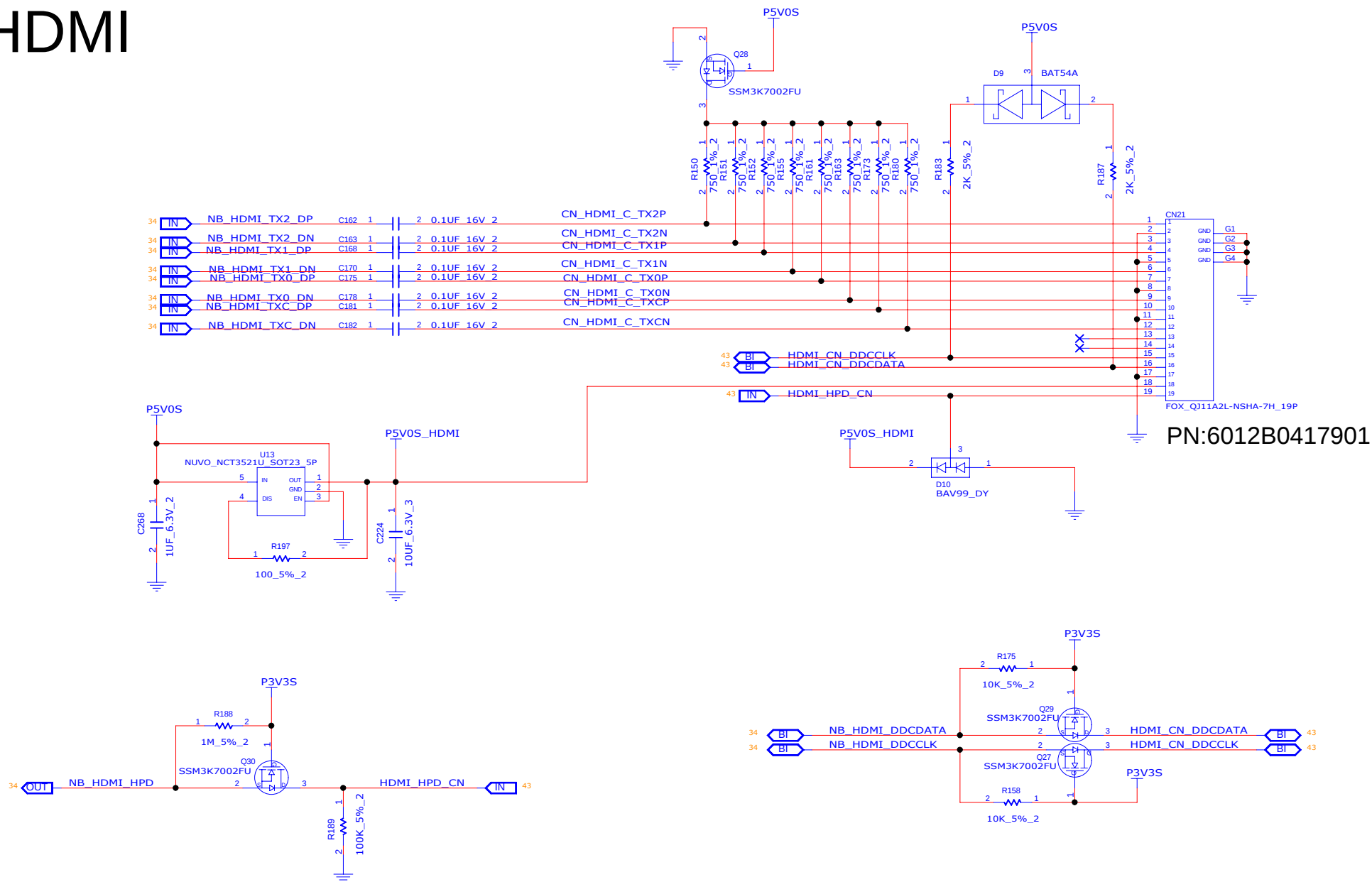


INVENTEC

TITLE			
LV1.1 Lauren DISPLAY PORT CONN			
SIZE A3	CODE CS	DOC.NUMBER 1310A24893-0 MTR	REV AX1
SHEET 42 of 67			

CHANGE by	David Cheng	DATE	12-12-2011
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A



INVENTEC

TITLE

LV1.1_Lauren

HDMI CONN

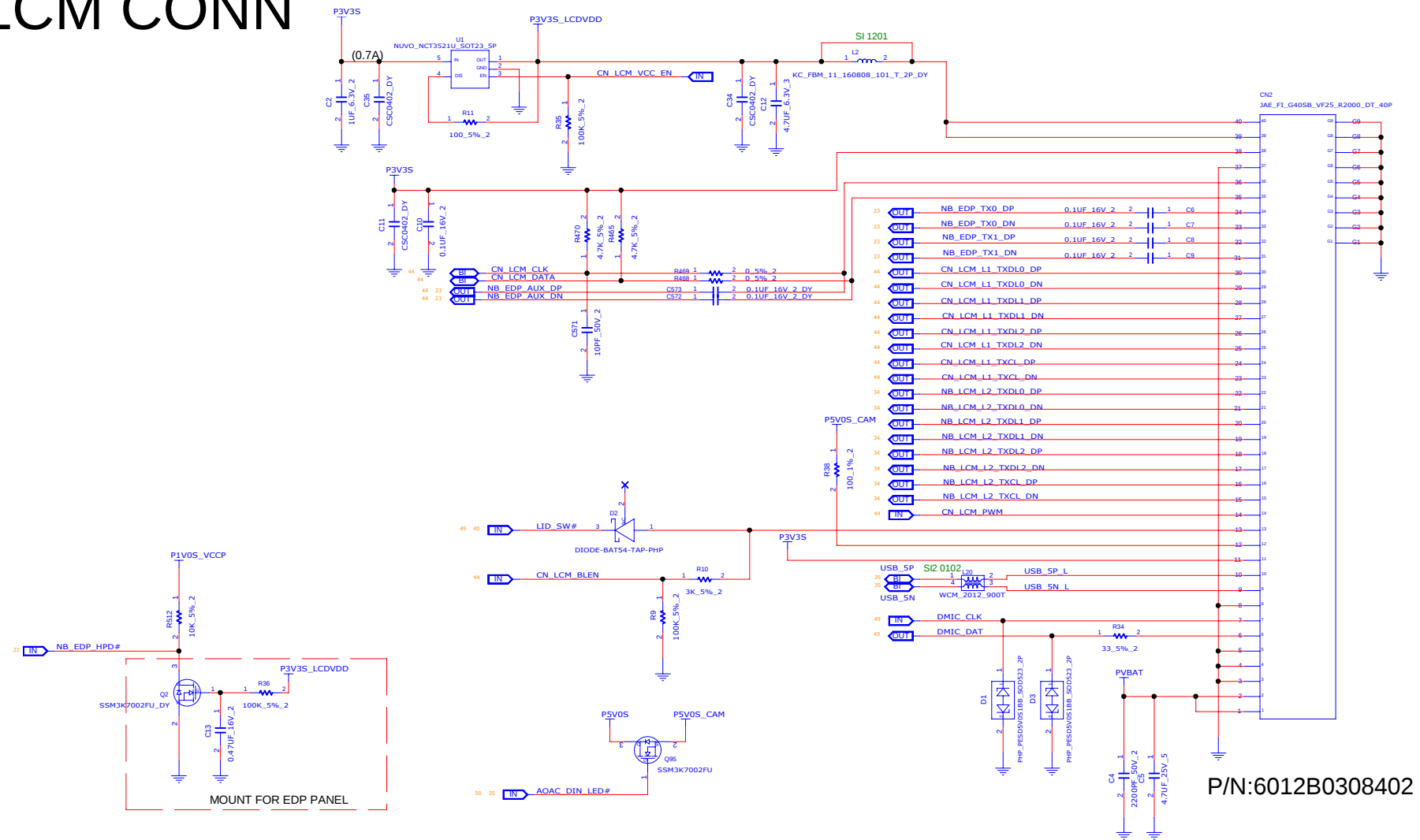
SIZE A3	CODE CS	DOC.NUMBER 1310A24893-0	MTR	REV AX1
------------	------------	----------------------------	-----	------------

SHEET 43 of 67

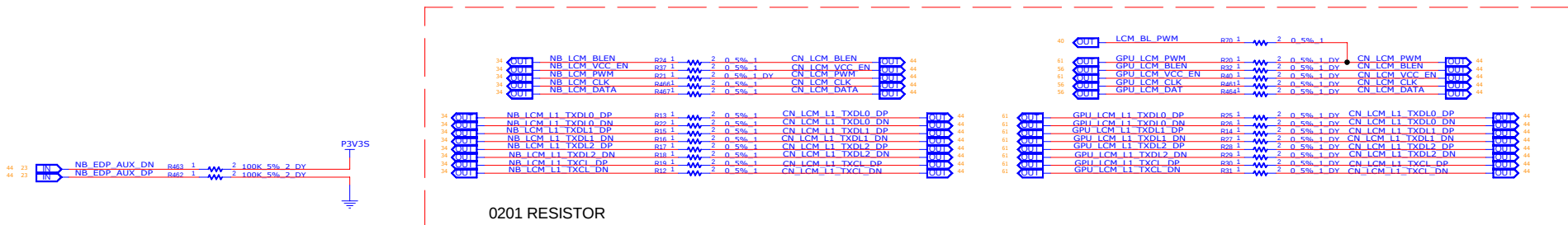
CHANGE by	David Cheng	DATE	12-12-2011
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SHEET 43 of 67

LCM CONN



P/N:6012B0308402

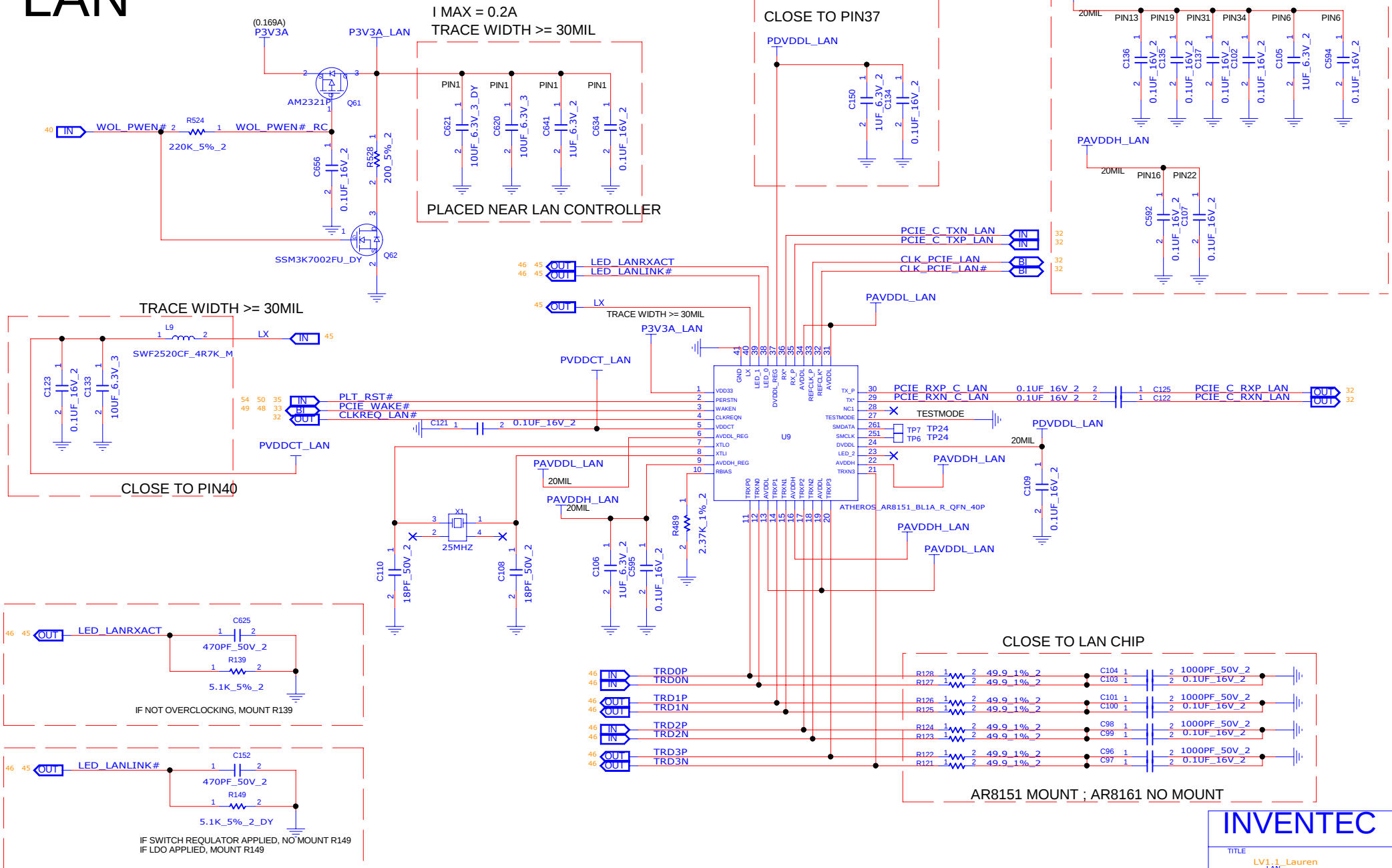


INVENTEC

Title			
LV1.1_Lauren			
LVDS			
Size	Code	Doc Number	Rev
L	LS	1310A24853-5	A1.1
Sheet		44	of 67

CHANGE by David_Chang DATE 12-12-2011

LAN

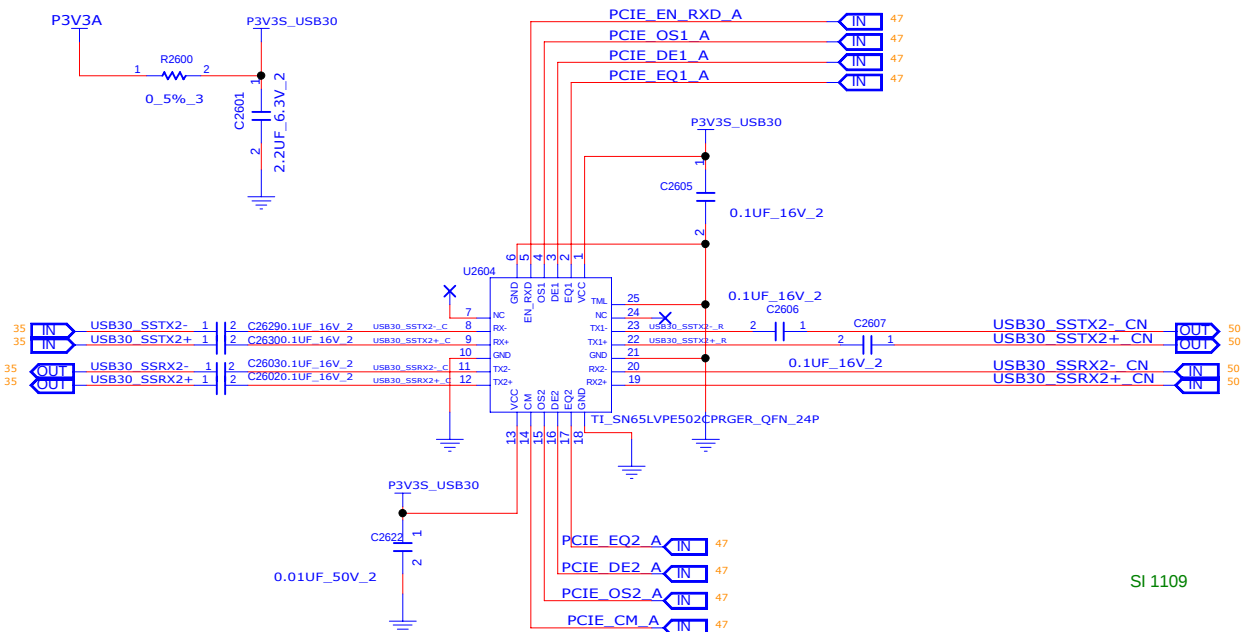


A

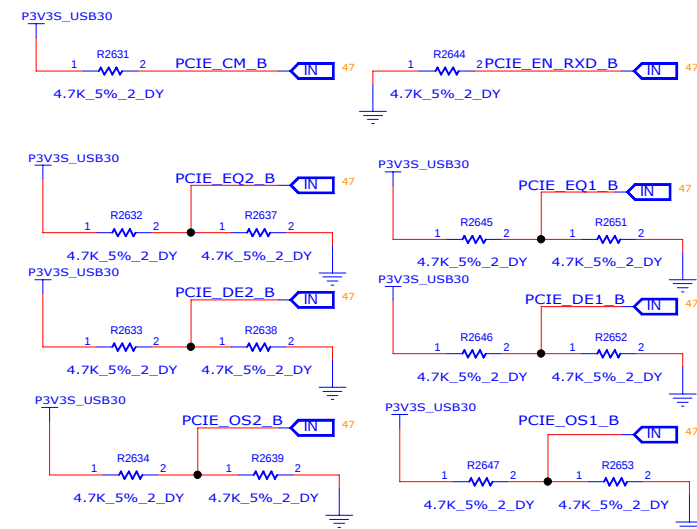
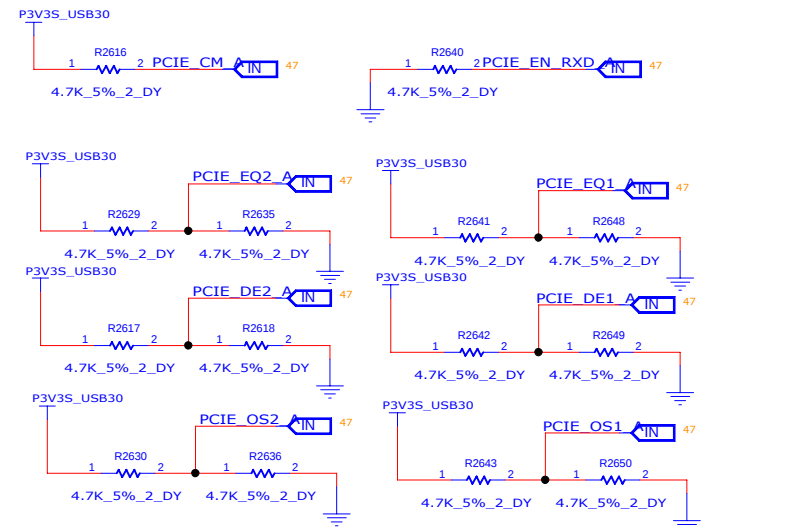
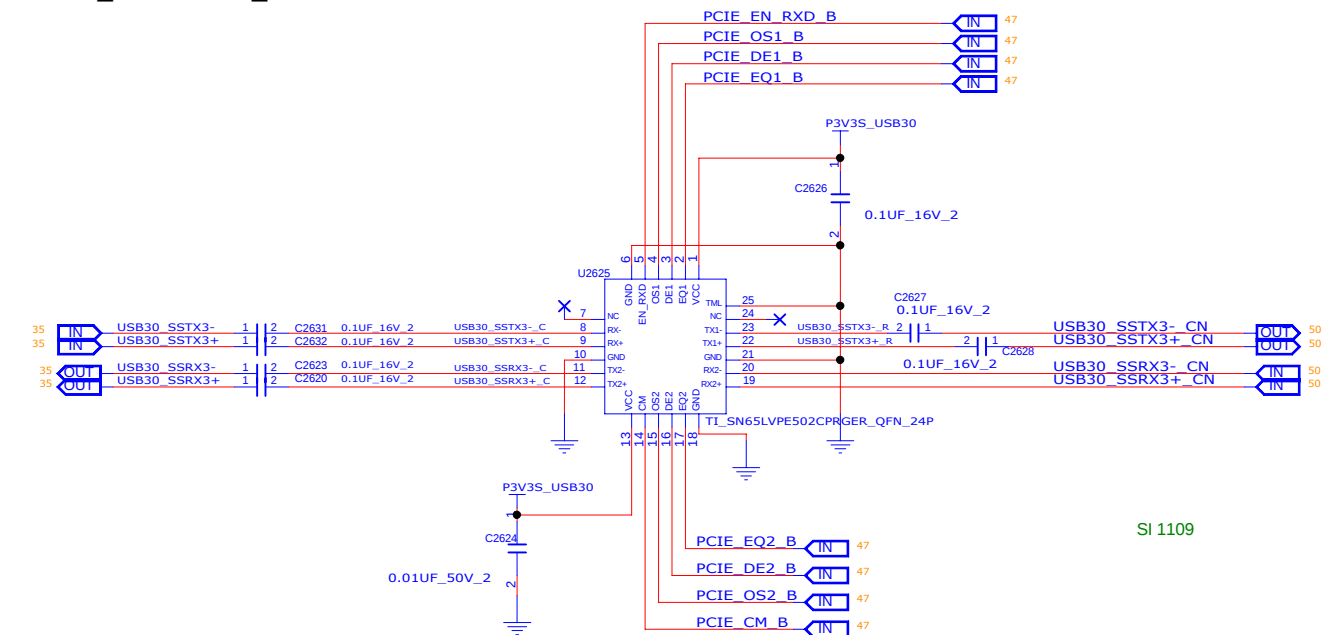


SHEET 46 of 67

USB30_REDRIIVER_PORT2



USB30_REDRIIVER_PORT3



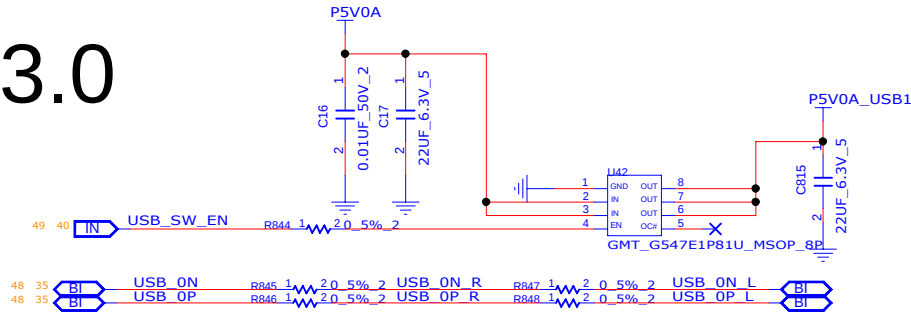
INVENTEC

TITLE			
LV1.1 Lauren			
USB30_REDRIIVER			
DOCNUMBER			
1310A24893-0			
SIZE	CODE	MTR	REV
A3	CS		AX1

CHANGE by David Cheng DATE 12-12-2011

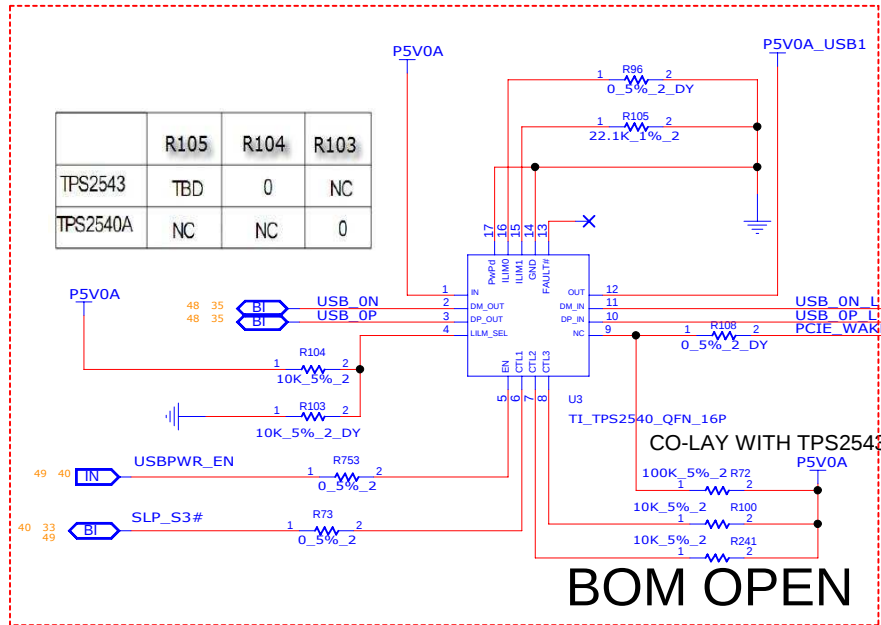
SHEET 47 of 67

USB 3.0



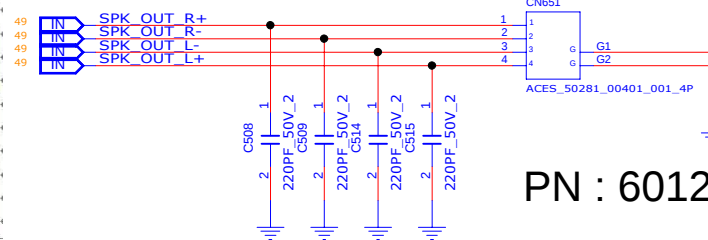
TPS2543/44 Control Truth Table

Input Logic Level				Logic Output	
CTL1	CTL2	CTL3	ILIM_SEL	Charging Mode	WAKE Output (active low)
0	0	0	x	OUT discharge, power switch off	off
0	x	1	0	Dedicated charging port, auto-detect	off
0	x	1	1	Dedicated charging port, auto-detect, w/ Mouse/Keyboard detect + pass through enabled (back to auto detect when detached)	active low if DCP load present
x	1	0	x	Standard downstream port, USB 2.0 Mode	off
1	0	0	x	Dedicated charging port, BC1.2 only	off
1	0	1	x	Dedicated charging port, divider	off
1	1	1	0	Standard downstream port, USB 2.0 Mode	off
1	1	1	1	Charging downstream port, BC1.2	active low if CDP load present



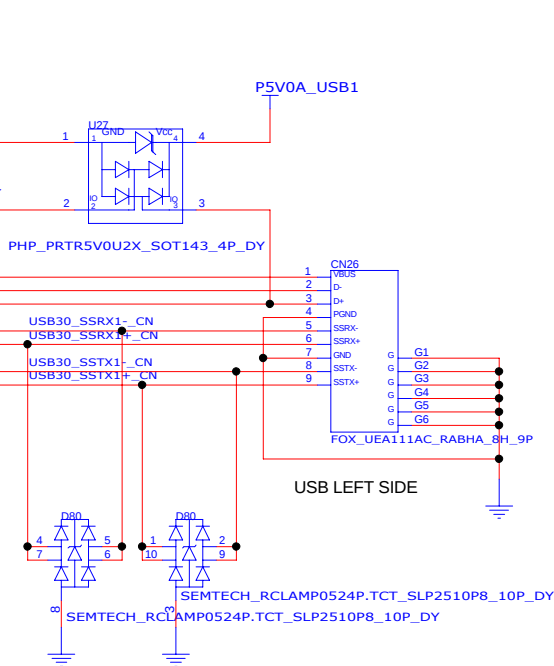
BOM OPEN

REAR SPEAKER



PN : 6012B0096202

USB CONN01



	USB charger port ^o			Standard USB port ^o		
	S0 ^o	S3 ^o	S4,S5 ^o	S0 ^o	S3 ^o	S4,S5 ^o
AC ^o	ON (EN=1) ^o (1,1,1) ^o (CDP MODE) ^o	ON (EN=1) ^o (0,1,1) ^o (DCP auto MODE) ^o	ON (EN=1) ^o (0,1,1) ^o (DCP auto MODE) ^o	ON ^o	ON ^o	ON ^o
DC ^o (normal batt) ^o (10%-100%) ^o	ON (EN=1) ^o (1,1,1) ^o (CDP MODE) ^o	ON (EN=1) ^o (0,1,1) ^o (DCP auto MODE) ^o	ON (EN=1) ^o (0,1,1) ^o (DCP auto MODE) ^o	ON ^o	OFF ^o	OFF ^o
DC ^o (low batt 10%) ^o	ON (EN=1) ^o (1,1,1) ^o (CDP MODE) ^o	OFF (EN=0) ^o (0,1,1) ^o	OFF (EN=0) ^o (0,1,1) ^o	ON ^o	OFF ^o	OFF ^o

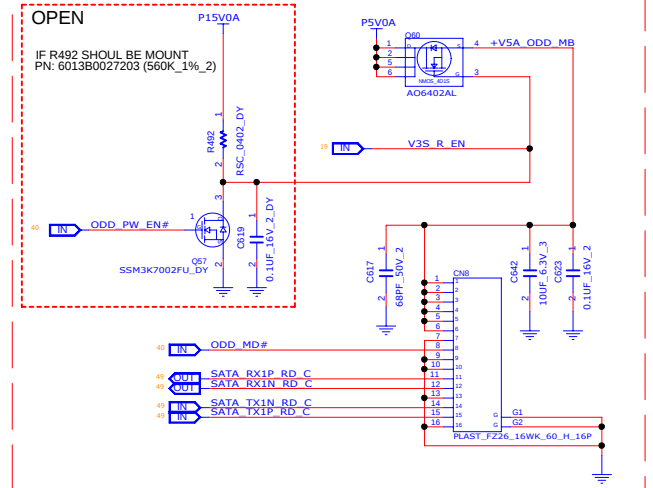
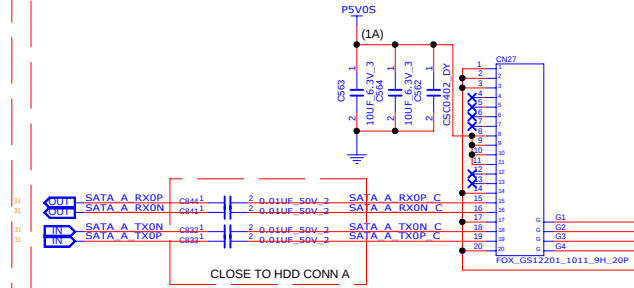
INVENTEC

TITLE			
LV1.1 Lauren USB3.0 CONN			
SIZE A3	CODE CS	DOC NUMBER 1310A24893-0	REV AX1

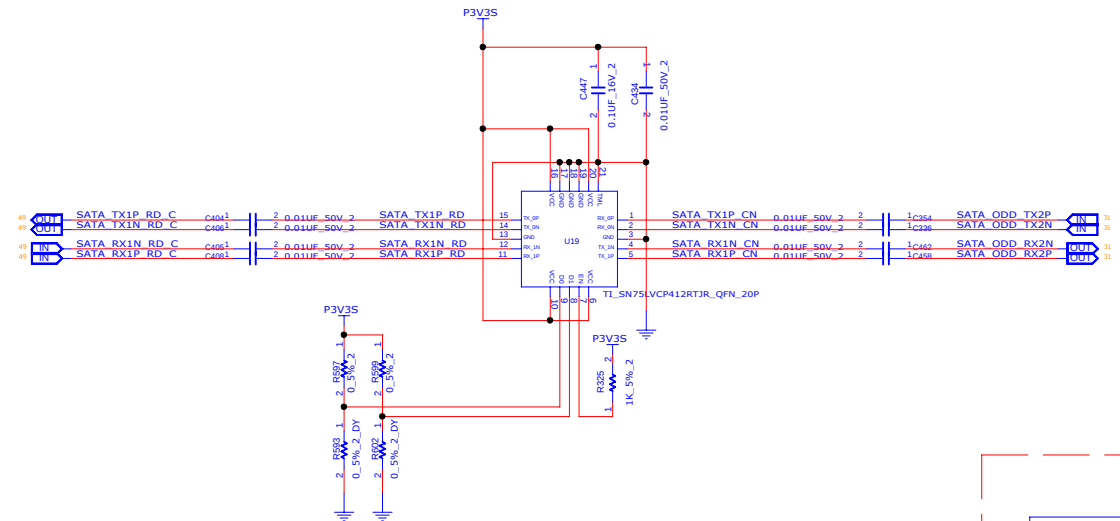
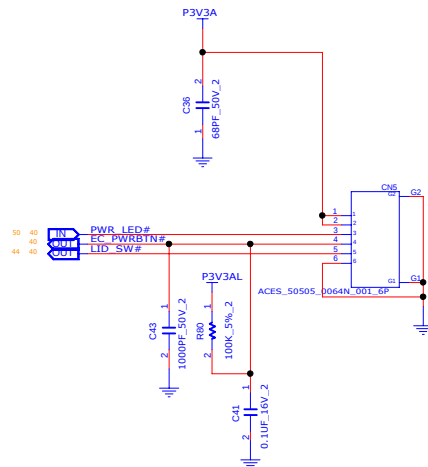
AU BOARD CN ON MB

SATA HDD CONN A

SATA ODD FFC CONN ON MB

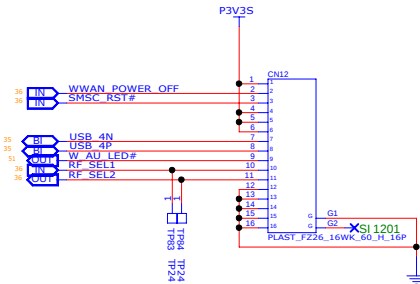


PBN CABLE CN ON MB

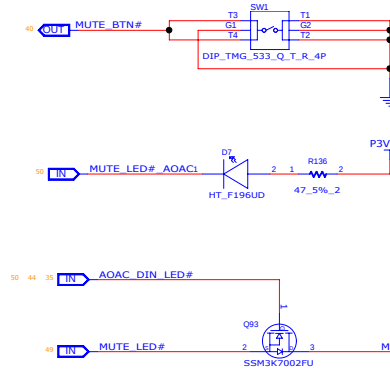


INVENTEC			
TITLE			
LV1.1_Lauren AUDIO, PWB, ODD BOARD & HDD CONN			
SIZE C	CODE CS	DOC NUMBER 1310A24893-D	REV AX1
SHEET		49	OF 67

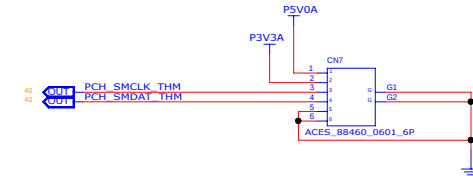
WIRELESS AUDIO BOARD CN ON MB MUTE BUTTON



MUTE BUTTON SWITCH

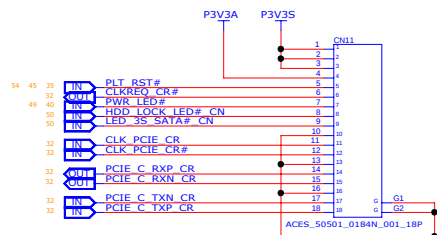


LDPS BOARD CN ON MB

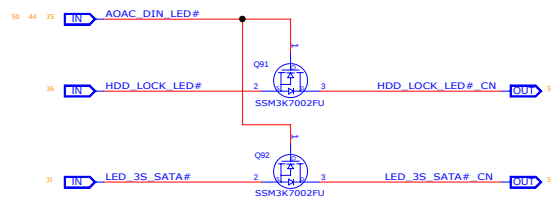


PN : 6012B0073304

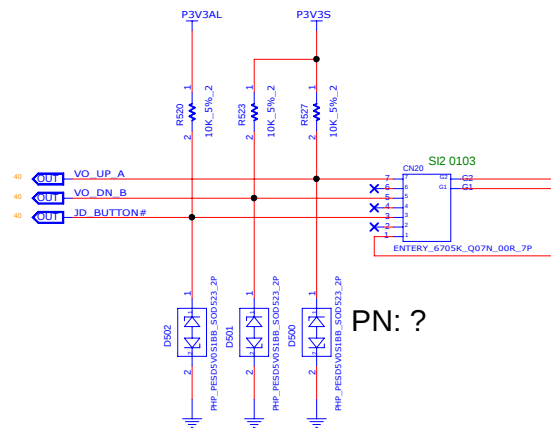
CR BOARD CN ON MB



PN : 6012B0354701

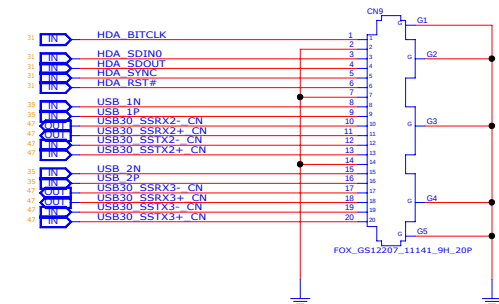


JOG DIAL CN ON MB



PN: ?

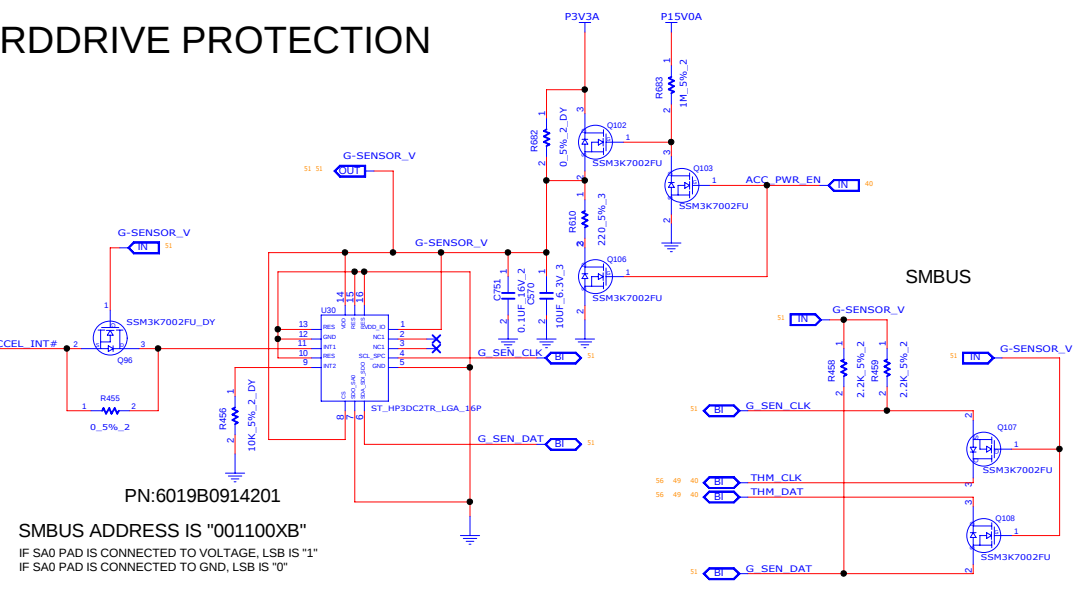
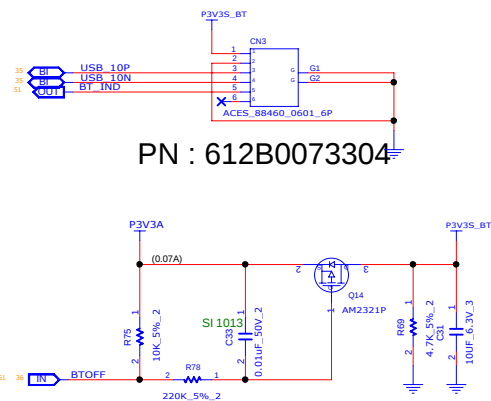
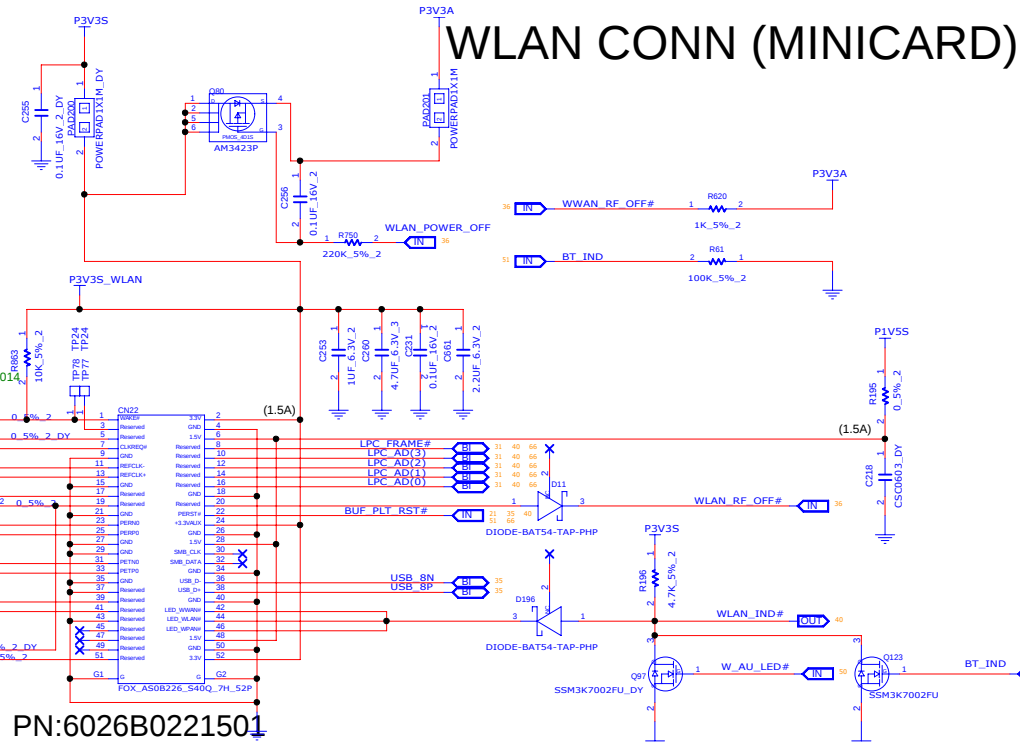
AU-USB30 CN ON MB (WIRE OR COXIAL)



INVENTEC

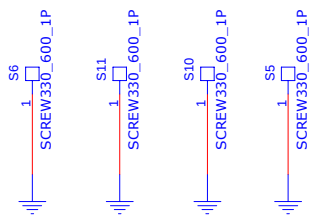
Title			
LV1.1_Lauren			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310A24853-5	A/1
SHEET		51	67

CHANGE BY: David_Chaud DATE: 12-12-2014

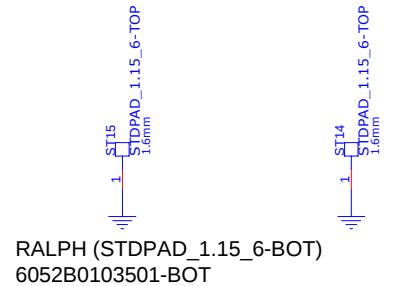


TITLE				
LV1.1_Lauren WLAN & HDD PROTECTION				
SIZE C	CODE CS	DOCNUMBER 1310A24893-0	MTR	REV AX1

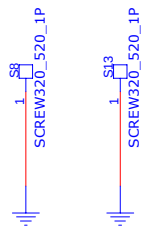
CPU SCREWS



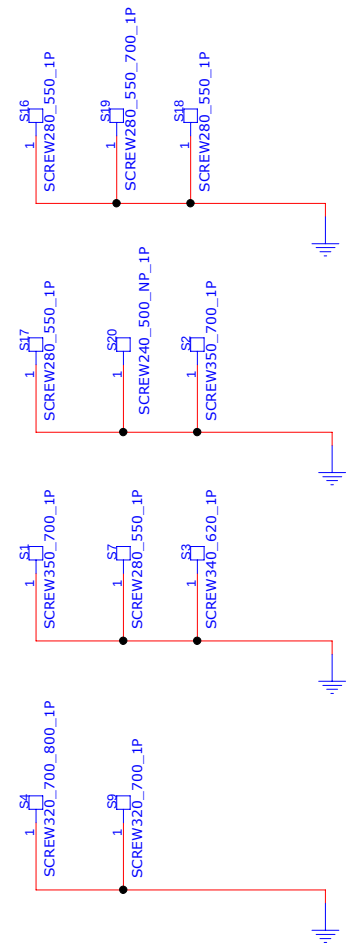
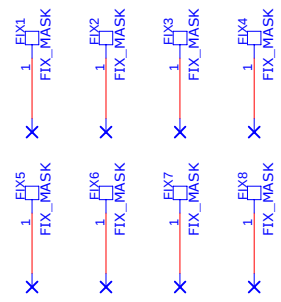
WWAN SCREWS



VGA SCREWS



FAN SCREWS



INVENTEC

TITLE			
LV1.1-Lauren EMI CAP & SCREW			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1

GPU

INVENTEC

TITLE

LV1: J. Lauren
BLANK

SIZE
A3

CODE
CS

DOC NUMBER
1310A24893-0

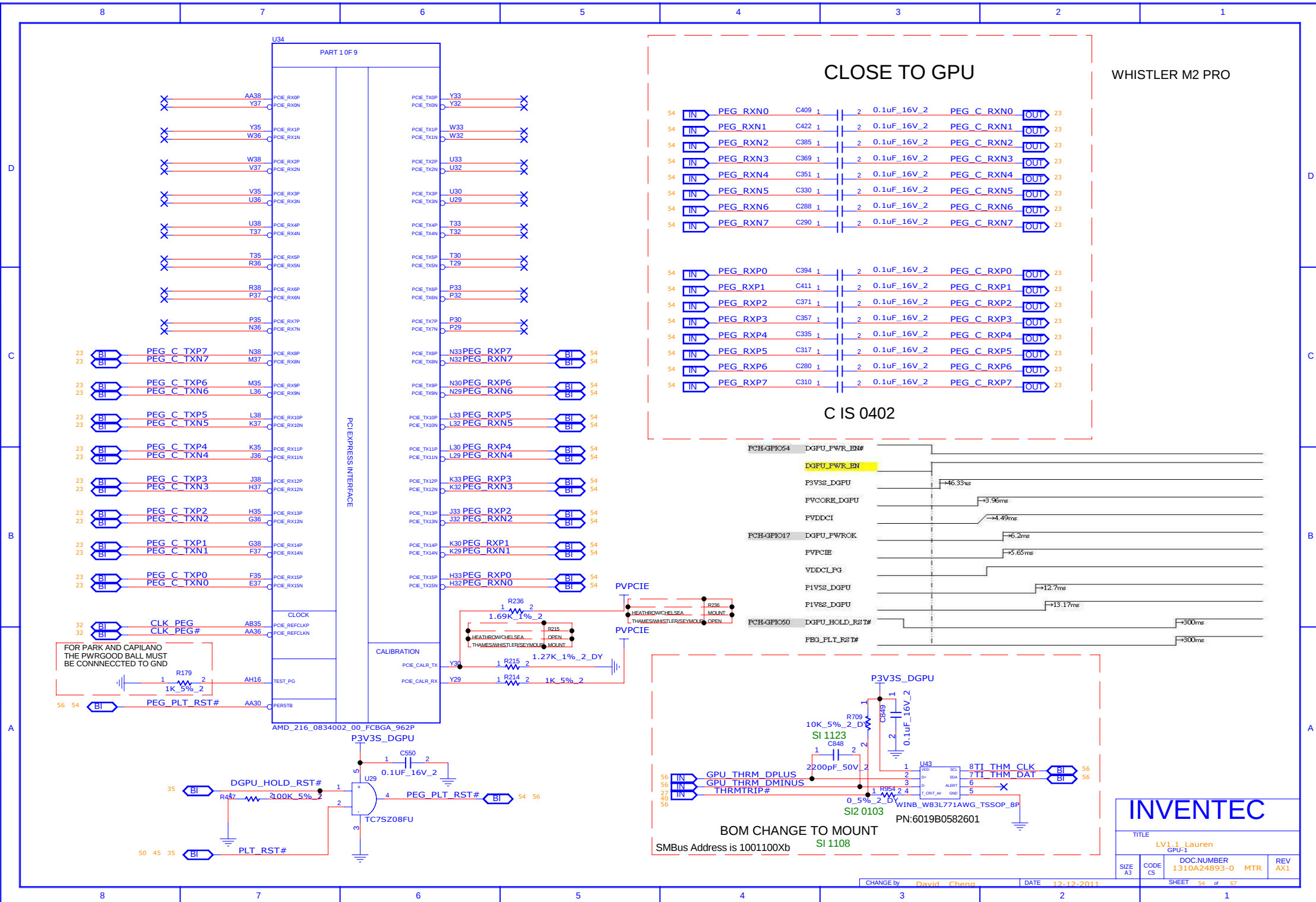
REV
AX1

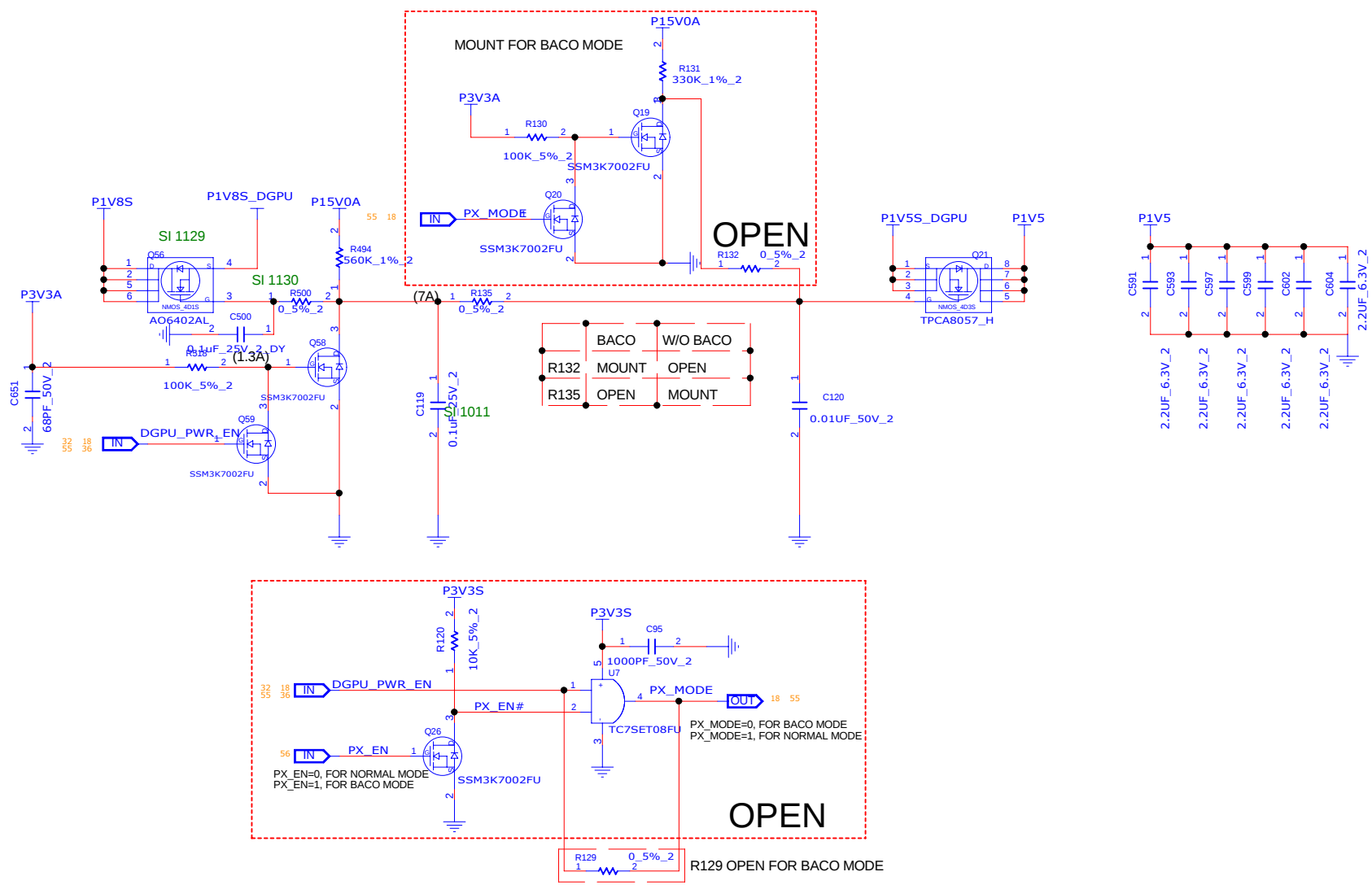
MTR

CHANGE by David Cheng

DATE 12-12-2011

SHEET 53 of 67



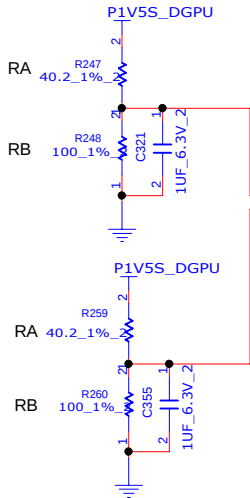




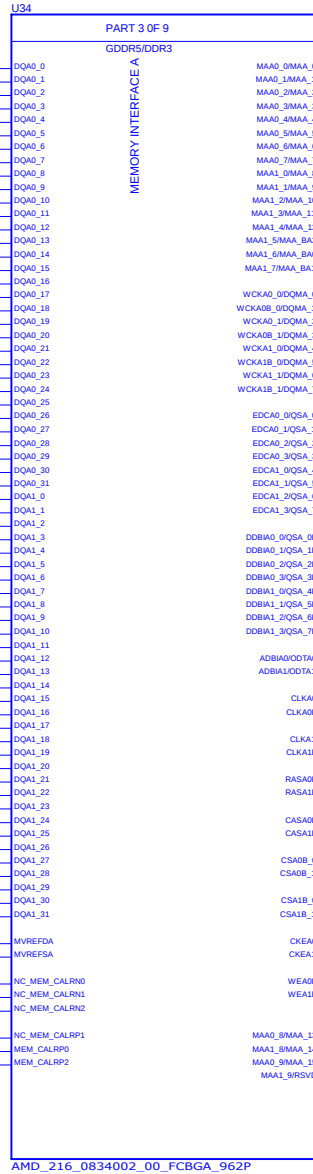
CHANNEL A

DDR3/GDDR3MEMORY STUFF OPTION

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
RA	40.2R	40.2R	40.2R
RB	100R	100R	100R



FOR M97,BROADWAY,MADISO AND PARK ONLY
IF R258,R249,R146,R244 SHOULD BE MOUNT,
P/N 6013A0087806 (243OHM,1%)



INVENTEC

TITLE
LV1.1_Lauren

SIZE A3	CODE CS	DOC NUMBER 1310A24893-0	MTR	REV AX1
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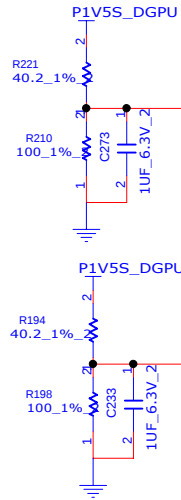
CHANGE by David Cheng DATE 12-12-2011

SHEET 57 of 67

CHANNEL B

DDR3/GDDR3MEMORY STUFF OPTION

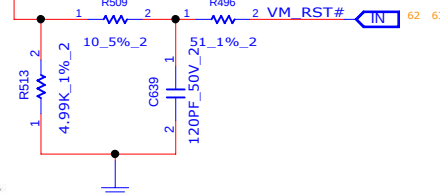
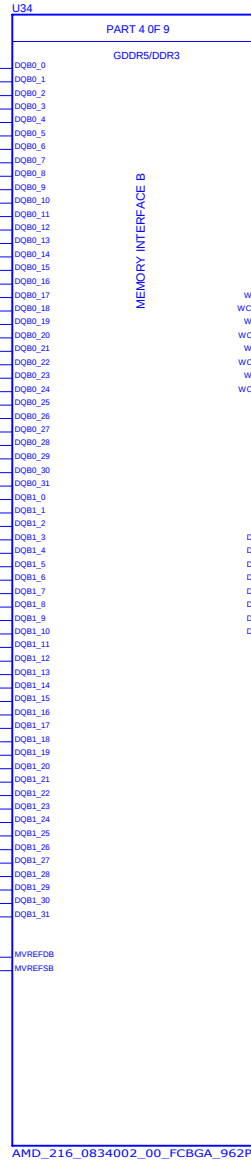
	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
RA	40.2R	40.2R	40.2R
RB	100R	100R	100R



MVREFDB_GPU

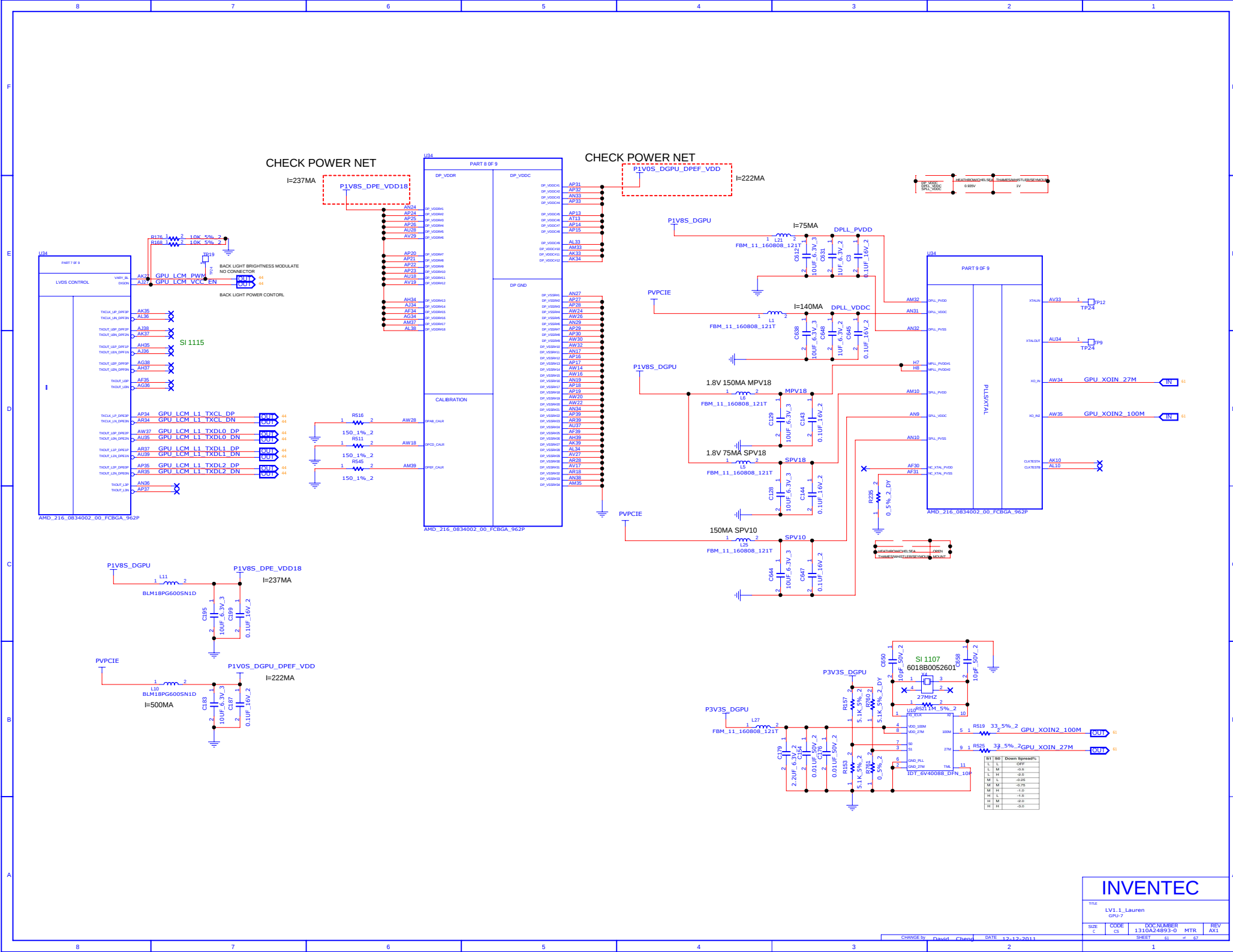
MVREFSB_GPU

Place all these components vry close to GPU (Within 25mm)
and keep all component close to each other (within 5mm) except Rser2
** This basic topology should be used for DDR3/GDDR3/GDDR5
These Capacitors and Resistor values are an example only.
The Series R and || Cap values will depend on the DRAM load and will have to be
calculated for different Memory , DRAM Load and board to pass Reset Signal Spec.



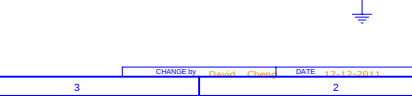
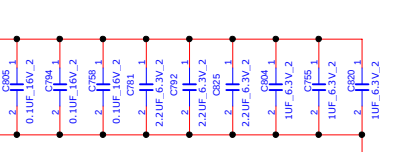
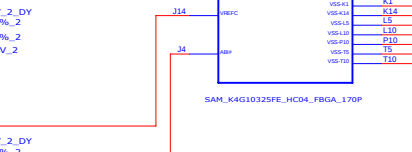
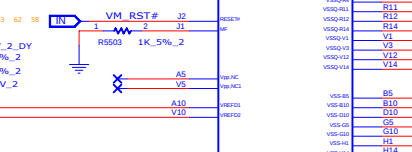
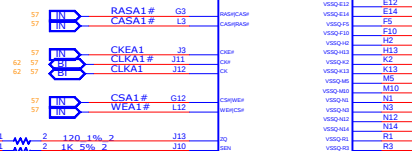
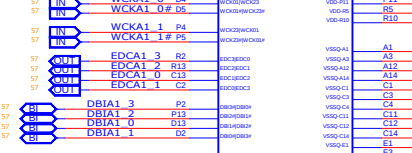
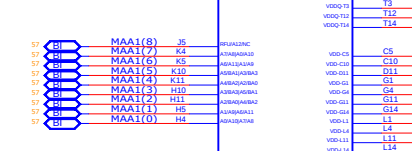
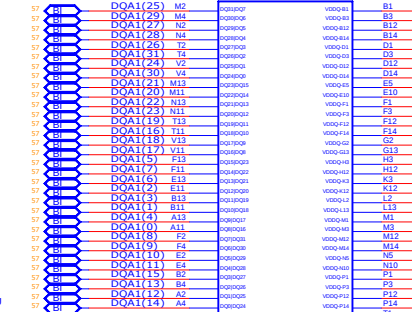
INVENTEC			
TITLE			
LV1.1_Lauren			
DOC NUMBER	1310A24893-0	MTR	REV AX1
SHEET 58 of 67			

CHANGE by David Cheng DATE 12-12-2011



CHANNEL A MEMORY

VENDER	DENSITY	VENDER PN	IEC PN
SAMSUNG	32MX32	K4G10325FC-HC04	6019B0899201
HYNIX	32MX32	H5GQ1H24BFR-T2C	6019B0866701
SAMSUNG	64MX32	K4G20325FC-HC04	6019B0842201
HYNIX	64MX32	H5GQ2H24MFR-T2C	6019B0843001



INVENTEC

FILE
LV1.1_Lauren

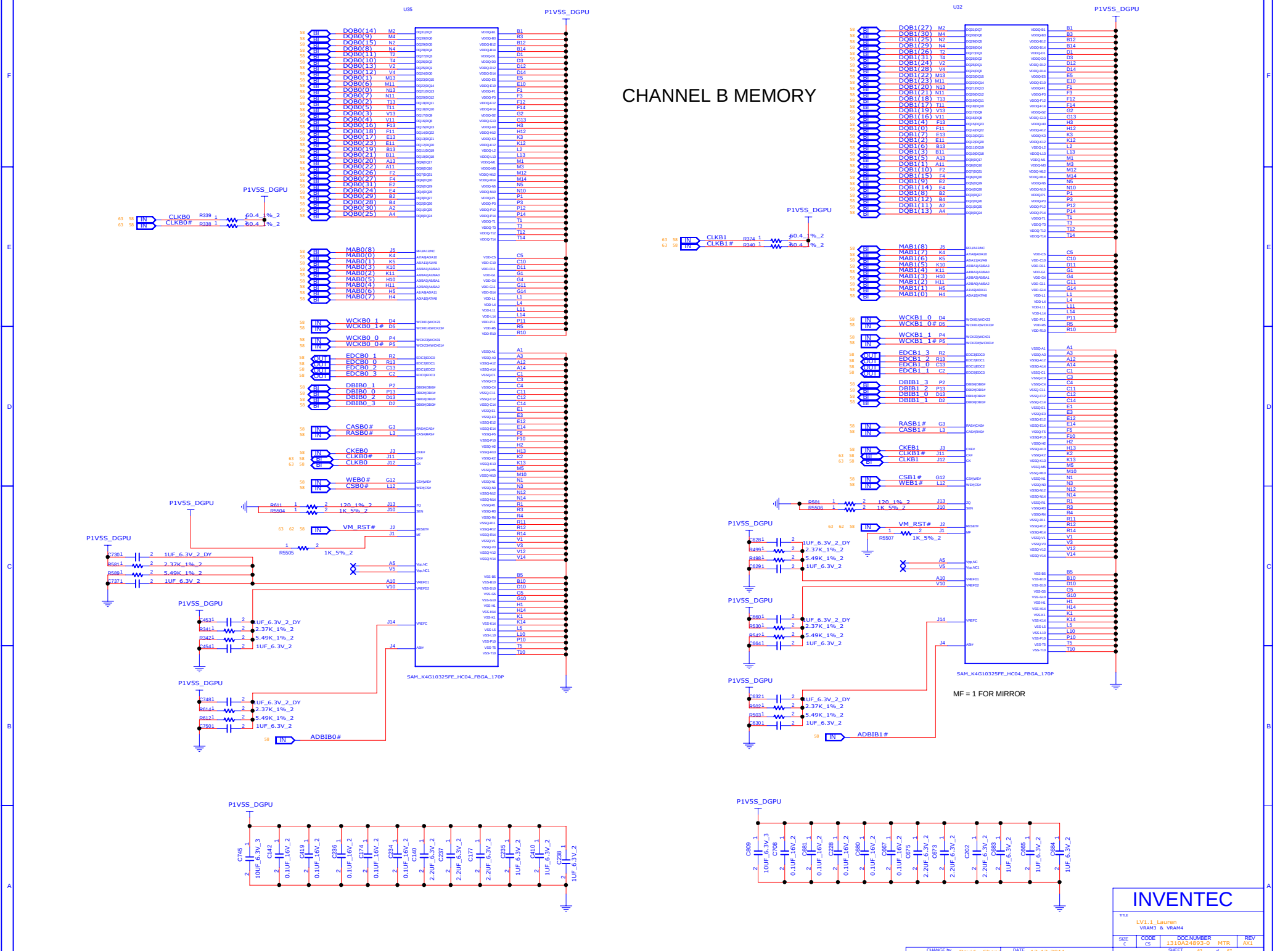
VRAM1 & VRAM2

SIZE CODE CS DOC NUMBER
1310A24853-5 MTR REV
A/1

CHANGE BY: Claudio Chaves DATE: 12-12-2014

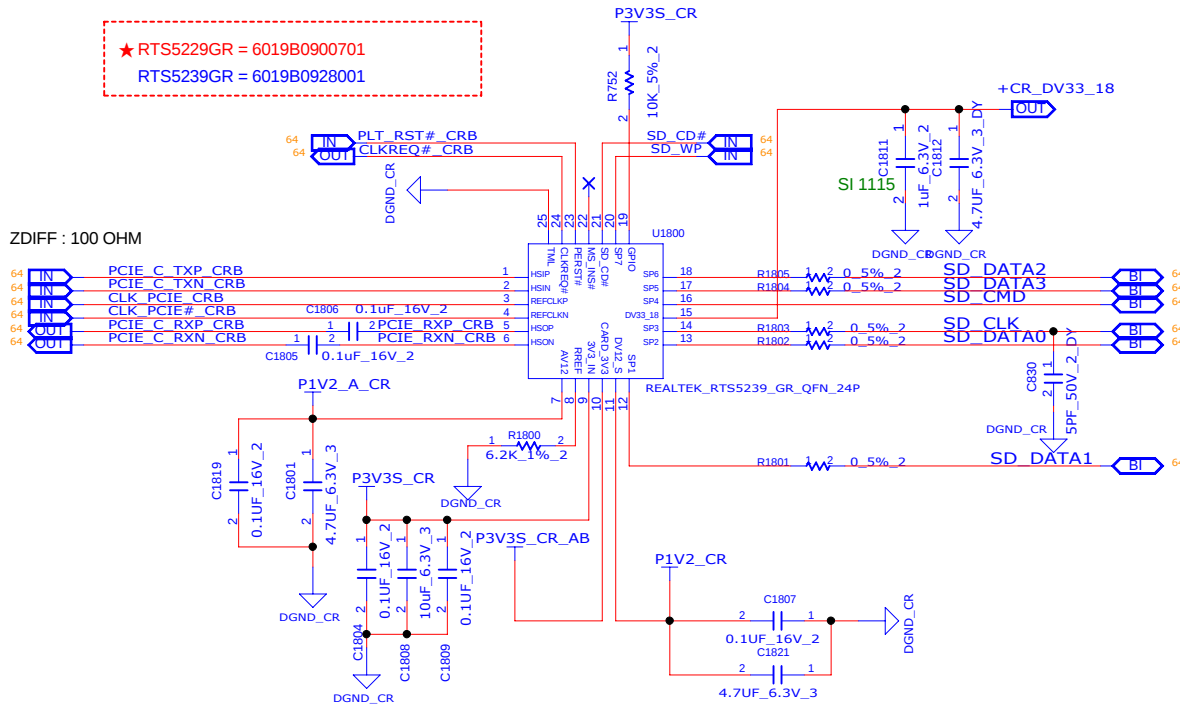
SHEET 52 OF 57

CHANNEL B MEMORY

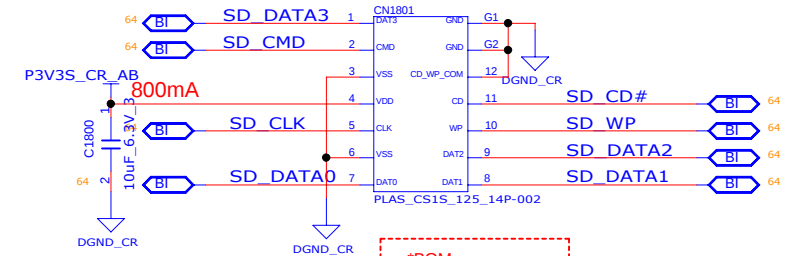


CARDREADER BOARD

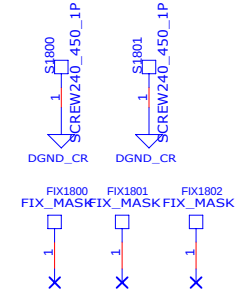
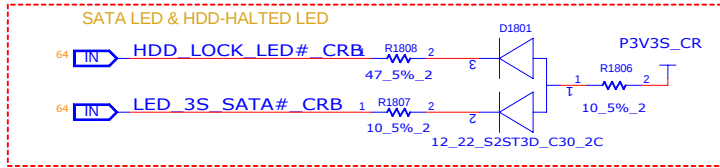
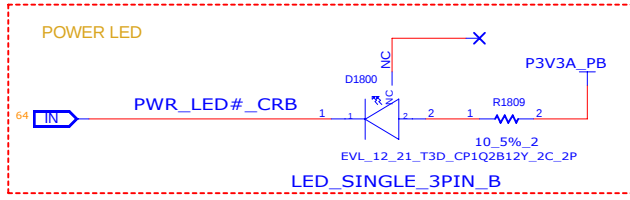
★ RTS5229GR = 6019B0900701
RTS5239GR = 6019B0928001



CARD READER CONN

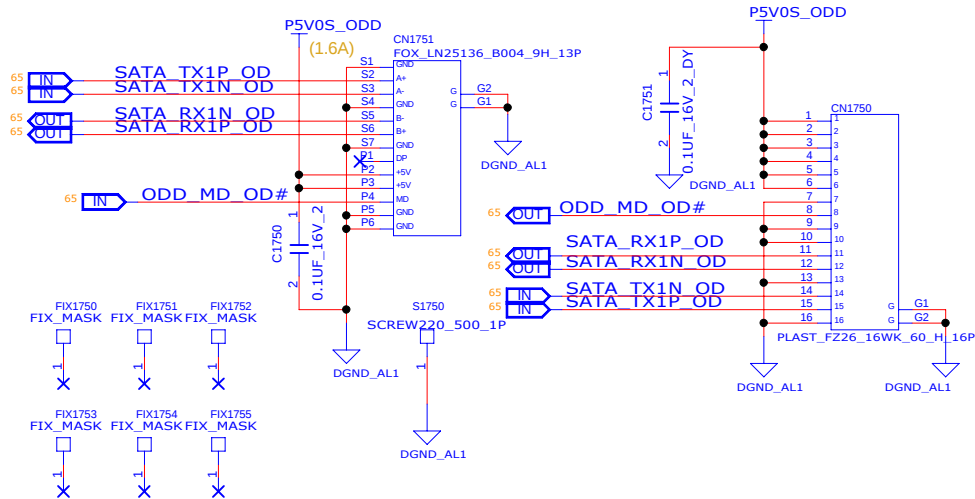


*BOM
CN1801
PN: 6026B0103603

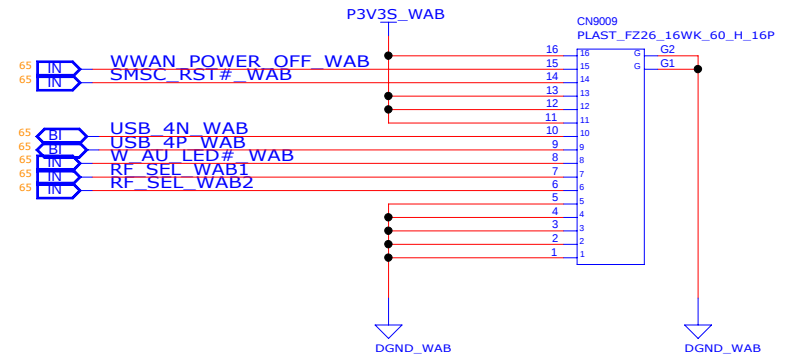


INVENTEC				
TITLE				
LV1.1 Lauren CARDREADER BOARD				
DOC NUMBER				
1310A24893-0 MTR				
REV				
AX1				
SIZE	CODE	SHEET		64 of 67
A3	CS			

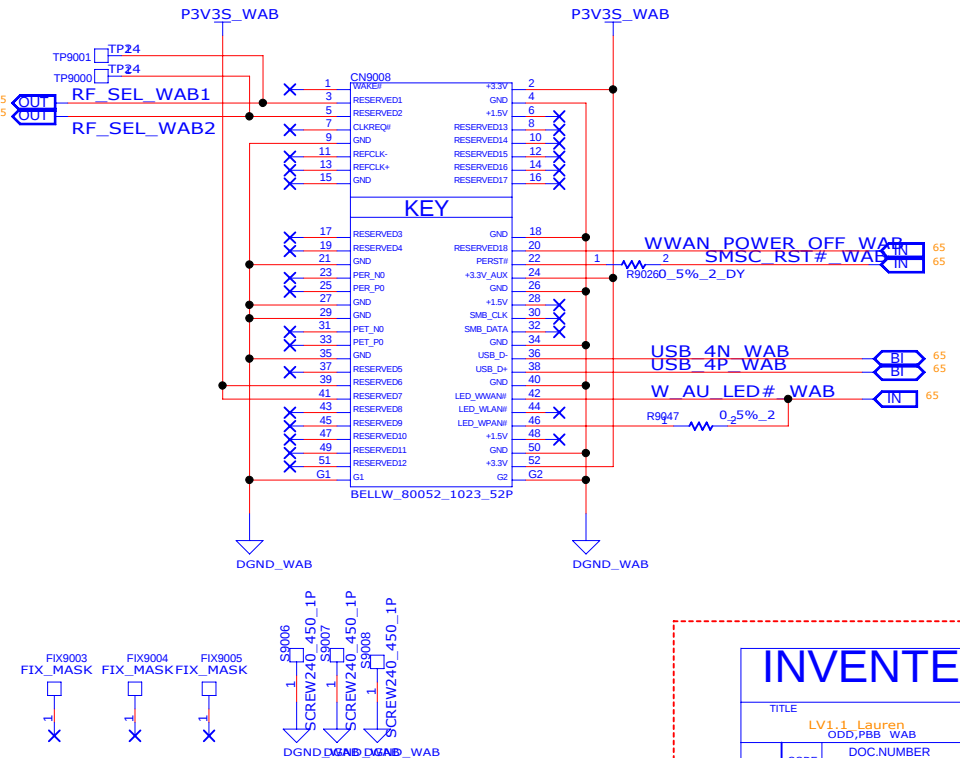
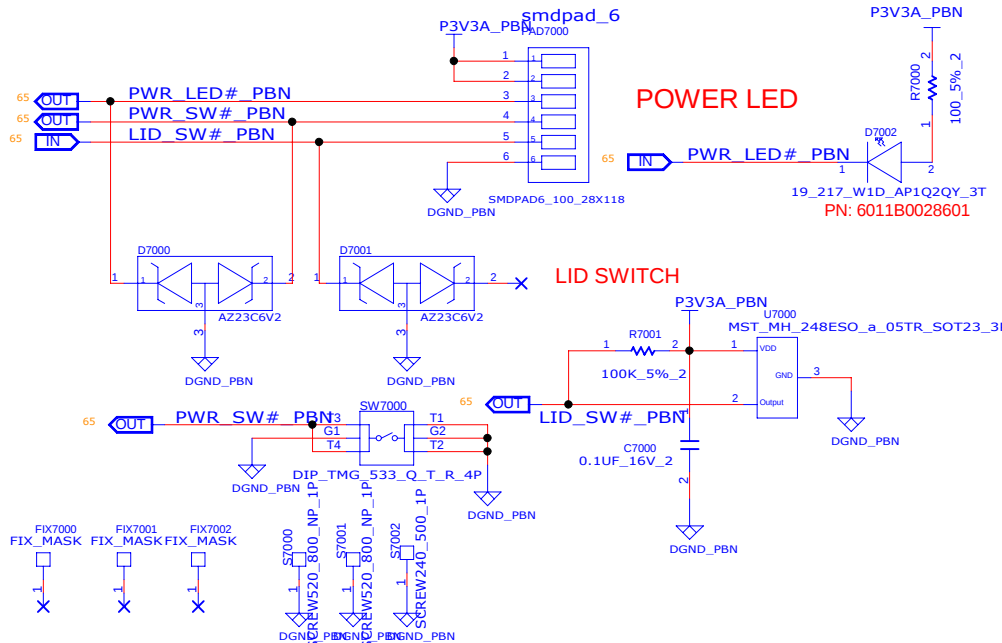
ODD Board



WIRELESS AUDIO BOARD



Power Button Board

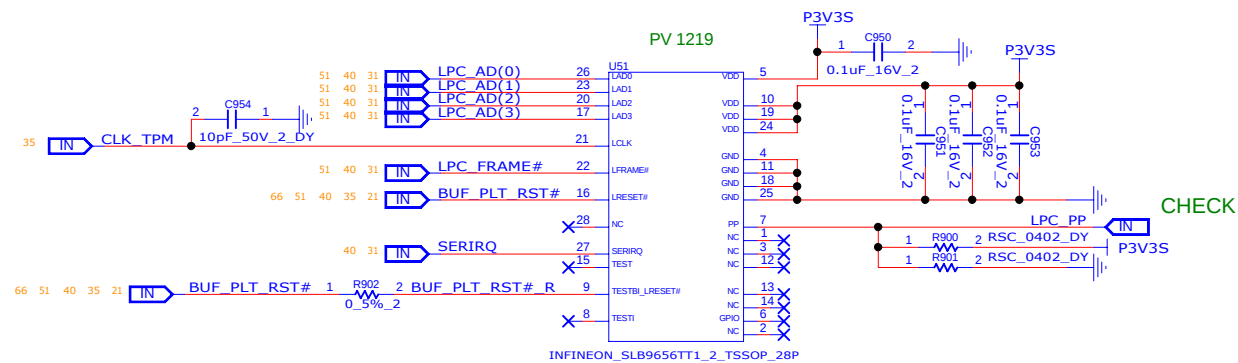


INVENTEC

TITLE				
LV1.1 Lauren ODD_PBB_WAB				
DOC NUMBER				
1310A24893-0				
REV				
AX1				
SIZE	CODE	MTR		REV
A3	CS			AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 65 of 67



TPM1.2

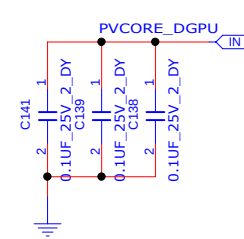
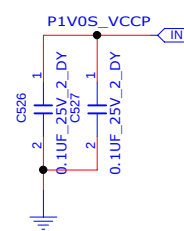
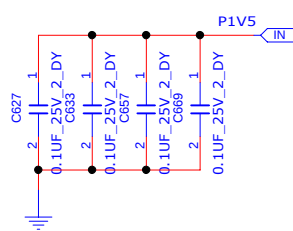
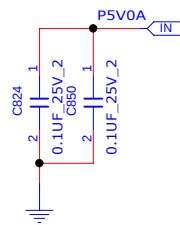
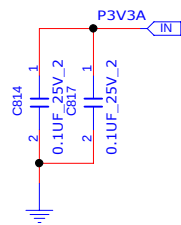
INVENTEC

TITLE			
LV1.1_Lauren Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1

CHANGE by David Cheng DATE 12-12-2011

SHEET 66 of 67

EMI



INVENTEC

TITLE			
LV1.1_Lauren Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310A24893-0 MTR	AX1