

Kapalua Schematic

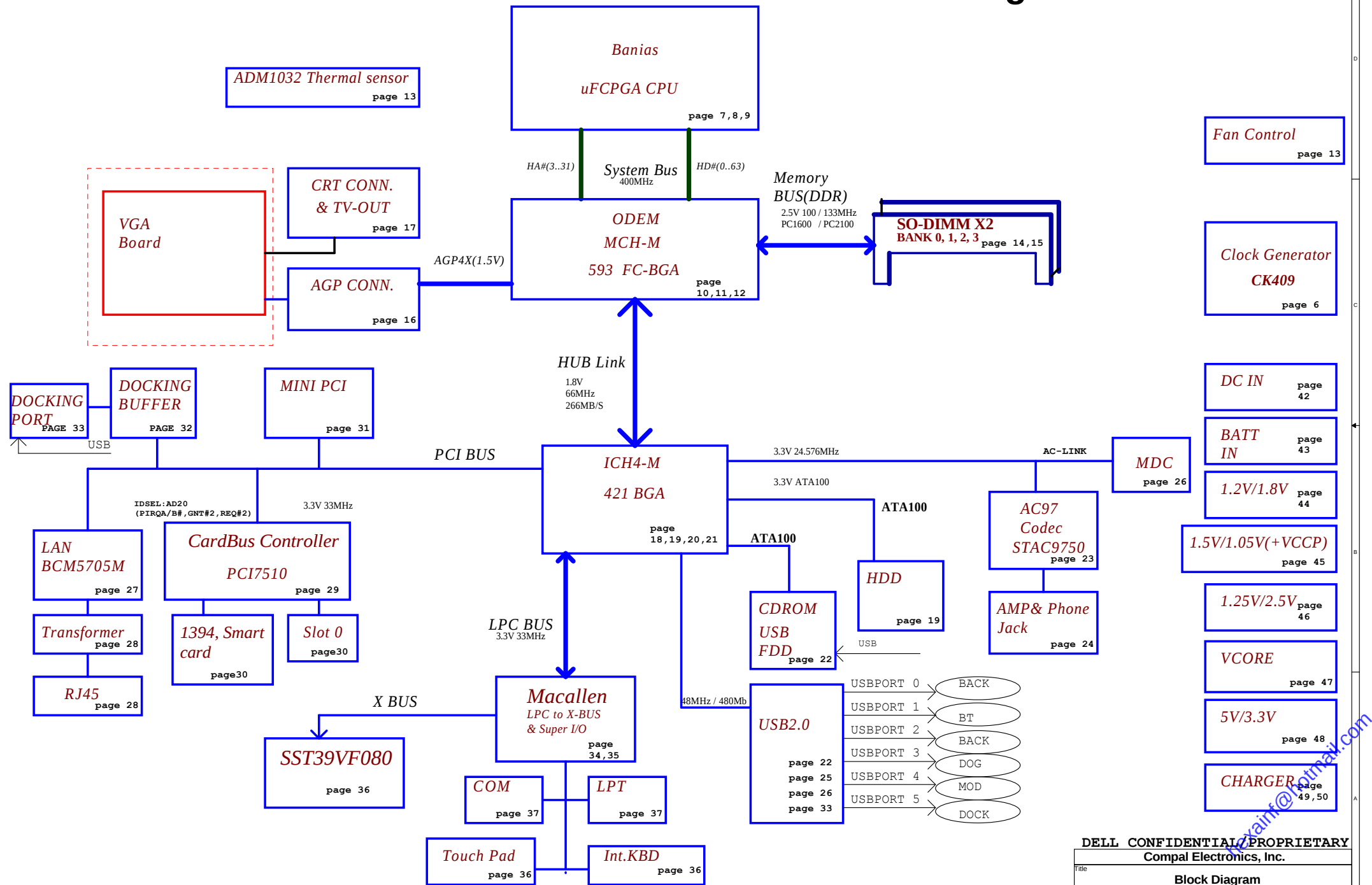
REV : X03

BDQ11/LA1601 Schematic with Capture CIS and Function field
uFCPGA Banias
02-12-2003
REV: 0.5

@	: Depop Component
1@	: Depop Component
2@	: Pop Component

DELL CONFIDENTIAL/PROPRIETARY		
Compal Electronics, Inc.		
Title		
Cover Sheet		
Size	Document Number	Rev
	BDQ11/LA-1601	0.5
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Block Diagram



PM TABLE

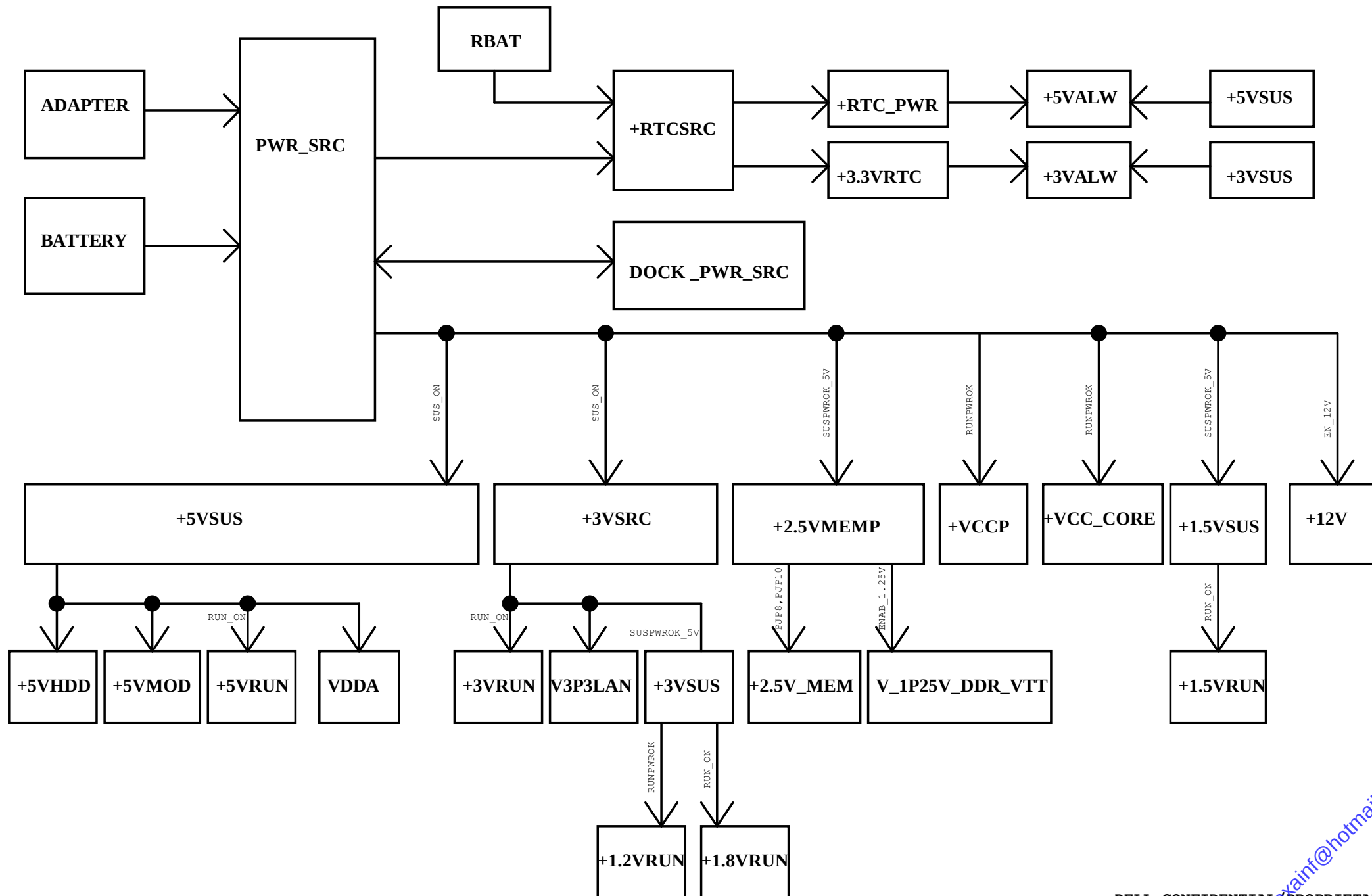
State \ power plane	+3VALW +5VALW	+3VSUS +5VSUS +2.5V_MEM +1.5VSUS	+5VRUN +3VRUN +1.8VRUN +1.5VRUN +1.2VRUN +VCC_CORE +VCCP +12V
S0	ON	ON	ON
S1	ON	ON	ON
S3	ON	ON	OFF
S5 S4/AC	ON	OFF	OFF
S5 S4/AC don't exist	OFF	OFF	OFF

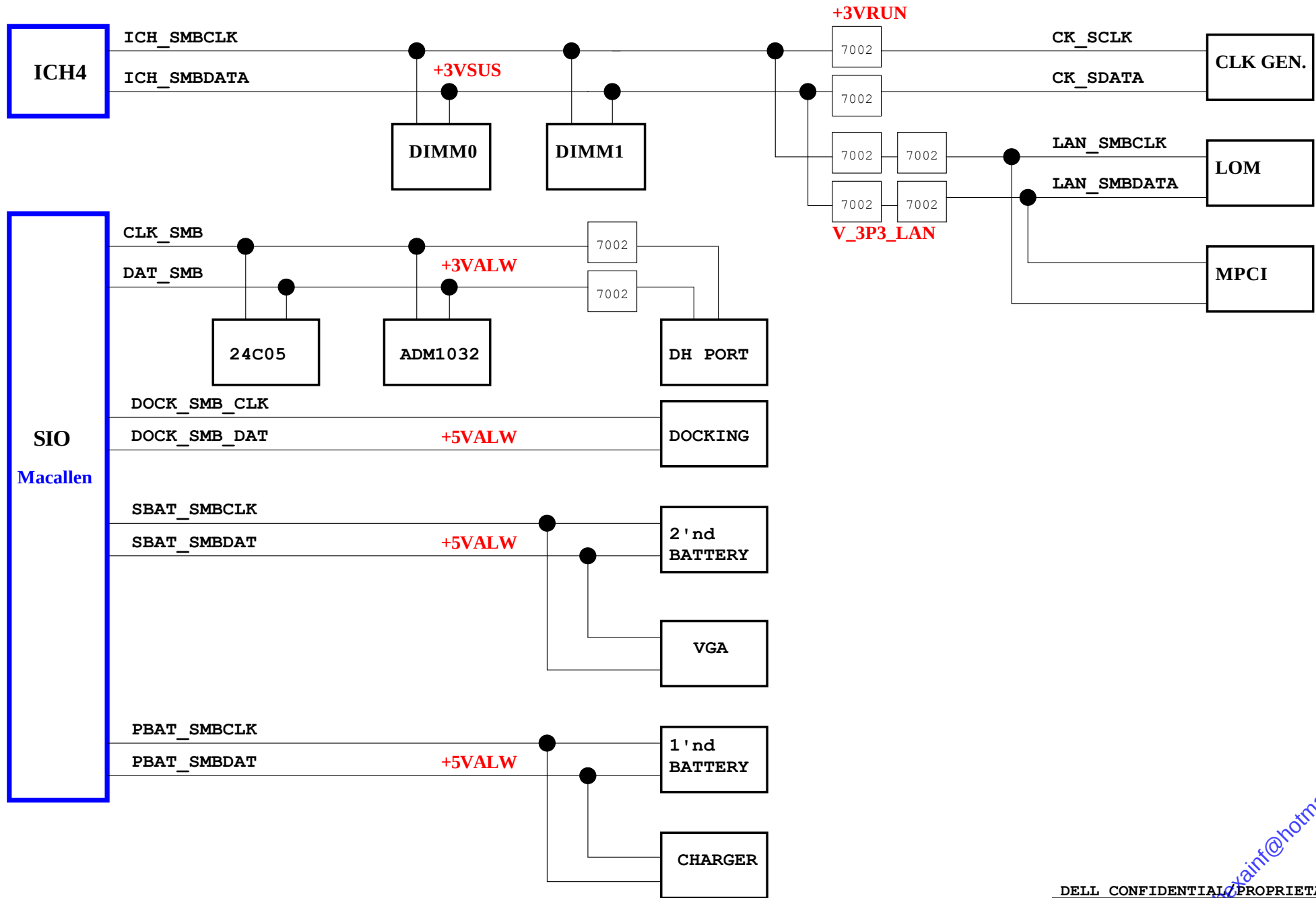
PCI TABLE

PCI DEVICE	IDSEL	REQ#/GNT#	PIRQ
CARD BUS	AD17	1	D,C
LAN	AD16	4	C
DOCK	AD24	0	B
MINI PCI	AD19	3	D,B(NP)
VGA			A,B(NP)

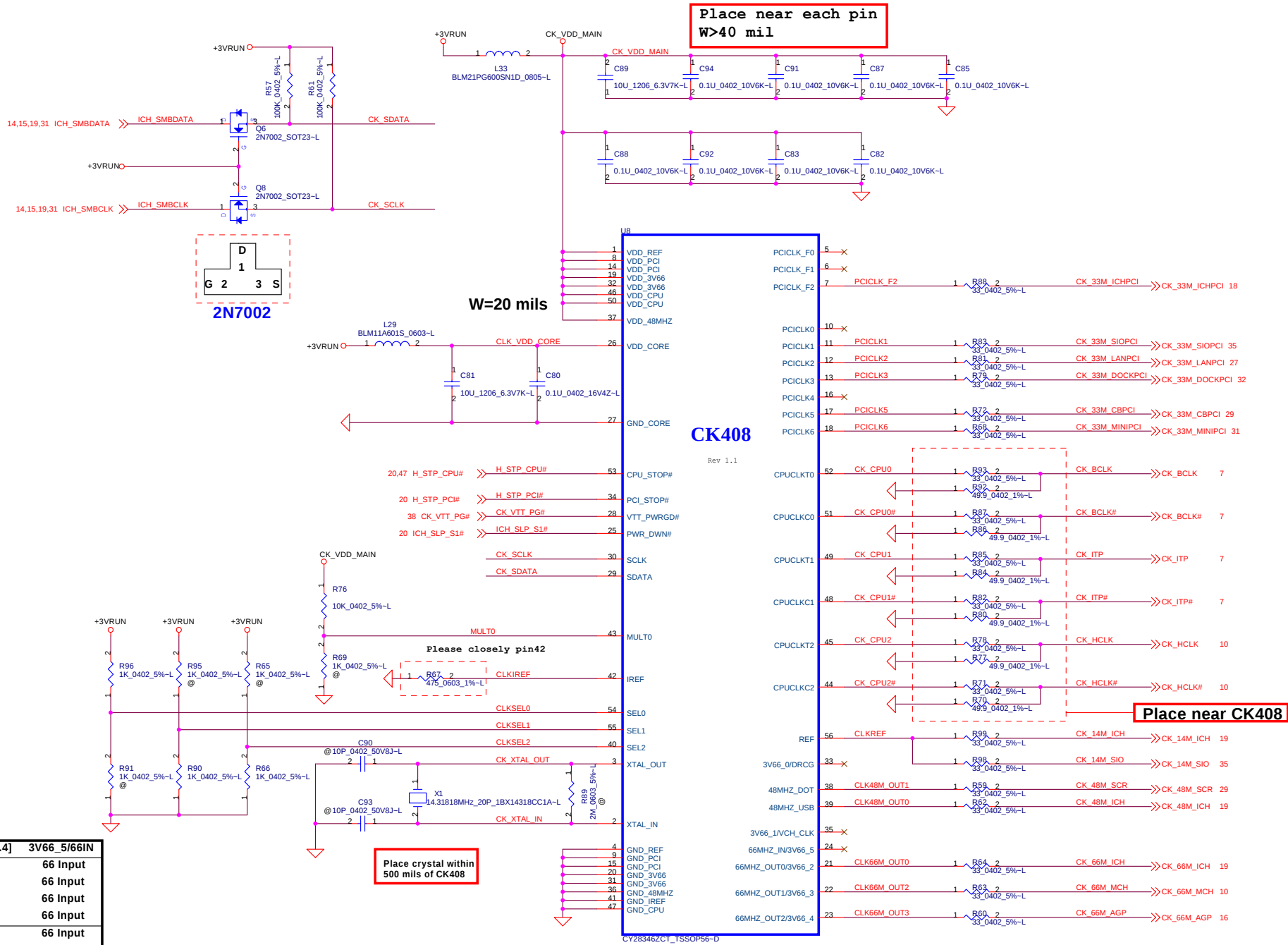
USB TABLE

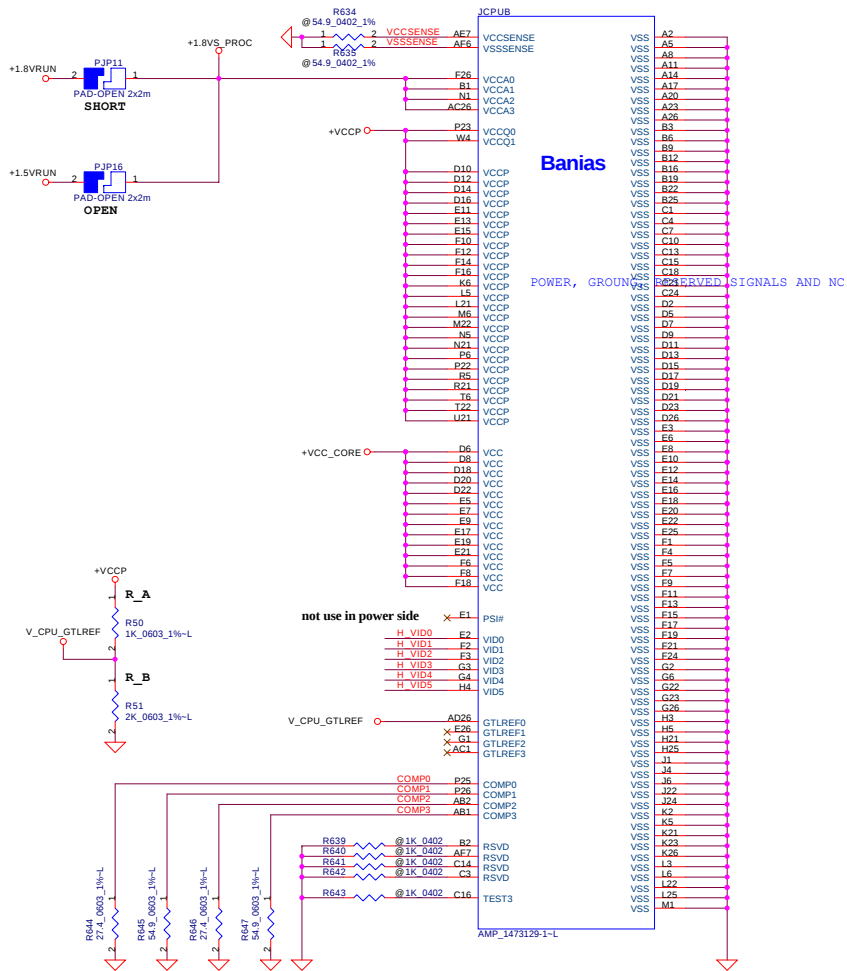
USB PORT#	DESTINATION
0	BACK
1	BLUETOOTH
2	BACK
3	DOG
4	MOD
5	DOCK





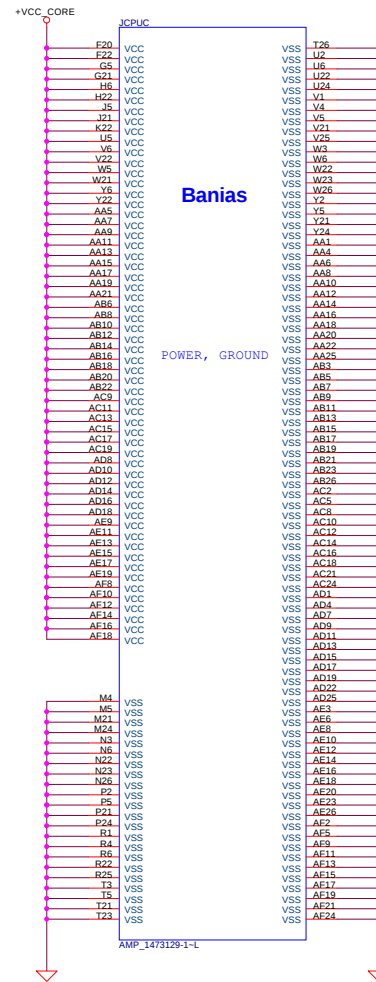
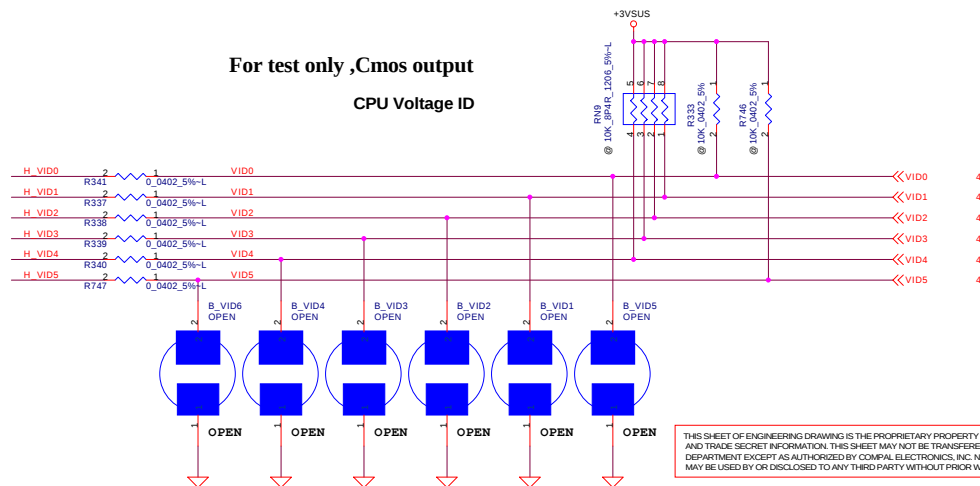
S2	S1	S0	CPU	3V66[0..4]	3V66_5/66IN
1	0	0	66	66IN	66 Input
1	0	1	100	66IN	66 Input
1	1	0	200	66IN	66 Input
1	1	1	133	66IN	66 Input
0	0	0	66	66	66 Input
0	0	1	100	66	66 Input
0	1	0	200	66	66 Input
0	1	1	133	66	66 Input
Mid	0	0	Hi-z	Hi-z	Hi-z
Mid	0	1	TCLK/2	TCLK/2	TCLK/2
Mid	1	0	Reserve	Reserve	Reserve
Mid	1	1	Reserve	Reserve	Reserve





Resistor placed within 0.5" of CPU pin. Trace should be at least 25 miles away from any other toggling signal.

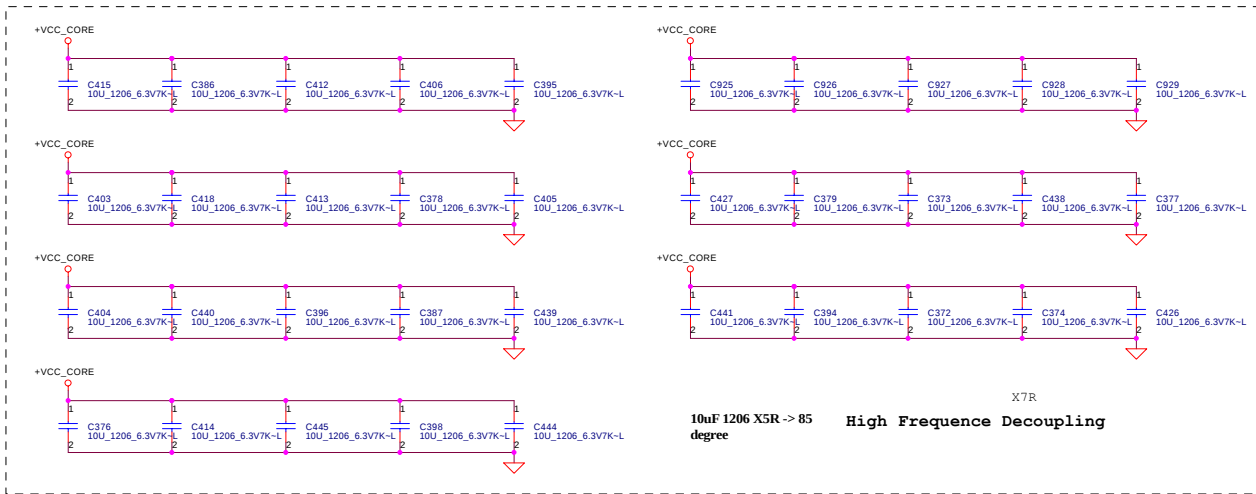
For test only ,Cmos output CPU Voltage ID



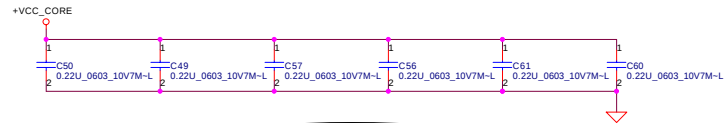
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Title		Banias Processor in mFCPGA479	
Size	C	Document Number	BDQ11/LA-1601
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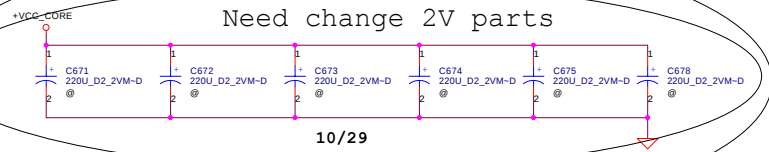
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X7R



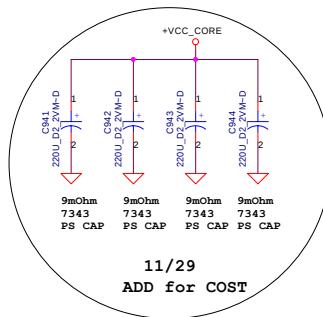
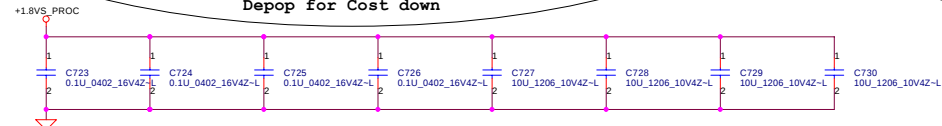
Need change 2V parts



10/29
CPU Bulk

11/29

Depop for Cost down



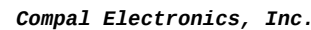
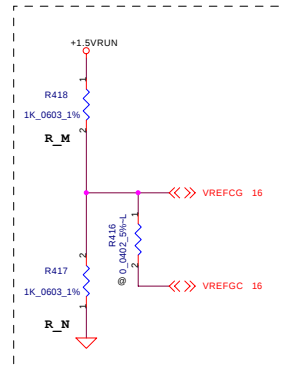
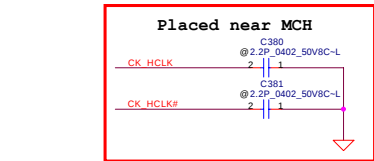
01/03
Item 91

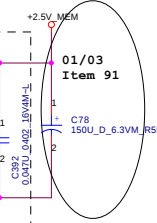
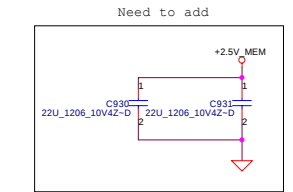
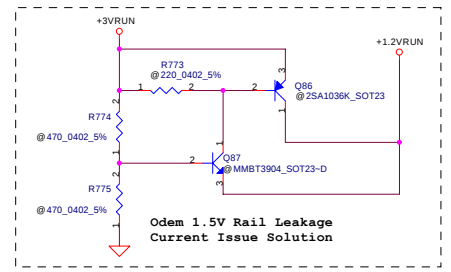
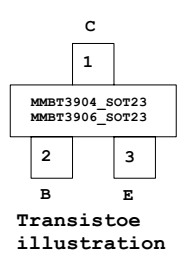
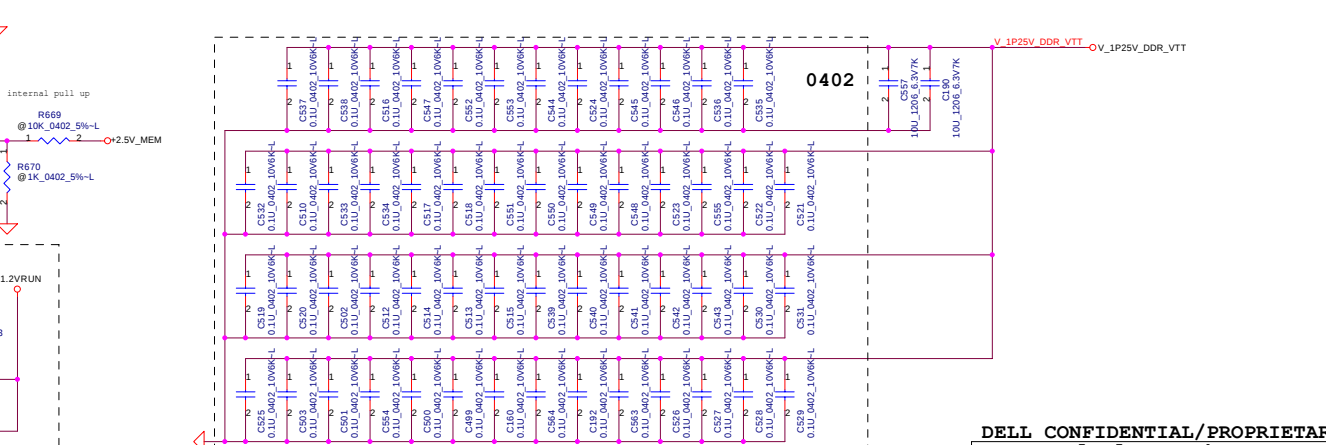
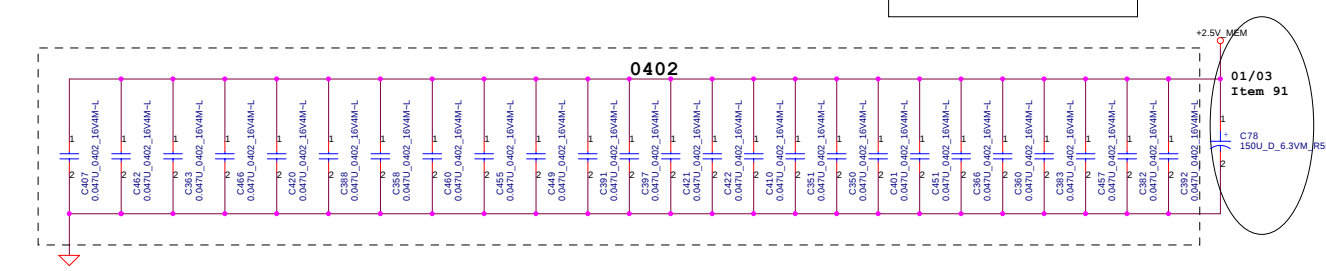
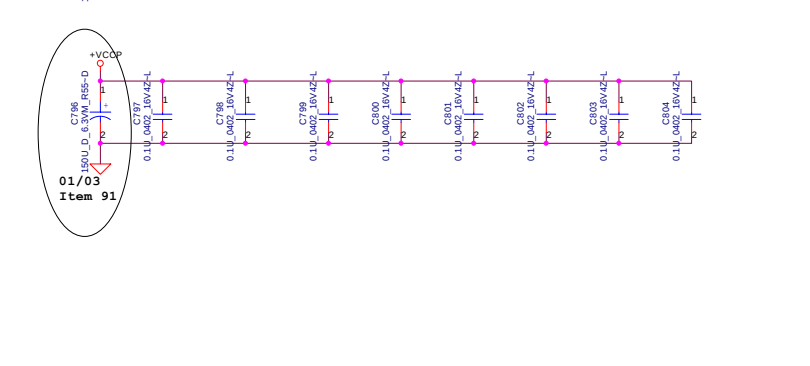
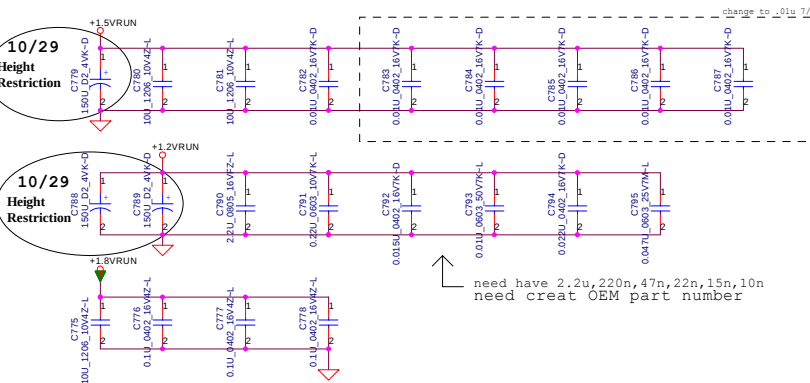
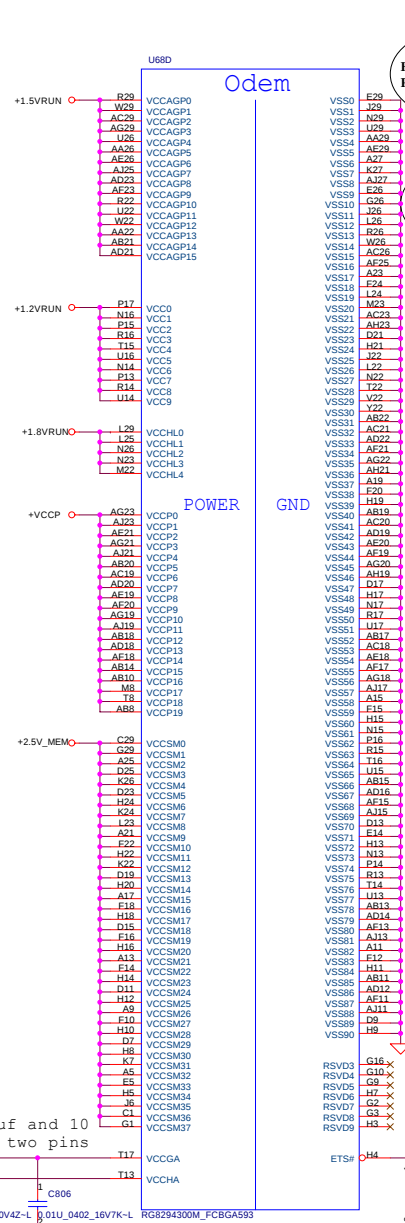


10/29
Height Restriction

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Title CPU Bypass		
Size C	Document Number BDQ11/LA-1601	Rev 0.5
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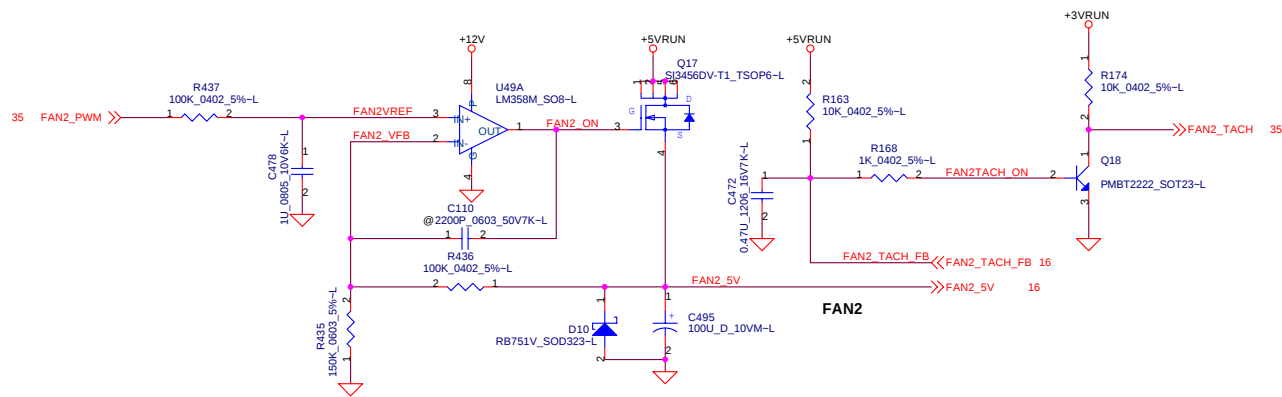
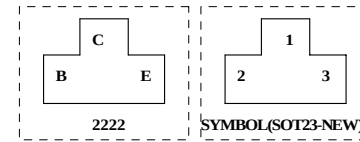
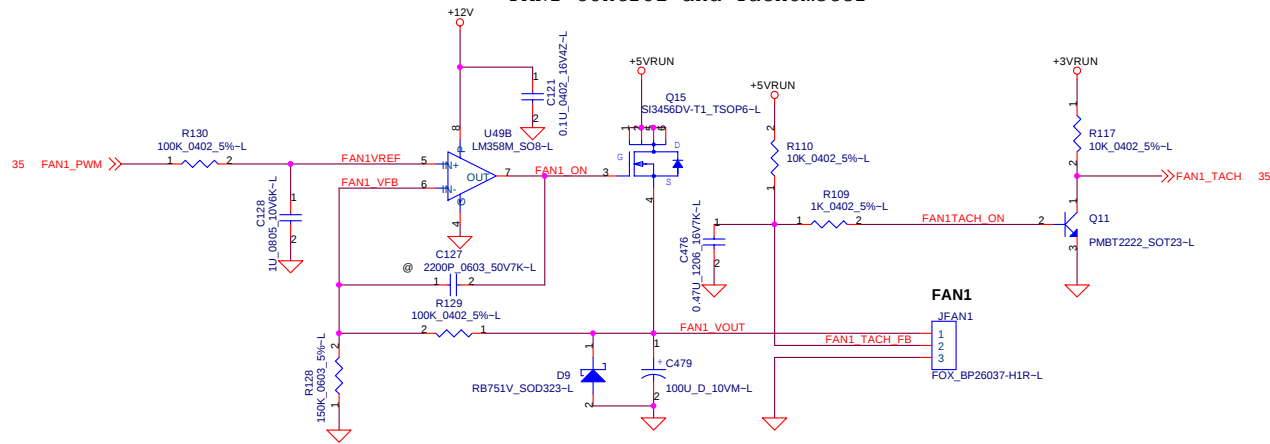


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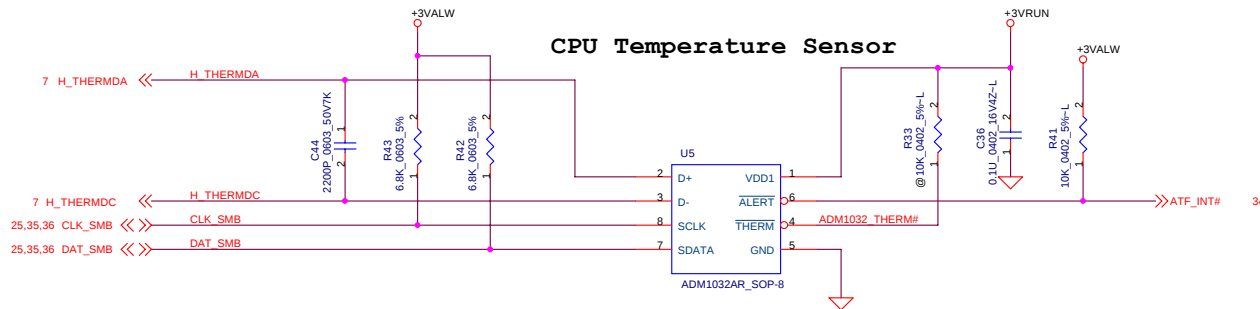
Title		Odem(3 of 3)	
Size	Document Number	BDQ11/LA-1601	
C			Rev 0.5
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FAN1 Control and Tachometer

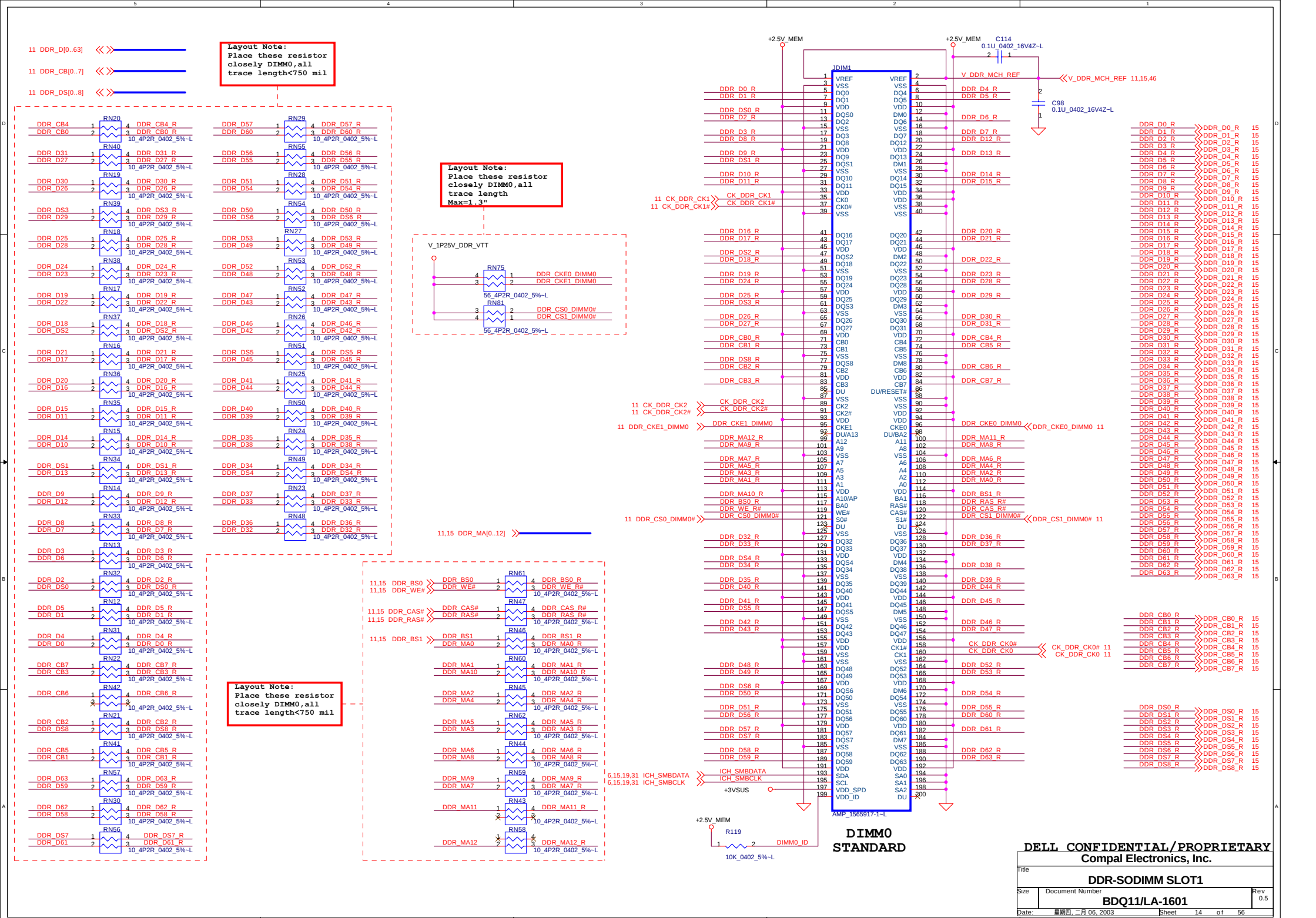


CPU Temperature Sensor



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Title		
ITP Debug CONN. & FAN		
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Layout Note:
Place these resistors
closely DIMM1,all
trace length<800 mil

Layout Note:
Place CAP
near DIMM1

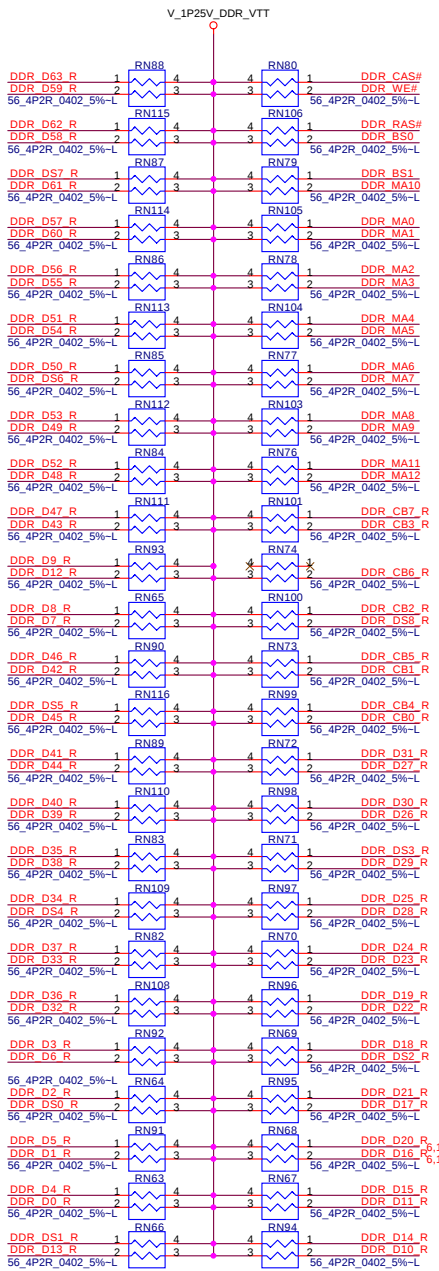
11.14 DDR_MA[0..12] >>

14 DDR_D0_R >> DDR_D0_R
14 DDR_D1_R >> DDR_D1_R
14 DDR_D2_R >> DDR_D2_R
14 DDR_D3_R >> DDR_D3_R
14 DDR_D4_R >> DDR_D4_R
14 DDR_D5_R >> DDR_D5_R
14 DDR_D6_R >> DDR_D6_R
14 DDR_D7_R >> DDR_D7_R
14 DDR_D8_R >> DDR_D8_R
14 DDR_D9_R >> DDR_D9_R
14 DDR_D10_R >> DDR_D10_R
14 DDR_D11_R >> DDR_D11_R
14 DDR_D12_R >> DDR_D12_R
14 DDR_D13_R >> DDR_D13_R
14 DDR_D14_R >> DDR_D14_R
14 DDR_D15_R >> DDR_D15_R
14 DDR_D16_R >> DDR_D16_R
14 DDR_D17_R >> DDR_D17_R
14 DDR_D18_R >> DDR_D18_R
14 DDR_D19_R >> DDR_D19_R
14 DDR_D20_R >> DDR_D20_R
14 DDR_D21_R >> DDR_D21_R
14 DDR_D22_R >> DDR_D22_R
14 DDR_D23_R >> DDR_D23_R
14 DDR_D24_R >> DDR_D24_R
14 DDR_D25_R >> DDR_D25_R
14 DDR_D26_R >> DDR_D26_R
14 DDR_D27_R >> DDR_D27_R
14 DDR_D28_R >> DDR_D28_R
14 DDR_D29_R >> DDR_D29_R
14 DDR_D30_R >> DDR_D30_R
14 DDR_D31_R >> DDR_D31_R
14 DDR_D32_R >> DDR_D32_R
14 DDR_D33_R >> DDR_D33_R
14 DDR_D34_R >> DDR_D34_R
14 DDR_D35_R >> DDR_D35_R
14 DDR_D36_R >> DDR_D36_R
14 DDR_D37_R >> DDR_D37_R
14 DDR_D38_R >> DDR_D38_R
14 DDR_D39_R >> DDR_D39_R
14 DDR_D40_R >> DDR_D40_R
14 DDR_D41_R >> DDR_D41_R
14 DDR_D42_R >> DDR_D42_R
14 DDR_D43_R >> DDR_D43_R
14 DDR_D44_R >> DDR_D44_R
14 DDR_D45_R >> DDR_D45_R
14 DDR_D46_R >> DDR_D46_R
14 DDR_D47_R >> DDR_D47_R
14 DDR_D48_R >> DDR_D48_R
14 DDR_D49_R >> DDR_D49_R
14 DDR_D50_R >> DDR_D50_R
14 DDR_D51_R >> DDR_D51_R
14 DDR_D52_R >> DDR_D52_R
14 DDR_D53_R >> DDR_D53_R
14 DDR_D54_R >> DDR_D54_R
14 DDR_D55_R >> DDR_D55_R
14 DDR_D56_R >> DDR_D56_R
14 DDR_D57_R >> DDR_D57_R
14 DDR_D58_R >> DDR_D58_R
14 DDR_D59_R >> DDR_D59_R
14 DDR_D60_R >> DDR_D60_R
14 DDR_D61_R >> DDR_D61_R
14 DDR_D62_R >> DDR_D62_R
14 DDR_D63_R >> DDR_D63_R

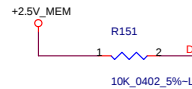
14 DDR_C80_R >> DDR_C80_R
14 DDR_C81_R >> DDR_C81_R
14 DDR_C82_R >> DDR_C82_R
14 DDR_C83_R >> DDR_C83_R
14 DDR_C84_R >> DDR_C84_R
14 DDR_C85_R >> DDR_C85_R
14 DDR_C86_R >> DDR_C86_R
14 DDR_C87_R >> DDR_C87_R

14 DDR_D50_R >> DDR_D50_R
14 DDR_D51_R >> DDR_D51_R
14 DDR_D52_R >> DDR_D52_R
14 DDR_D53_R >> DDR_D53_R
14 DDR_D54_R >> DDR_D54_R
14 DDR_D55_R >> DDR_D55_R
14 DDR_D56_R >> DDR_D56_R
14 DDR_D57_R >> DDR_D57_R
14 DDR_D58_R >> DDR_D58_R

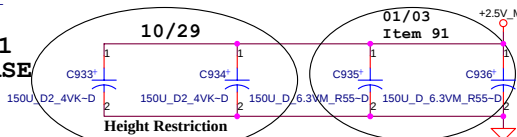
11.14 DDR_BS0 >> DDR_BS0
11.14 DDR_BS1 >> DDR_BS1
11.14 DDR_WE# >> DDR_WE#
11.14 DDR_CAS# >> DDR_CAS#
11.14 DDR_RAS# >> DDR_RAS#



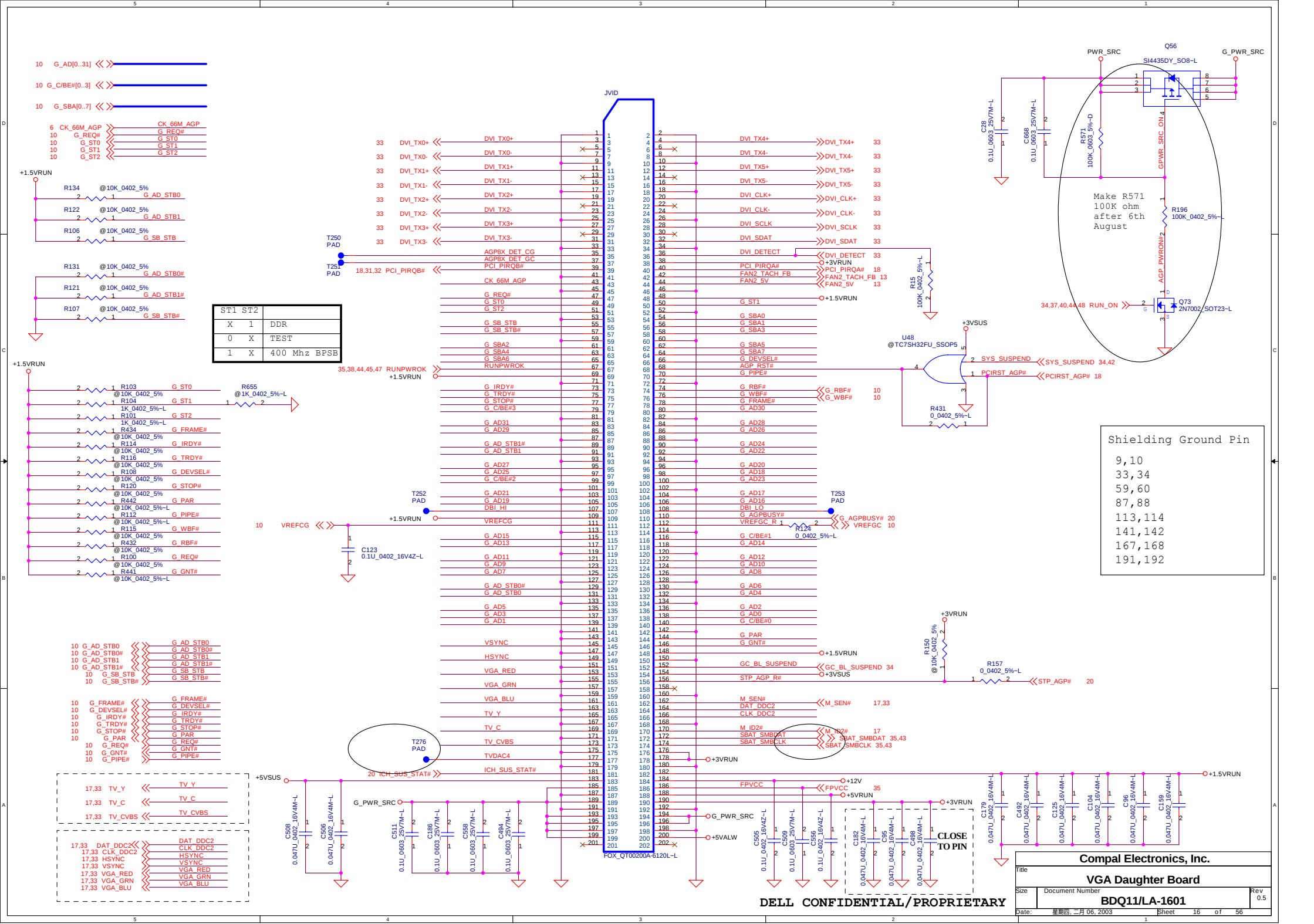
6.14,19.31 ICH_SMBDATA >>
6.14,19.31 ICH_SMBCLK >>

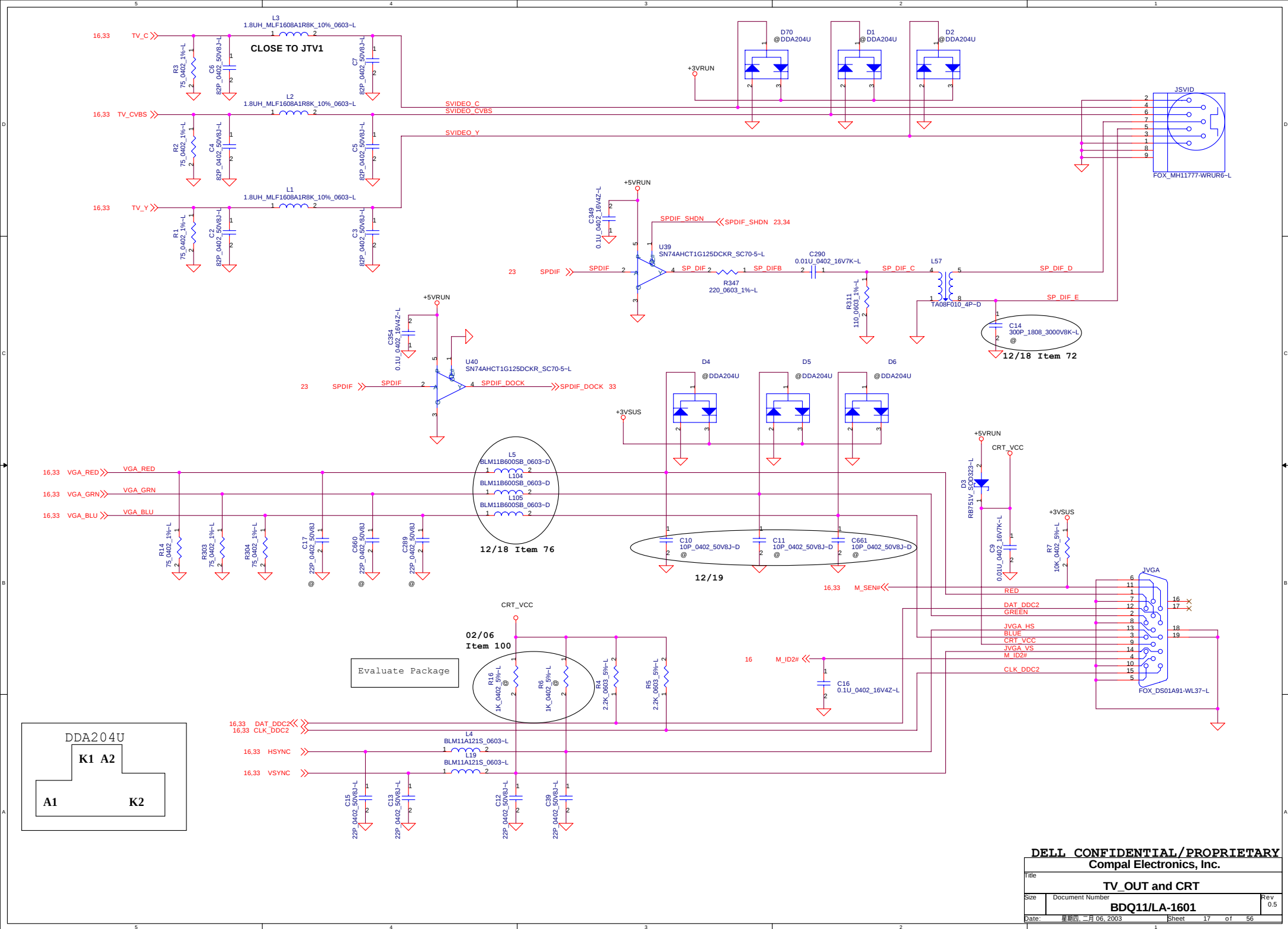


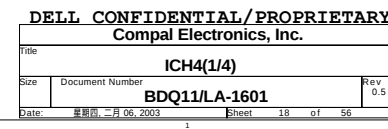
DIMM1
REVERSE

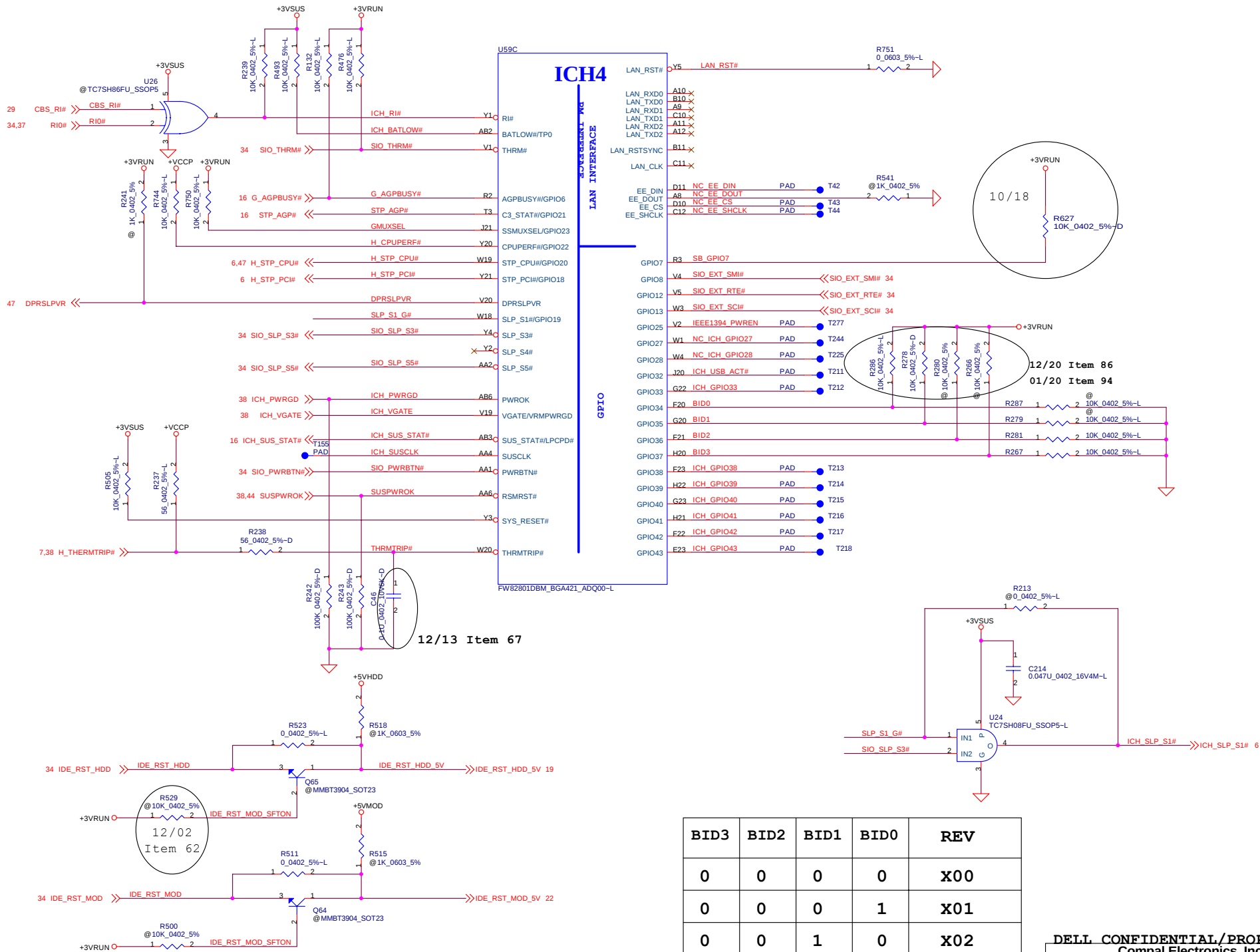


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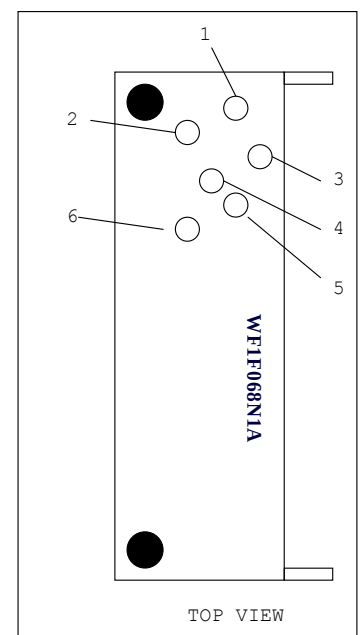
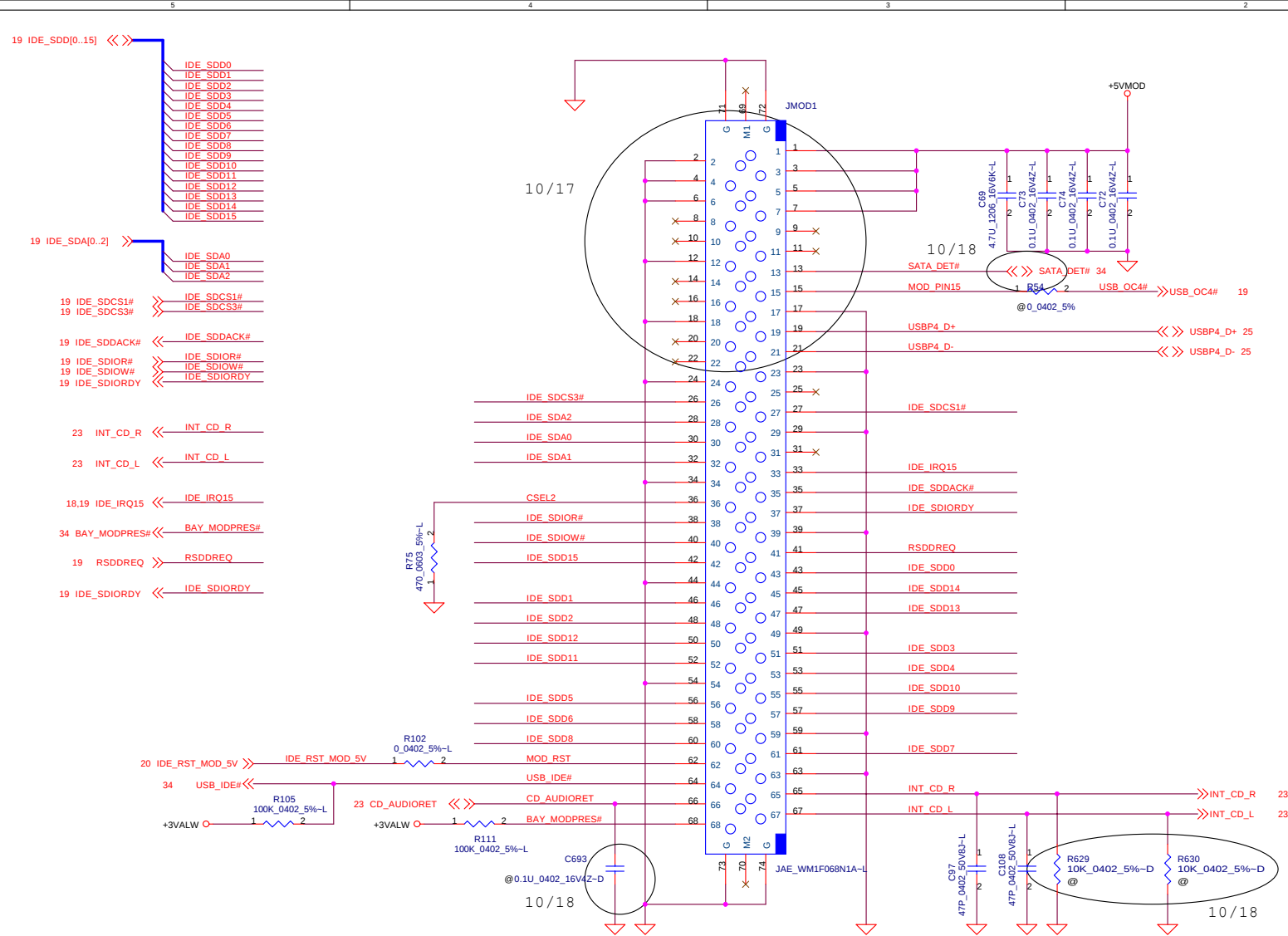


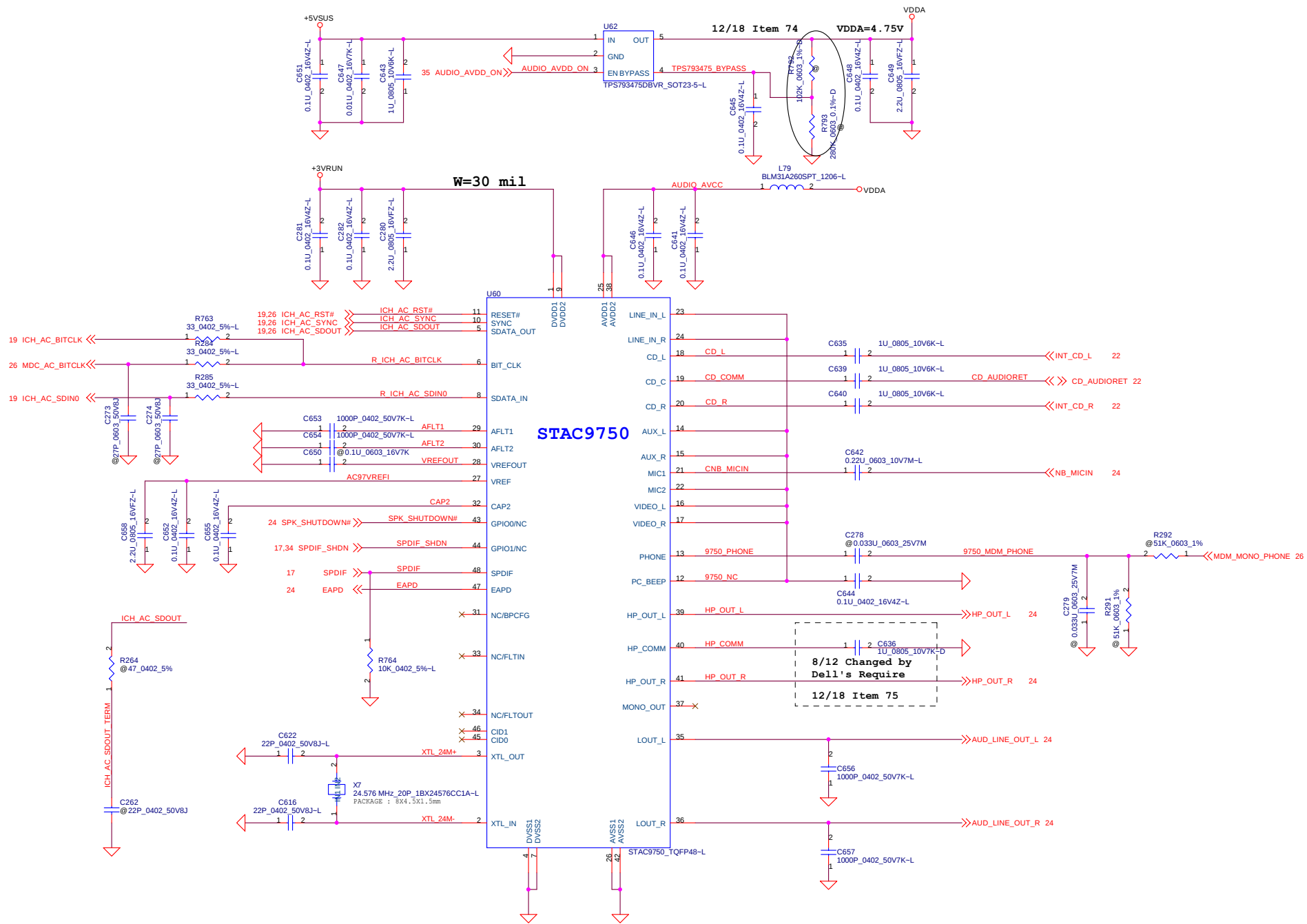


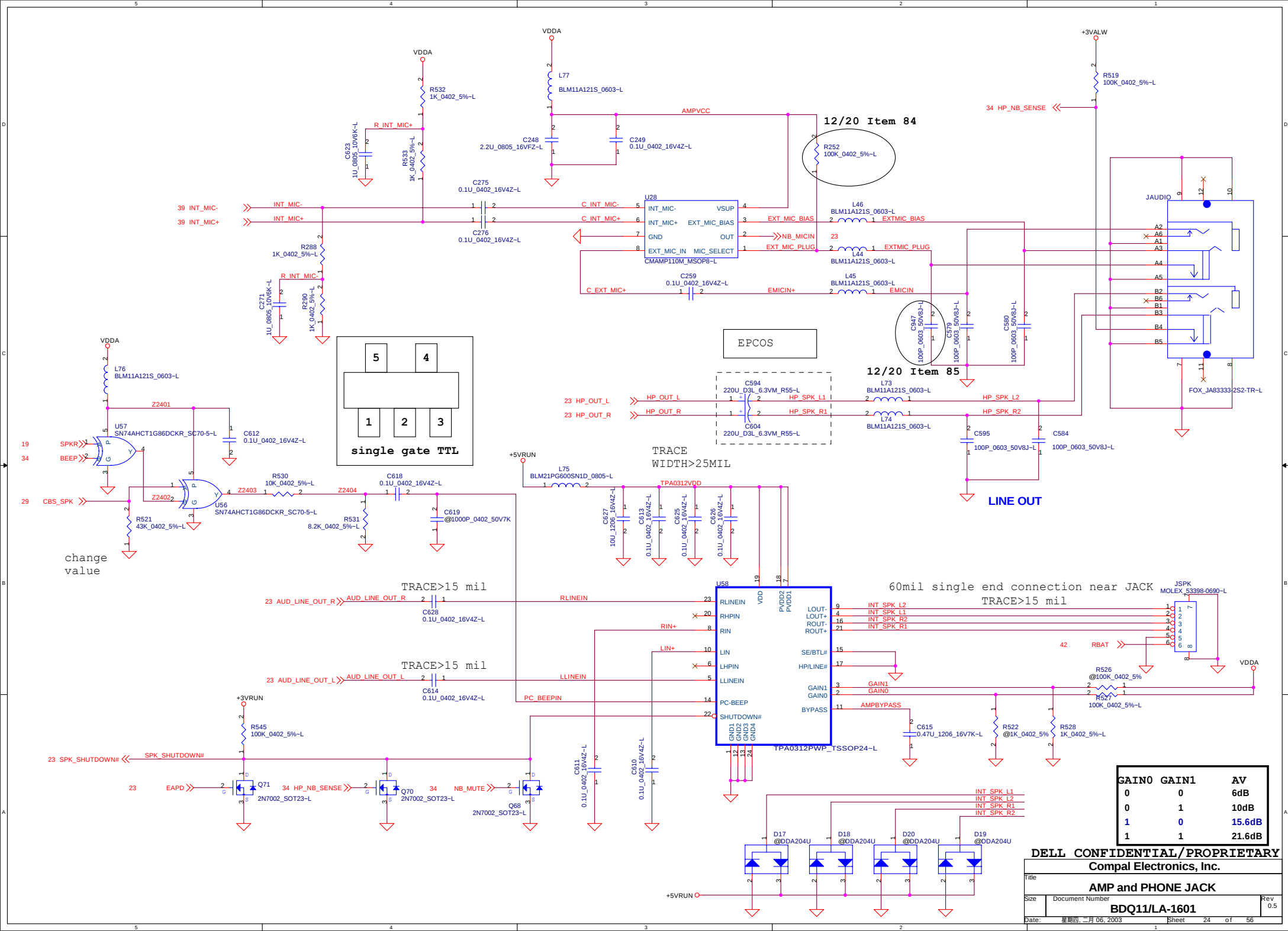




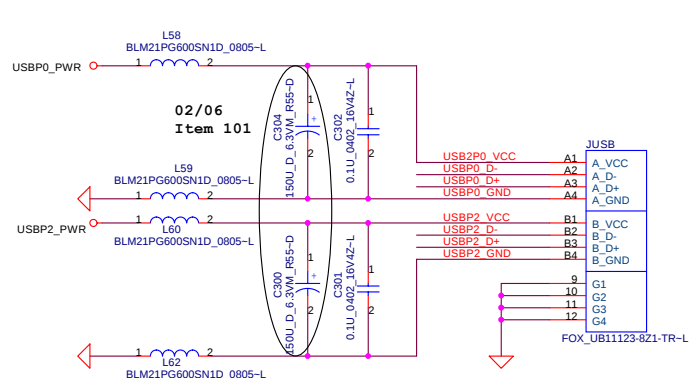
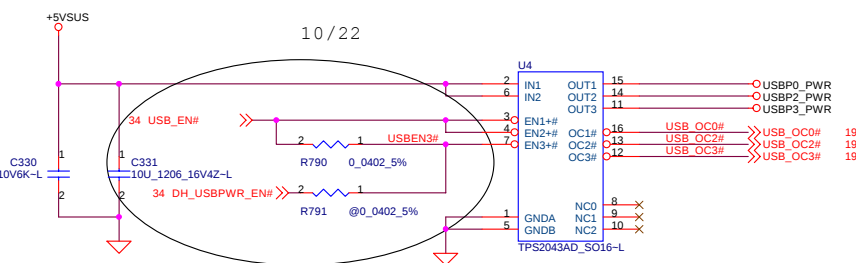
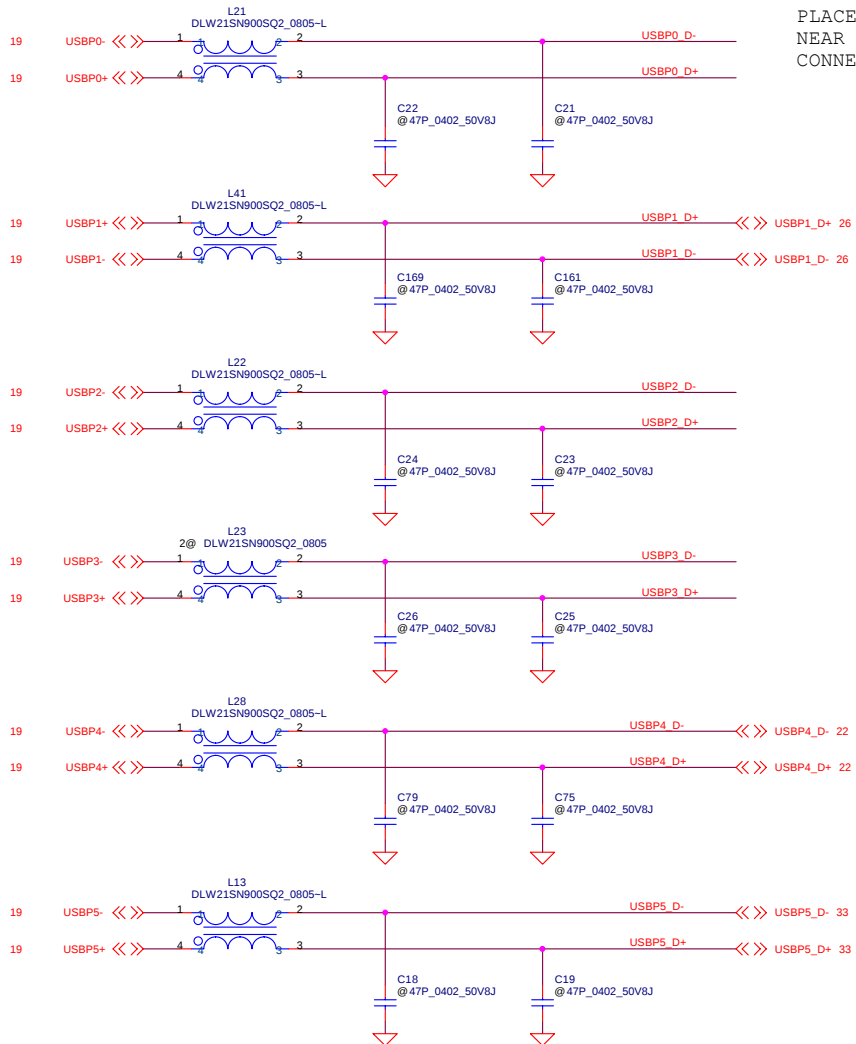
BID3	BID2	BID1	BID0	REV
0	0	0	0	X00
0	0	0	1	X01
0	0	1	0	X02
0	0	1	1	X03



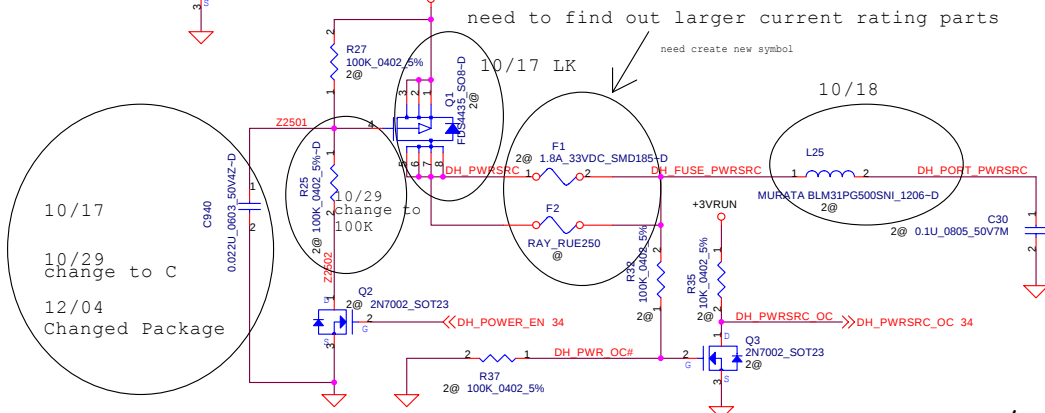
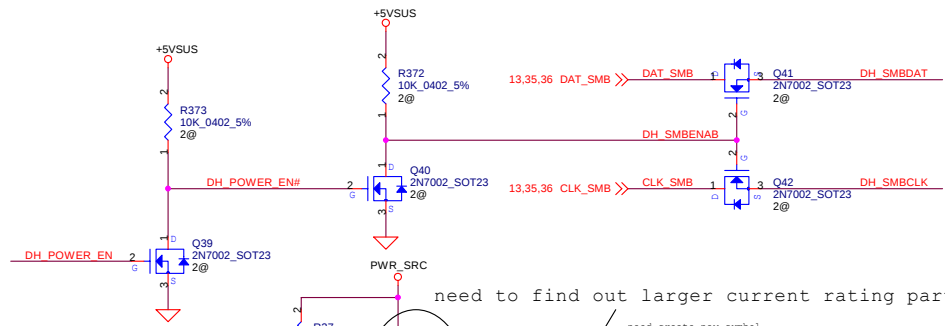
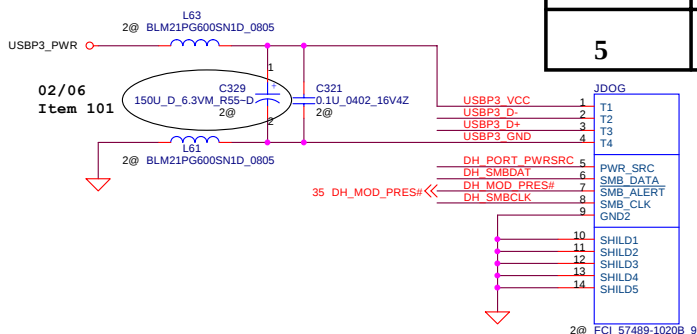


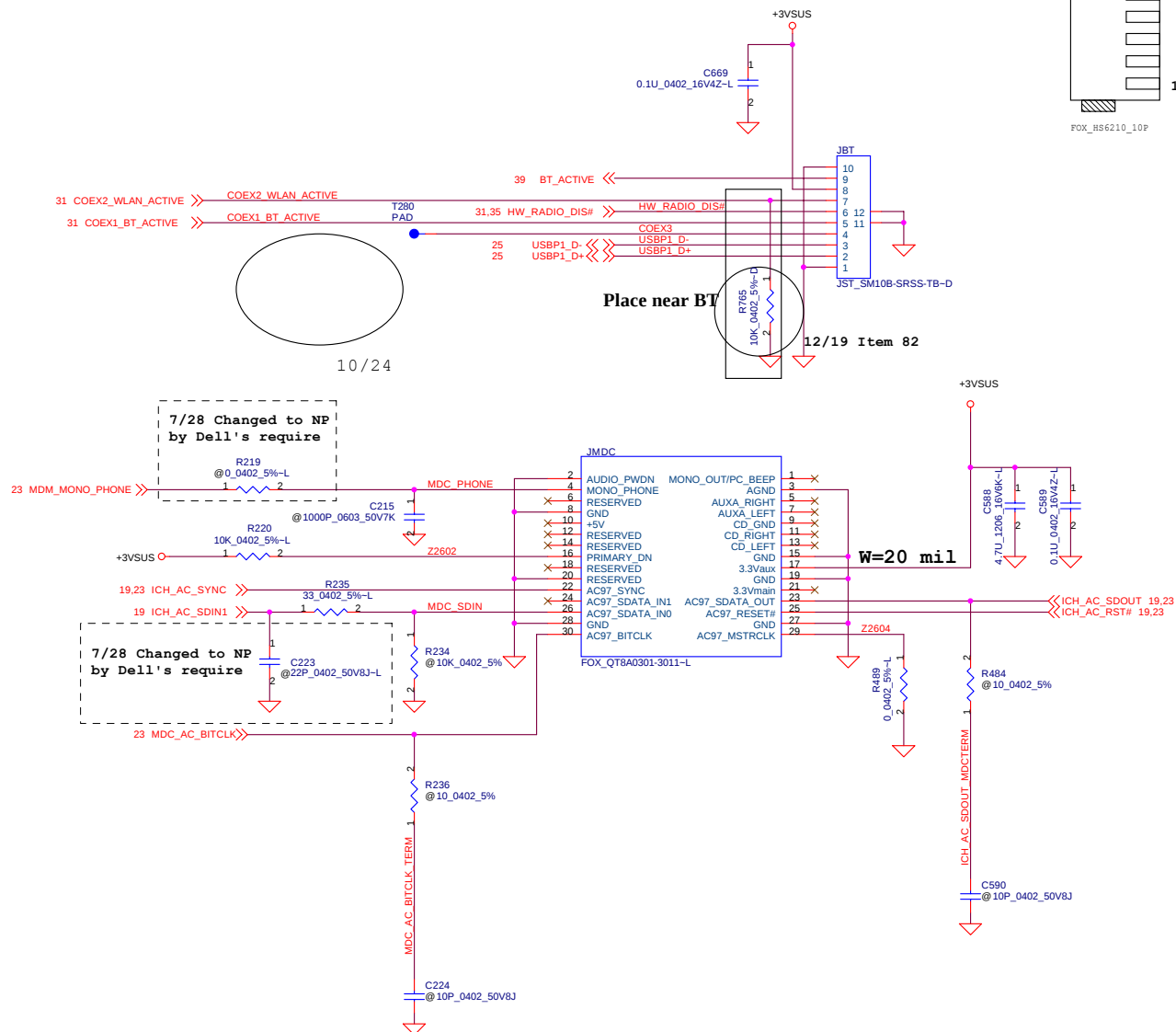


PLACE CHOKE
NEAR
CONNECTOR

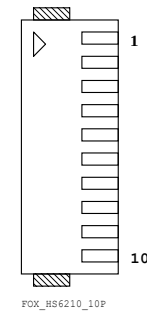


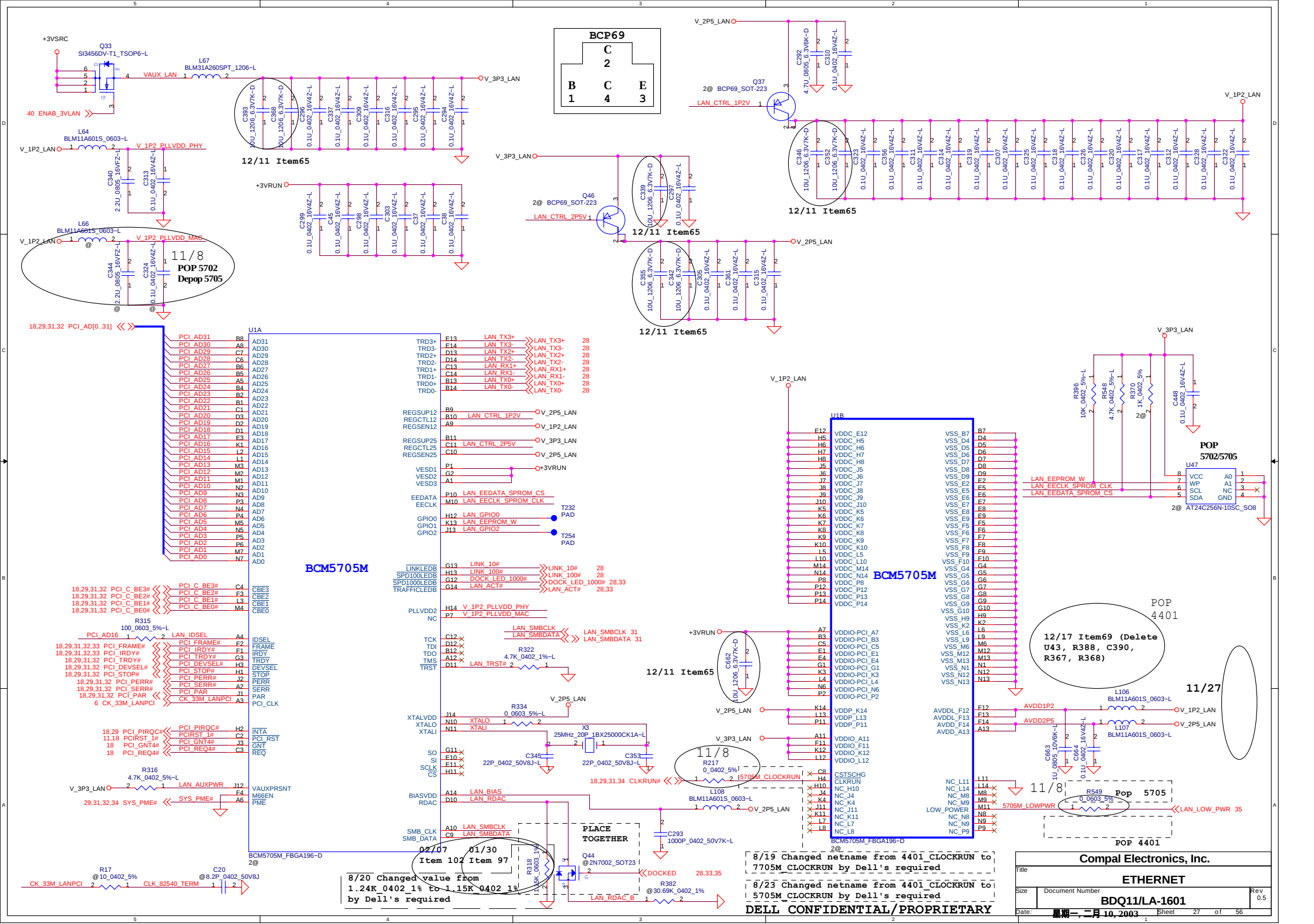
USB PORT#	DESTINATION
0	BACK
1	BT
2	BACK
3	DOG
4	MOD
5	DOCK

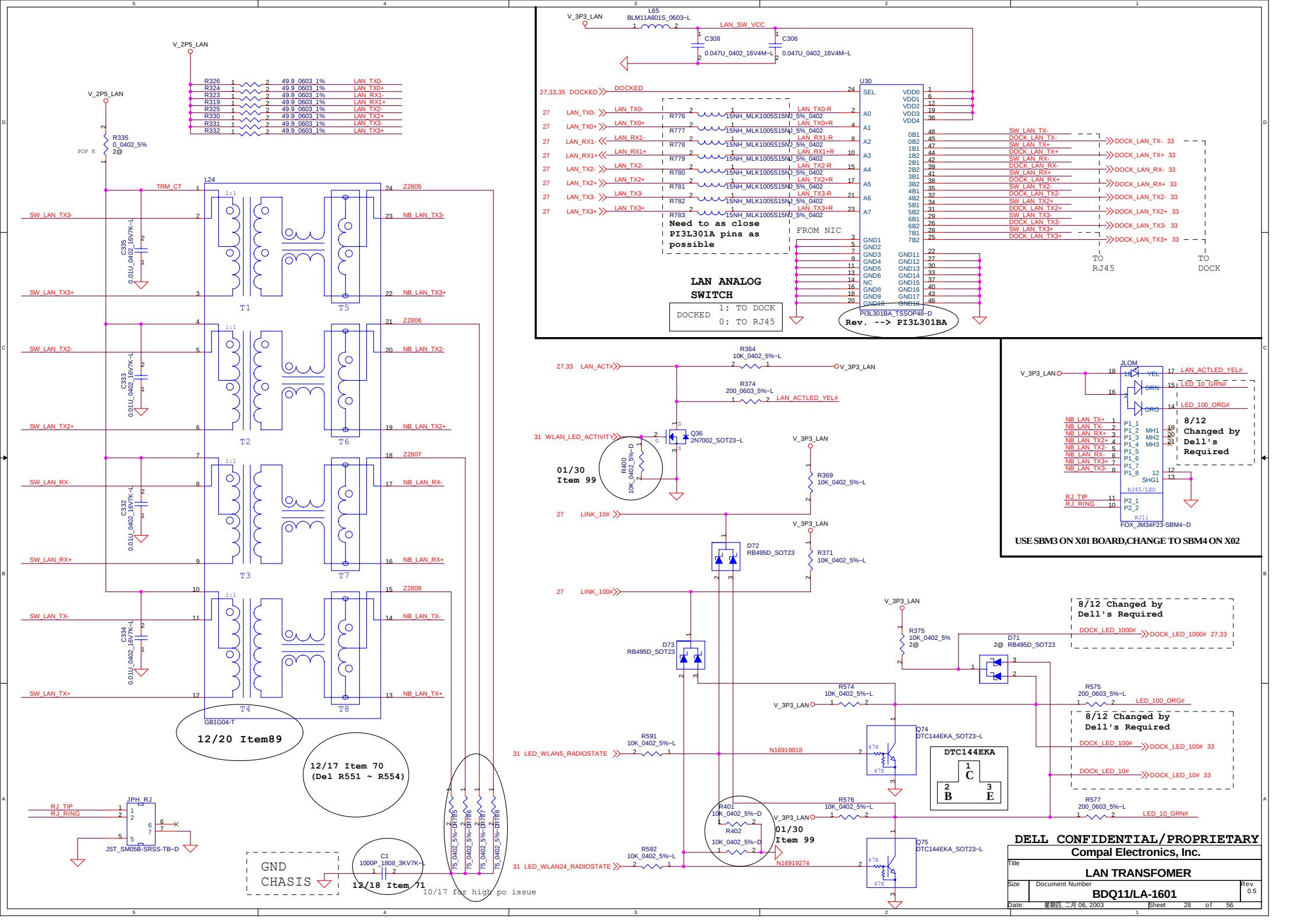


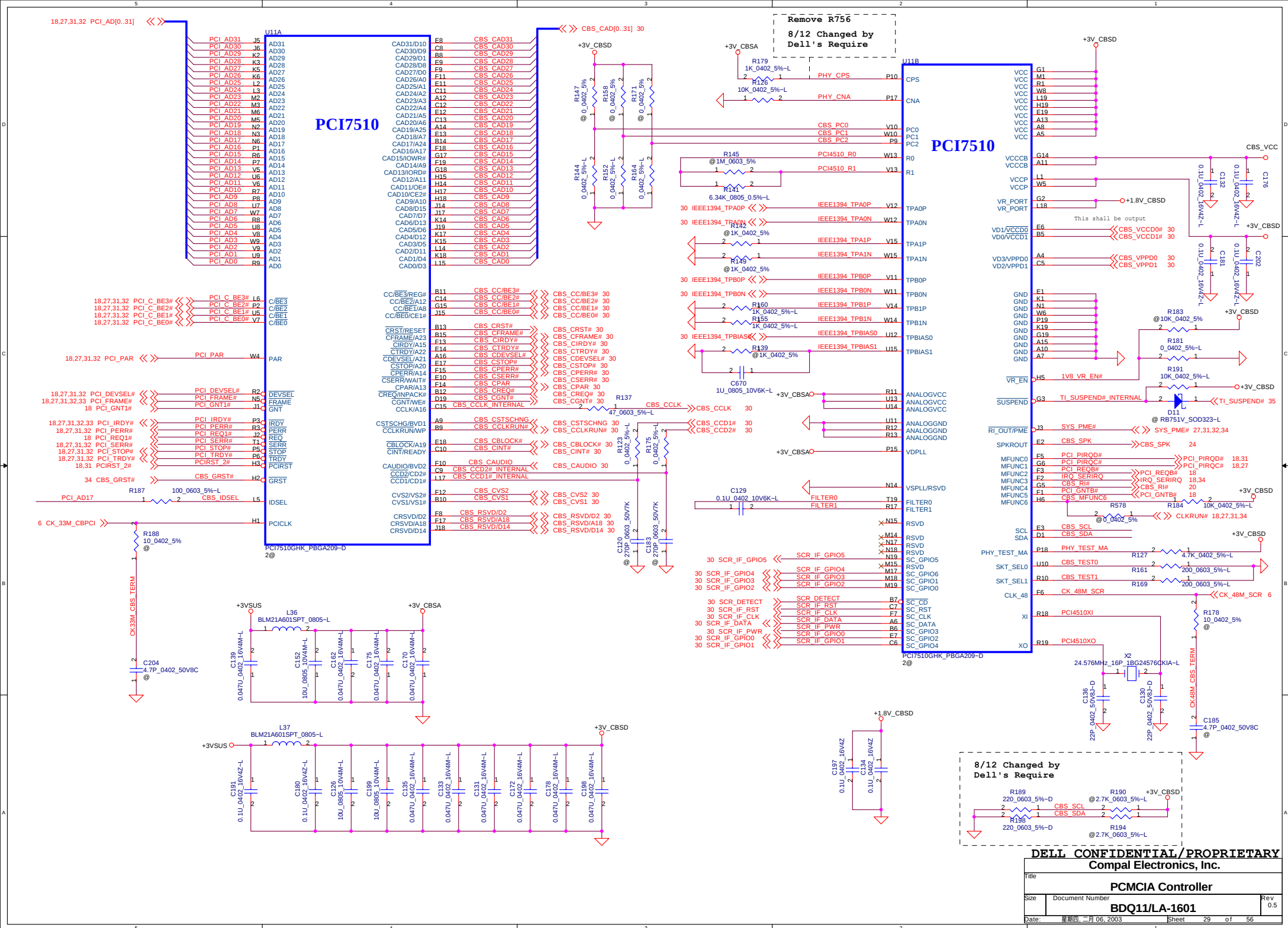


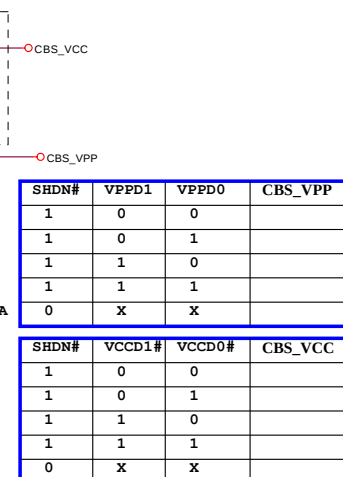
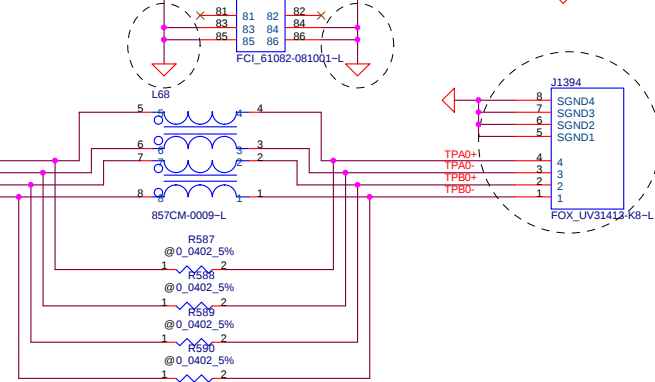
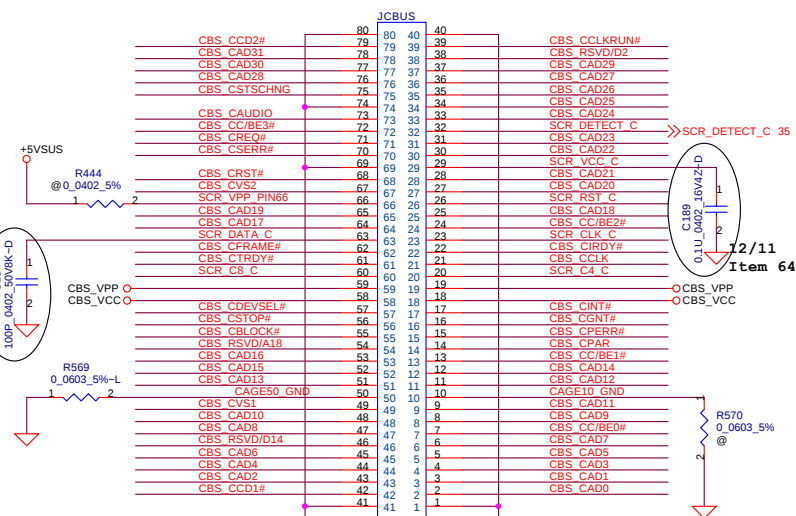
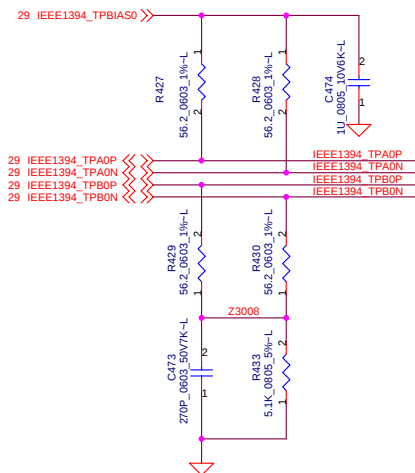
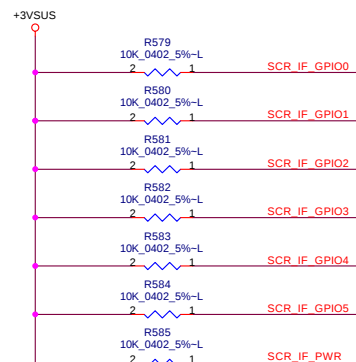
TOP view











CBS_CCLK	>>>	CBS_CCLK_29
CBS_CCBE#3	>>>	CBS_CCBE#3_29
CBS_CCBE#2	>>>	CBS_CCBE#2_29
CBS_CCBE#1	>>>	CBS_CCBE#1_29
CBS_CCBE#0	>>>	CBS_CCBE#0_29
CBS_CRST#	>>>	CBS_CRST#_29
CBS_CFRAME#	>>>	CBS_CFRAME#_29
CBS_CIRDY#	>>>	CBS_CIRDY#_29
CBS_CTRDY#	>>>	CBS_CTRDY#_29
CBS_CDEVELSEL#	>>>	CBS_CDEVELSEL#_29
CBS_CSTOP#	>>>	CBS_CSTOP#_29
CBS_CPERRR#	>>>	CBS_CPERRR#_29
CBS_CSERRR#	>>>	CBS_CSERRR#_29
CBS_CPAR	>>>	CBS_CPAR_29
CBS_CREQ#	>>>	CBS_CREQ#_29
CBS_CGNT#	>>>	CBS_CGNT#_29
CBS_CSTSCHNG	>>>	CBS_CSTSCHNG_29
CBS_CCLKRUN#	>>>	CBS_CCLKRUN#_29
CBS_CBLOCK#	>>>	CBS_CBLOCK#_29
CBS_CINT#	>>>	CBS_CINT#_29
CBS_CAUDIO	>>>	CBS_CAUDIO_29
CBS_CVS2	>>>	CBS_CVS2_29
CBS_CVS1	>>>	CBS_CVS1_29
CBS_RSVD/D14	>>>	CBS_RSVD/D14_29
CBS_RSVD/D12	>>>	CBS_RSVD/D12_29
CBS_RSVD/A18	>>>	CBS_RSVD/A18_29

CBS_CAD[0..31] 29

CBS CAD31

CBS CAD30

CBS CAD29

CBS CAD28

CBS CAD27

CBS CAD26

CBS CAD25

CBS CAD24

CBS CAD23

CBS CAD22

CBS CAD21

CBS CAD20

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CBS CAD11

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CBS CAD9

CBS CAD8

CBS CAD7

CBS CAD6

CBS CAD5

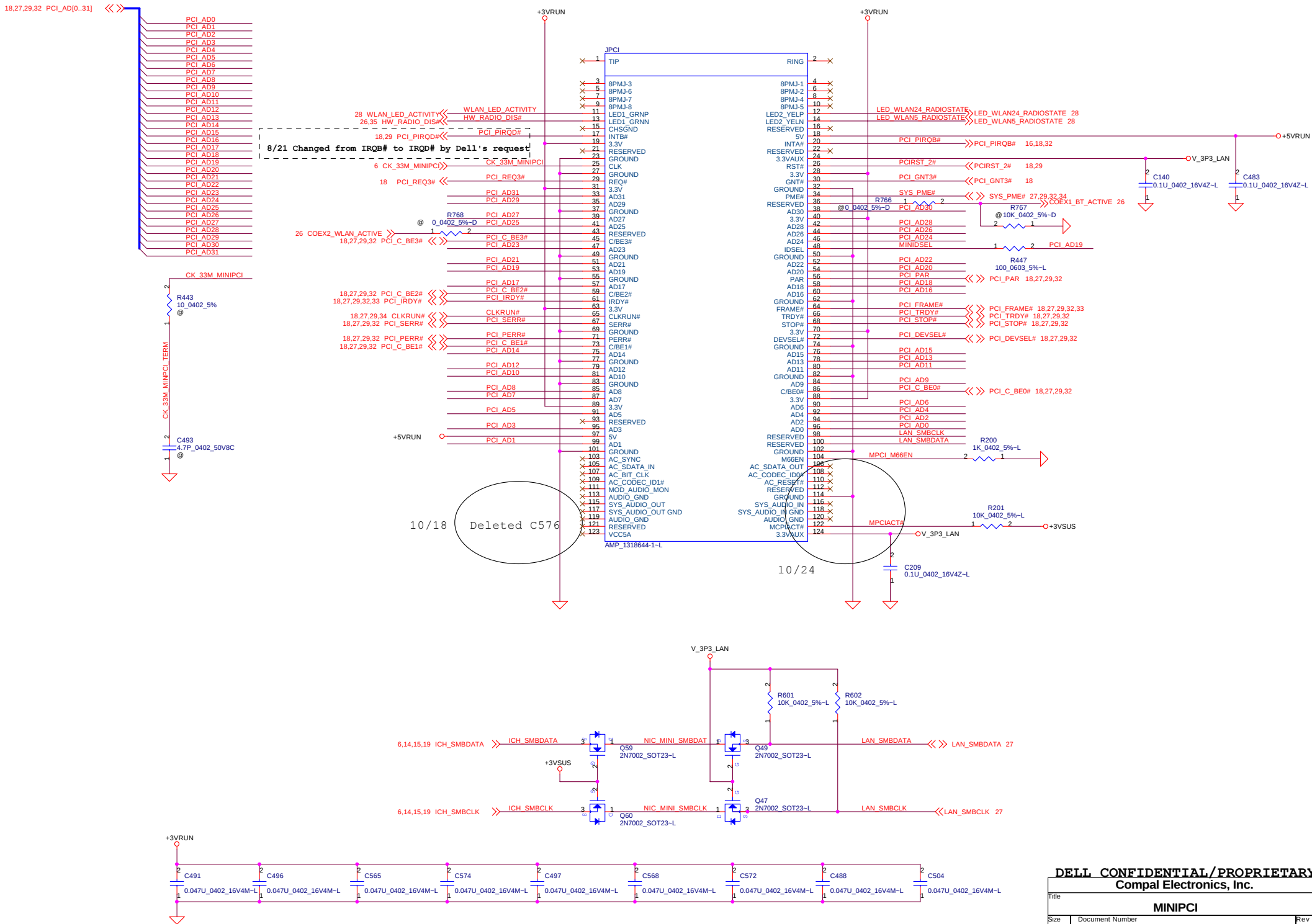
CBS CAD4

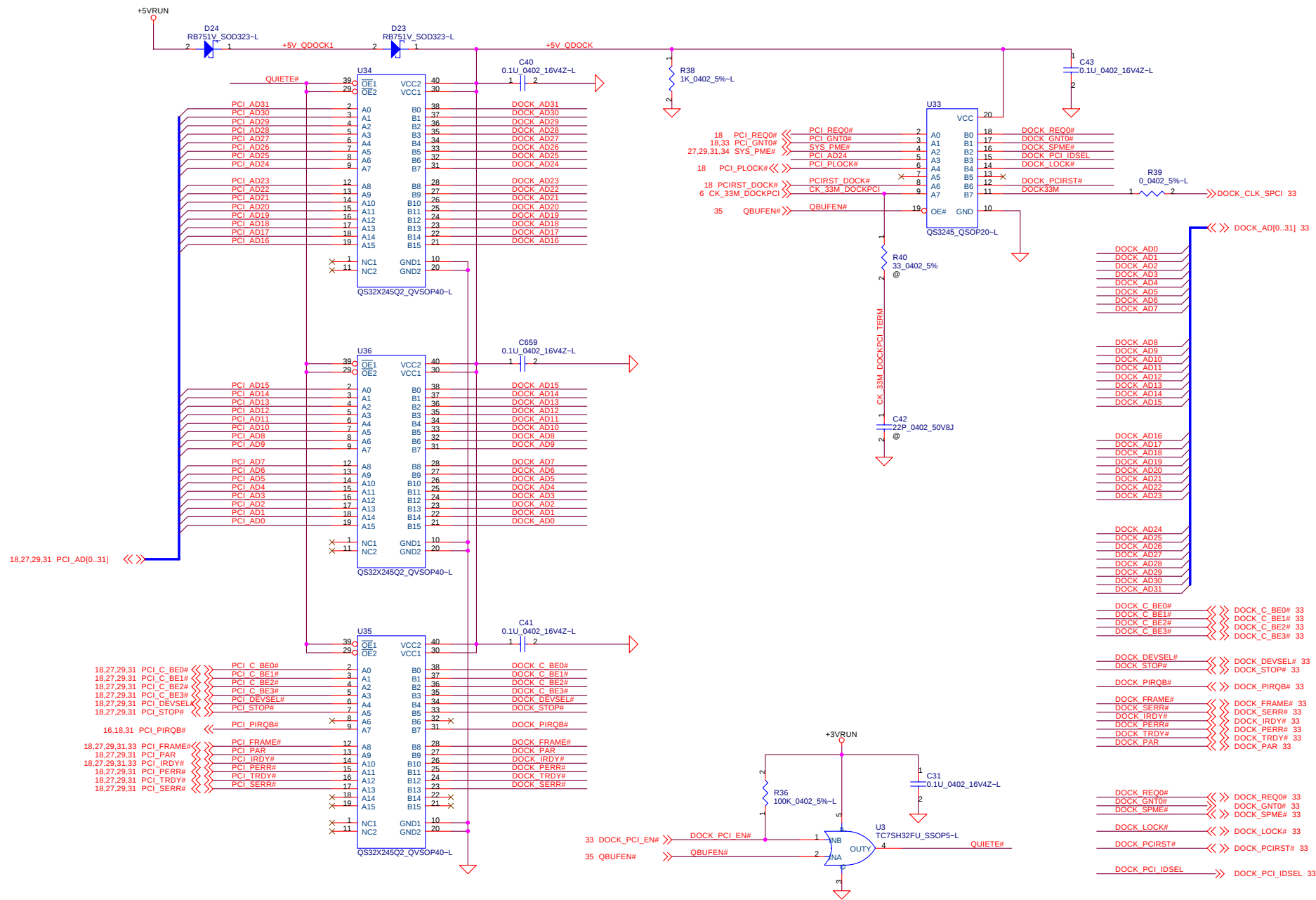
CBS CAD3

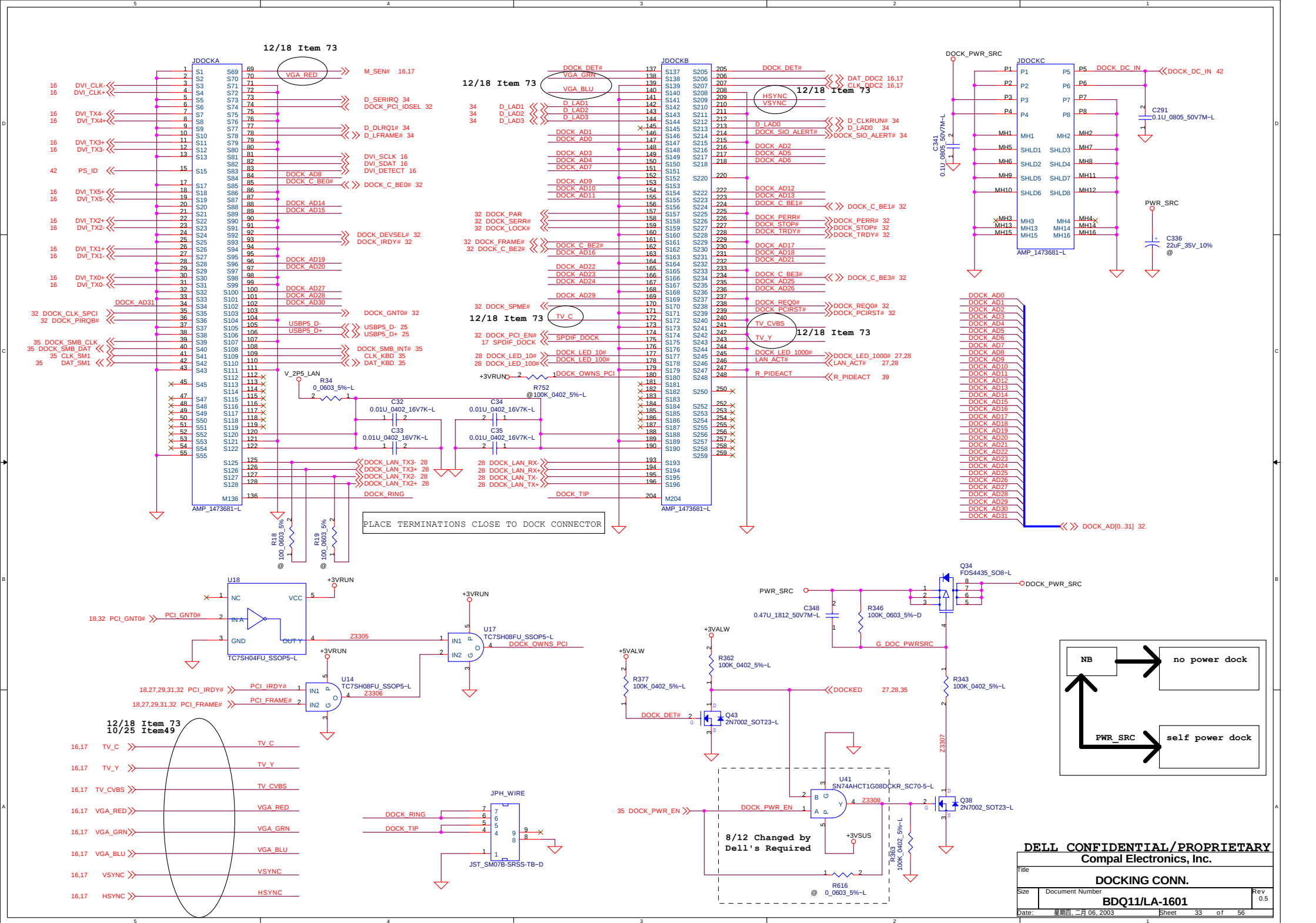
CBS CAD2

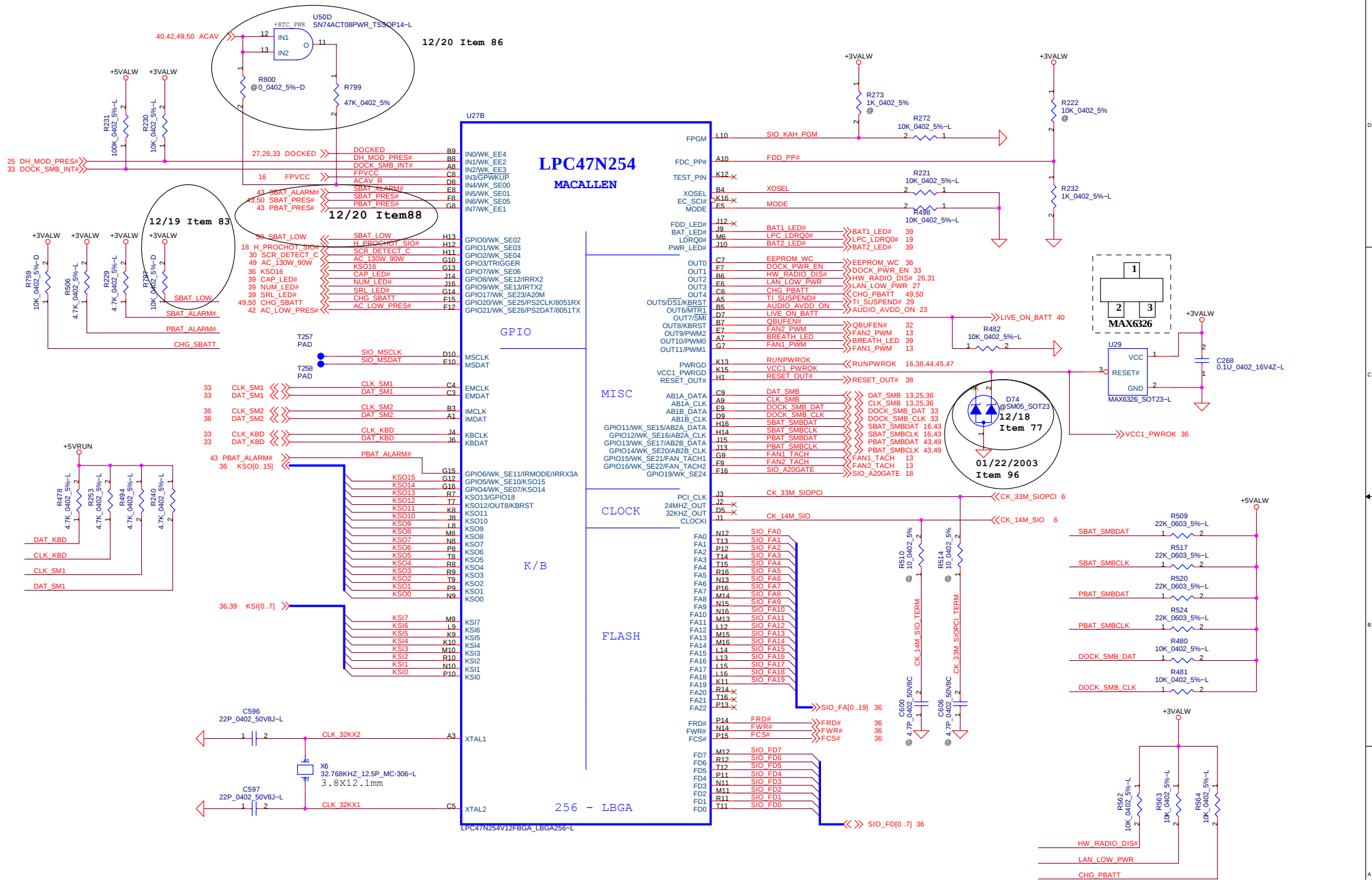
CBS CAD1

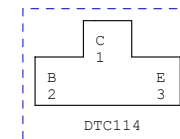
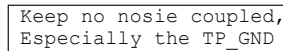
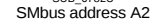
CBS CAD0



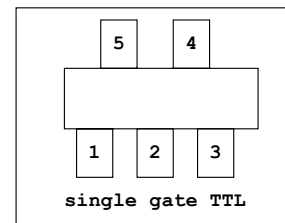
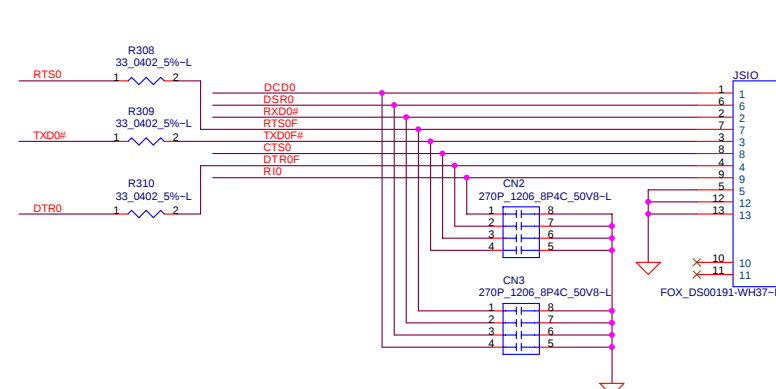
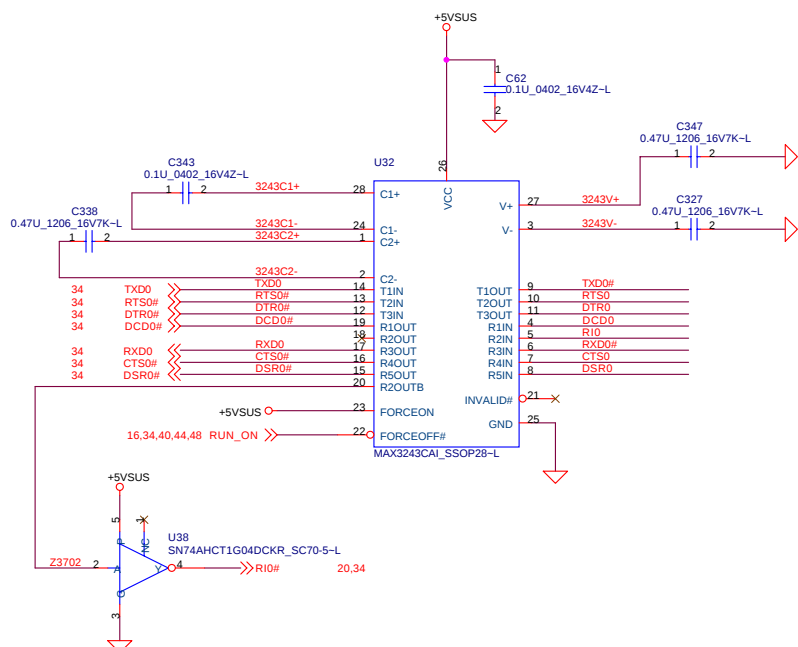
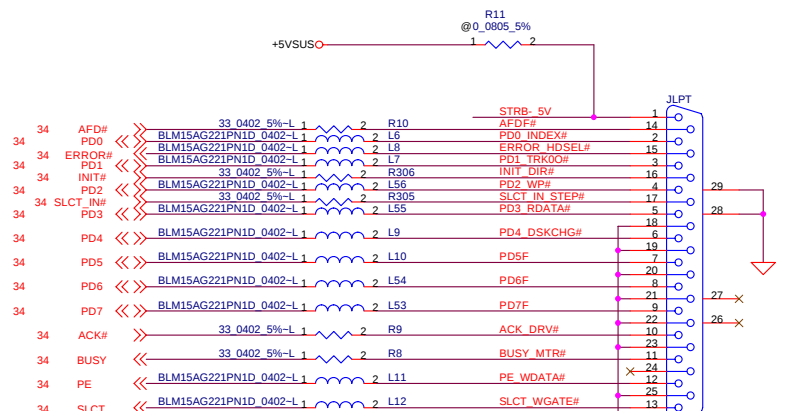
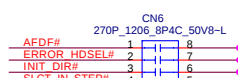
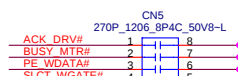
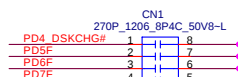
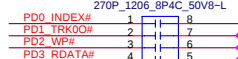
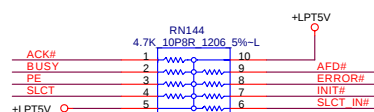
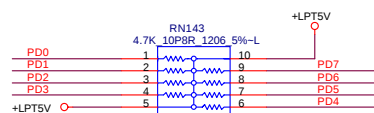
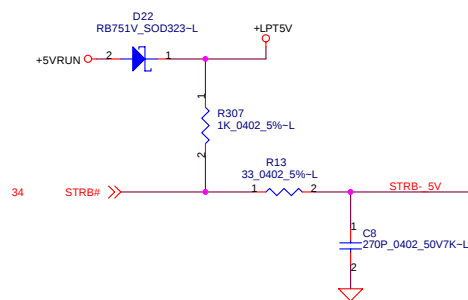


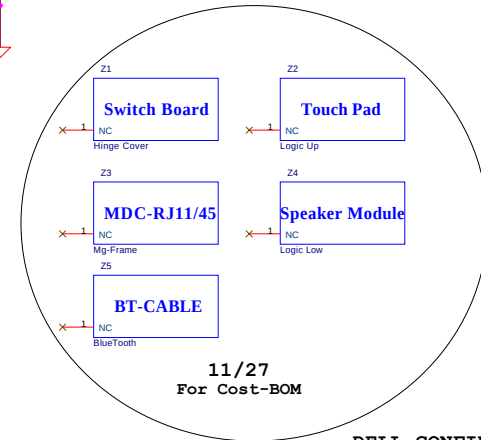
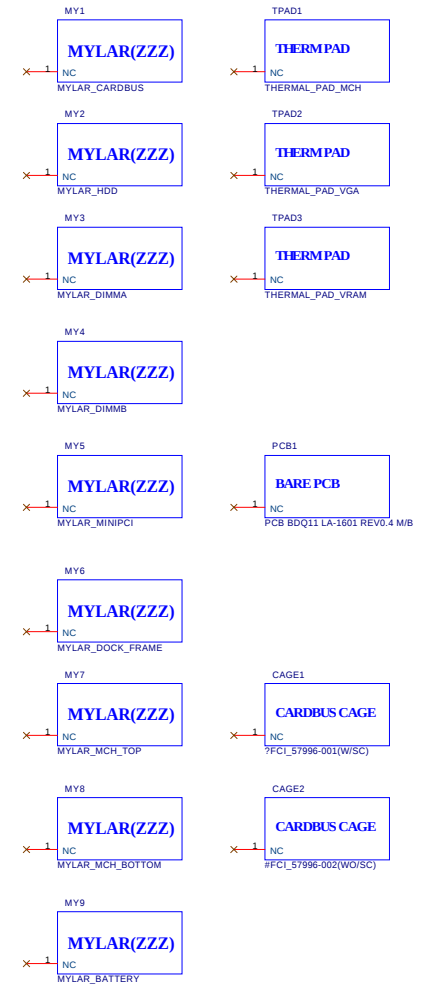
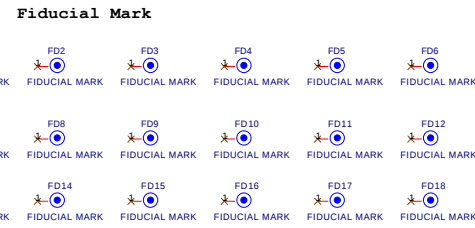
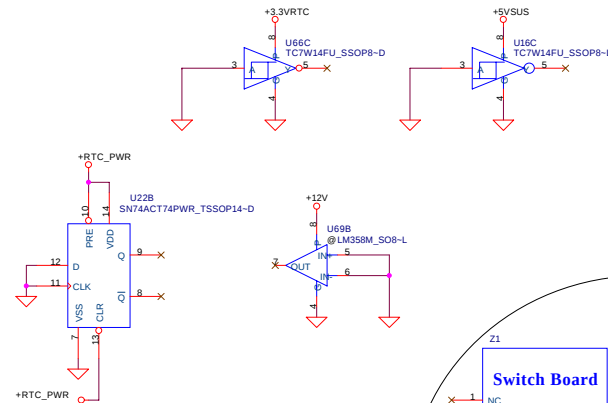
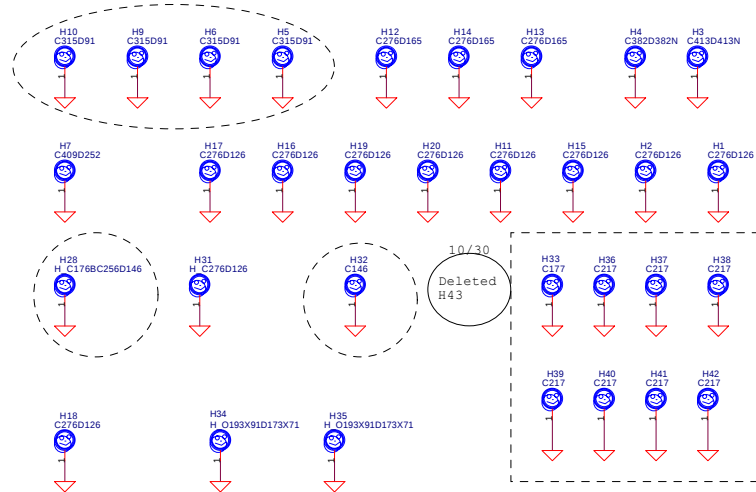
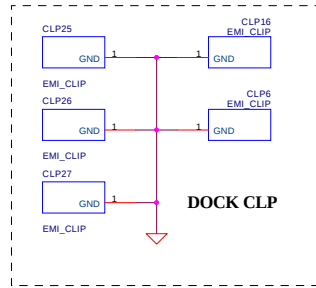
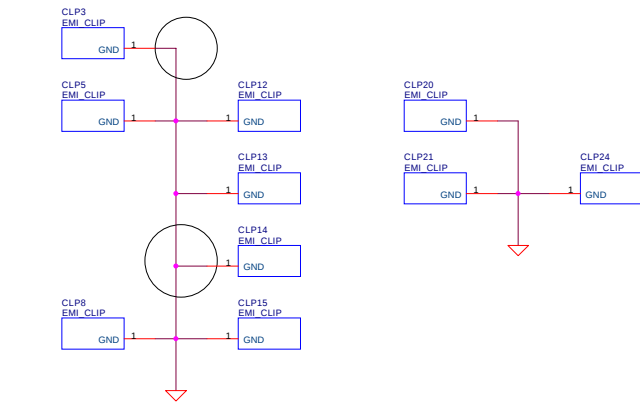






Parallel Port

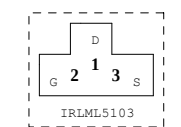
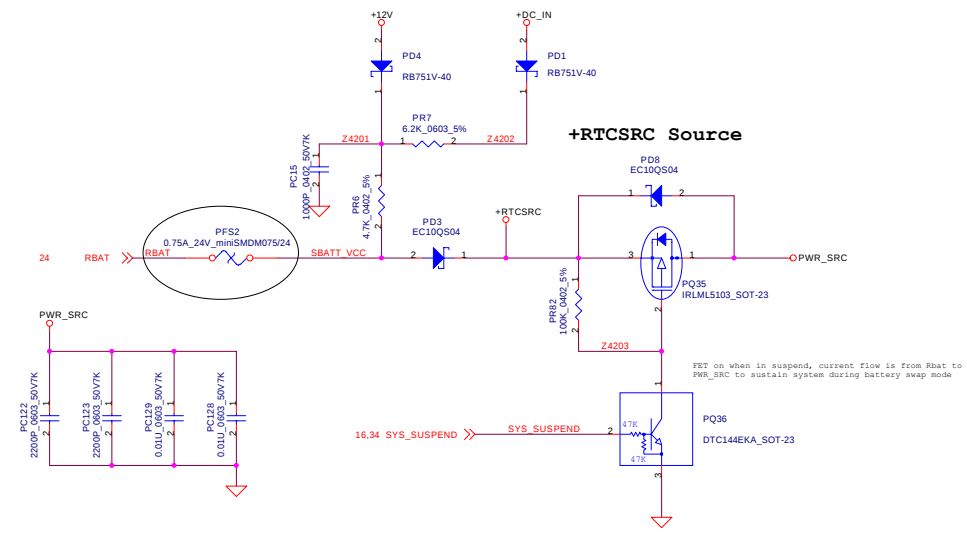




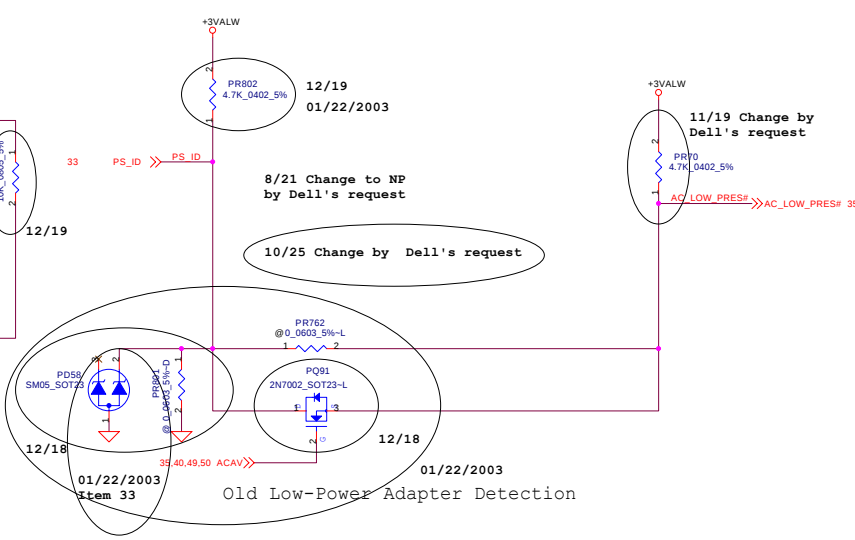
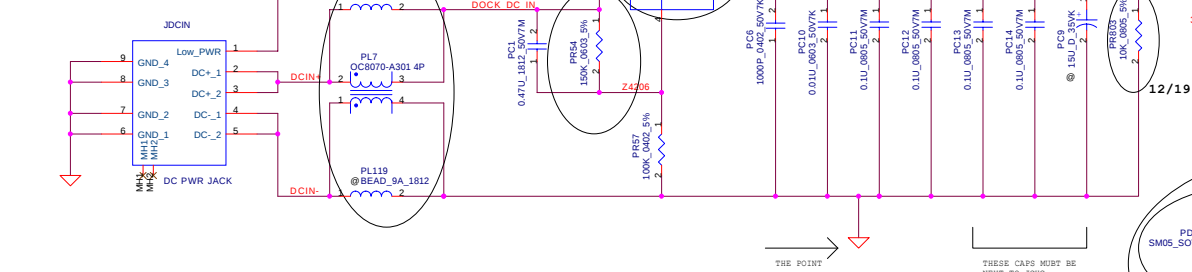
11/27
For Cost-BOM

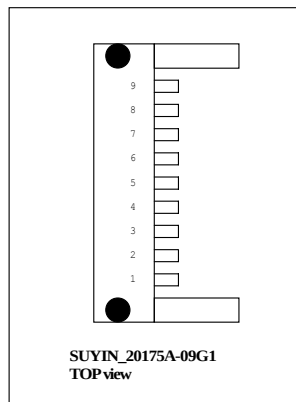
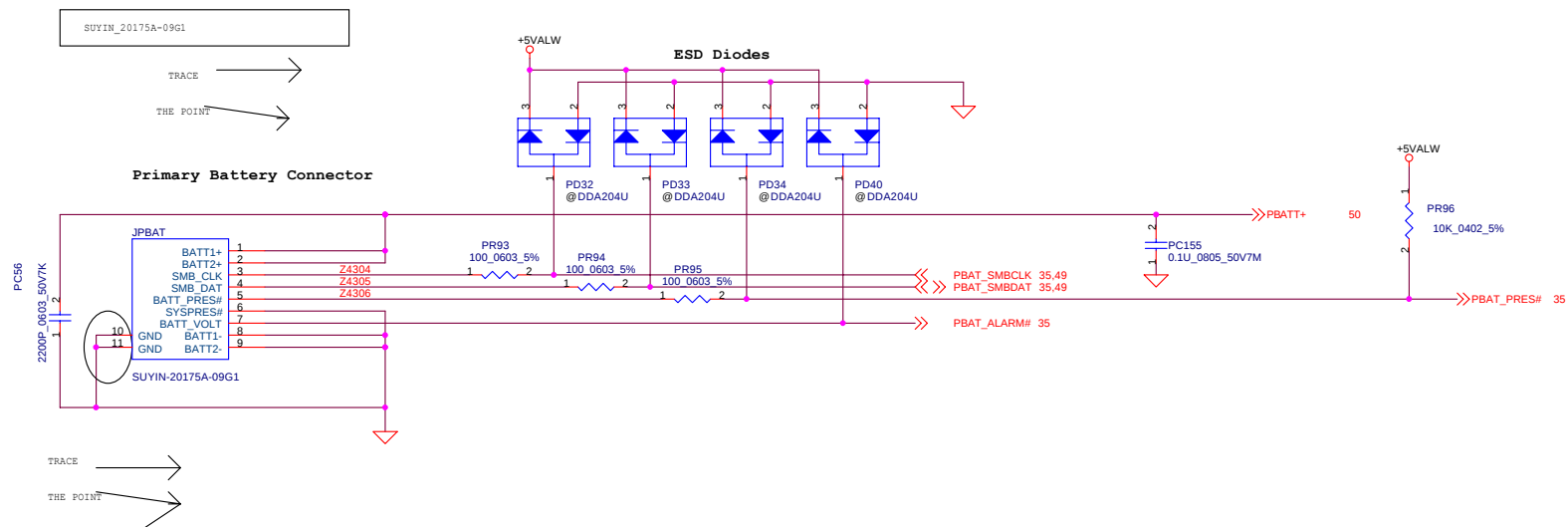
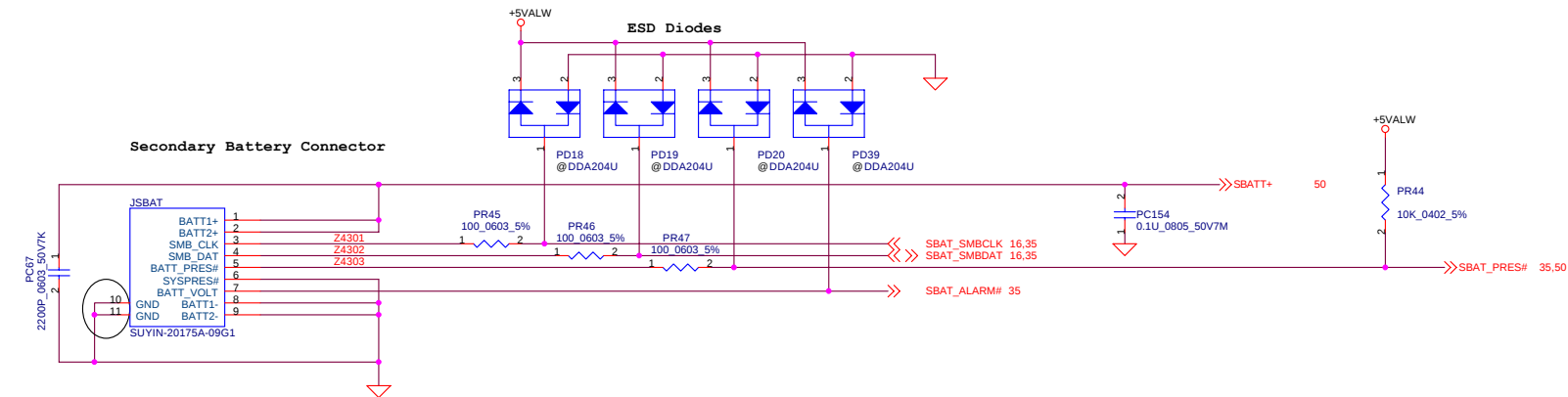
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Compal Electronics, Inc.

Title			PAD and Standoff
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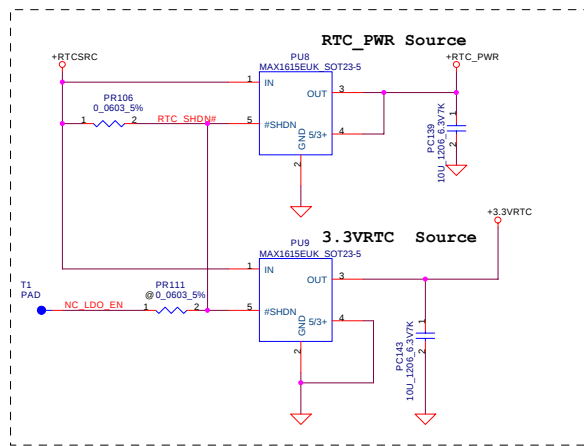
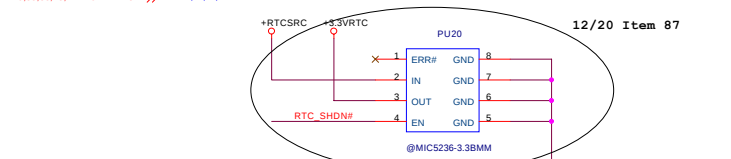
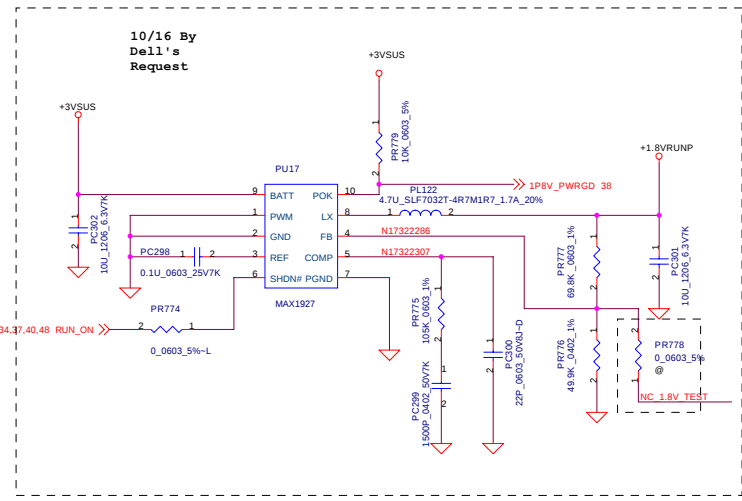
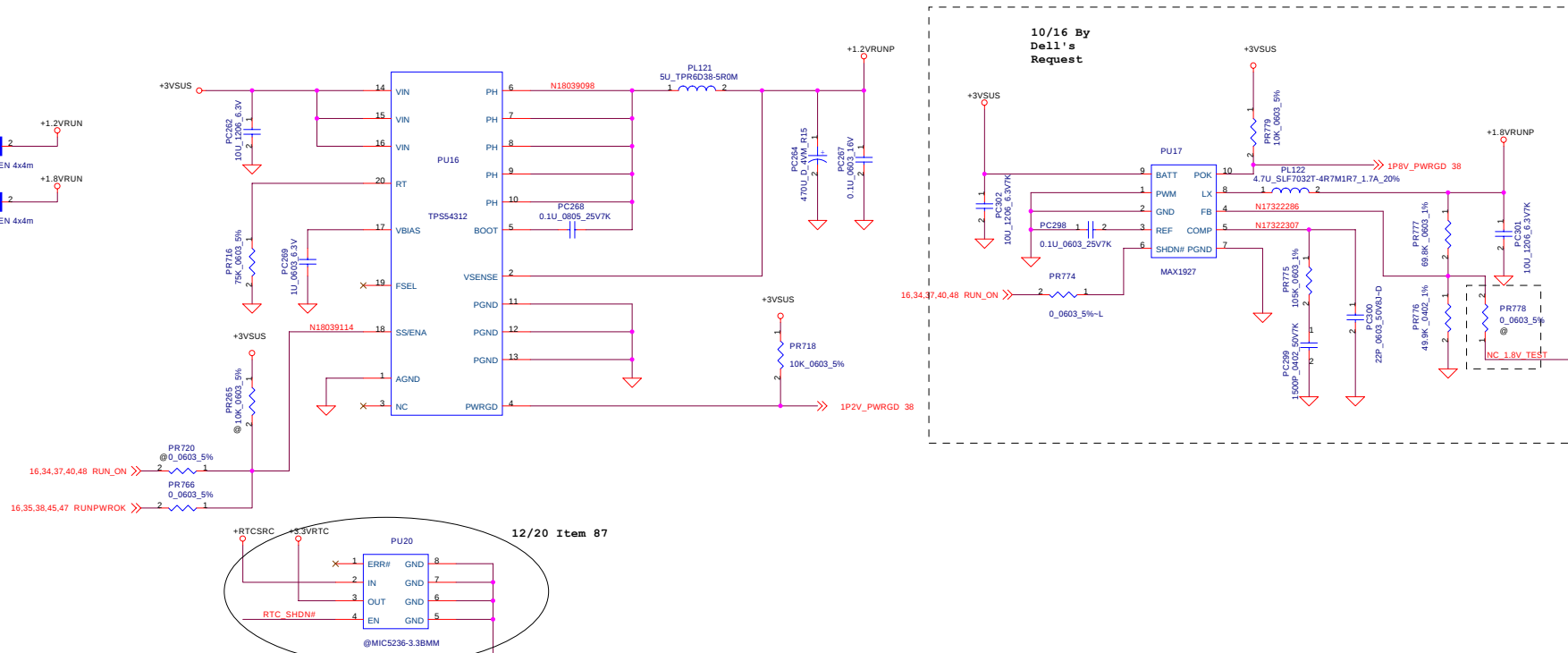


Z-series AC Adaptor Connector

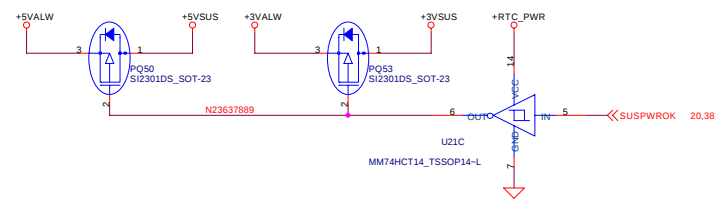




+1.2VRUN/+1.8VRUN SOURCE



Allows SUS planes to power ALW planes when possible



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COMPAL ELECTRONICS, INC			
Title	+1.2V & +1.8V		
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ICN internal pull up, this
part not need

value provided
1.04% Offset;
old value was
for old 2.67%
offset.

BG waveforms
improved, ma-
delete from
future
revision.

20 mil Trace list for layout

The +5VRUN (PIN38
AND PIN20)
BST1_VCORE
BST2_VCORE

Added
filter for
PBOOT

V I D							Vcore
VID 5	VID 4	VID 3	VID 2	VID 1	VID 0	V	
0	1	0	1	1	1	1.340	
0	1	1	0	0	0	1.324	
0	1	1	0	1	0	1.292	
0	1	1	1	1	0	1.260	
0	1	1	1	1	0	1.244	

100 mil Trace list for layout

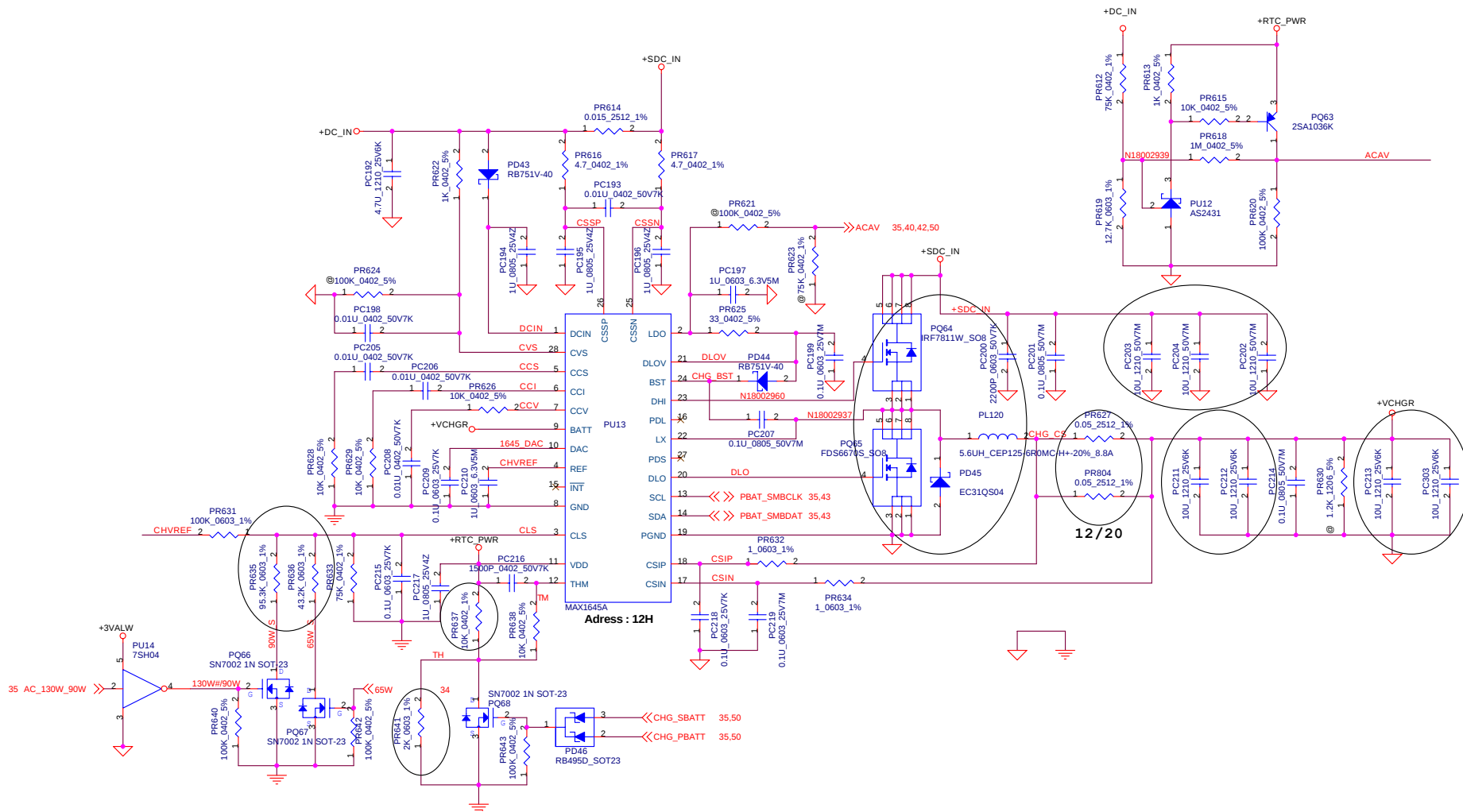
DH_VCORE
LX_VCORE
DL_VCORE
DH_VCORE2
LX_VCORE2
DL_VCORE2

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Compal Electronics, Inc.

Title			VCC_CORE
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	65W (2.93A)	90W (4.0A)	130W (5.85A)
65W	H	L	L
AC_130W_90W	H	L	H



Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	27	Ethernet	8/18/2002	Dell	R318 use the same value as Lindbergh	R318 change the value from 1240Ω to 1140Ω	X00
2	27	Ethernet	8/18/2002	Dell	Netname change for A-2 chip	Change netname from 4401_CLOCKRUN to 7705M_CLOCKRUN	X00
3	27	Ethernet	8/18/2002	Dell	Populate R549 for 5705m controller	5702 controller do not need to change	X00
4	27	Ethernet	8/18/2002	Dell	Delete "POP SMBUS ISOLATION FOR 5702" text	Delete text	X00
5	28	LAN Transformer	8/18/2002	Dell	No need for RN1 pins 1,2,3,4 to ground connection	Delete RN1 pins 1,2,3,4 to ground connection	X00
6	28	LAN Transformer	8/18/2002	Dell	Delete the net name LIN_1000# and rename net of Pin G12 of U1A, Page 27, to DOCK_LED_1000#	Delete the net name LIN_1000#, rename net of Pin G12 of U1A to DOCK_LED_1000#	X00
7	8	Banias Processor	8/20/2002	Dell	PJP11 & PJP16 text discribe error	Change PJP11 to Short, and PJP16 to Open.	X00
8	27	Ethernet	8/20/2002	Dell	So for now we can use 1150 Ohm 1% resistor for R318 and we will continue with the 5702 controller	R318 change the value from 1140Ω to 1150Ω	X00
9	31	MINI PCI	8/20/2002	Dell	Change pin 17 of JPCI from PCI_PIRQB#to PCI_PIRQD#	Change pin 17 of JPCI connection to PCI_PIRQD#	X00
10	42	Power DC-IN	8/20/2002	Dell	Change PQ32, PR73, PR74, PR77 to NP	Updated PQ32, PR73, PR74, PR77 to NP	X00
11	1	Cover page	8/22/2002	Dell	On the title page place the name of the project Kapalua	Updated Schematic cover page text	X00
12	27	Ethernet	8/23/2002	Dell	Changed netname from 7705M_CLOCKRUN to 5705M_CLOCKRUN	Updated netname from 7705M_CLOCKRUN to 5705M_CLOCKRUN	X00
13	28	LAN Transformer	8/23/2002	Dell	Add 0Ω (0402 resistors) resistors to LAN_TX0-/LAN_TX0+, LAN_RX1-, LAN_RX1+, LAN_TX2-, LAN_TX2+, LAN_TX3-, LAN_TX3+	Add R776, R777, R778, R779, R780, R781, R782 and R783	X00
14	40	Power Control	8/29/2002	Dell	Using wrong Control signal of 1.8V rail drain on Q85.2	change the control signal of 1.8V rail drain from RUN_ON# to RUN_ON	X00
15	44	1.8V / 1.2V	8/29/2002	Compal	Change the control signal of 1.2VRUN from RUN_ON to RUNPWROK	Add PR766 connect to RUNPWROK, and reserved PR720 to NP	X00
16	47	Vcc_core	9/03/2002	Dell	Banias update PBOOT voltage spec with 1.2V	change PR755 to 16.5K and PR757 to 15K	X00
17	40	Power Control	9/04/2002	Compal	System doesn't work in using battery only	change R606 value from 470K to 200K	X00
18	40	Power Control	9/04/2002	Compal	Reduce +3VSUS power rail up response time and solve unexpected step waveform	Depoplate C680	X00
19	16	VGA Board	9/04/2002	Compal	VGA no reference voltage detected	Poplate R124	X00
20	45	1.5V / 1.05V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR767 and reserved PR742 to NP	X00
21	46	1.25V / 2.5V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR768 and reserved PR605 to NP	X00
22	40	Power Control	9/26/2002	Dell	Fixes the 3VSRC to 3VRUN leakage problem	Change R603 to 22Ω	X01
23	40	Power Control	9/26/2002	Dell	We need a Soft Start feature	Add C680 to 0.1uF	X01
24	20	ICH4-M	9/26/2002	Dell	Change Board Rev resistor to X01	Depopp R287, and Pop R286	X01
25	45	1.5V / 1.05V	9/26/2002	Dell	Add 0 ohm resistor at PU18 pin 15 and pin 16	Add PR769 and PR770 to 0Ω	X01
26	46	1.25V / 2.5V	9/26/2002	Dell	Add a 0 Ohm resistor at PU11 pin 15	Add PR771 to 0Ω	X01

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Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
27	28	NIC high po issue	10/17/2002	DELL	High po test fail because RN1 pitch too close	change RN1 to R785~r788 to enlarge the pitch	X01
28	26	Add PD for BT	10/17/2002	DELL	leverage from LK	Add R789 10K PD to COEX1_BT_ACTIVE	X01
29	25	Dog h power rating	10/17/2002	Compal	The current rating of MOS and Bead is too small in Dog house	change Q1,F1,F2,L25 part to meet Doghouse power current requirement (F1&F2 create new symbol now)	X01
30	13	Fan soft start	10/17/2002	Compal	No soft start will cause +5VRUN droop low	change C127 and C110 pop(don't do it per Bo's command)	X01
31	22	cd audio pull down	10/18/2002	Compal	when we don't plug in CDrom, audio trace will floating	add R, R to tie the INT_CD_L/R ground	X01
32	22	reserve series ATA	10/18/2002	DELL	change some power/gnd to NC for reserve series ATA interface for module bay	change JMOD1pin8,10,14,16,9,11, to NC,conecct SATA_DET# to pin13	X01
33	20,22,34	add SATA detect	10/18/2002	DELL	Add series ATA detect in module bay	Add net sdat_det# from module bay to EC and remove original sio_lid_ec#	X01
34	31	change MINI PCI pin NC	10/18/2002	DELL	change MINI-PCI pin NC (No use)	Change JPCI pin109 and pin 123 to NC and remove R457,C576	X01
35	22	cr_ref return cap.	10/18/2002	DELL	add a pull down cap. for cd_ref near JMOD	Add C from CD_REF to gnd near JMOD and let it unpop	X01
36	29	Reserve PCMCIA suspend pin	10/18/2002	DELL	Reserve PCMCIA suspend pin not use	depop D11 and reserve it for TI_SUSPEND#	X01
37	40	Power Q-SW	10/18/2002	DELL	Lindberg change Q12,Q55,Q77 to big rating, we will wait the EA report and make a decision	TBD	X01
38	38	Add 1.8V PWRGD	10/18/2002	DELL	Add the 1.8V power good to power sequence	change the U12 pin 13 from RUN_ON to 1P8V_PWRGD	X01
40	25,34	change DH enable	10/22/2002	DELL	change dog house power enable pin independent	Add R and R seprate the control signal of DH	X01
41	10	change to normal part	10/22/2002	Compal	change to vender normal specification part	change C743 and C744 from 50V to 16V rating	X01
42	31	remove pin of MINI-PCI	10/24/2002	DELL	change pin def. of MINI-PCI	romove pin 118, 120 ground for JPCI	X01
43	42	remove some PS_ID	10/24/2002	DELL	remove some reserve PS_ID component	Remove PQ32, PR77, PR74, PR73	X01
44	26,31	duplicate pull down	10/24/2002	DELL	remove duplicate pull down	Remove R789 , only use R767	X01
45	29	remove not use part	10/24/2002	DELL	remove the reserve part for carrbus controller	Delete R185	X01
46	38	change the thermaltrip	10/24/2002	DELL	change the thermal shut down solution from MAX to discrete	del U6, R30, R24, add U2, C29, R28, R20~R23, C27, R53, C65	X01
47	28	ESD Safty issue	10/24/2002	Compal	JPH_RJ layout change connection from GND to NC.	JPH_RJ pin 6 change connection from GND to NC.	X01
48	40	+3VRUN, +3VSUS FET ISSUE	10/25/2002	DELL	+3VSUS AND +3VRUN POWER RAIL MOSFET PARTS CHANGED	Q12 AND Q77 CHANGE PARTS FROM SI3456 TO SI4810	X01
49	33	RGB Beads value changed	10/25/2002	DELL	Using 0Ω instead of beads	Change the value of L101, L102, L103, L14, L15, L16, L17, L18	X01
50	33	Rev. changed	10/25/2002	Compal	PI3L301A revision change to PI3L301BA	U30 Rev. changed	X01
51	25	Dog House	10/29/2002	DELL	Must Design as Lindberg	R25 changed value to 100K and R26 (10K) change to C940 (0.022u_0805)	X01
52	9,12,15	Bulk Cap.	10/29/2002	Compal	Height Restriction	Changed Height limit of C933,C934,C731,C779,C788,C789 to 1.9mm	X01
53	9	CPU Bypass	10/29/2002	DELL	Changed Voltage Limit of Bulk Cap. to 2V	C671~C675,C678 Changed Voltage Limit to 2V	X01

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Version Change List (P. I. R. List)

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
54	41	PAD & ME Parts	10/30/2002	Compal	Need to BLOW OUT 2.5mm Radius from Vias used to transition Tip and Ring from InnerLayer 4 to Bottom layer	Deleted H43 to meet 2.5mm Radius gap	X01
55	28	Magnetic & RJ45	10/30/2002	DELL	LAN Analog Switch (PI3L301BA) serial Resistors change type to Inductors	R776~R783 changed value from 0Ω to 15nH Inductor (TDK MLK1005S15NJ)	X01
56	27	POP option to 5705	11/08/2002	Compal	change the POP option from 5702 to 5705	Depop L66, C344, C324 Pop R217, R549	X01
57	38	Thermal shut down to 6509	11/08/2002	DELL	change the thermal shut down solution from discrete to MAX	Add U6, R30, R24 Depop U2, C29, R28, R20~R23, C27, R53, C65	X01
58	27	LOM	11/27/2002	Compal	Depop dummy parts	Depop R388 (10K_0402) and C390 (0.01U_0402)	X02
59	41	PAD, ME, SPARE PARTS	11/27/2002	Compal	Add Cost-BOM parts	Add Z1(Switch-FPC), Z2(Touch_pad-FPC), Z3(MDC-Cable), Z4(Spk-mODULE), Z5(BT_Cable).	X02
60	9	Vcore Bypass CAP.	11/29/2002	Compal	Vcore by pass capacitors adjust for cost	Add C941~C944 (220UF_2V) Depop C671~C675, C678 (220UF_2V)	X02
61	40	Power Control	11/29/2002	Compal	package change for increase voltage tolerance	C680 changed package from 0402 to 0603	X02
62	20	ICH4	12/02/2002	Compal	Depop dummy part	Depop R529 (10K_0402)	X02
63	25	Dog House	12/04/2002	Compal	package change for component easy to buy	C940 changed package from 0805 to 0603	X02
64	30	Smart Card	12/11/2002	DELL	Need to implement 5 Smart Card changes.	Add R12, R26, C184 , C189 , Change C171 value from 390P to 470P	X02
65	27	BCM5705 By Pass	12/11/2002	DELL	Need to change package from 0805(4.7UF) to 1206(10UF) for cost	C339, C346, C352, C355, C393, C662, C368, C342	X02
66	All Pages	Page Symbol	12/13/2002	DELL	Put "DELL CONFIDENTIAL/PROPRIETARY" on Schematics	DELL CONFIDENTIAL/PROPRIETARY	X02
67	20	ICH4	12/13/2002	Compal	Delay Thermtrip to ICH4 so Intruder have time to get latch	Add C46 (0.1UF_0402)	X02
68	38	Thermtrip	12/17/2002	DELL	For Charger Selector Used (SIO_THERM_PWRDN net changed to SBAT_LOW)	Delete R206 & Q22	X02
69	27	BCM5705	12/17/2002	DELL	Delete 4401 Components	Delete U43, R388, C390, R367, R368	X02
70	28	LAN Transformer	12/17/2002	DELL	Delete 4401 Components	Delete R551 ~ R554	X02
71	28	LAN Transformer	12/18/2002	DELL	Changed Spec 2KV to 3KV	C1	X02
72	17	TV OUT	12/18/2002	DELL	Depop C14 for Cost	Depop C14	X02
73	33	DOCKING CONN.	12/18/2002	DELL	Remove RGB Bead	Delete L101, L102, L103, L14 ~ L18 & Trace	X02
74	23	AC,97 CODEC	12/18/2002	Compal	Added Resistors for TPS793475 LDO	Add R792, R793 (@)	X02
75	23	AC,97 CODEC	12/18/2002	DELL	Change the Value from 0 ohm to 1UF_0805	C636	X02
76	17	TV OUT	12/18/2002	DELL	Change RGB Bead Value & Package	Changed L5, L104, L105	X02
77	35	SIO	12/18/2002	Compal	ESD Cap is Needed on Reset to McCallen	Add D74 for ESD (@)	X02
78	30	Smart Card	12/19/2002	DELL	Added Cap (4.7uF_0805) for SCR_VCC_C to Depop	Add C945 for depop (@)	X02
79	17	TV OUT	12/19/2002	Compal	Depop C10, C11, C661 for RGB Signals	Depop C10, C11, C661(@)	X02

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
80	40	Power Control	12/19/2002	Dell	Change ALW_ENABLE# control methos through OR gate	Added R794, R795, R796	X02
81	38	Thermtrip	12/19/2002	Dell	THERM_PWRDN add RC delay	Added R798, C946	X02
82	26	MDC, BT	12/19/2002	Dell	Pop JPT pin 7 resistor	Added R765	X02
83	35	SIO	12/19/2002	Dell	SBAT_LOW pull-up through 4.7K resistor	Added R797	X02
84	24	Phone JACK	12/20/2002	Dell	R252 to be connected to EXT_MIC_PLUG instead of EXTMIC_PLUG	R252	X02
85	24	Phone JACK	12/20/2002	Dell	add a 100PF cap to GND on Sheet 24 on the EXTMIC_PLUG so that signal is terminated as well for protection purposes	Added C947	X02
86	35	SIO	12/20/2002	Compal	To prevent ACAV from leaking Current for OTP event	Added R799, R800 & ACAV through AND gate	X02
87	20	ICH4M	12/20/2002	Dell	Board ID X02	Pop R278, R287 & Depop R279, R286	X02
88	35	SIO	12/20/2002	Dell	Add Battery Selector Circuit	U27.H13 netname changed to SBAT_LOW	X02
89	28	Magantic	12/21/2002	Dell	Change Magantic vendor to Transpower	L24 (GB1G04-T)	X02
90	19	ICH4	01/02/2003	Compal	Change RTC 32.768KHz Crystal to 10ppm	X5	X02
91	09,12,15	CPU,Odem,DDR bypass	01/03/2003	Dell	Reduce Component Hight from 3.1mm to 2.8mm	C78, C796, C910, C935, C936	X02
92	19	ICH4	01/07/2003	Compal	Change Cap Value from 15pF to 12pF to Improve Frequency Tolerance	C581, C586	X02
93	21	ICH4	01/07/2003	Dell	Change Cap Height Dimension for placing Height Restriction Area	C585	X02
94	20	ICH4	01/20/2003	Dell	Change Board ID to X03	Pop R286 Depop R287	X03
95	39	JLID	01/20/2003	Compal	Change for Switch-Board EMI solution	Depop R214	X03
96	35	SIO	01/22/2003	Compal	Change Footprint for ESD	D74 (@)	X03
97	27	LOM	01/30/2003	Dell	Change Resister Value for RDAC	R318 (1.18K)	X03
98	38	Thermtrip	01/30/2003	Dell	Change Trip Temperature Point to 86 C for OTP	R24 (22.6K)	X03
99	28	Magantic	01/30/2003	Compal	Added Pull Down Resister to prevent LOM LED from doing abnormal behavior.	Added R400, R401, R402	X03
100	17	TV-Out / CRT	02/06/2003	Dell	CRT H/V sync Need to NO-POP	Depop R6, R16	X03
101	25	USB 2.0	02/06/2003	Dell	Reduce Component Hight from 3.1mm to 2.8mm	C300, C304, C329	X03
102	27	LOM	01/30/2003	Dell	Back to The Original Resister Value for RDAC	R318 (1.15K)	X03
103	38	Thermtrip	02/12/2003	Dell	Change Trip Temperature Point to 84.5 Degree C for OTP	R24 (24K)	X03

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1	42	Power DC-IN	8/20/2002	Dell	Change PQ32, PR73, PR74, PR77 to NP	R318 change the value from 1240Ω to 1140Ω	X00
2	44	1.8V / 1.2V	8/29/2002	Compal	Change the control signal of 1.2VRUN from RUN_ON to RUNPWROK	Change netname from 4401_CLOCKRUN to 7705M_CLOCKRUN	X00
3	47	Vcc_core	9/03/2002	Dell	Banias update PBOOT voltage spec with 1.2V	Change PR755 to 16.5K and PR757 to 15K	X00
4	45	1.5V / 1.05V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR767 and reserved PR742 to NP	X00
5	46	1.25V / 2.5V	9/05/2002	Compal	SUSPWROK_1P5V control signal detected glitch issue	Add PR768 and reserved PR605 to NP	X00
6	45	1.5V / 1.05V	9/26/2002	Dell	Add 0 ohm resistor at PU18 pin 15 and pin 16	Add PR769 and PR770 to 0Ω	X01
7	46	1.25V / 2.5V	9/26/2002	Dell	Add a 0 Ohm resistor at PU11 pin 15	Add PR771 to 0Ω	X01
8	45	1.5V / 1.05V	10/17/2002	Compal	Turn off have glitch because +3VSUS issue, so pull high to +5VSUS can improved	Add PR780 to 10KΩ	X01
9	45	1.5V / 1.05V	10/17/2002	Compal	Intersil Issue can solved by Fairchild solution Change the control signal of 1.05VSUS from RUNPWROK to SUSPWROK_5V	Add PR772, PR773 and change PC282 to 0.01U_0805 size	X01
10	44	1.8V / 1.2V	10/17/2002	Dell	Change the 1.8VRUN Regulator from CM3718 to MAX1927	Change +1.8V Regulator	X01
11	45	1.5V / 1.05V	10/17/2002	Dell	+1.5VSUS POWER GOOD ISSUE	change PL124 to 5.0U_20%_3.0A to 4.7U_20%_3.9A	X01
12	44-48	All Regulators	10/17/2002	Dell	Add test point for DELL testing	Add PR778, PR783, PR784, PR781, PR782, PR785, PR786 to NP for Test point	X01
13	49	Battery charger	10/23/2002	Dell	Fast Charger for short time and Cost-Down	Add P303 and Change PC202, PC203, PC204, PC211, PC212, PC213 to 10U_1210 and Delete PU15's circuit Change PR636 to 43.2K ohms for 65W Adpater plug in	X01
14	45	1.5V / 1.05V	10/23/2002	Dell	Change OCSETt and SEN Resistor	change PR735, PR736 to 75K ohms, Change PR727 to 330 ohms, Change PR728 to 634 ohms	X01
15	47	Vcc_core	10/28/2002	Dell	Cost-Down and change deepsleep offset	Change PR744, PC243, PC251, PC252, PR765 For adjusts slope and sleep offset PQ7, PQ11, PD24, PD27 to NP for Cost-Down	X01
16	47	Vcc_core	10/28/2002	Dell	EMI Broad band ISSUE	Add PC304 PC306 to 0.1U, Add PC305 PC307 to 2200P change PR747 PR756 to 3.3 ohms	X01
17	50	PWR_Selector	11/14/2002	Compal	Charger selector ISSUE	Add PR649, Del PD48,Change PD49 PD51 to B540C and Change PR657 PR662 to 33K	X02
18	42	Power DC-IN	11/20/2002	Dell	AC-IN Detect Issue	Change PR70 to 4.7K	X02
19	49	Battery charger	11/26/2002	Dell	Charger Current to 5.3A	Change PR627 to 28m ohms	X02

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