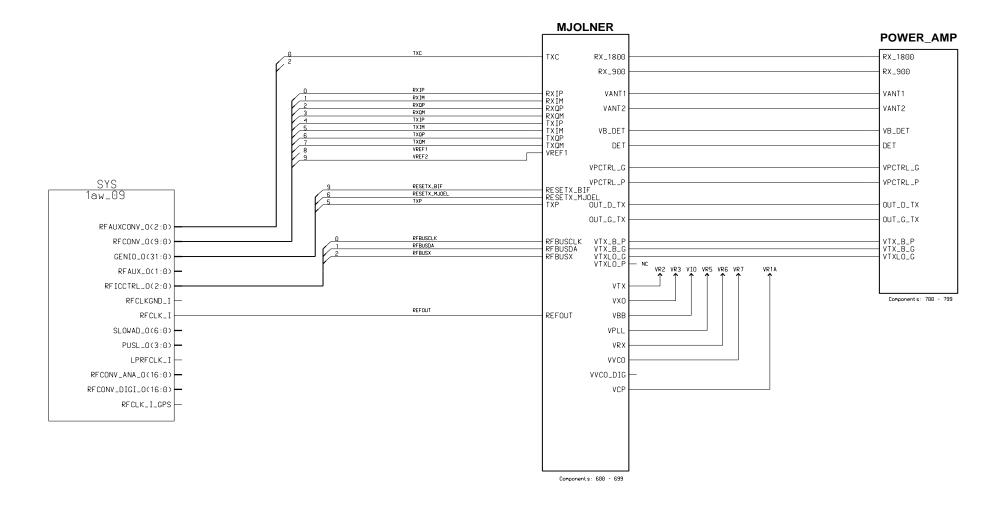
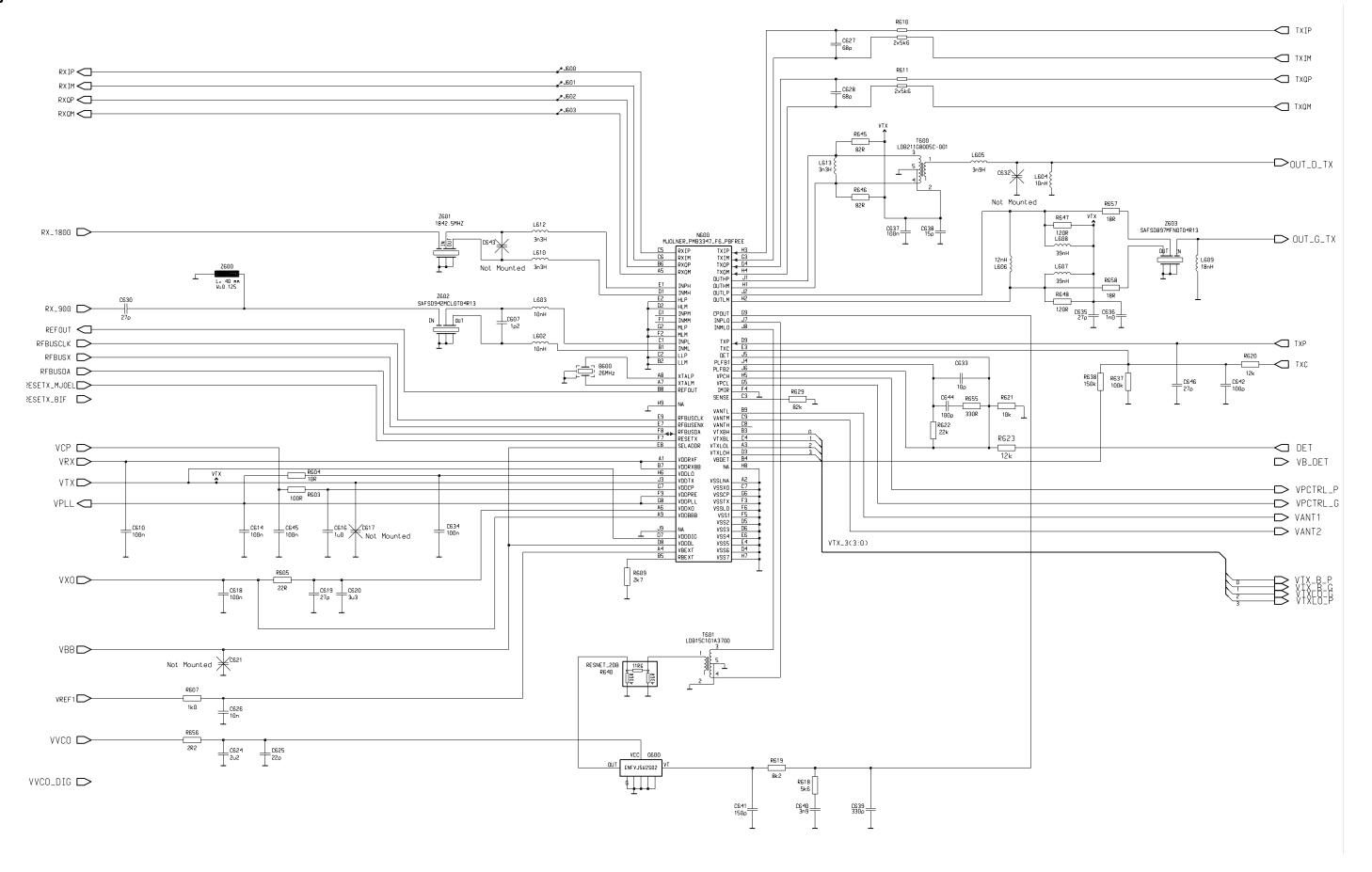
# RH-18/36/38 Schematic Diagrams

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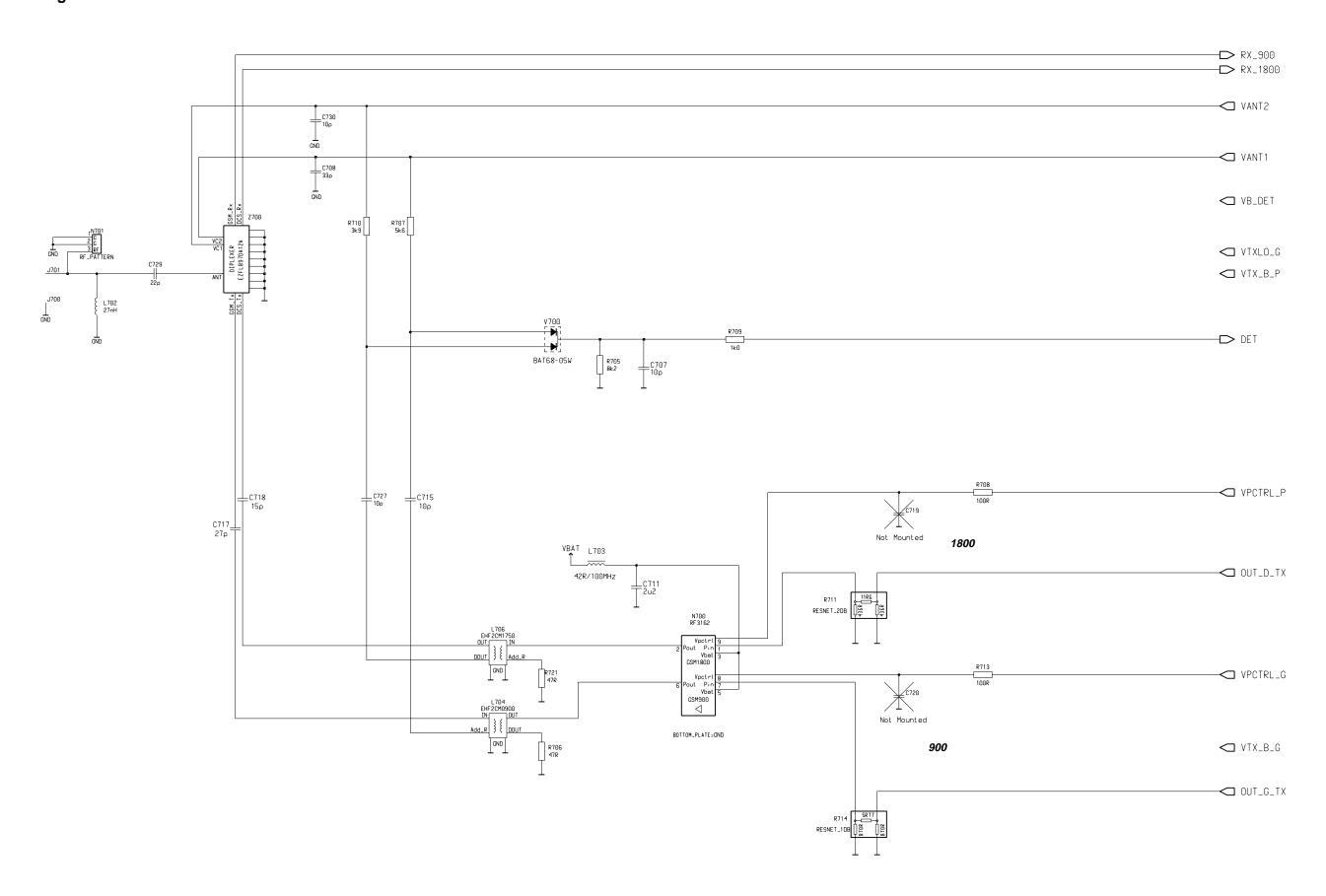
# BB/RF connection block diagram v.0.0 ed.79



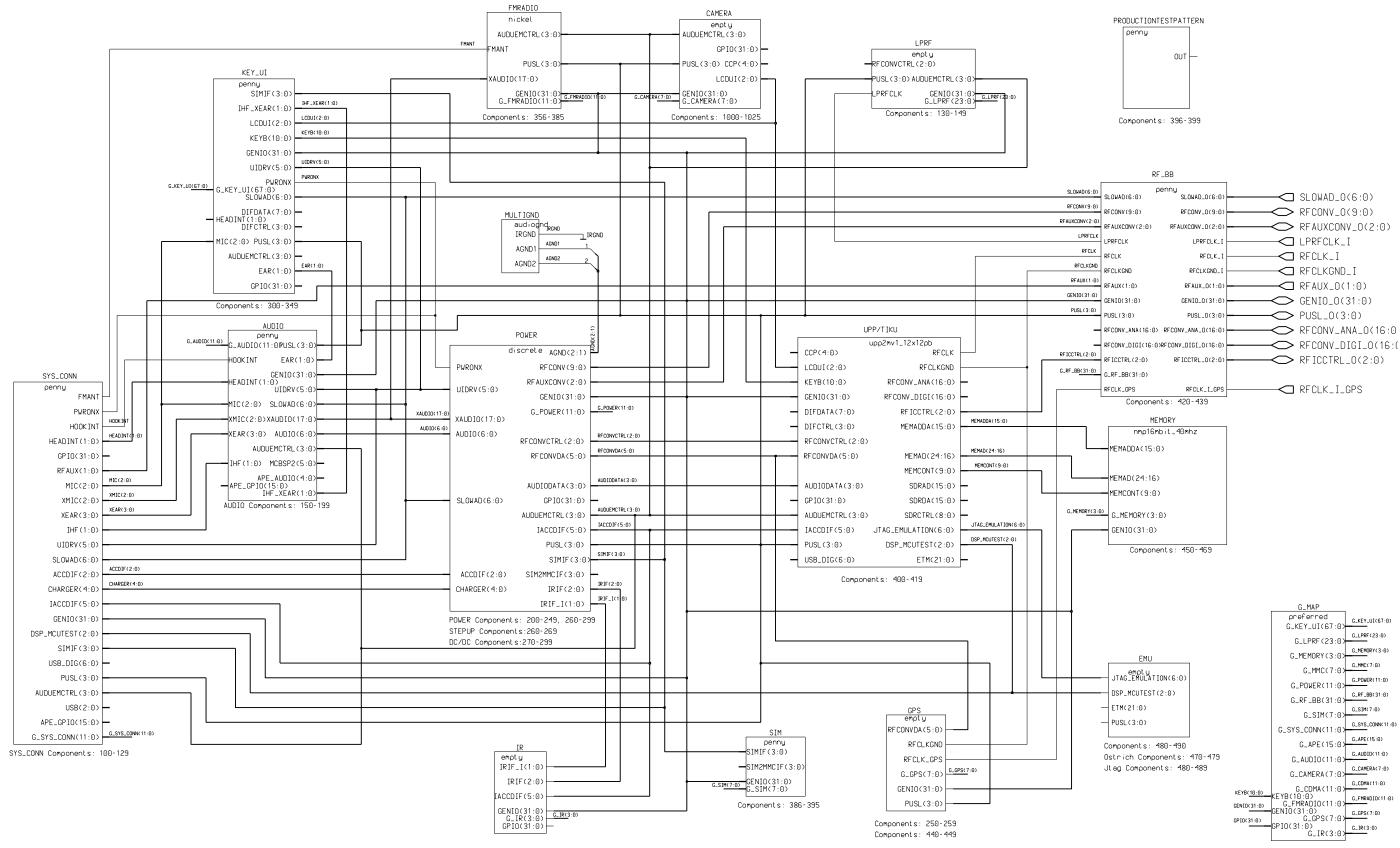
#### Mjölner IC v.0.0 ed. 229



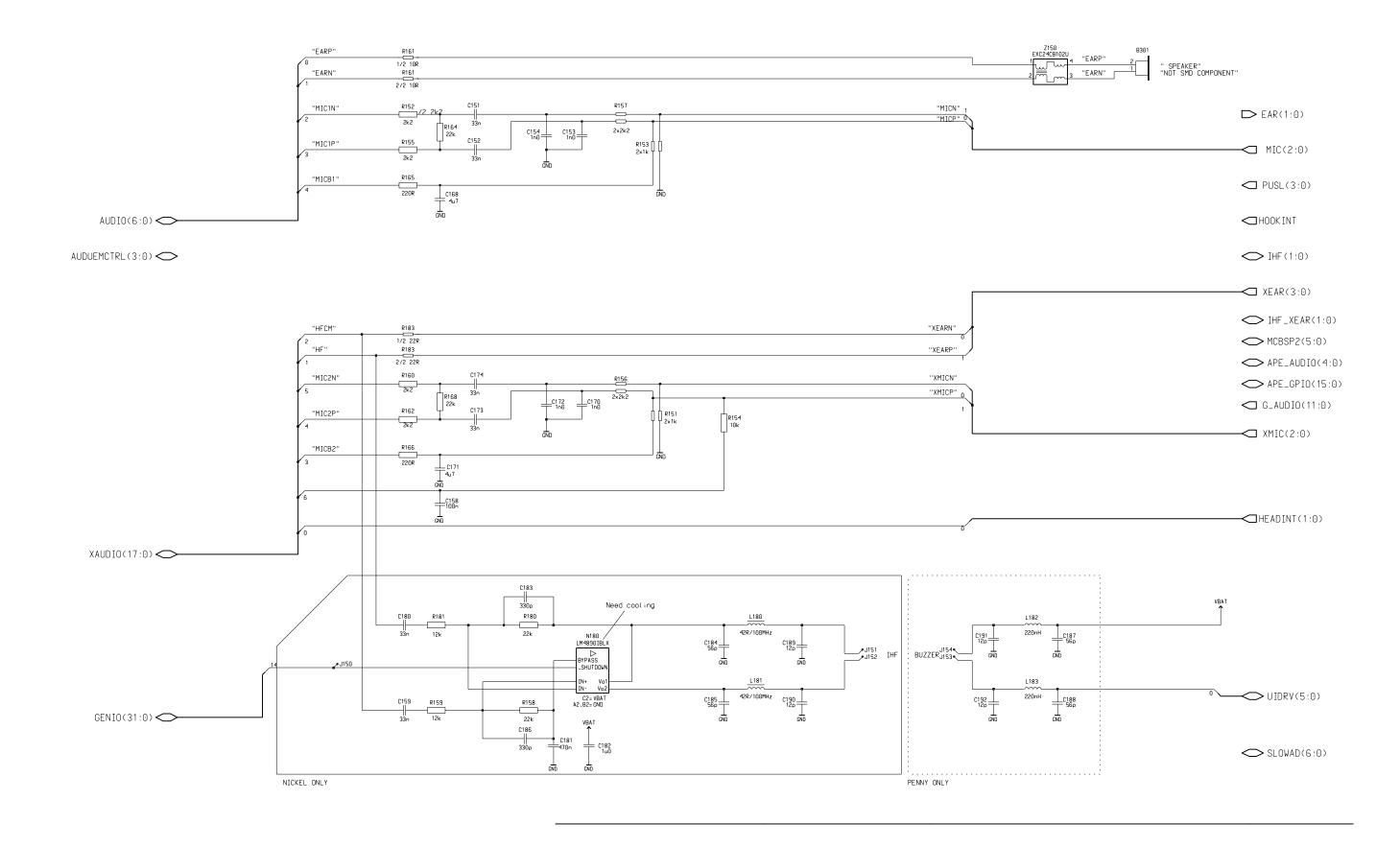
# Power amplifier diagram v. 0.0 ed.135



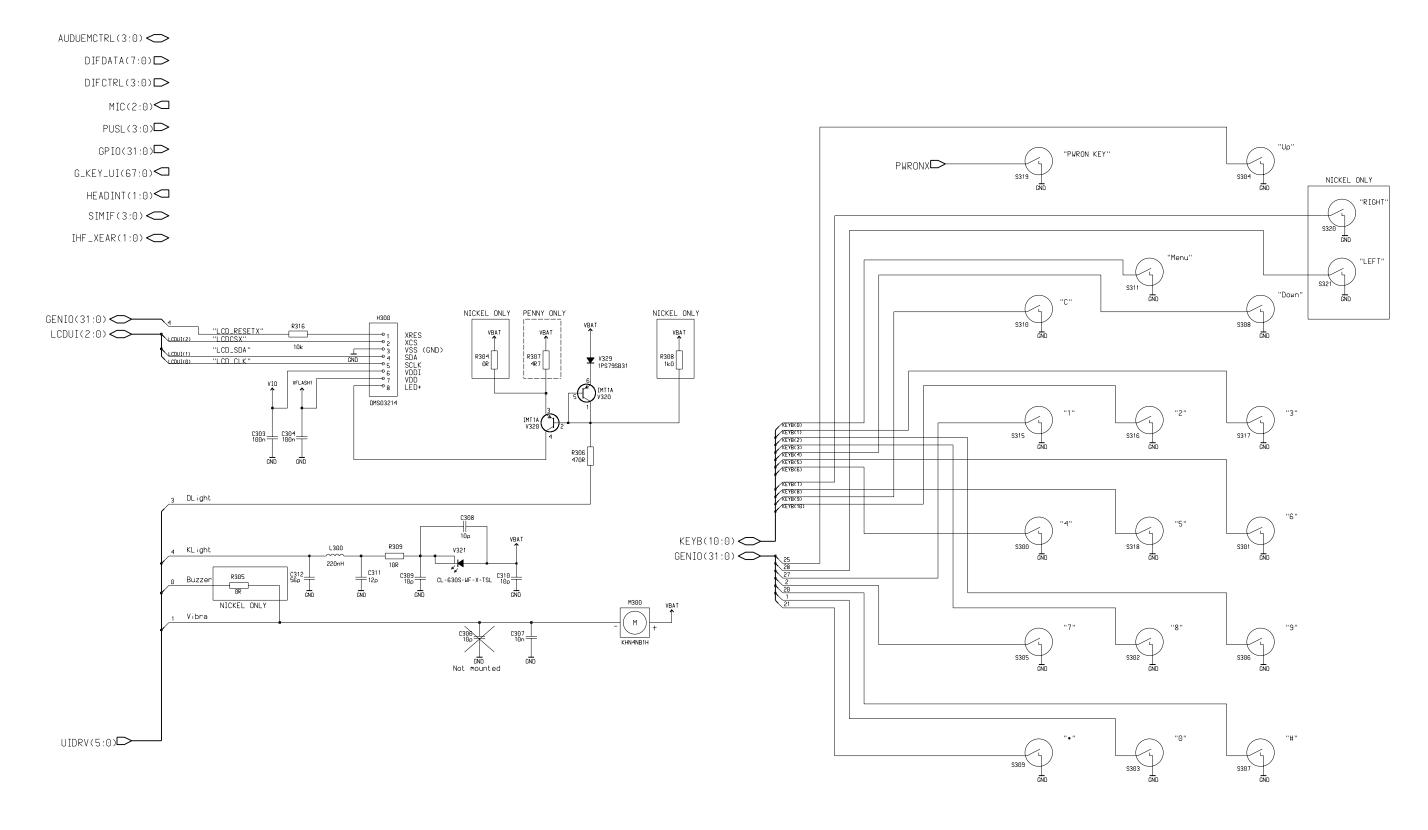
#### Baseband block diagram v. 0.0 ed. 343



## Audio schematic diagram v. 1.3 ed.48



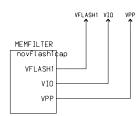
## Keyboard diagram v. 1.3 ed.52

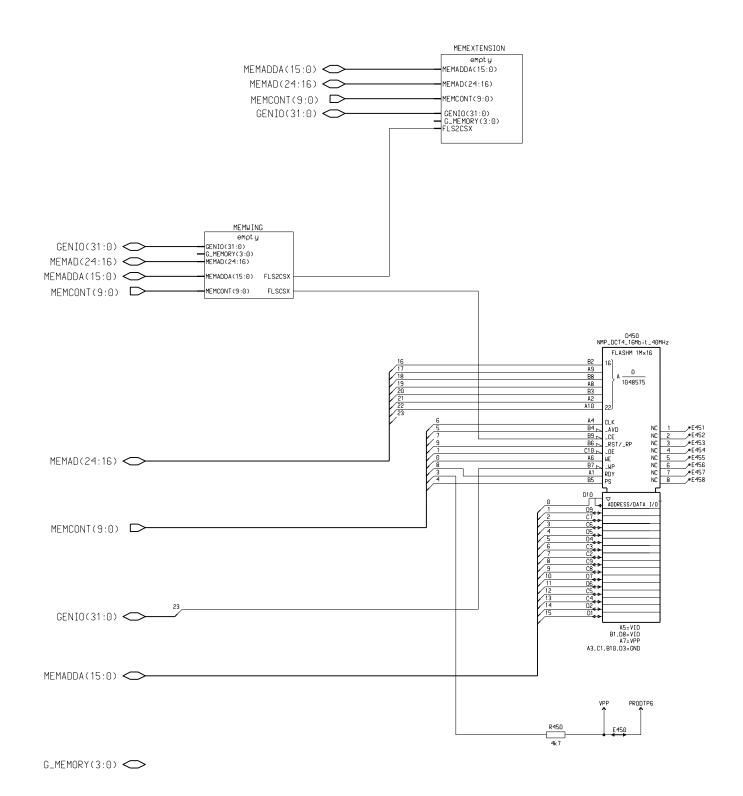


SLOWAD(6:0)✓

EAR(1:0)

## Flash memory diagram v. 2.0 ed.36





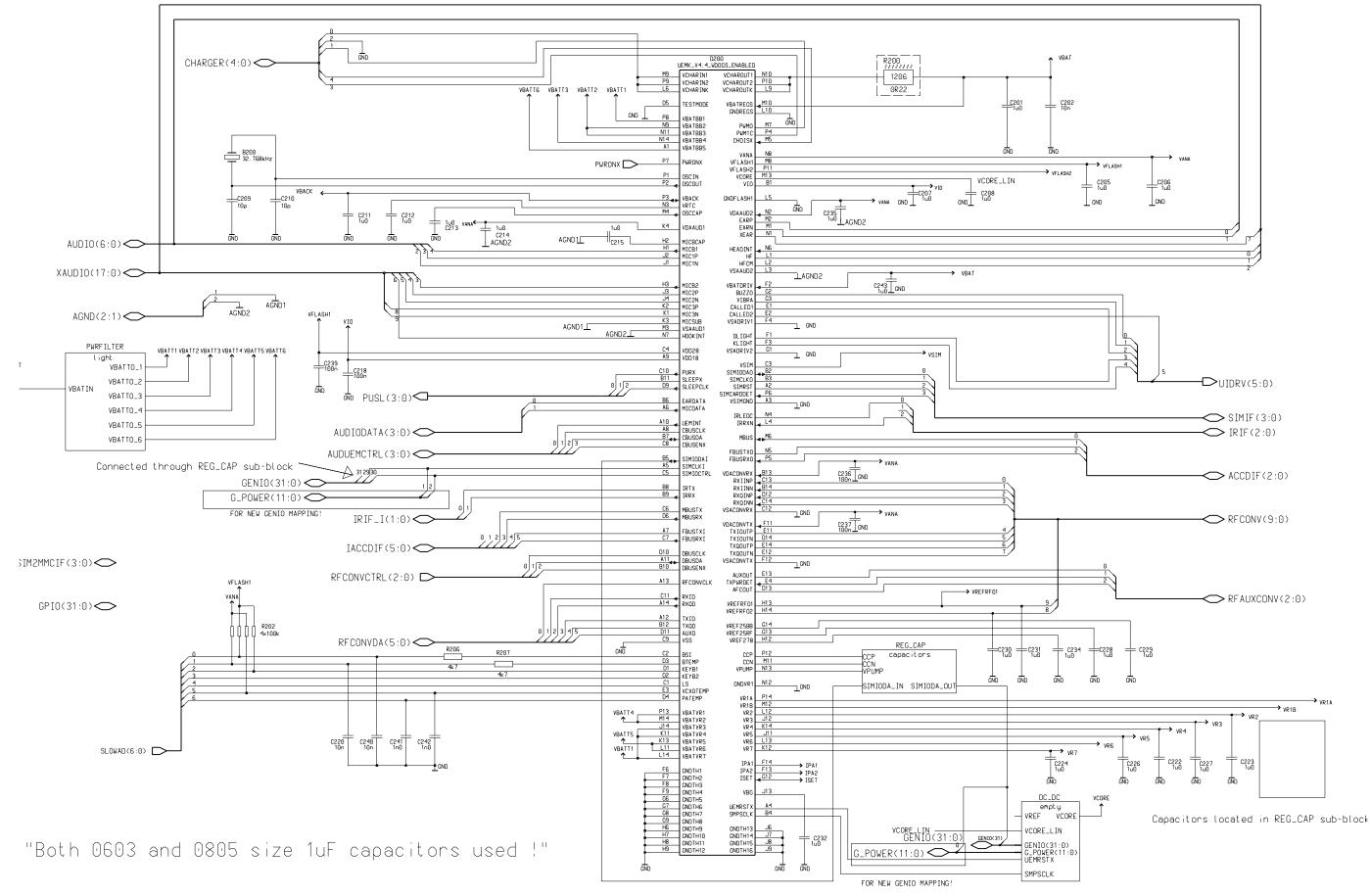
Discrete capacitors for memory without VFlash1 v. 1.3 ed.11



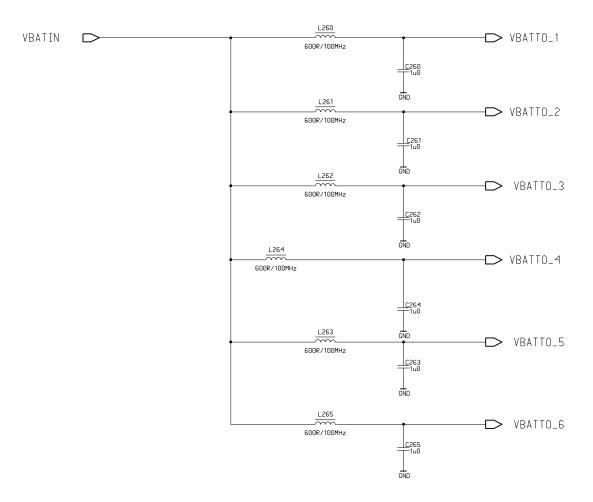
VFLASH1

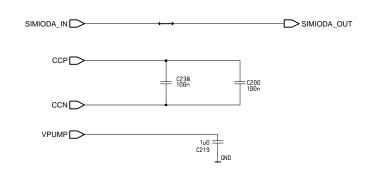


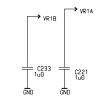
#### Discrete power v.1.3 ed.120



Used refs in REG\_CAP: C238, C200, C219, C233, C221 and R205

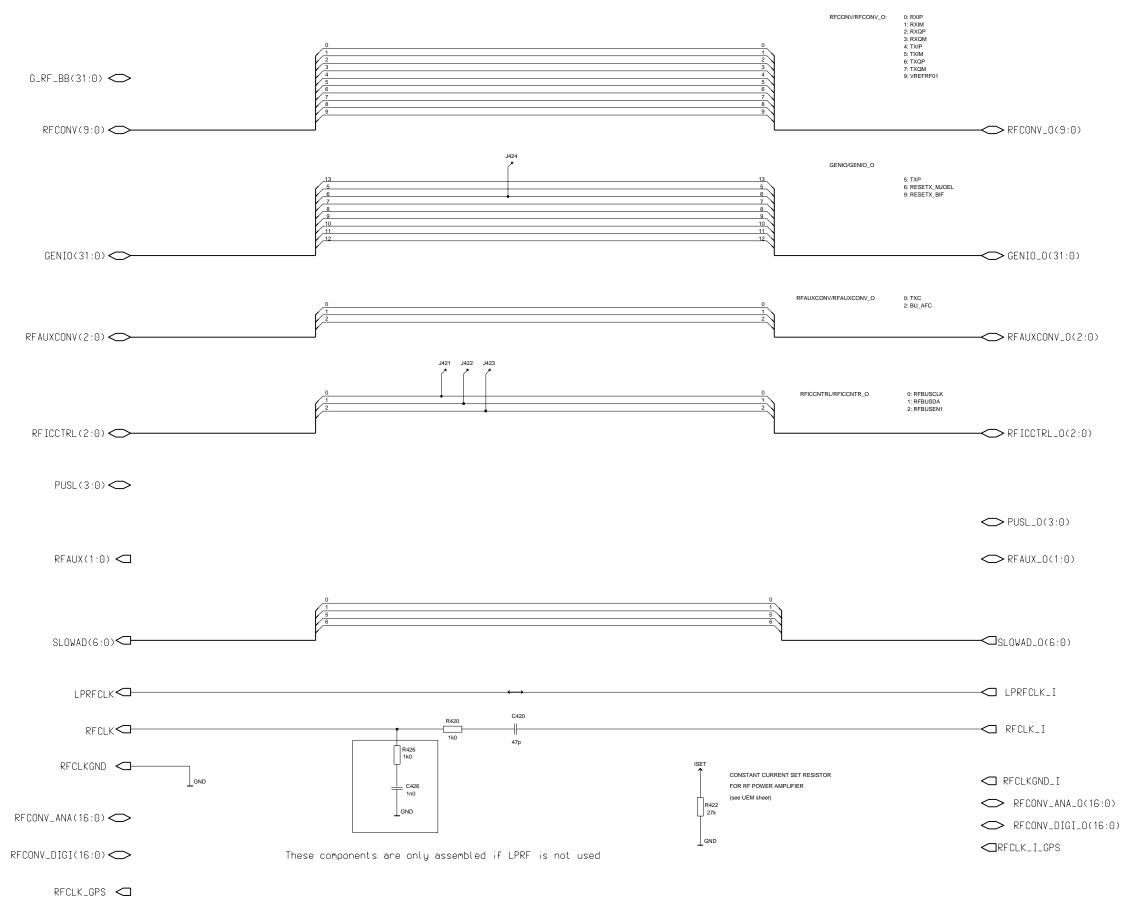




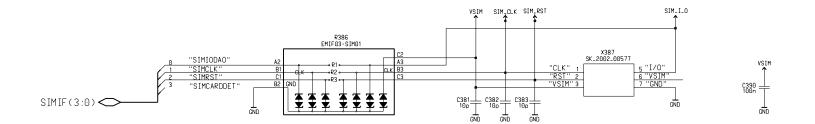


Power resistor v. 0.0 ed.4

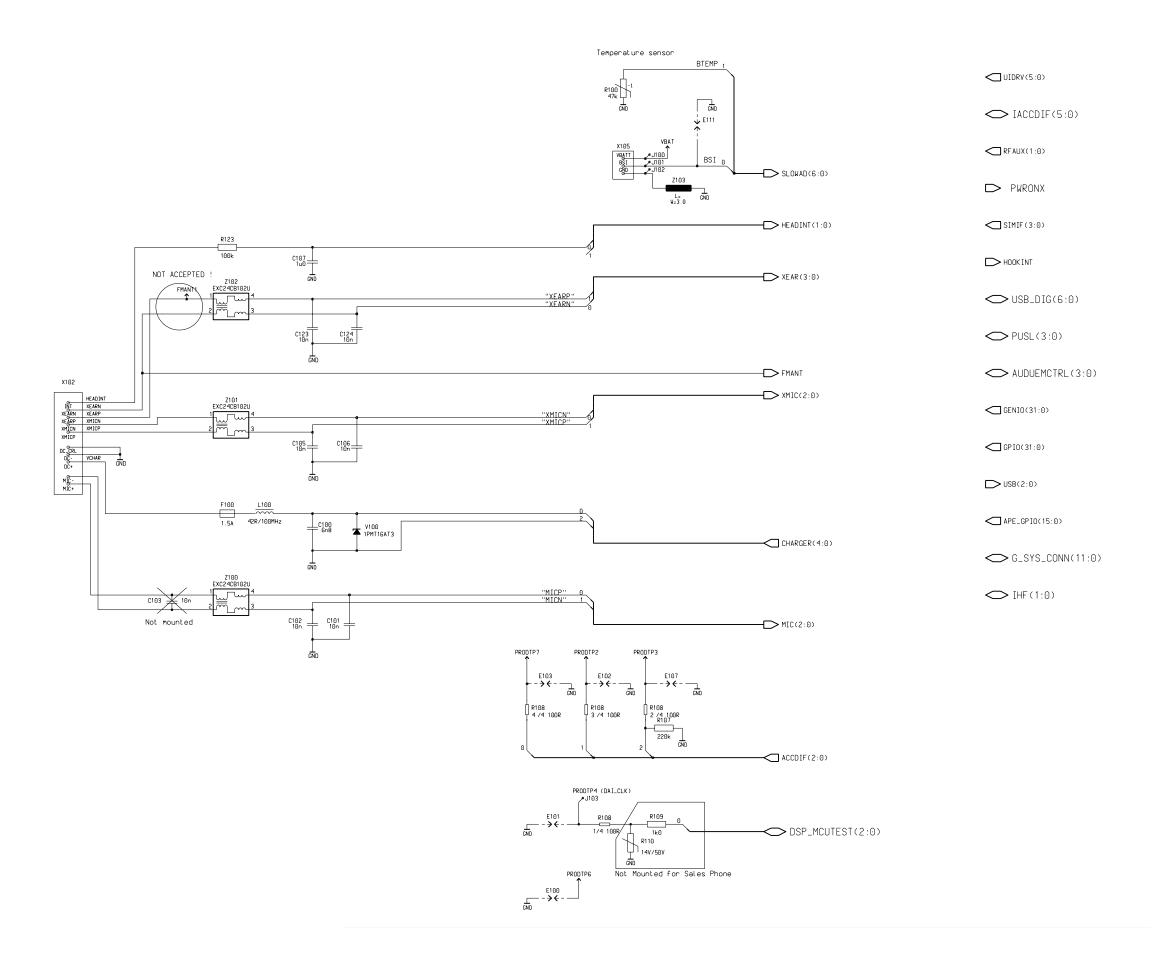
## RF/BB connection, v. 1.3 ed.28



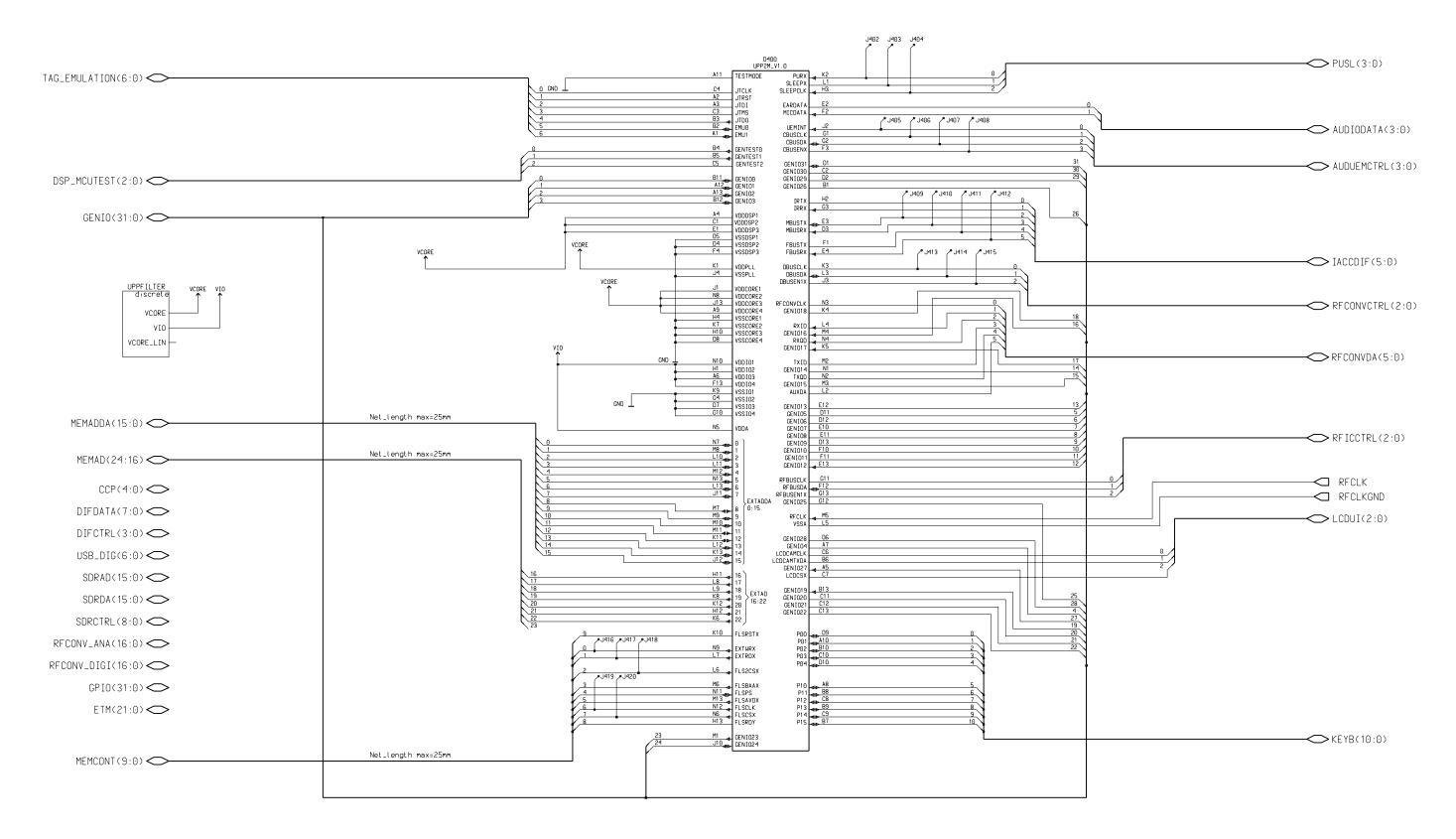
SIM v. 1.3 ed. 32

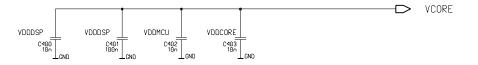


- SIM2MMCIF(3:0)
- GENIO(31:0)
- G\_SIM(7:0)

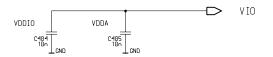


# UPP 8M diagram, v. 2.0 ed. 107

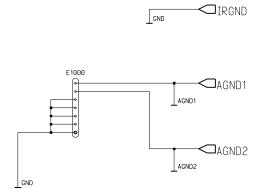


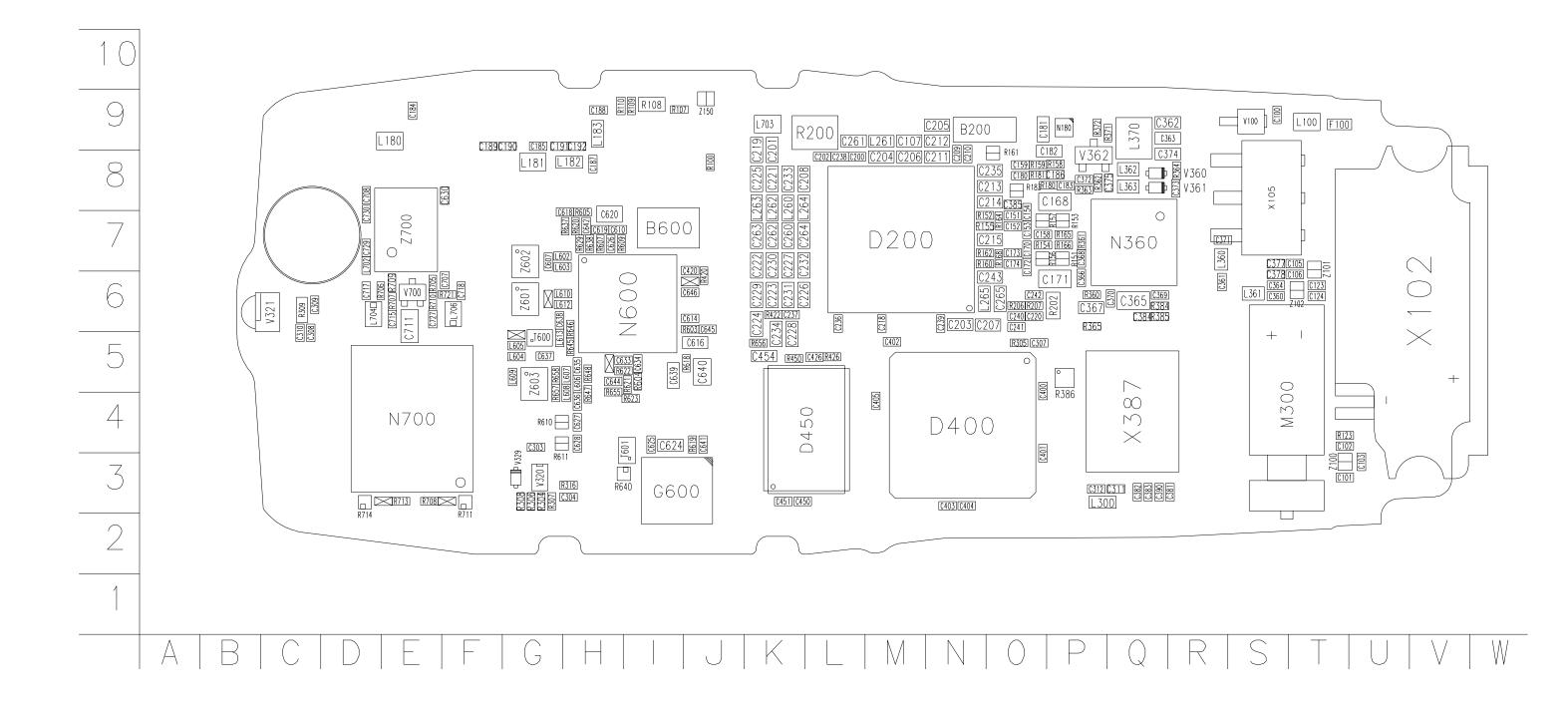


∨CORE\_LIN

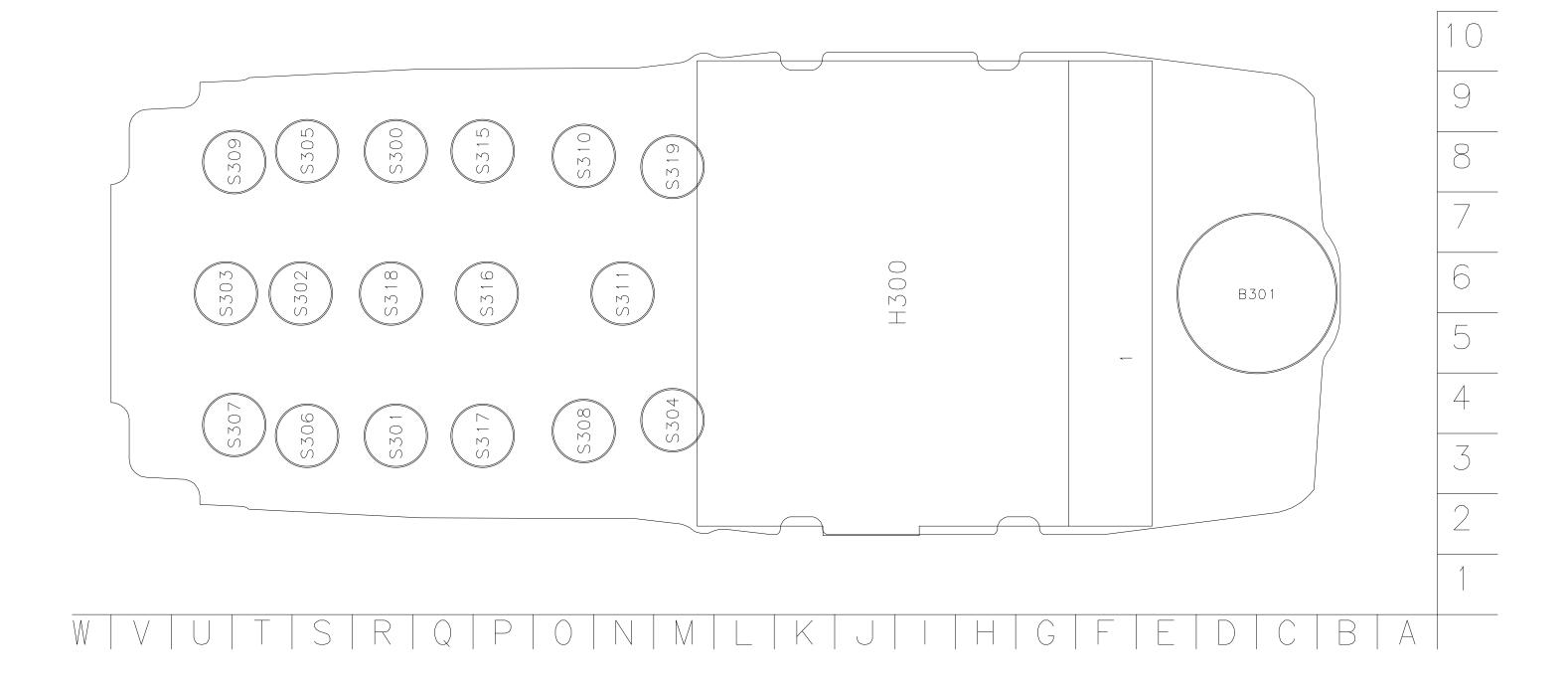


Audio and Global Ground v. 0.0 RH\*





## Component layout 1AW\_07a, bottom side



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#### Test Points, Baseband

