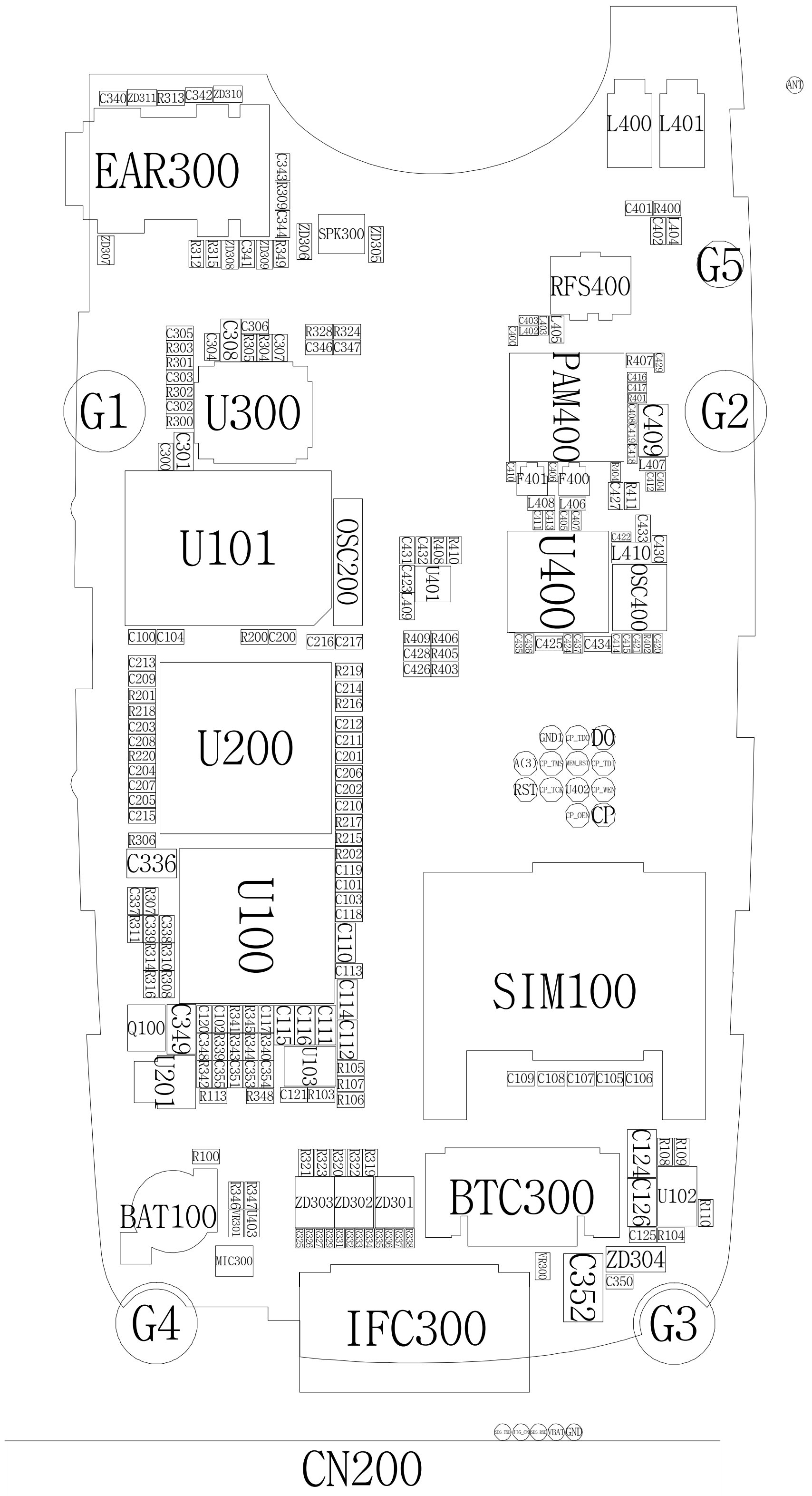
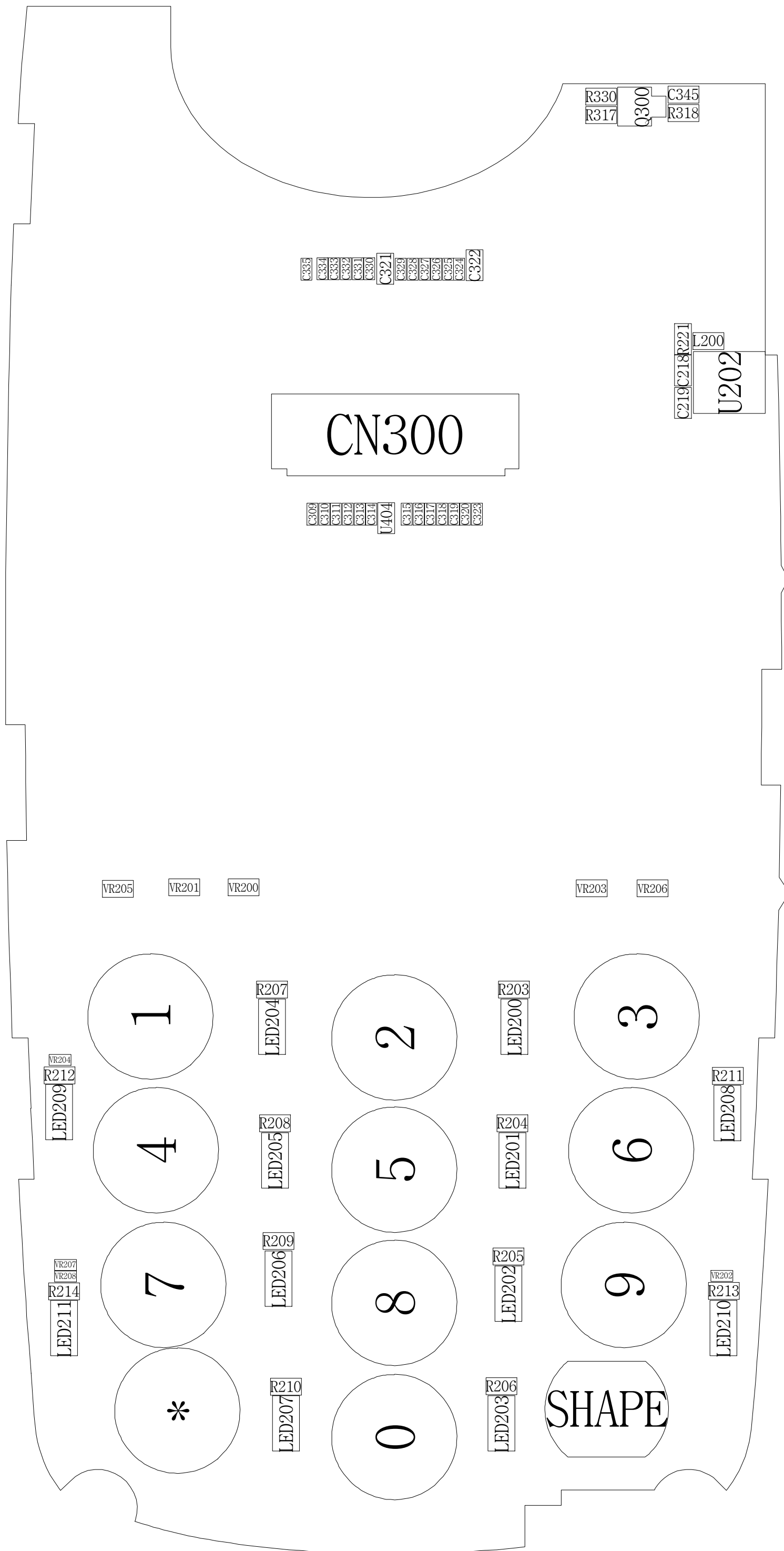
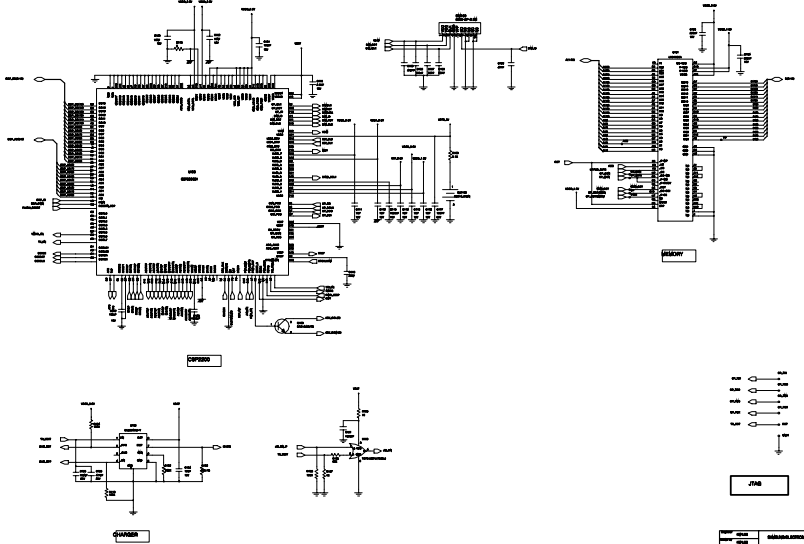


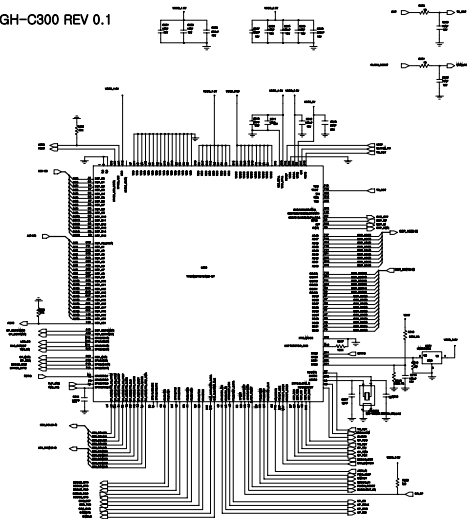




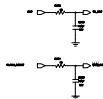


REV	0.1	DATE	2023-10-27
DESIGNED BY	SGH-C300	REV	0.1
CHECKED BY		DATE	
APPROVED BY		DATE	
PROJECT NAME	SGH-C300 REV 0.1		
REV	0.1	DATE	2023-10-27

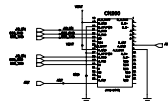
SGH-C300 REV 0.1



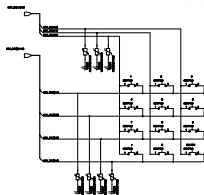
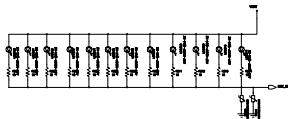
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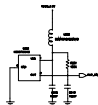
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EXT_FPGA



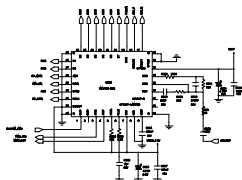
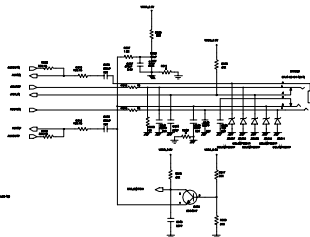
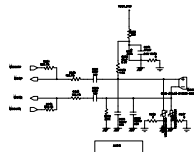
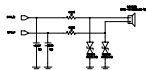
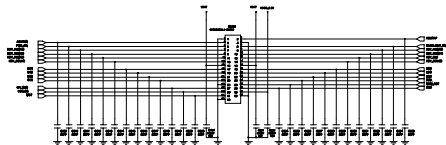
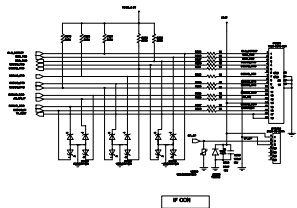
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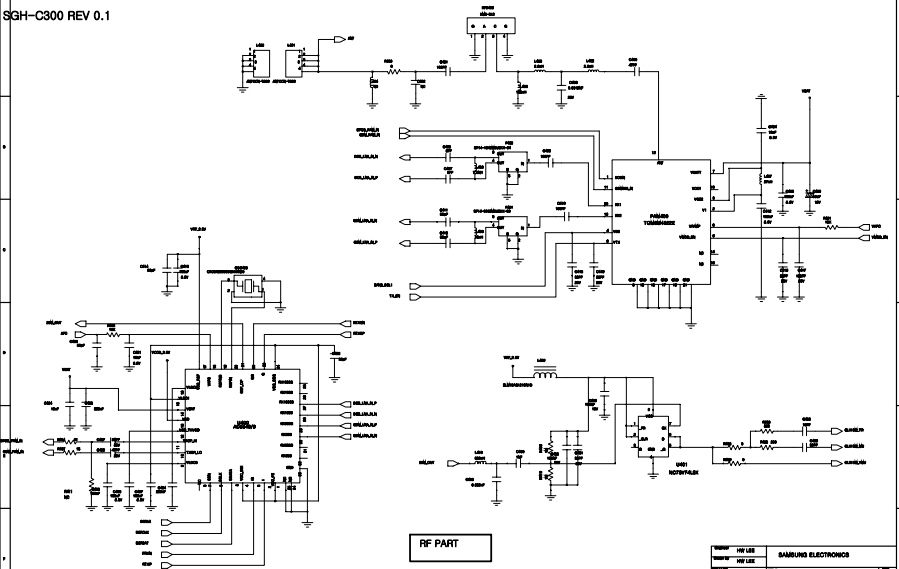


HALL IC

REV	DATE	DESCRIPTION
0.1	2008-01-10	Initial Release
0.2	2008-02-10	Added Hall IC
0.3	2008-03-10	Added Key
0.4	2008-04-10	Added Ext_FPGA
0.5	2008-05-10	Added Tracet
0.6	2008-06-10	Added Hole
0.7	2008-07-10	Added Key
0.8	2008-08-10	Added Key
0.9	2008-09-10	Added Key
1.0	2008-10-10	Added Key

SGH-C300 REV 0.1





RF PART

DESIGNER	HY LEE	SAMSUNG ELECTRONICS
REVIEWER	HY LEE	
APPROVED BY		
REVISION		
DATE		SGH-NUVO REV0.1