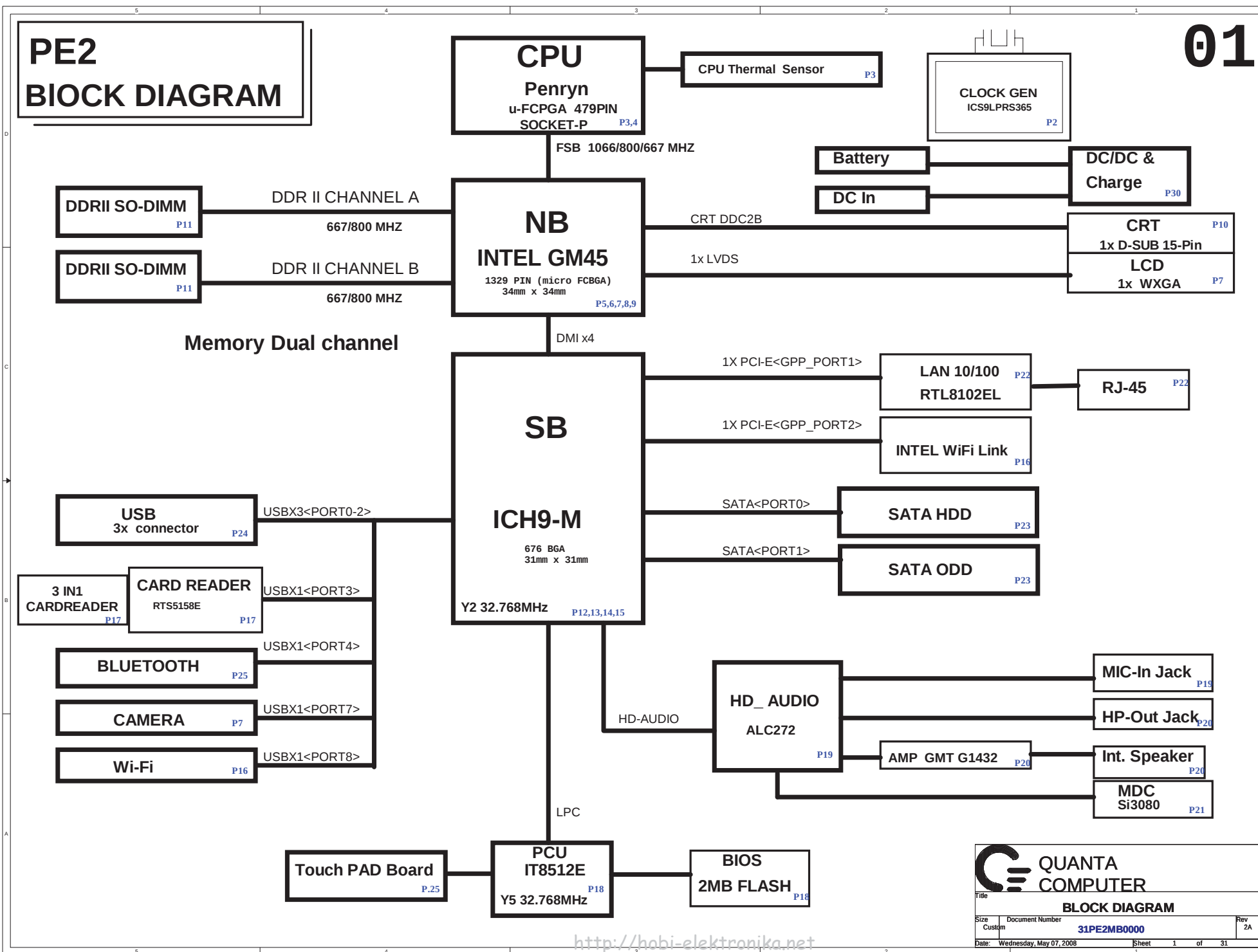


PE2 BLOCK DIAGRAM

01



EMI Reservation

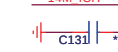
CLKCARD 48



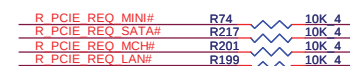
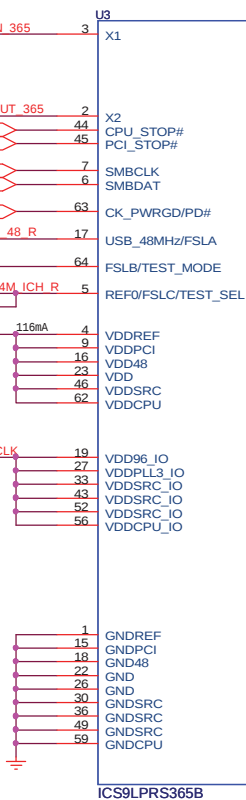
CLKUSB 48



14M ICH



FSB BSEL2	FSB BSEL1	FSA BSEL0	CPU	SRC	PCI	REF	USB	DOT	Spread %
0	0	0	266.66	100	33.33	14.318	48	96	0.5 Down
0	0	1	133.33	100	33.33	14.318	48	96	0.5 Down
0	1	0	200.00	100	33.33	14.318	48	96	0.5 Down
0	1	1	166.66	100	33.33	14.318	48	96	0.5 Down
1	0	0	333.33	100	33.33	14.318	48	96	0.5 Down
1	0	1	100.00	100	33.33	14.318	48	96	0.5 Down
1	1	0	400.00	100	33.33	14.318	48	96	0.5 Down
1	1	1	RESERVED						



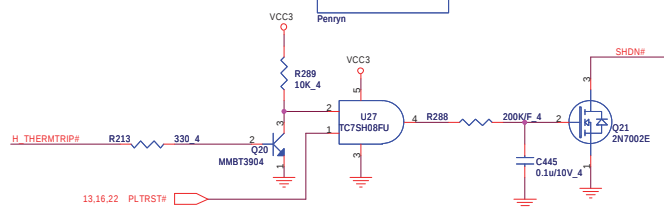
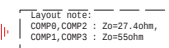
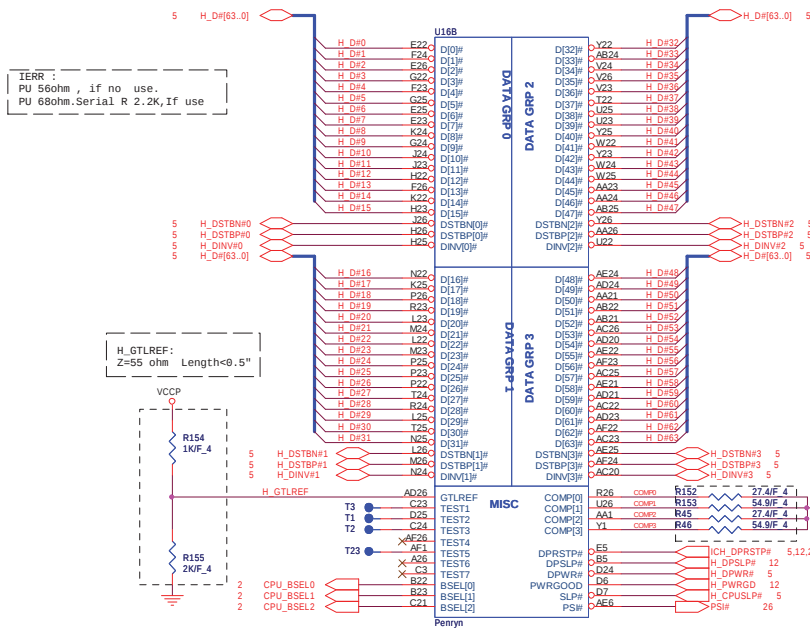
27 Select PIN13	PIN 20/21	PIN 24/25
* 0	DOT_96 / DOT_96#	LCDCLK / LCDCLK#
1	SRC_0 / SRC_0#	27M / 27M_SS

Byte0 Bit4: iAMT Enable/Disable Control

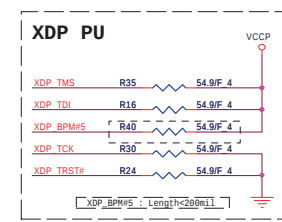
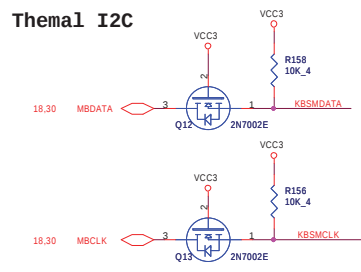
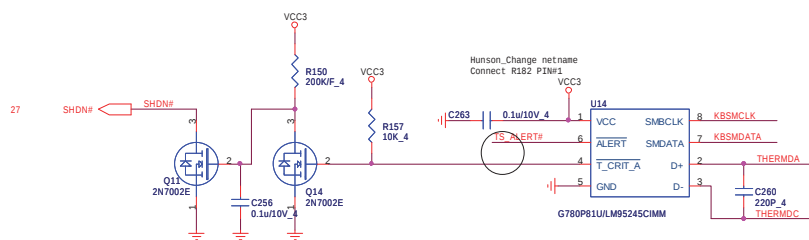
ITP_EN(PIN14)	PIN53/54
* 0	SRC8/SRC8#
1	ITP/ITP#

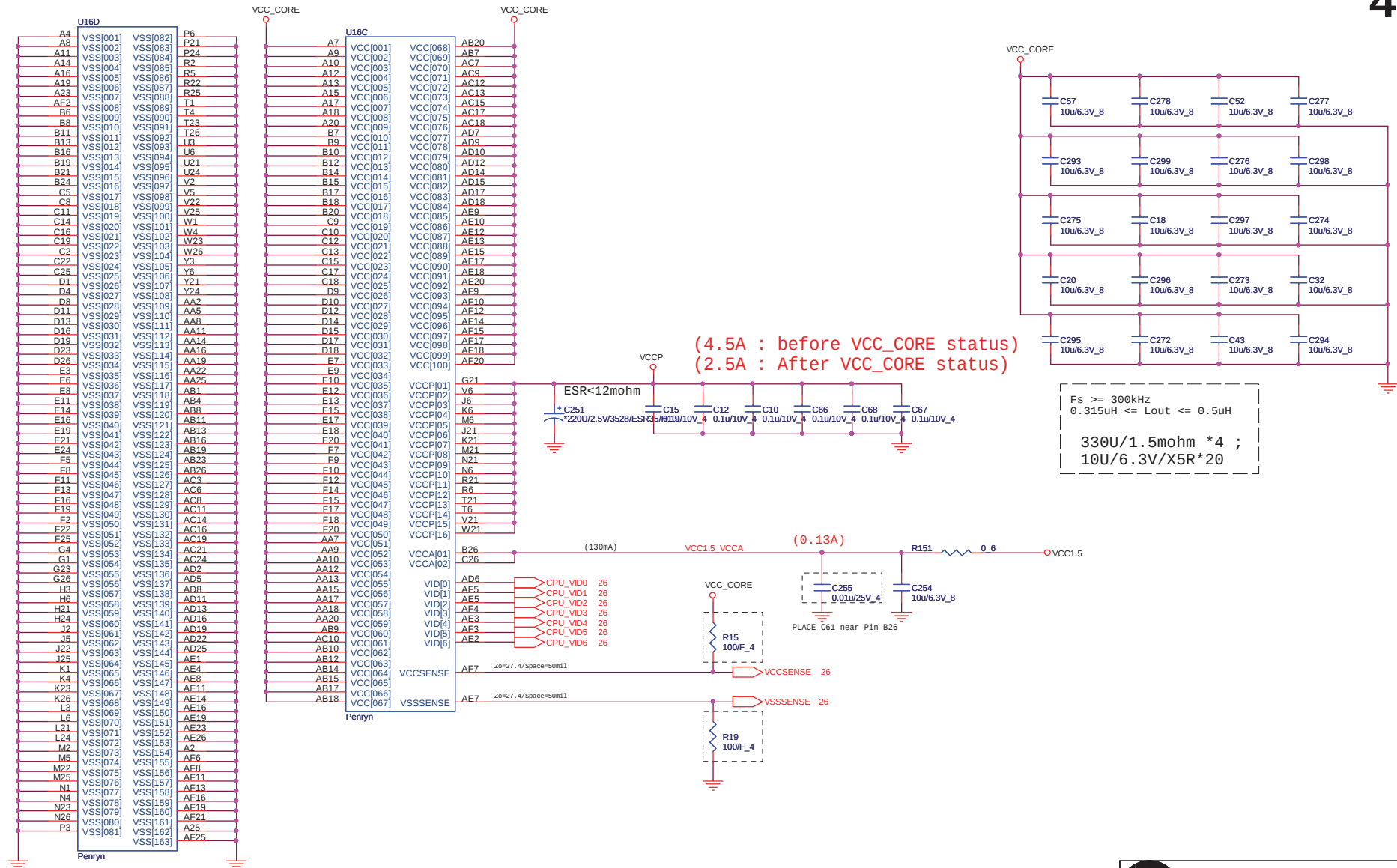
QUANTA
COMPUTER

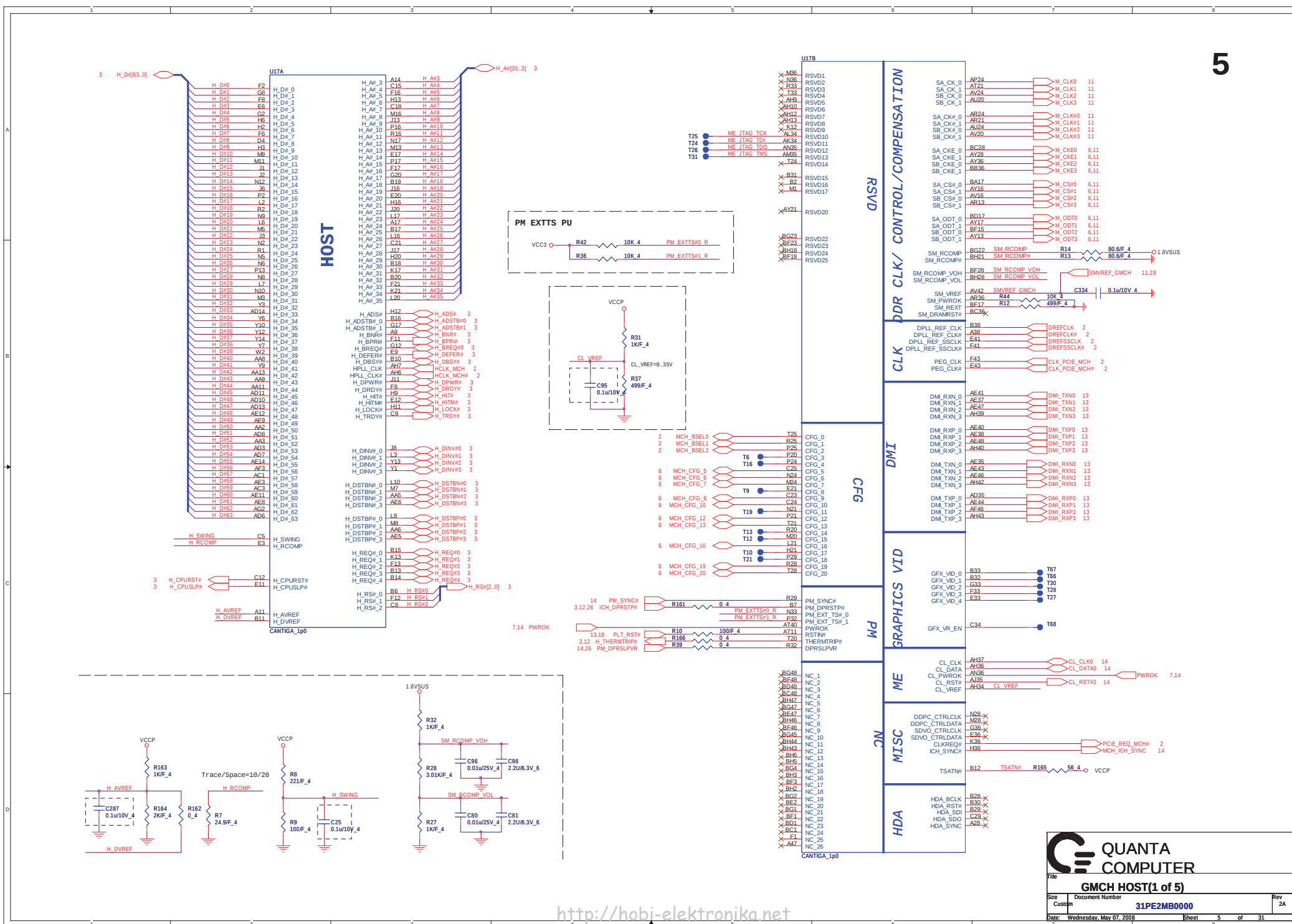
Title		
CLKGEN		
Size	Document Number	Rev
Custom	PE2 MAIN BOARD	2A
Date:	Wednesday, May 07, 2008	Sheet 2 of 31

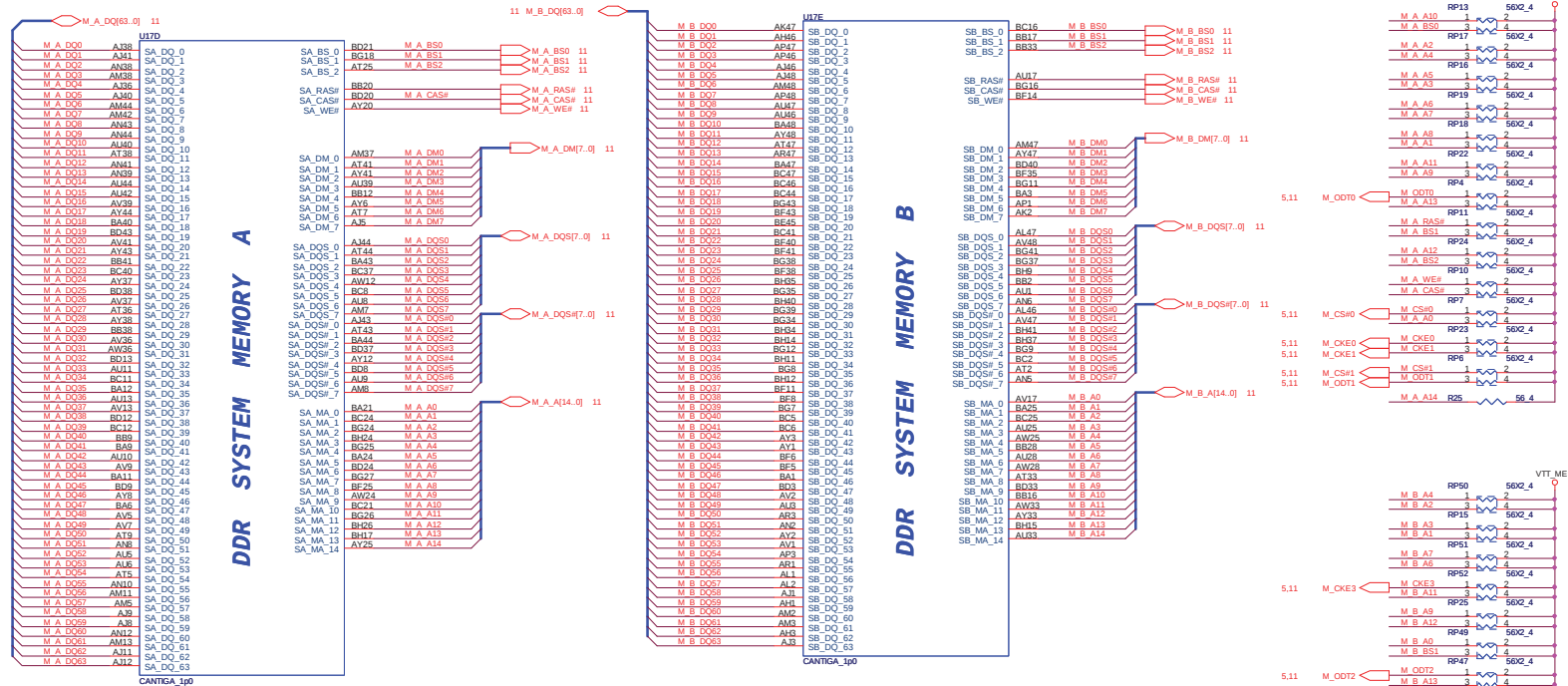


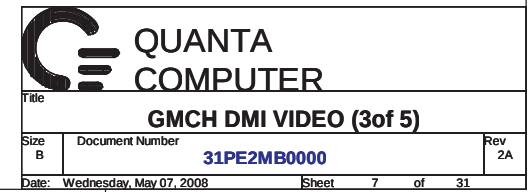
Thema1 I2C



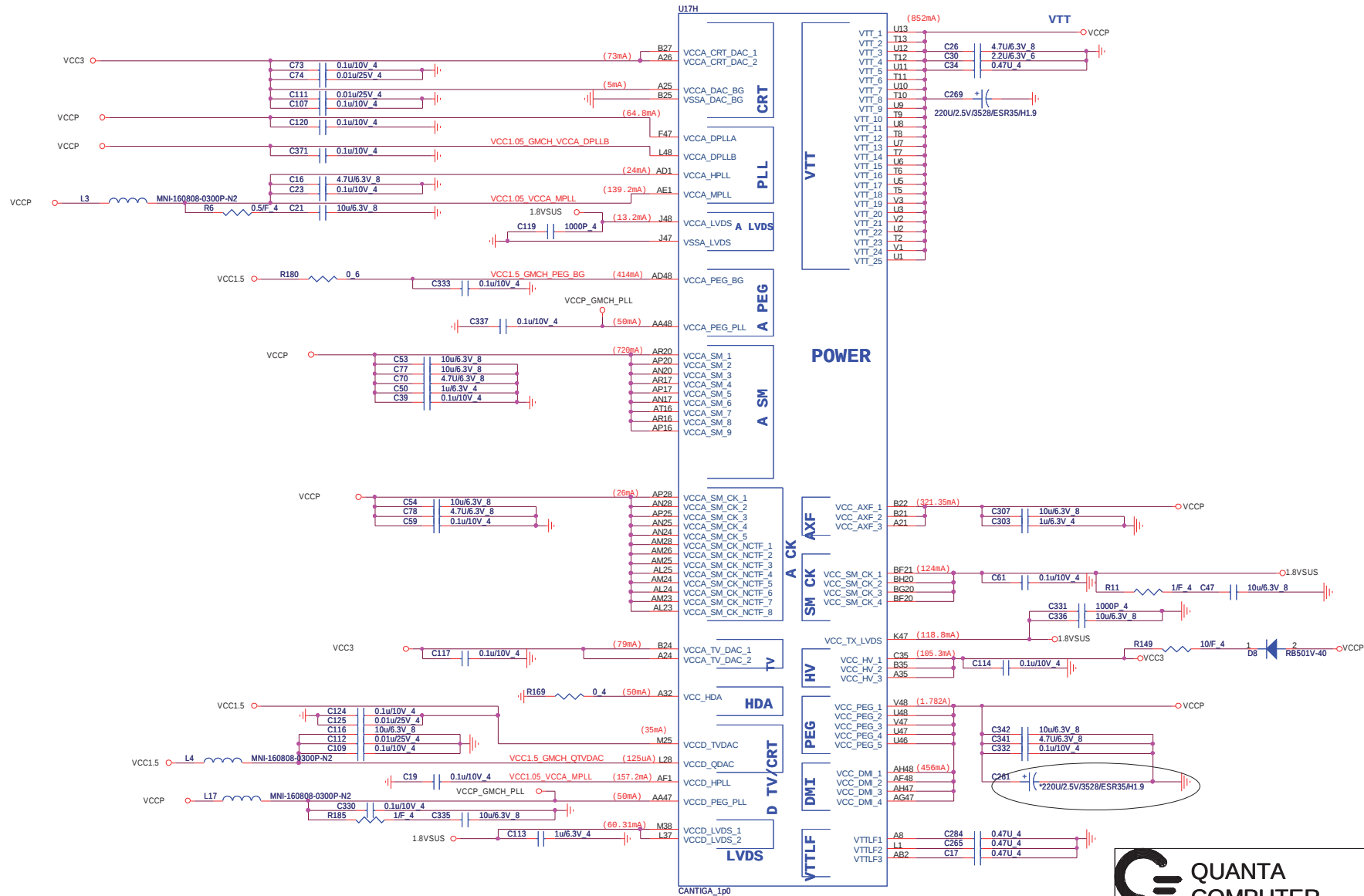




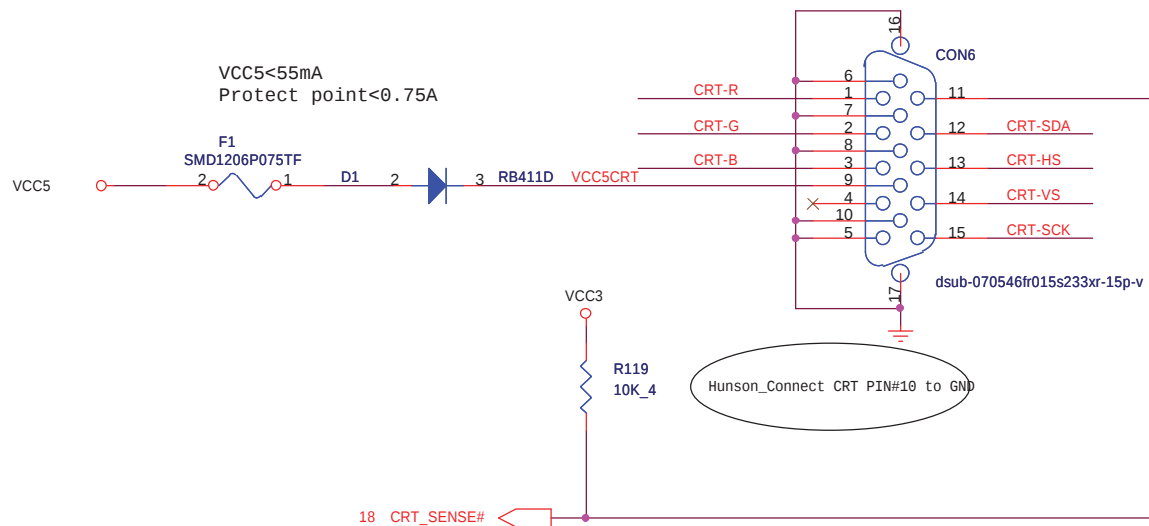
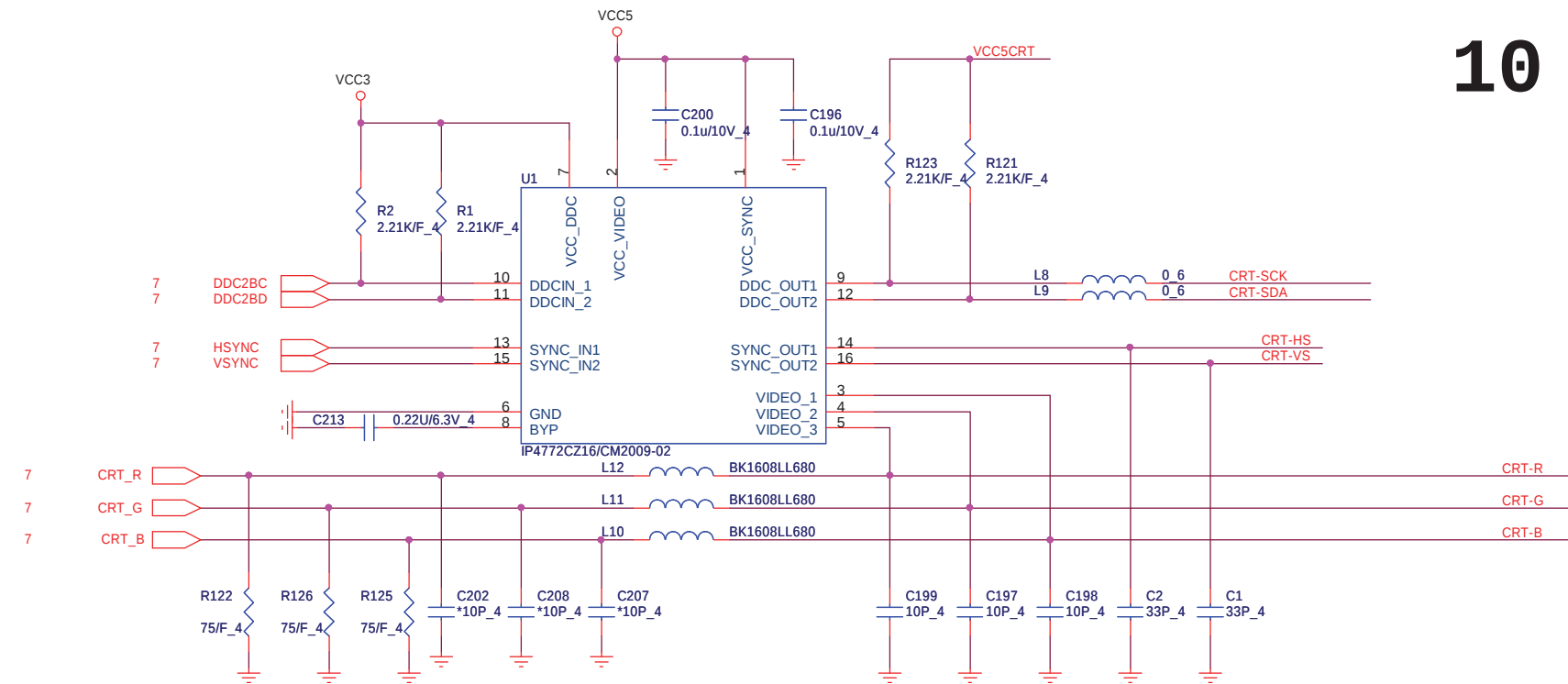




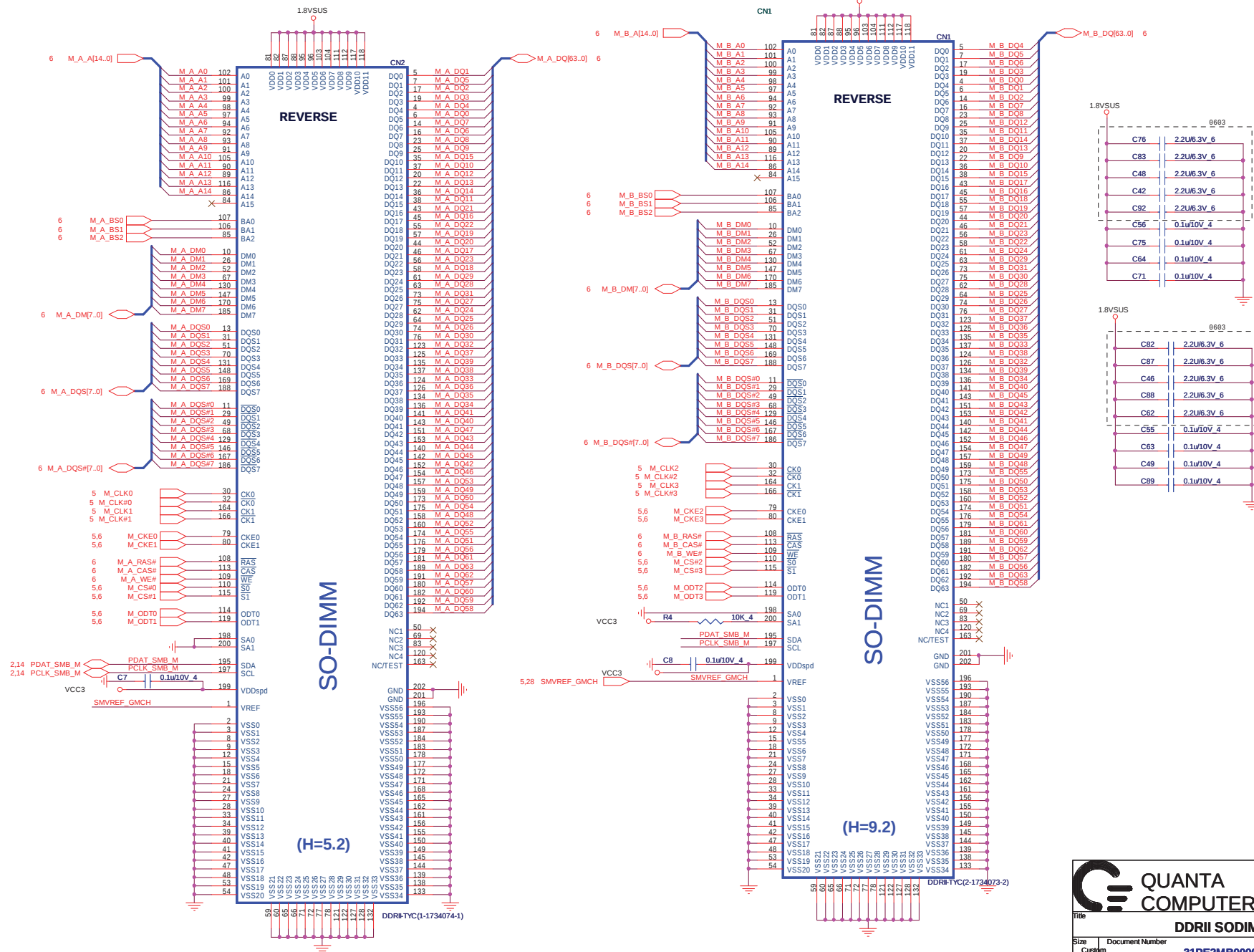




10



Title				CRT			
Size	Custom	Document Number	31PE2MB0000				Rev
							2A
Date:	Wednesday, May 07, 2008			Sheet	10	of	31



ML1220
AHL03014003



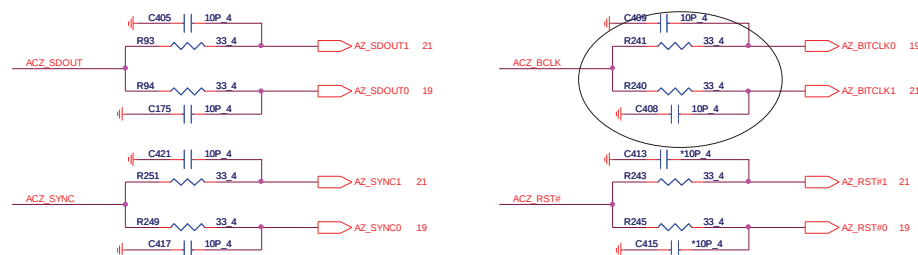
ICH_TP3	ACZ_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	* Normal operation
1	1	Set PCIe port config bit 1

```
Internal VRM Enable for Vccsus1_05,  
VccSus1_5 , VccCL1_5, vccLAN1_05,vccCL1_05)
```

	INTVRMEN
Enable (default)	1
Disable	0

Hunson_Change PN#CS43322FB15 (0402 SIZE)

HD to Audio Codec / Modem



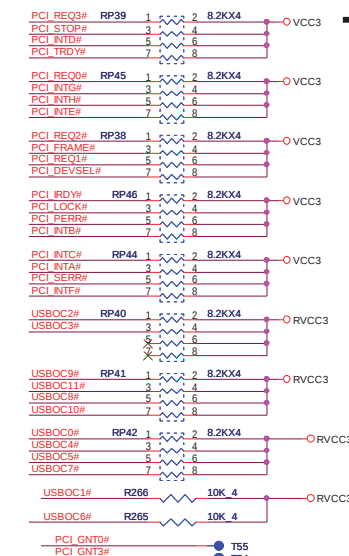
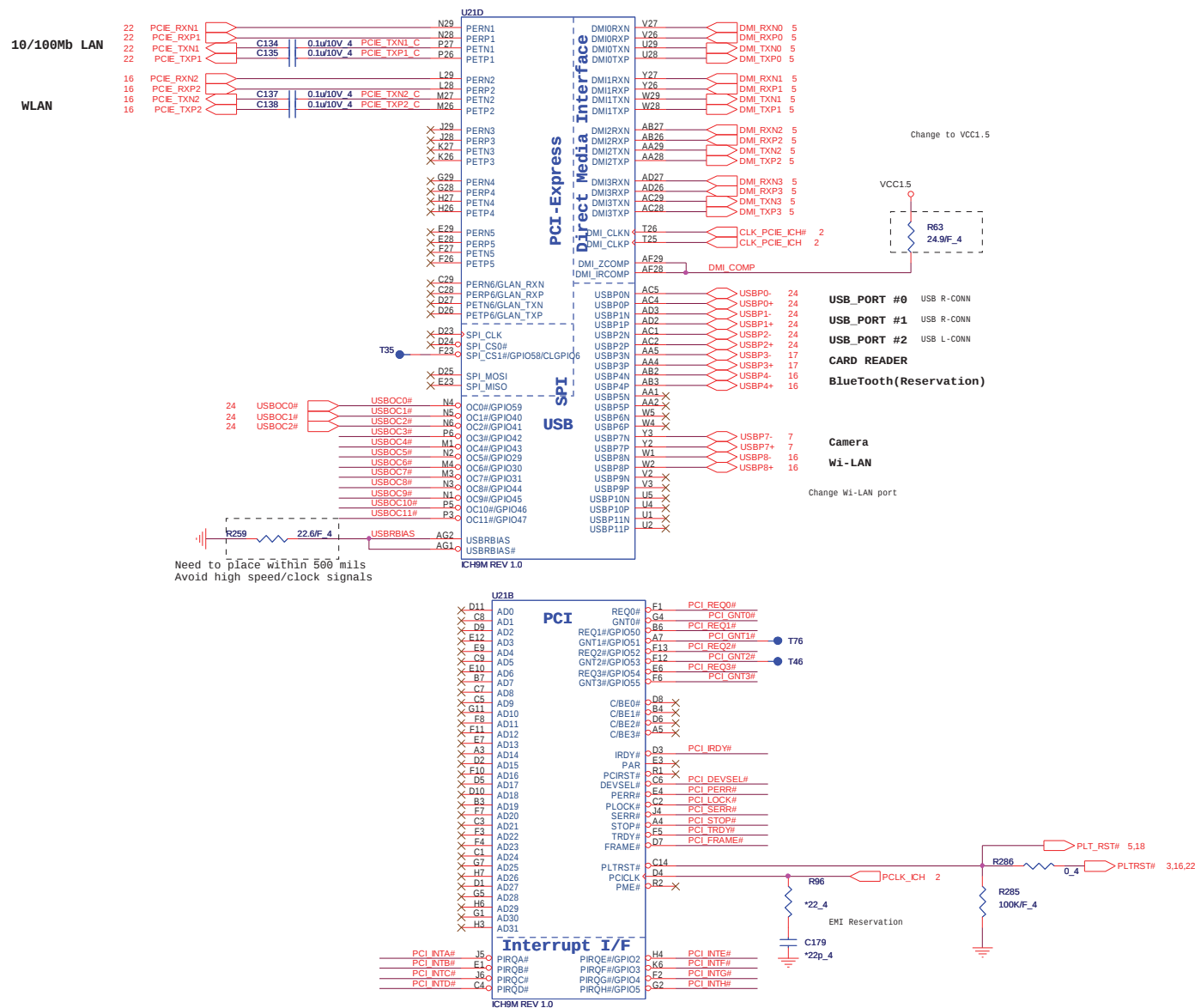
Hunson_Change R240, R241 to 68 ohm from 33ohm??
Check with EMI



Title ICH9-M HOST(1 of 4)

Size	Document Number
Custom	31PE2MB0000

Date: Wednesday, May 07, 2008 Sheet 12 of 31



GPIO49 : NC
GPIO20 : Can't pull high.

A16 SWAP Override strap

PCI_GNT#3	Low = A16 swap override enabled High = Default
-----------	---

ICH9 Boot BIOS select

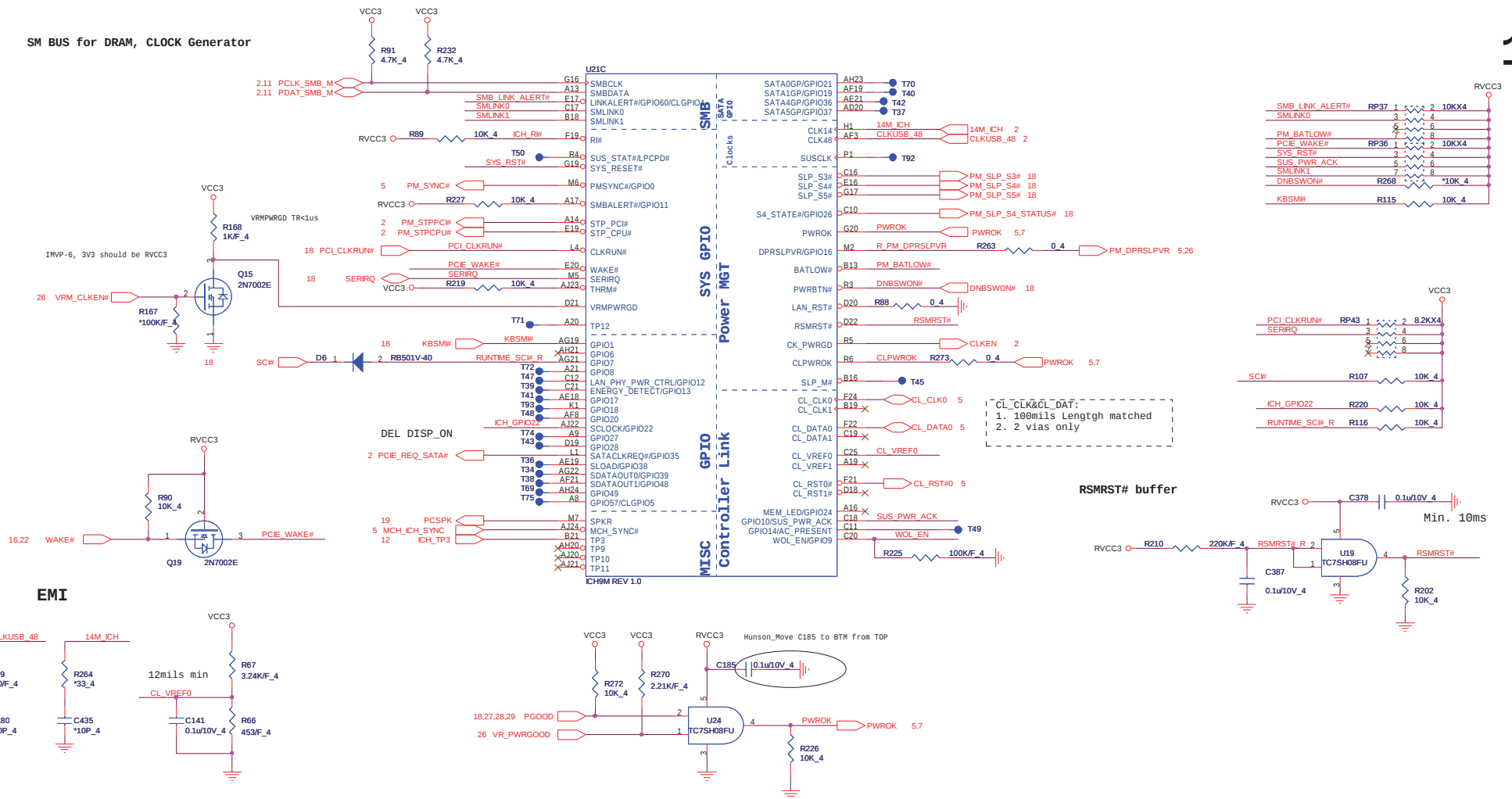
PCI_GNT#0	SPL_CS#1	Boot BIOS Location
0	1	SPI
1	0	PCI
1	1	LPC (Default)

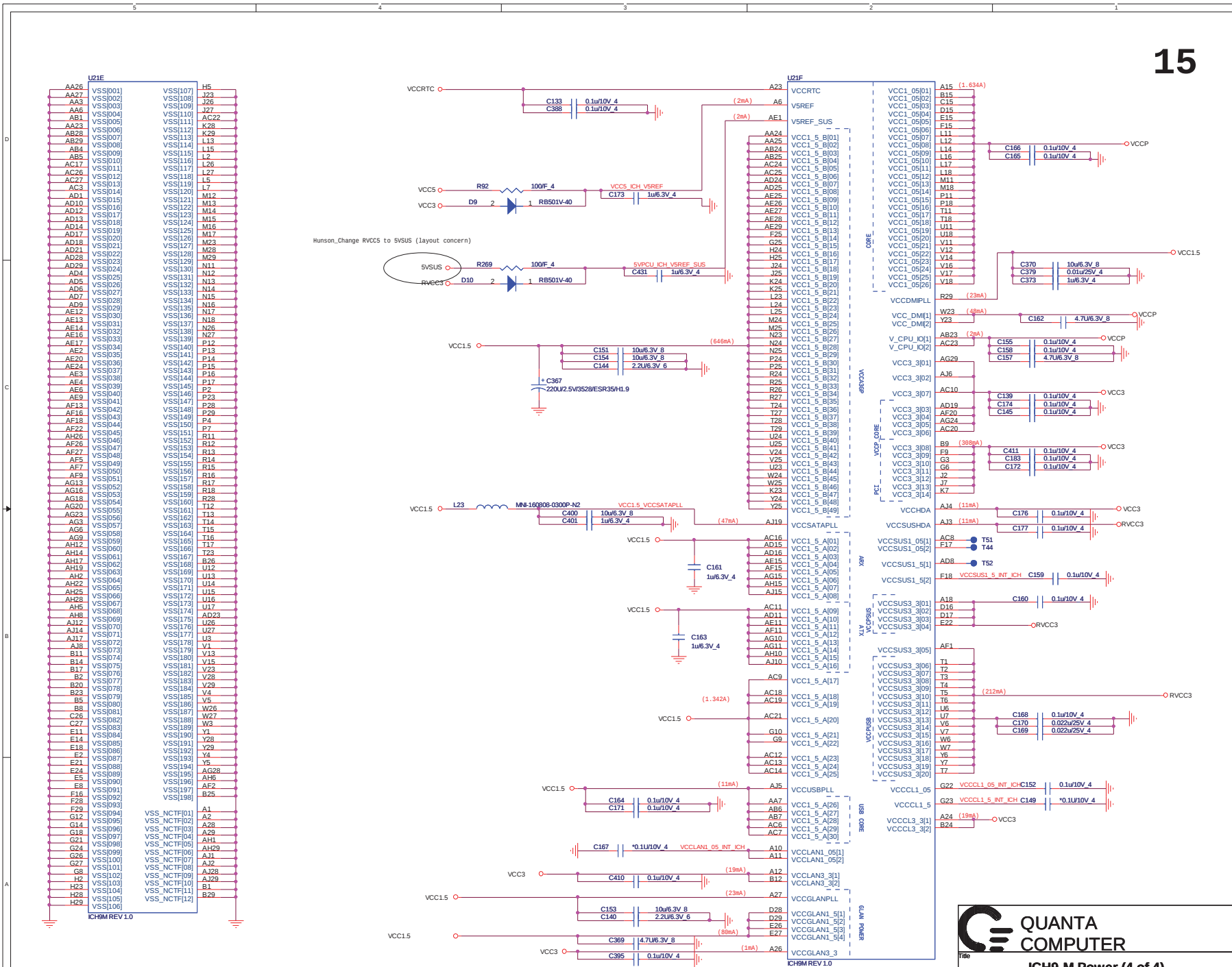
IRQ	Description
PIRQA	USB UHCI Controller #1, USB UHCI Controller #4
PIRQB	Audio, Modem, SMBus
PIRQC	USB UHCI Controller #3, SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	INT LAN, option for SCI, TC0, HPET #0, I, 2, R5C847
PIRQF	option for SCI, TC0, HPET #0, I, 2, R5C847
PIRQG	option for SCI, TC0, HPET #0, I, 2, R5C847
PIRQH	USB eHCI Controller, option for SCI, TC0, HPET #0, I, 2

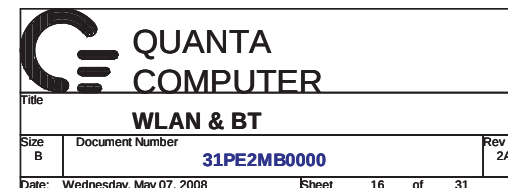


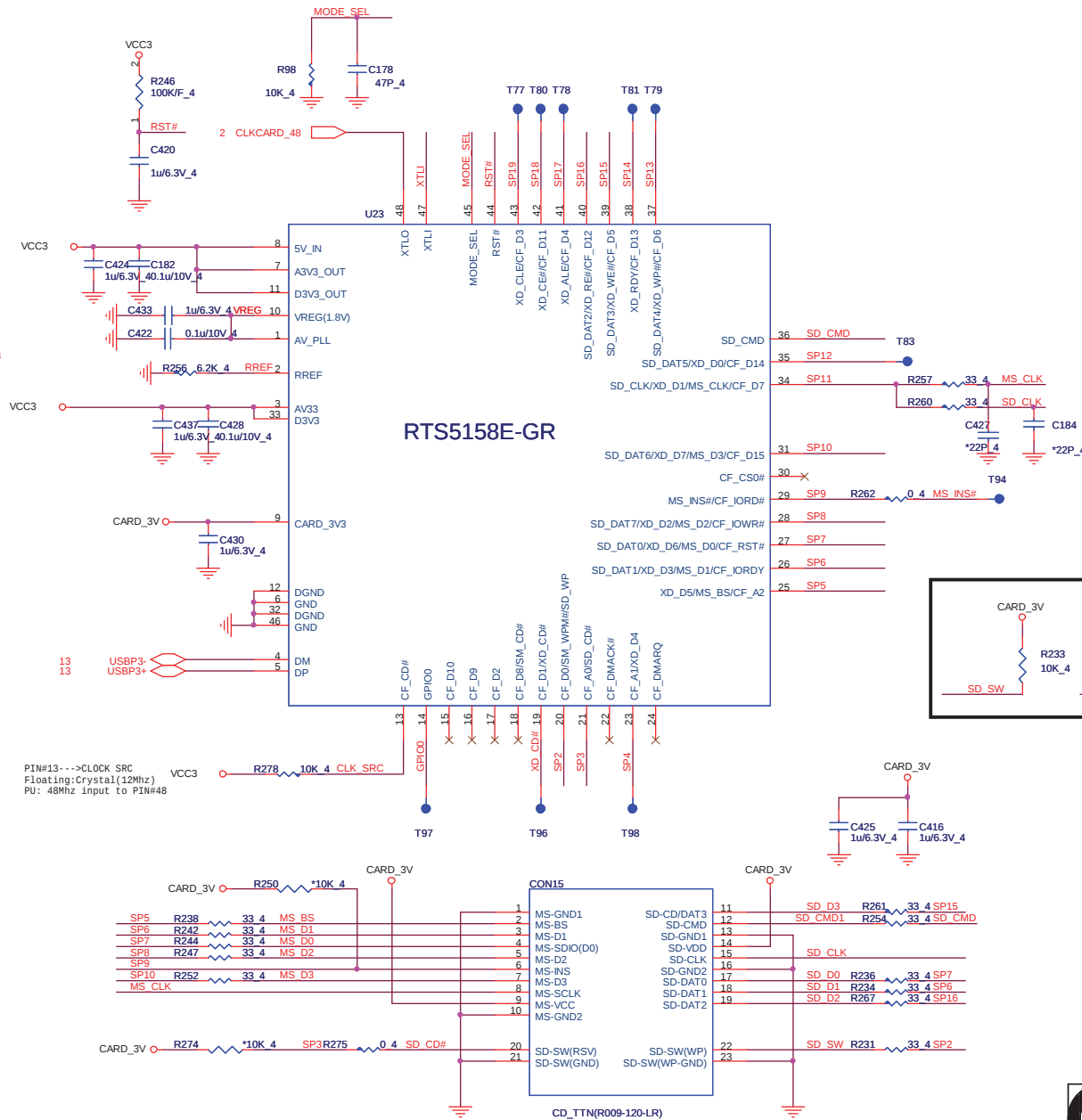
Title		
ICH9-M PCI-E, USB, PCI (2 of 4)		
Size	Document Number	Rev
Custom	31PE2MB0000	2A
Date:	Wednesday, May 07, 2008	Sheet 13 of 31

SM BUS for DRAM, CLOCK Generator

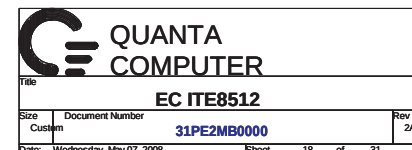


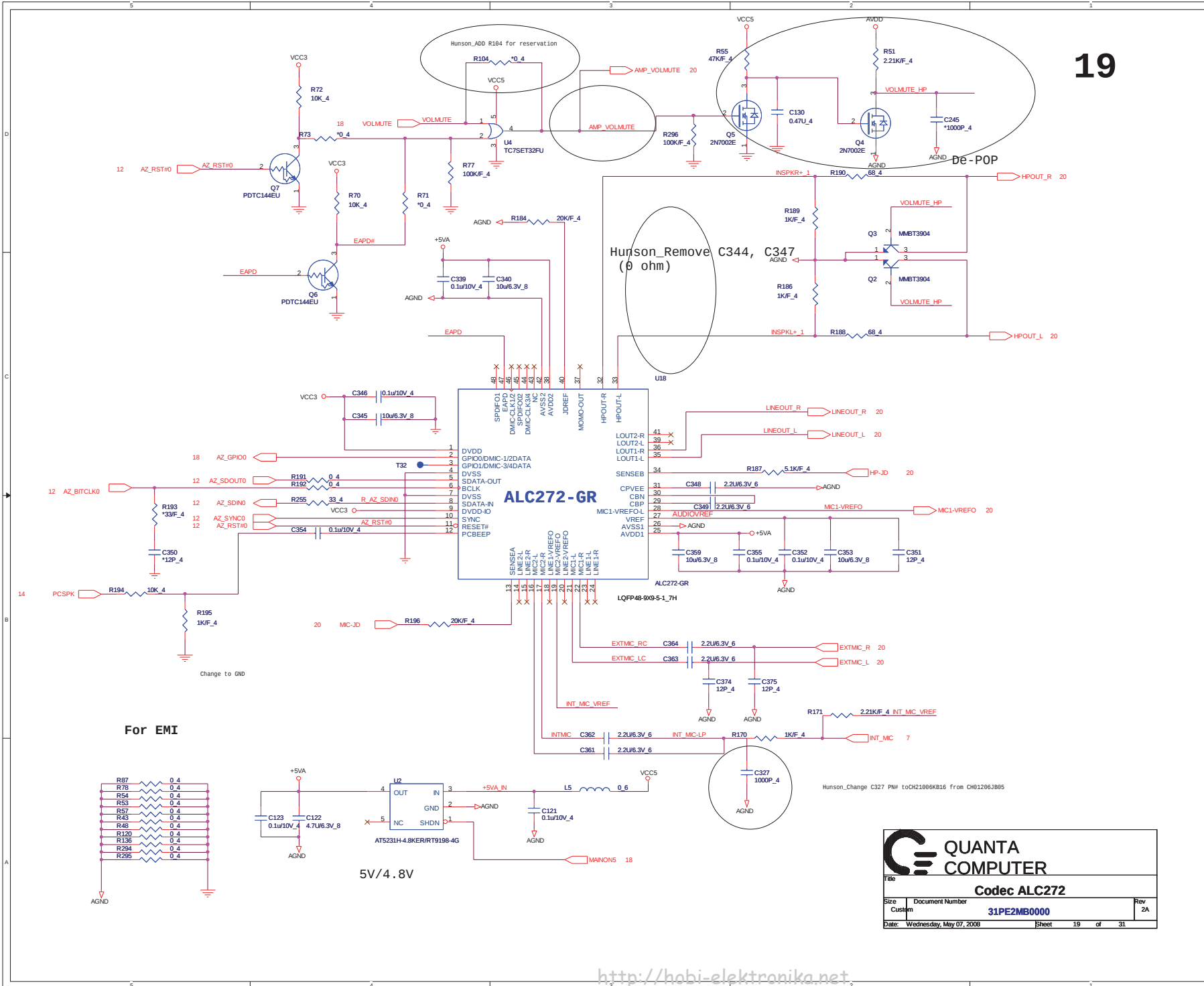




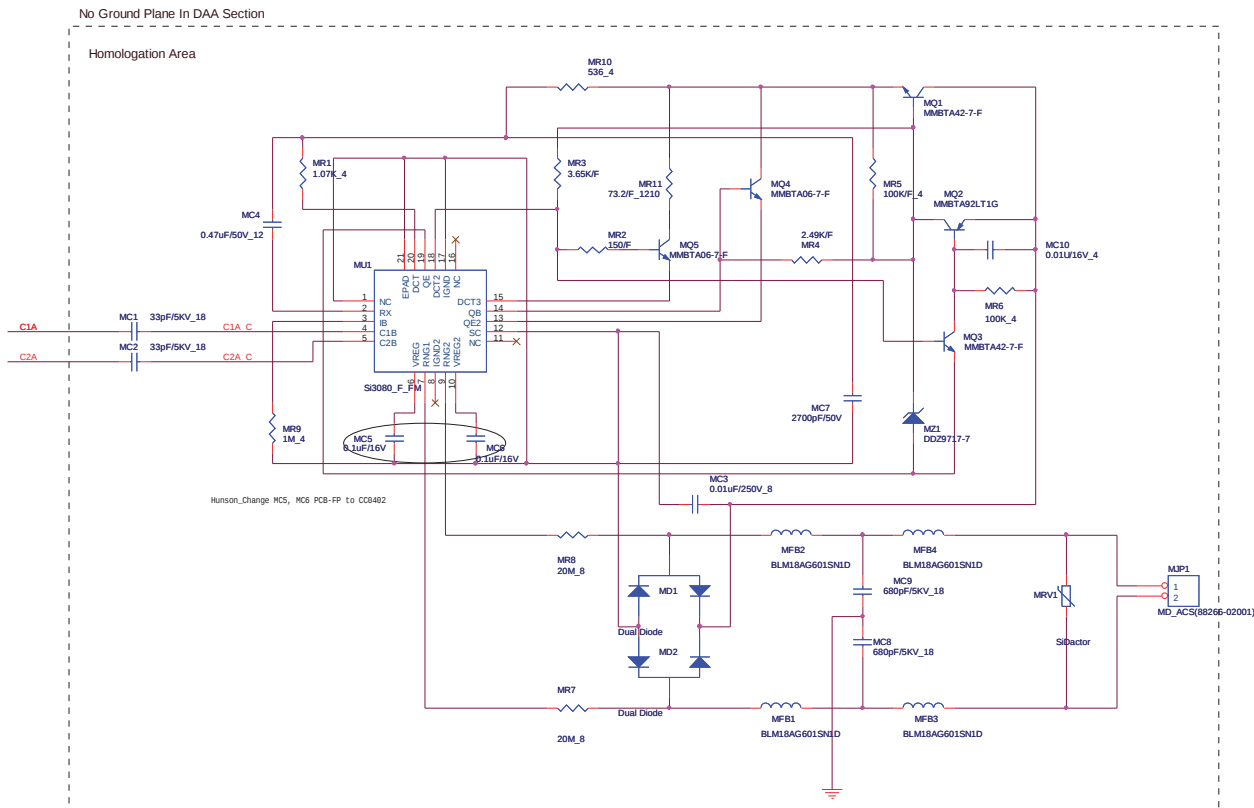


2ND:MLX(47265-0001)
Supporting MMC/SD/MS Cards

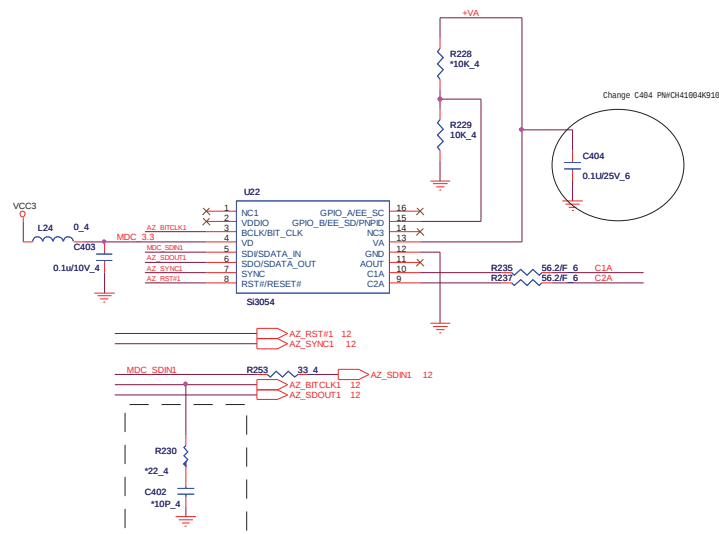




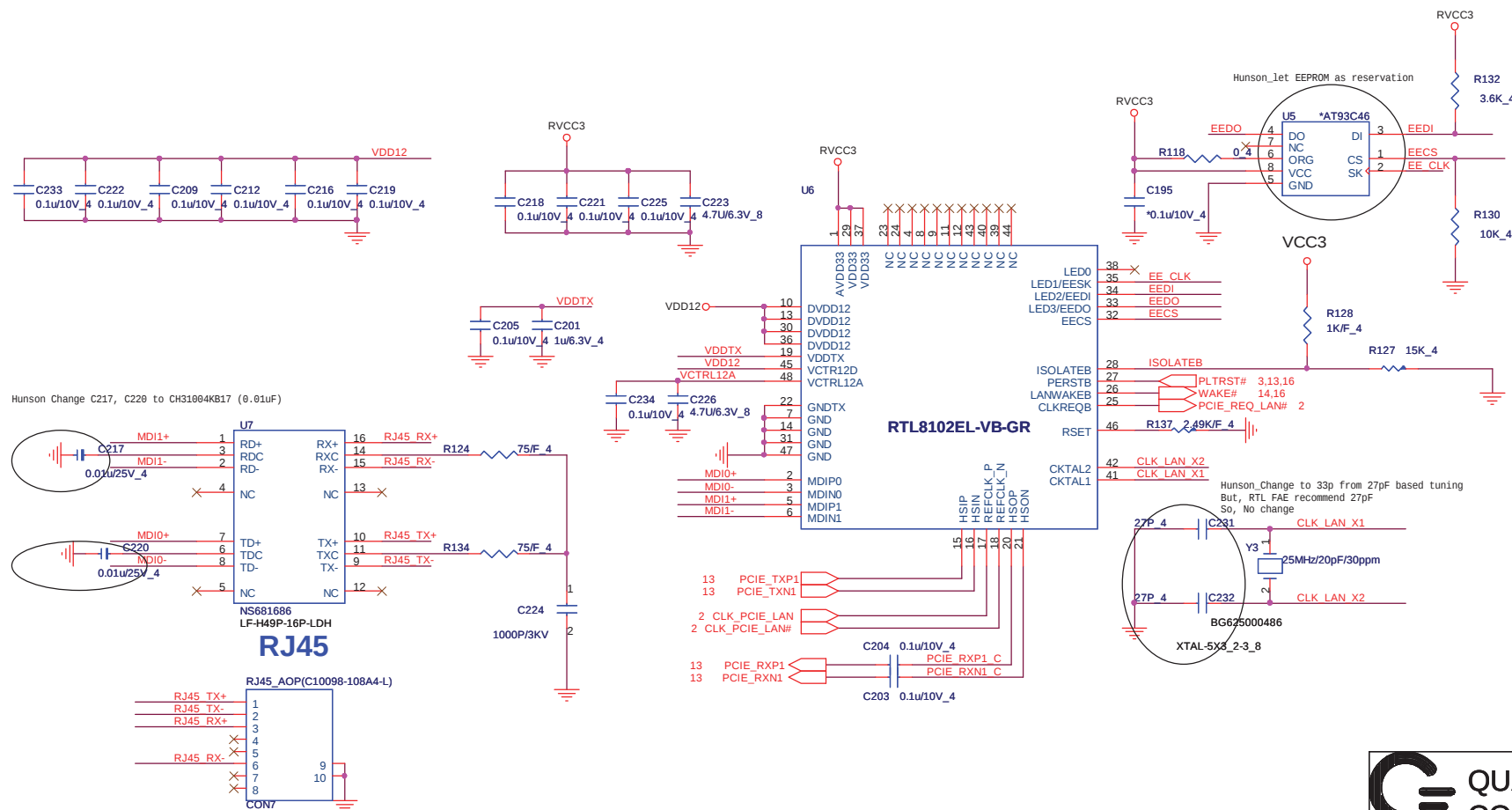




SILICON LABORATORIES CONFIDENTIAL

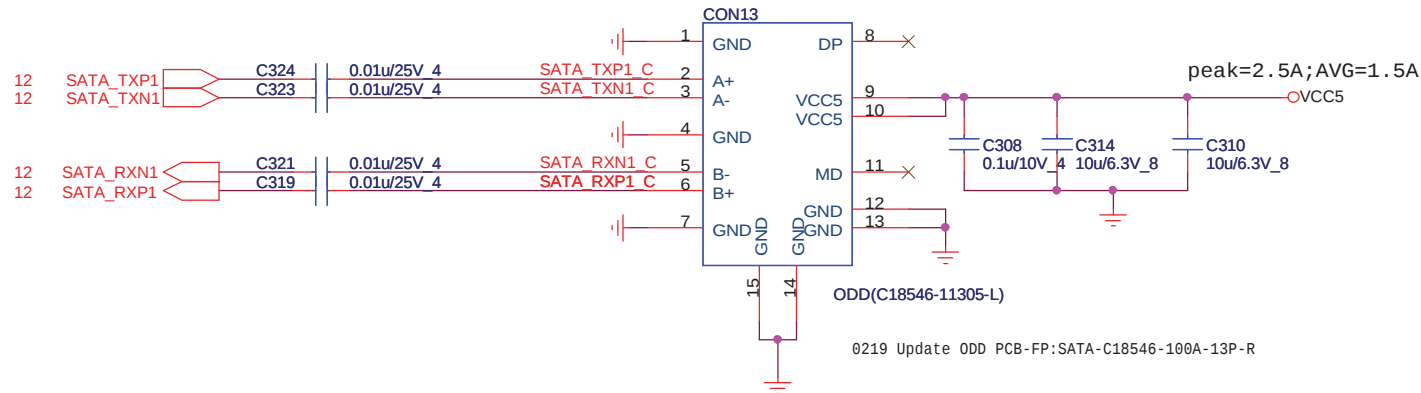


QUANTA COMPUTER		
SI3080 and MDC1.5		
File	Document Number	Rev
Size	31PE2MB0000	2A
Date: Wednesday, May 07, 2008	Sheet 21 of 31	

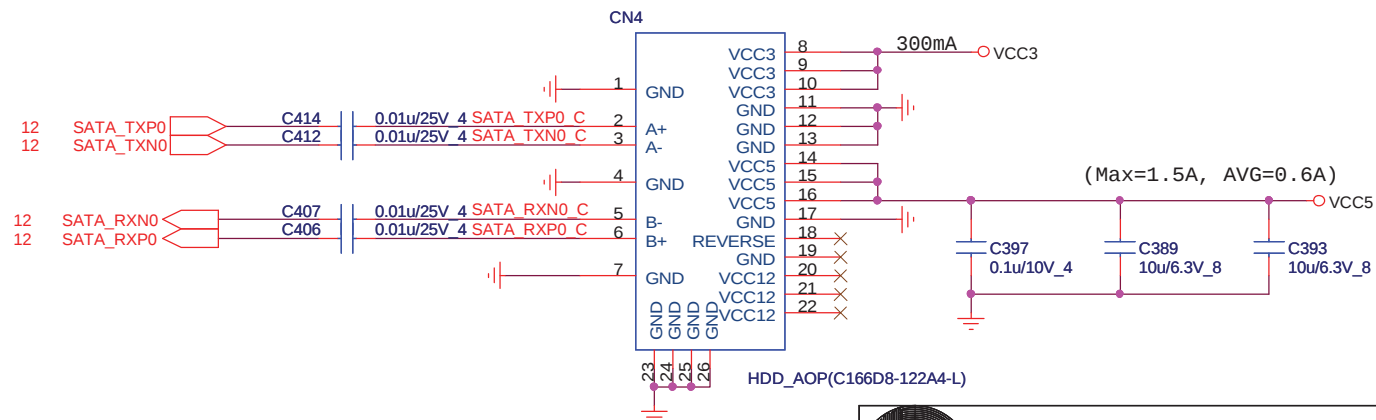


ODD CONNECTOR

23



HDD CONNECTOR



QUANTA
COMPUTER

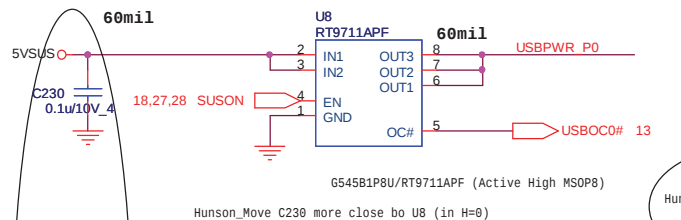
Title		
HDD, ODD		
Size	Document Number	Rev
Custom	31PE2MB0000	2A
Date: Wednesday, May 07, 2008		
Sheet 23 of 31		

<http://hdd-electronics.net>

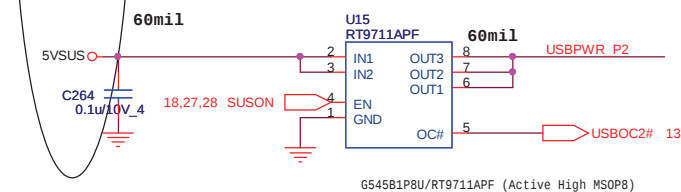
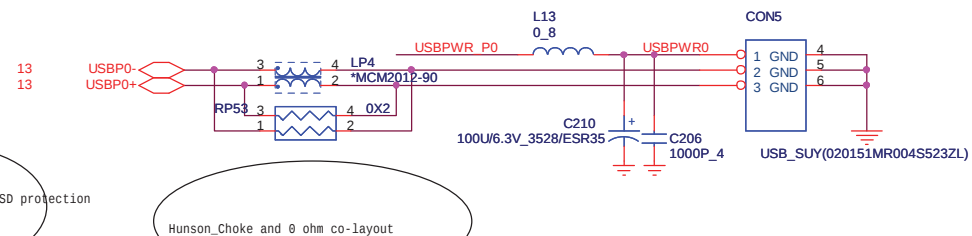
USB Connector

USB Power and Over current

Change from RVCC5 to 5VSUS



Hunson_Remove USB ESD protection



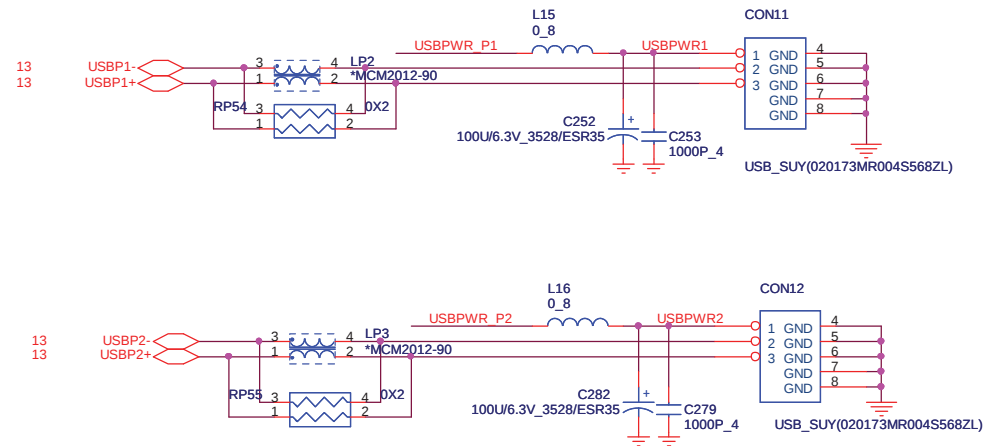
Hunson_ADD Poly switch for LPS

F2
SMD1812P260TF

POLY SWITCH 2.6A

RVCC5

5VSUS



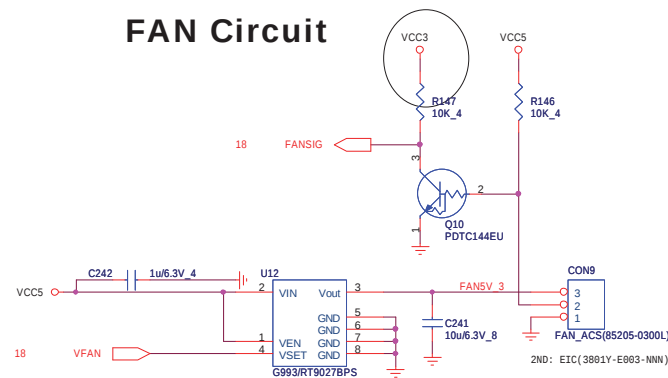
QUANTA
COMPUTER

Size Document Number
Custom 31PE2MB0000

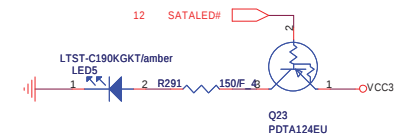
Date: Wednesday, May 07, 2008 Sheet 24 of 31

Rev 2A

FAN Circuit



HDD LED



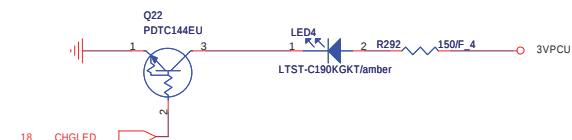
POWER STATUS LED

Power LED-->Low Active

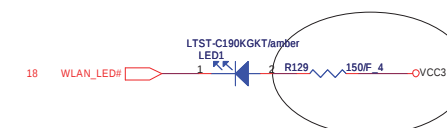


CHARGE LED-->High Active

BATTERY CHARGE LED

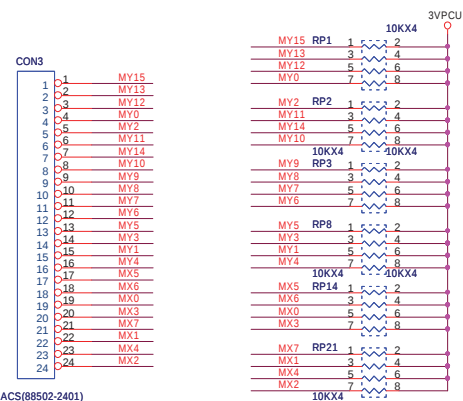
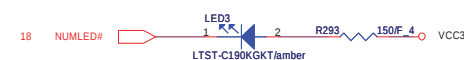


Wi-Fi CHARGE LED



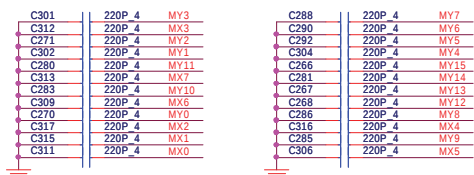
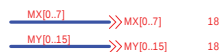
Hunson_Wake on WiLAN not supported

NumLED

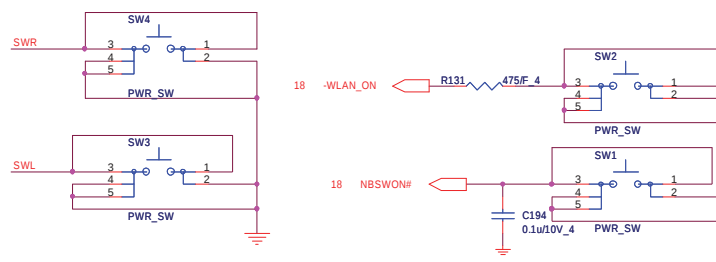
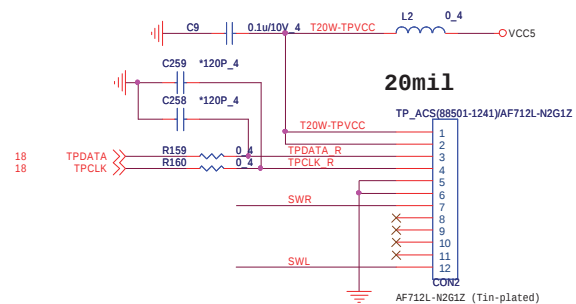


KB_ACS(88502-2401)

2ND:PTI(AFN240-N2G1V-P)



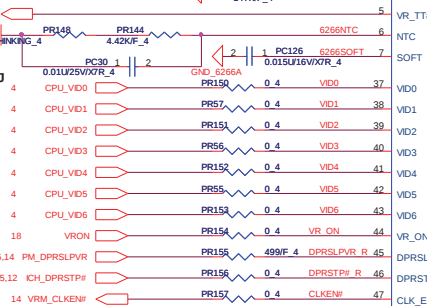
TOUCHPAD BOARD CON



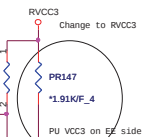
CPU_CORE

Throttling temp.
105 degree C

Panasonic
ERT-J0EV474J



ISL6262A



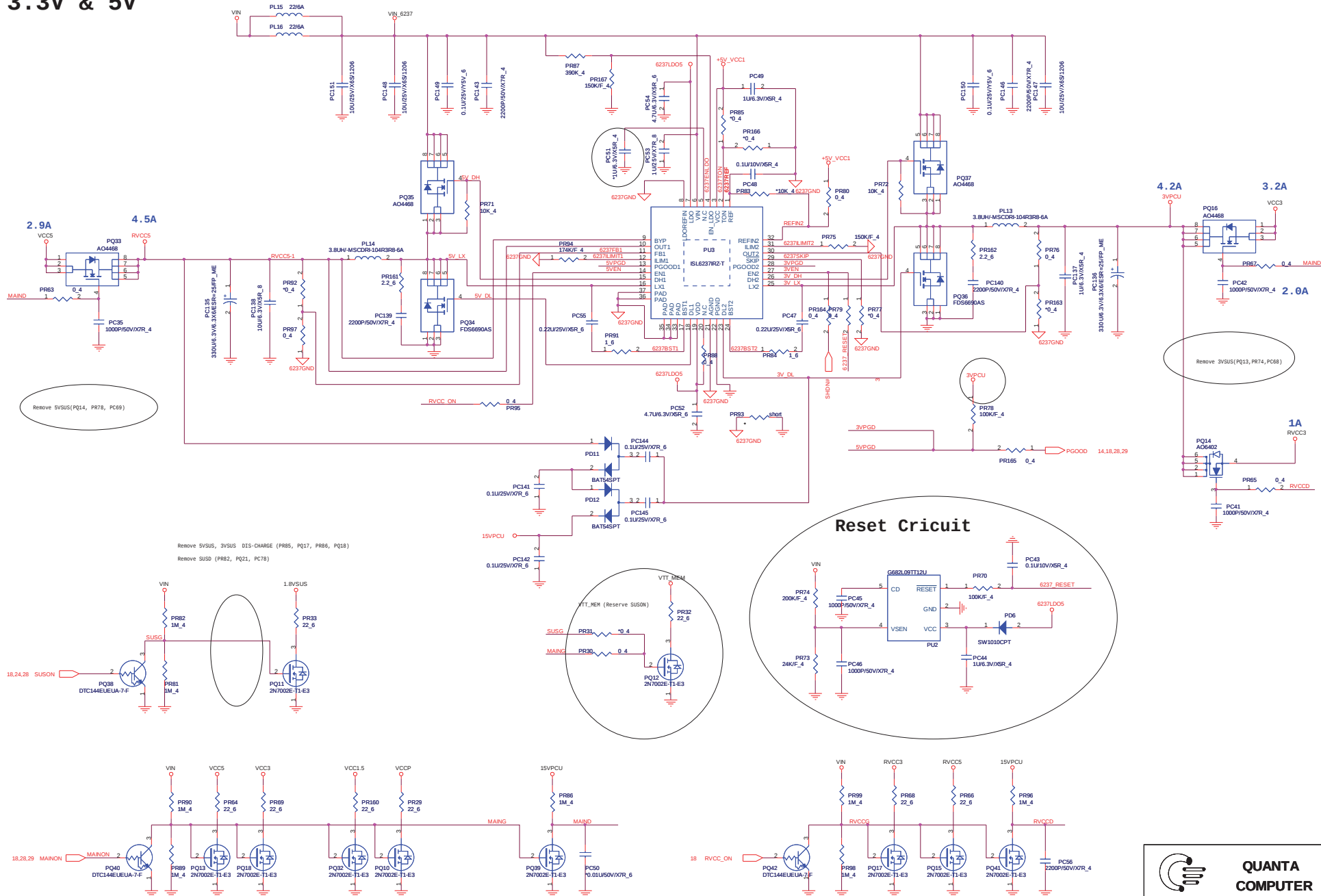
Close to Phase 1 Inductor

Parallel

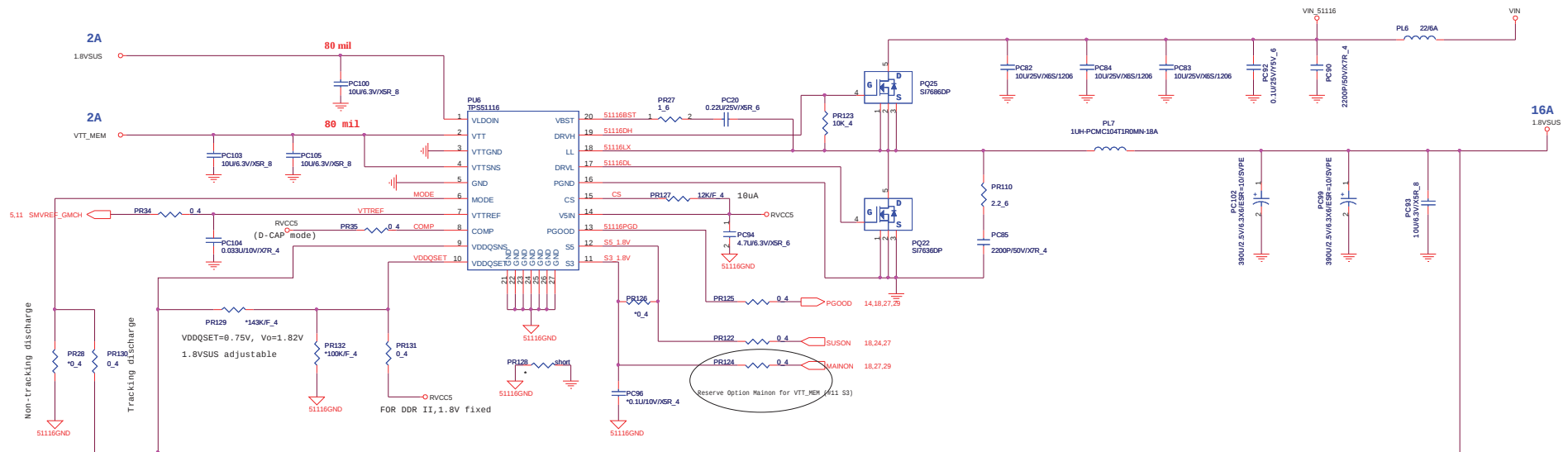
**QUANTA
COMPUTER**

Size	Document Number	Rev	2A
PE2 MAIN BOARD			
Date:	Wednesday, May 07, 2008	Sheet	26 of 31

<http://hobi-elektronika.net>



1.8VSUS & VTT_MEM



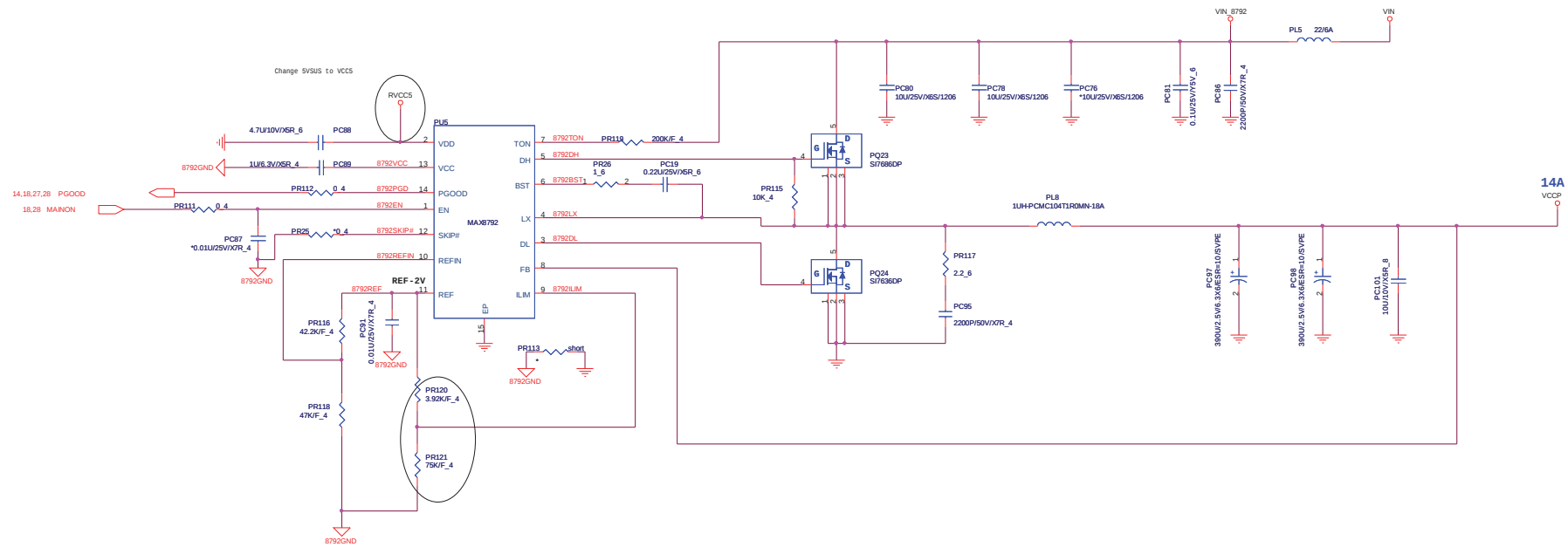
MODE	DISCHARGE MODE
+5V	No discharge
+1.8V	Tracking discharge
GND	Non-tracking discharge

VDDQSET	VDDQ(V)	VTTREF & VTT	NOTE
GND	2.5 fixed	VDDQSNS/2	DDR
5V	1.8 fixed	VDDQSNS/2	DDR2
FB-Resistor	Adjustable	VDDQSNS/2	1.5V<VDDQ<3V

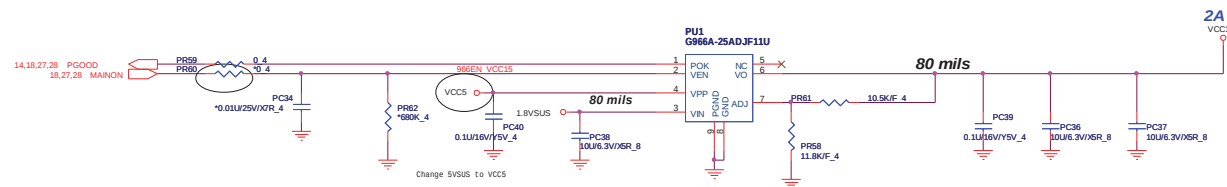
$$V_{TT} = V_{TTREF} = V_{DDQ_{SNS}}/2 = 0.9V$$

STATE	S3	S5	1.8VSUS	VTTREF	VTT
S0	1	1	on	on	on
S3	0	1	on	on	off
S4/S5	0	0	off	off	off

VCCP



VCC1.5



QUANTA
COMPUTER

Size	Document Number	Rev
	PE2 MAIN BOARD	2A
Date	Wednesday, May 07, 2008	Sheet 29 of 31

