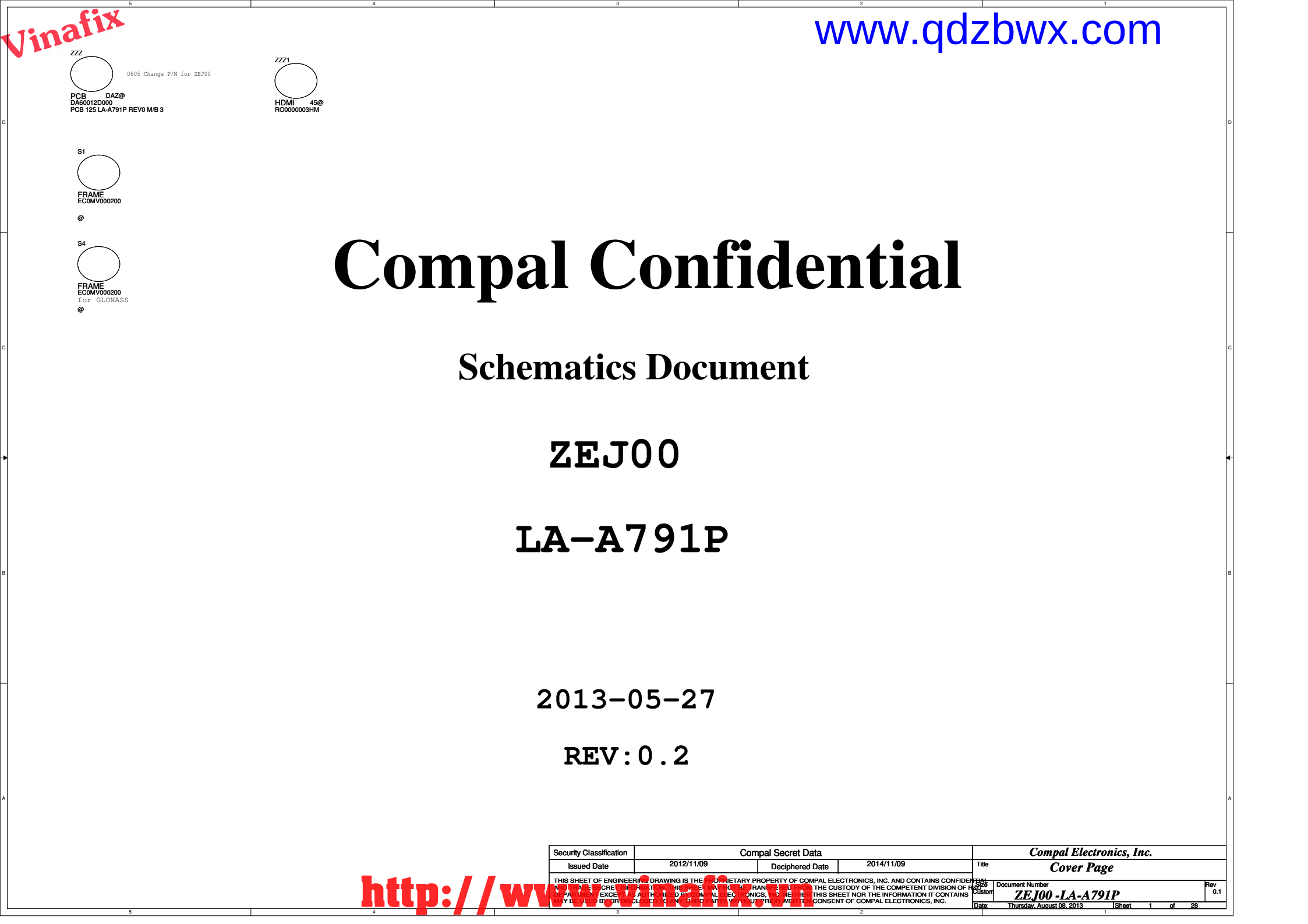




Vinafix

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ZZZ
0605 Change P/N for ZEJ00
PCB DAZ@
DA60012D000
PCB 125 LA-A791P REV0 M/B 3

ZZZ1
HDMI 45@
RC0000003HM

S1
FRAME
ECOMV000200
@

S4
FRAME
ECOMV000200
for GLONASS
@

Compal Confidential

Schematics Document

ZEJ00

LA-A791P

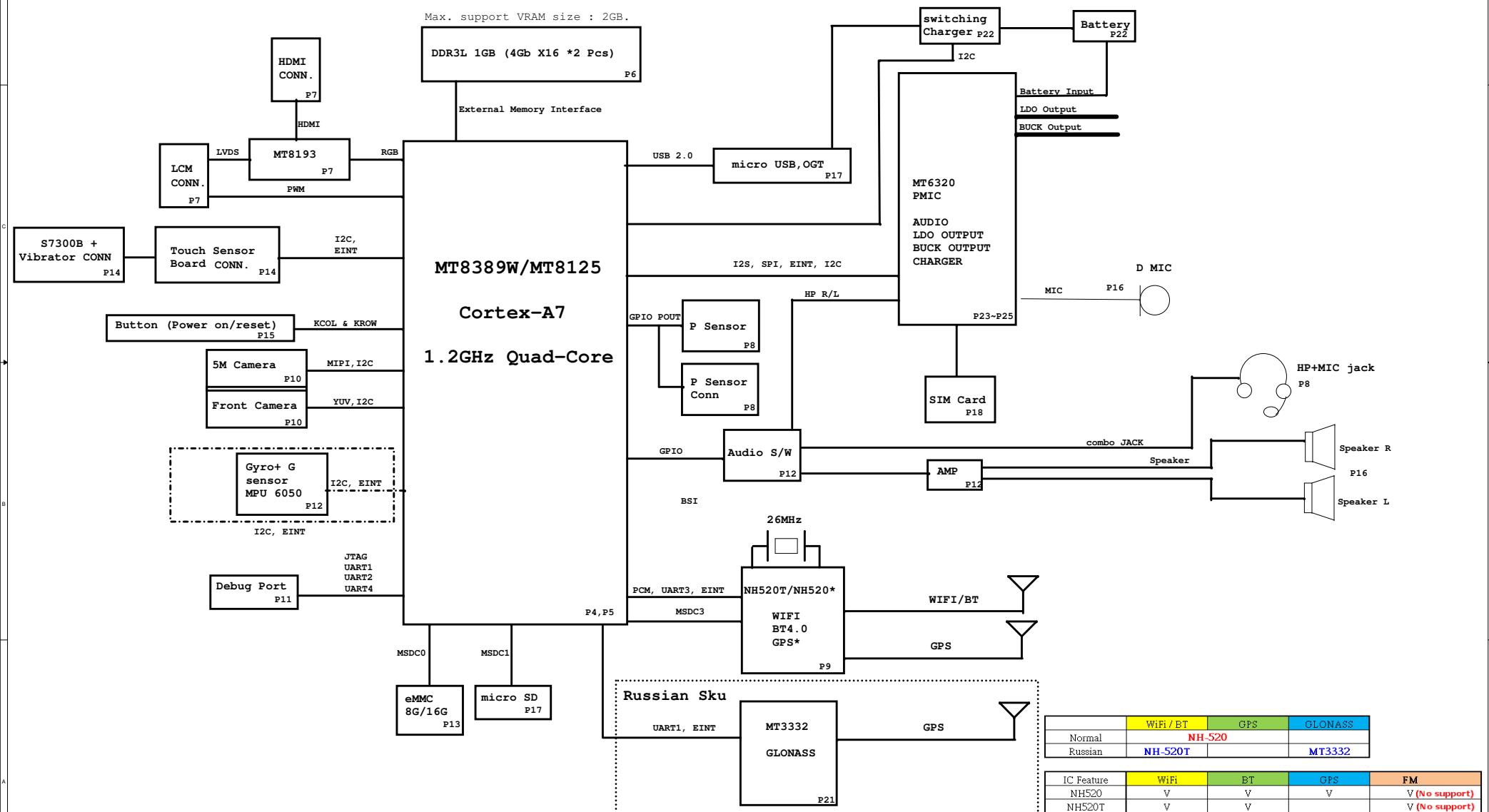
2013-05-27

REV: 0.2

http://www.vinafix.vn

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				ZEJ00-LA-A791P	0.1
				Date	Thursday, August 08, 2013
				Sheet	1 of 28

ZEJ00 block diagram



	WiFi / BT	GPS	GLONASS
Normal	NH-520		
Russian	NH-520T		MT3332

IC Feature	WiFi	BT	GPS	FM
NH520	V	V	V	V (No support)
NH520T	V	V	V	V (No support)

Voltage Rails

MT6320 Power Plane	Function	Power Level	IDLE	Sleep mode
VCORE_PMU	VCORE switching output	0.75 ~ 1.3	ON	Low voltage
VPROC_PMU	VPROC switching output	0.75 ~ 1.3	ON	Low voltage
VTCXO_1_PMU	LDO output for TCXO	2.8V	ON	OFF
VCAMA_PMU	LDO output for camaera analog	2.8V	ON	Gating by SW
VSRAM_PMU	LDO output used for 1.2V SRAM	1.2V	ON	Low voltage
VDD28_6583	LDO output used for 2.8V IO	2.8V	ON	Gating by SW
VGP2_PMU	LDO output for camaera 1.8V	1.8V	ON	Gating by SW
VEMC_3V3_PMU	LDO output for eMMC&P-sensor	3.3V	ON	Gating by SW
VMCH_PMU	LDO output for SD card	3.3V	ON	Gating by SW
VGP5_PMU	LDO output for Touch panel	2.8V	ON	Gating by SW
DDR3VCCIO	LDO output for DDR3L	1.35V	ON	Gating by SW
				Gating by SW
VDD18_6583	LDO output used for 1.8V IO	1.8V	ON	Gating by SW
VRF18_PMU	LDO output for RF_MT6167	1.8V	ON	Gating by SW

I2C address

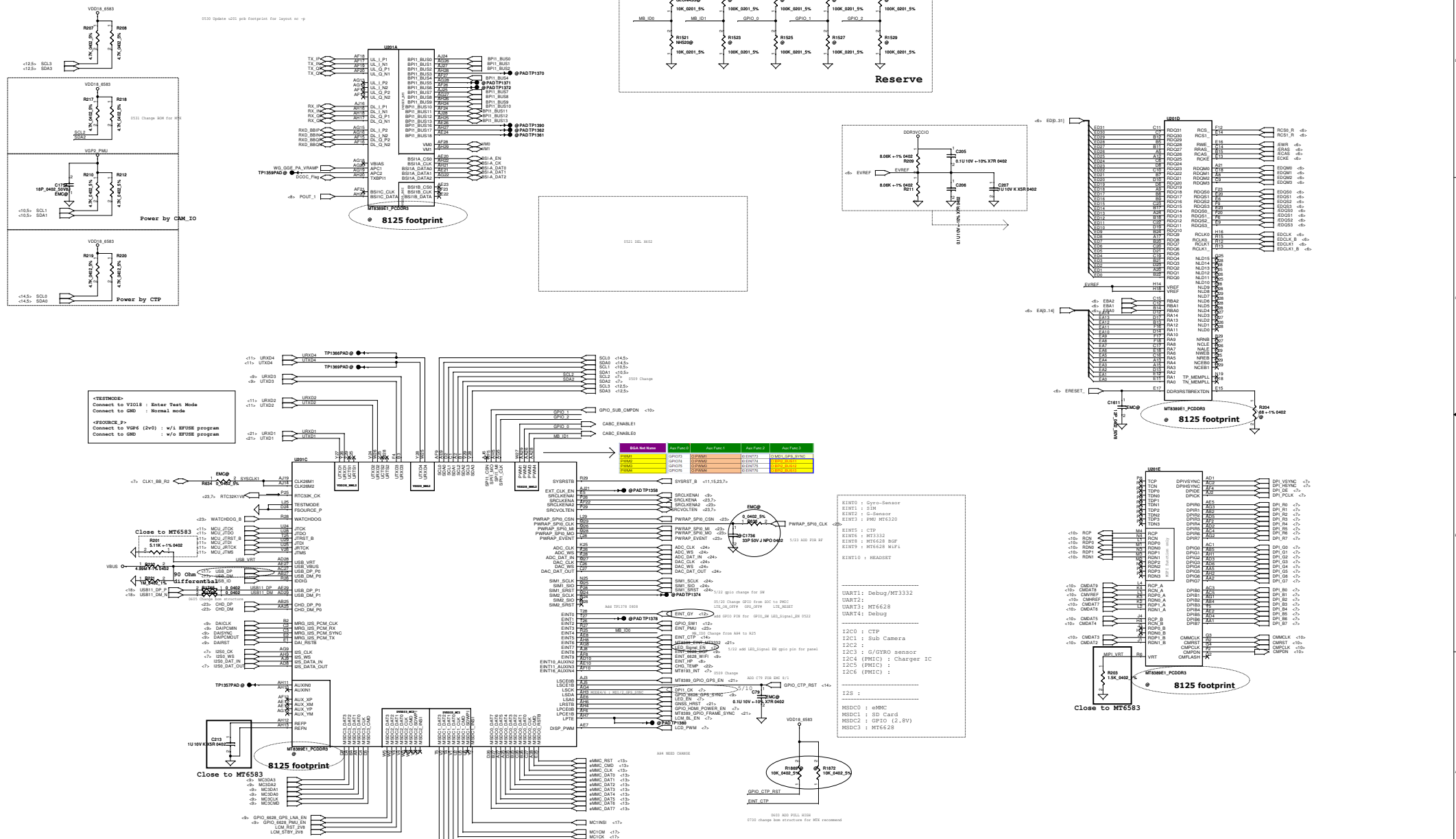
Device	Address(7 bit)	Address(8bit)	
		Write	Read
Gyro (MPU-6050)	0x68	0xD0	0xD1
G-sensor (MPU-6050)	0x68	0xD0	0xD1
Touch screen (S7300B)	0x20	0x40	0x41
Camera 0.3M	0x21	0x42	0x43
Camera 5M	0x36	0x6C	0x6D
PMIC			
Charger	0x6B	0xD6	0xD7
Battery	0x55	0xAA	0xAB

Main board ID

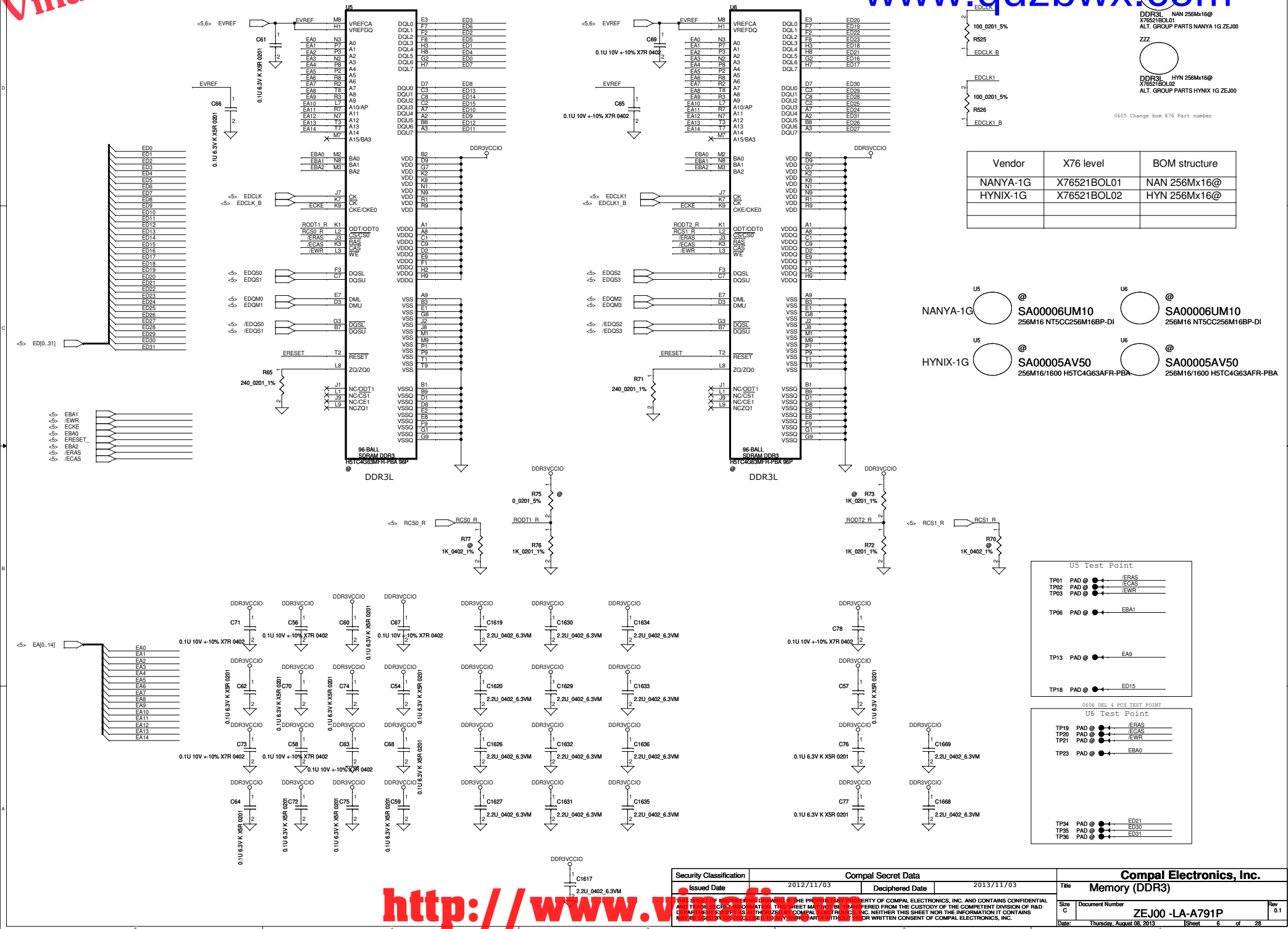
MB_ID0	MB_ID1	
0	0	EVT
0	1	DVT
1	0	PVT
1	1	MP

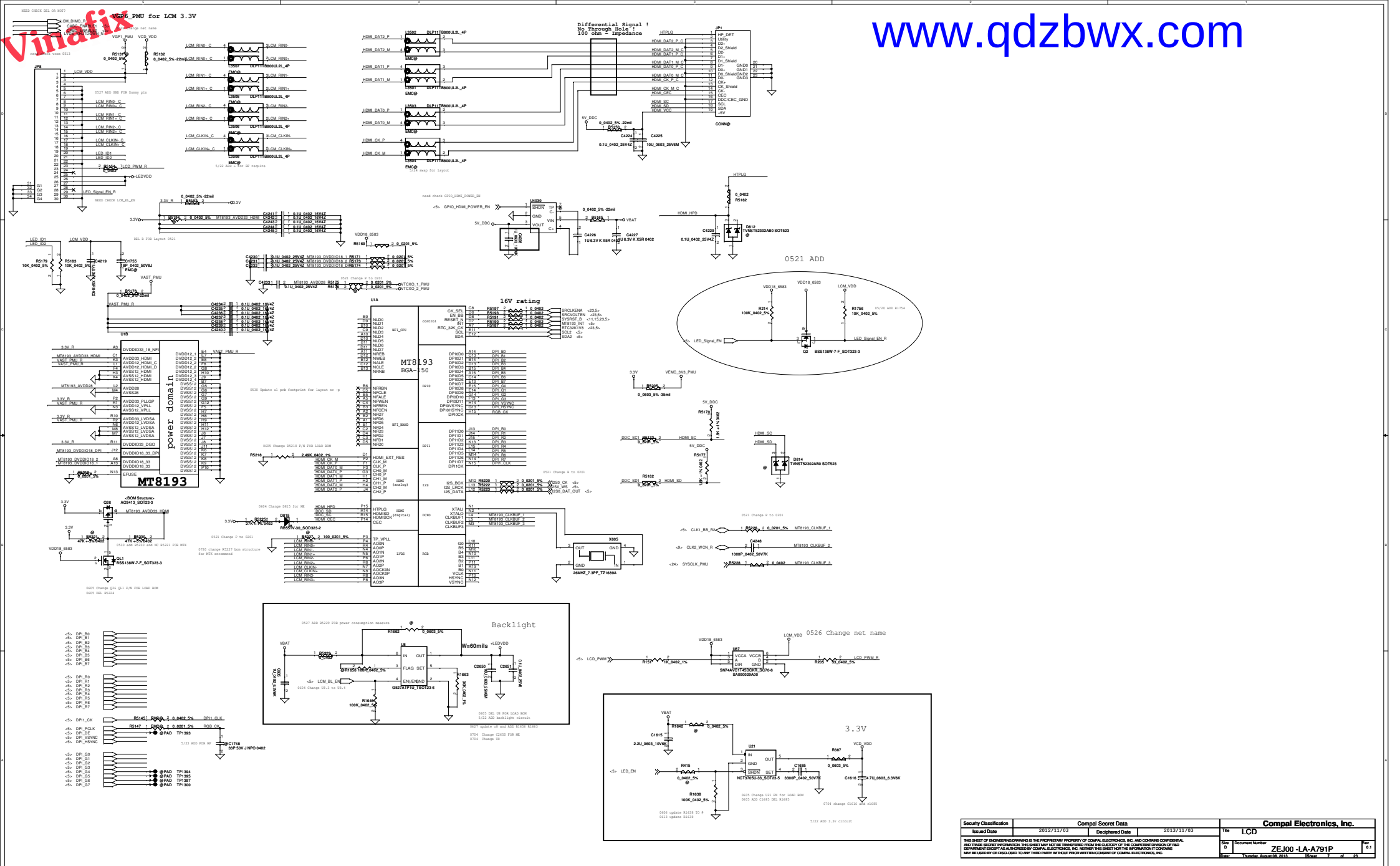
BOM structure

Name	Function	
3G@	3G only	
WIFI_ONLY@	WIFI ONLY	
DAZ@	PCB	
EMC@	for EMC request	
NH520@	AW-NH520	
NH520_EMC@	GPS EMC	
GLONASS@	MT3332	

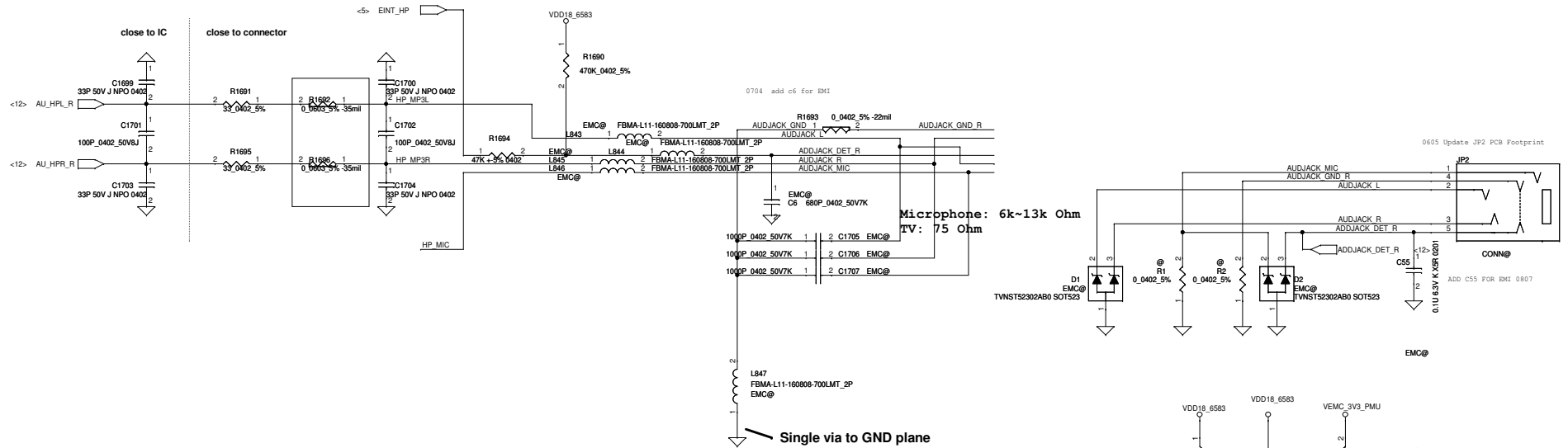


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Drawn By	ZEJ00	Checked By	LA-A791P	Rev	6.1
Date: Thursday, August 8, 2013 10:58 AM 5 of 28					

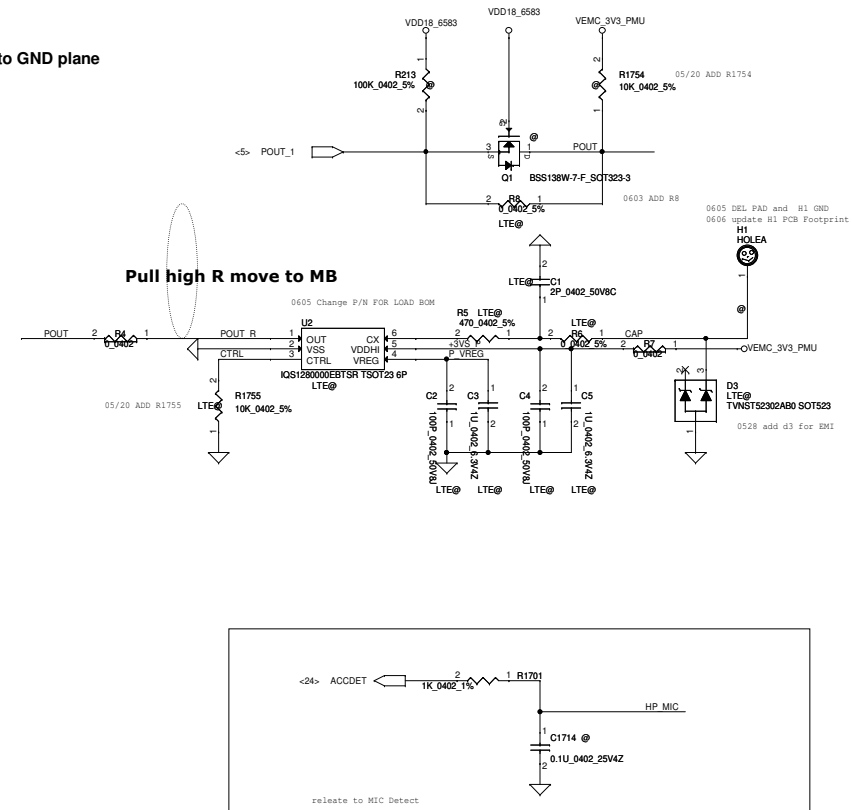
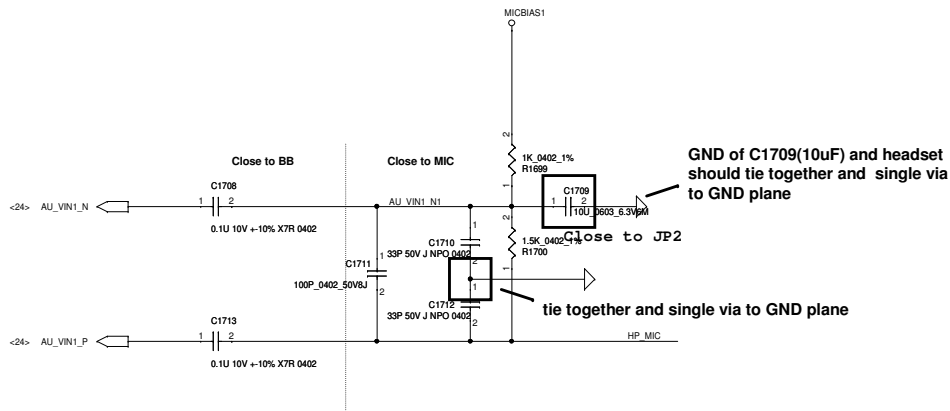




Earphone RECEIVER



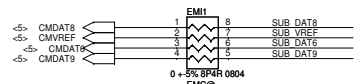
Earphone MICPHONE



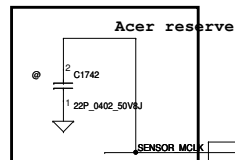
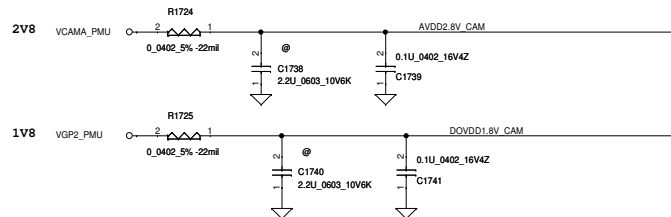
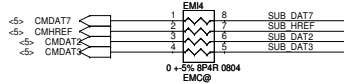
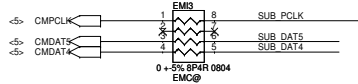
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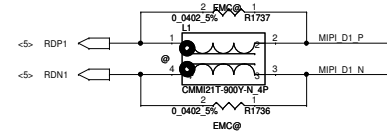
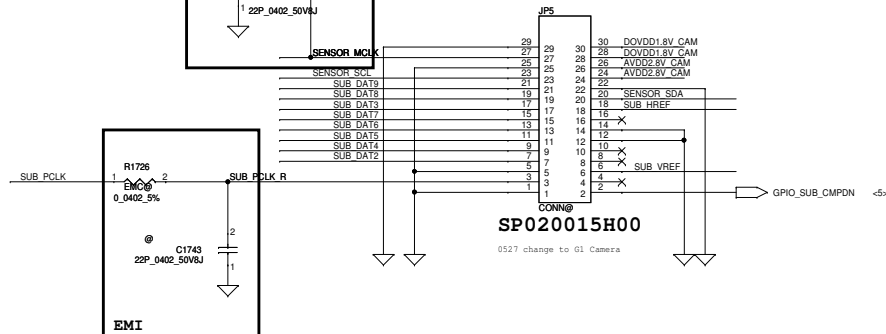
Date:	Thursday, August 08, 2013	Sheet	9	of	28
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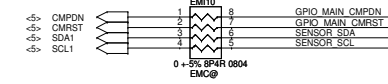
05/20 DEL EM12



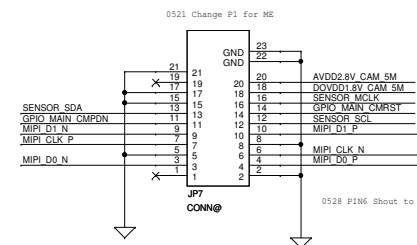
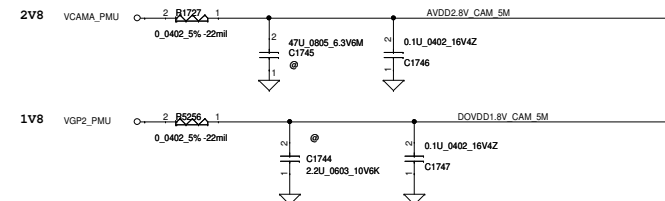
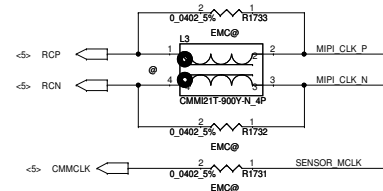
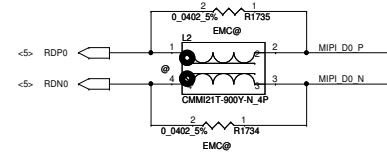
0.3M connector



05/20 SWAP FOR Layout



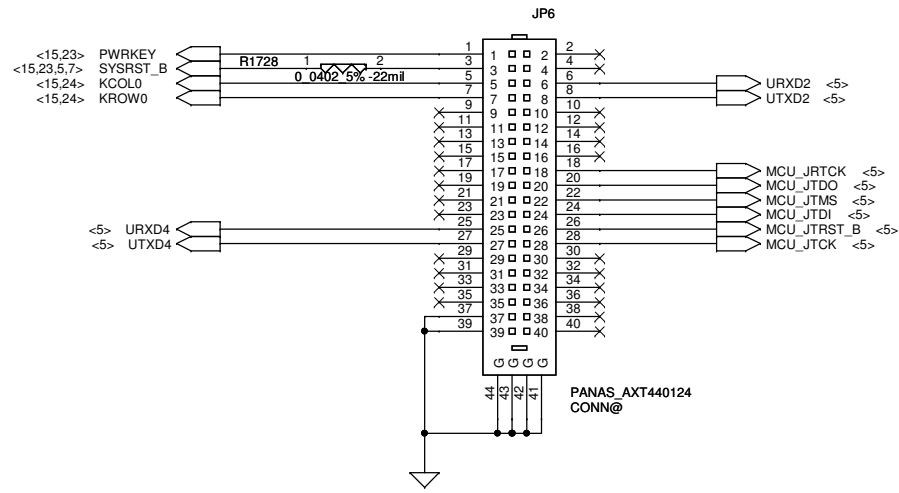
05/20 Change C1745 from 0603 to 0805



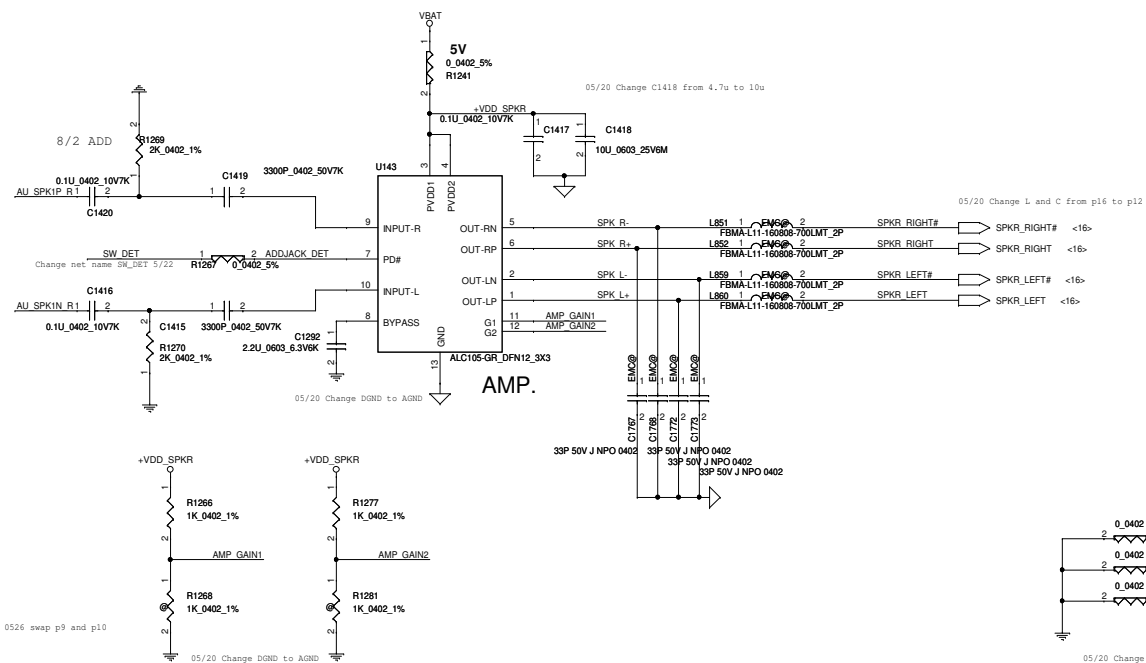
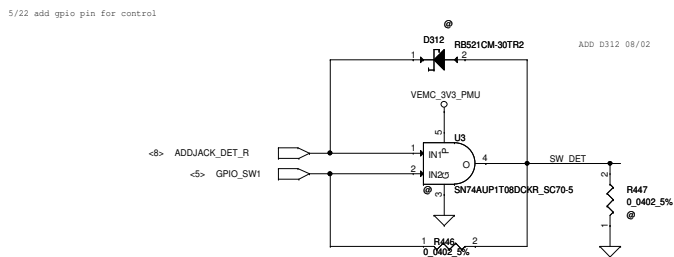
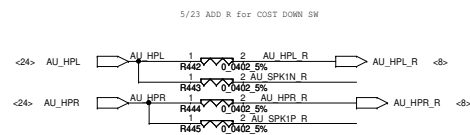
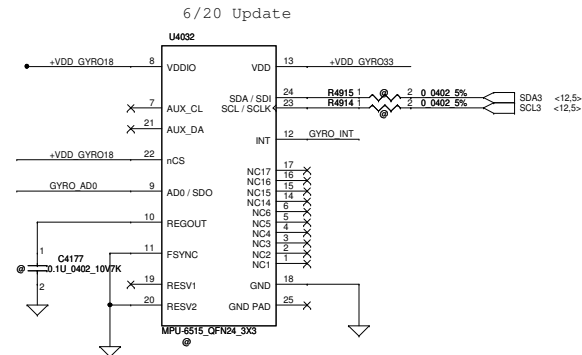
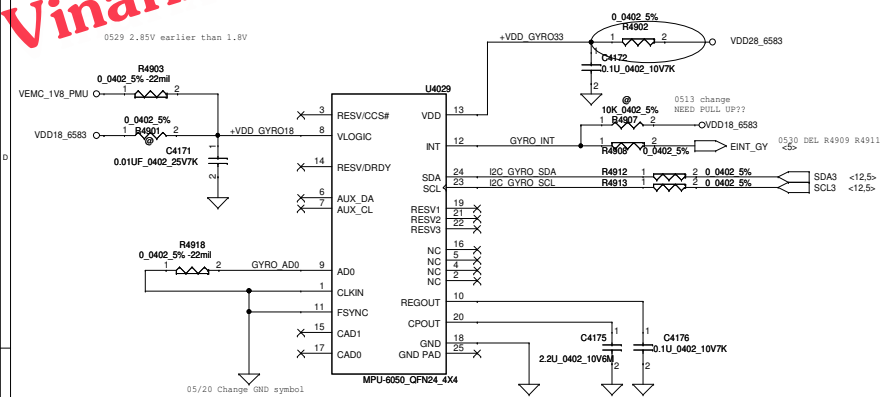
0521 Change P1 for MI

0528 PIN6 Shout to gn

Common Debug



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				Rev 0.1
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				Sheet 11 of 28

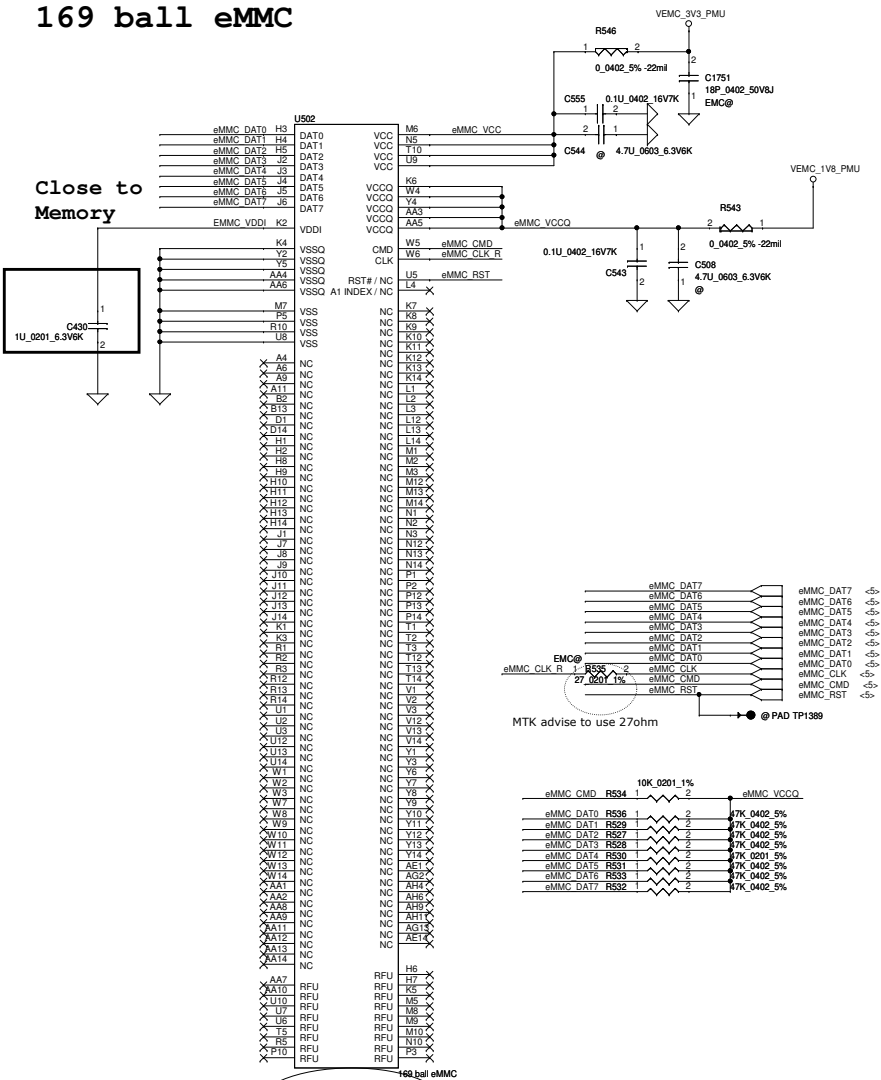


IN	NC to COM COM to NC	NO to COM COM to NO OFF	
L	ON		HP
H	OFF	ON	SPKER

```
0521 ADD CPIO_5W
0521 DEL 100K PULL DOWN
```

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169 ball eMMC



- U502

KIN 8GB@
SA00006MF30
Kingston 8G KE4CN3K6A
- U502

KIN 16GB@
SA00006MG30
Kingston 16G KE4CN4K6A
- U502

EMMC_SAM_8GB@
KLM8G2FE3B-B001
SA00005KM10
- U502

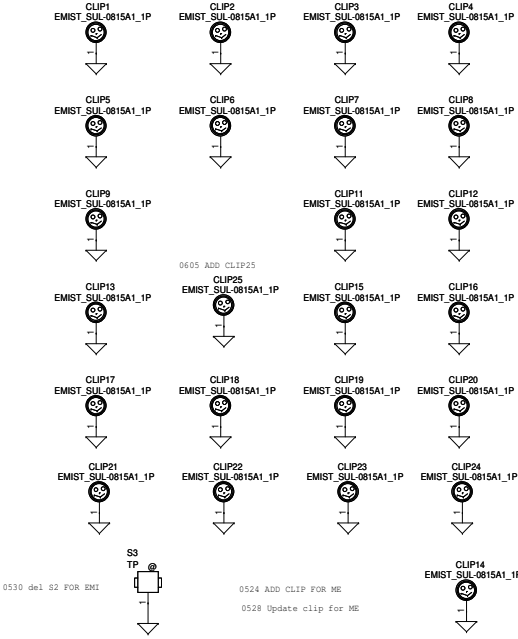
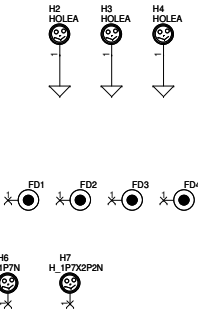
HYN 8GB@
SA00006PW10
HYNIX 8G H26M42002GMR
- U502

HYN 16GB@
SA00006VJ10
HYNIX 16G H26M52002EQR
- U502

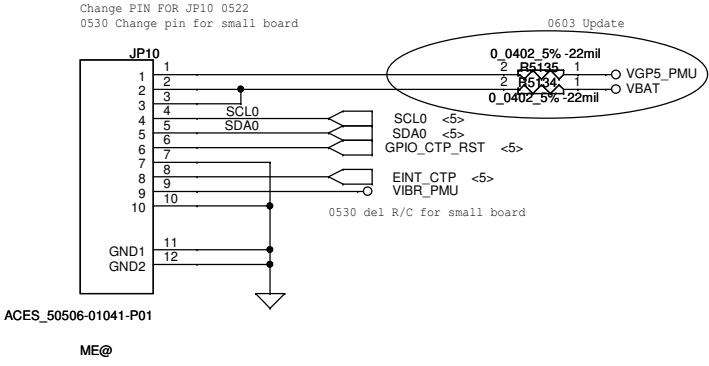
SAM 8GB@
SA00006VE10
Samsung KLM8G1WE4A-A001
- U502

SAM 16GB@
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SCREW HOLE

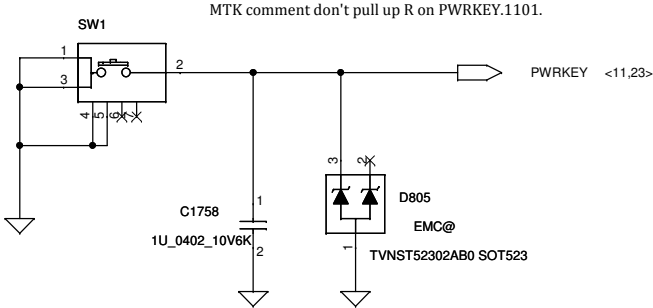


SDIM4E2-16G-T_TFBGA_169P-P
©compal footpint



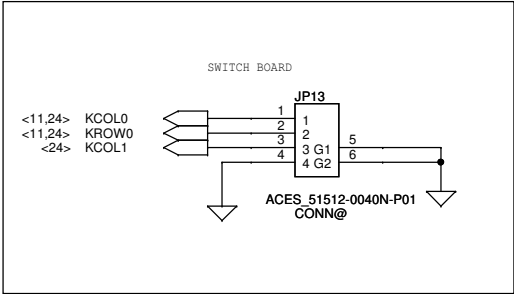
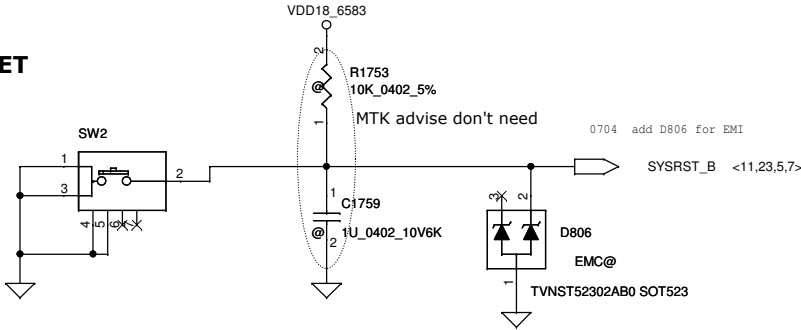
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Power-on Button



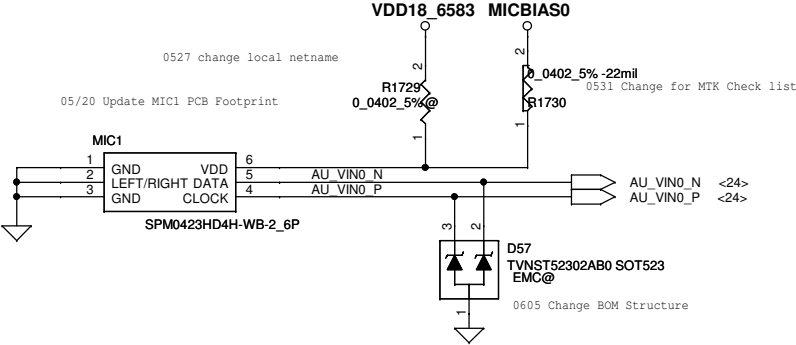
Compal GND MTK GND

RESET



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Digital MIC Conn.

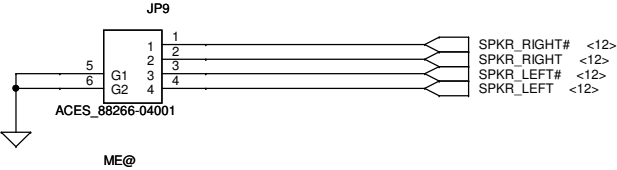


Compal GND MTK GND

AU_VIN0_P	DM_CLK	
AU_VIN0_N	DM_DATAR	

Vibrator

SPAKER



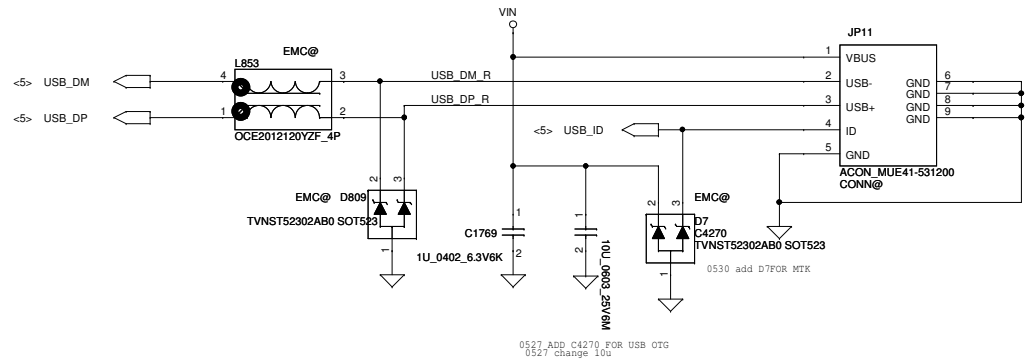
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2012/11/03	Deciphered Date	2013/11/03	Title Connector(MIC/SPK/HP)
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Micro-USB

If mini-A connector insert => CID < 0V => Low
If mini-B connector insert => CID > 1.2V => High
IDPULLUP pin is replaced by 1.2V power source.

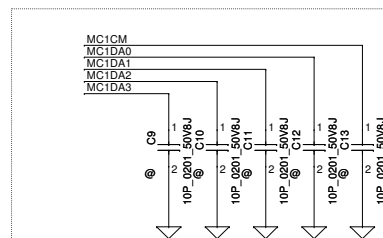
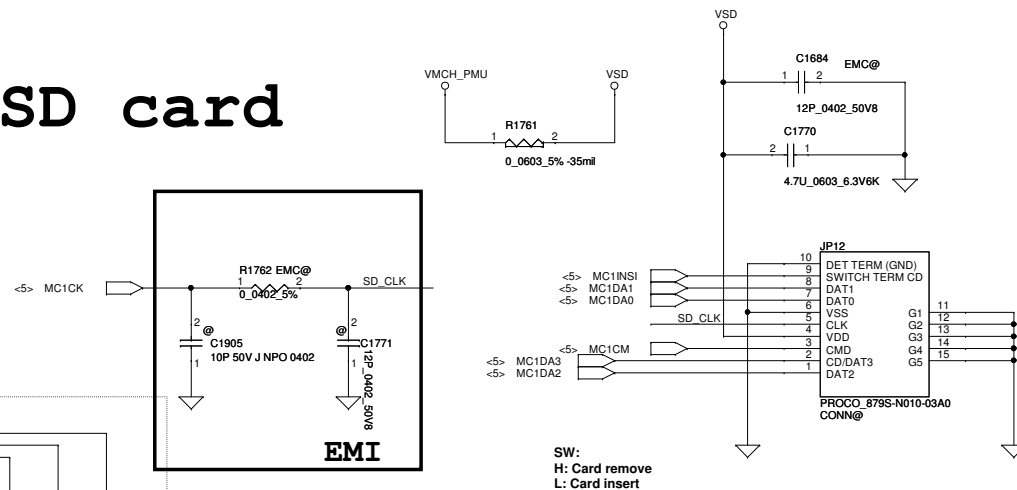
FOR RF request

TP1364PAD@ USB_DM_R
TP1365PAD@ USB_DP_R



Compal GND MTK GND

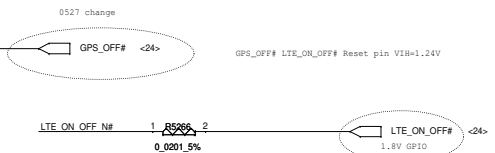
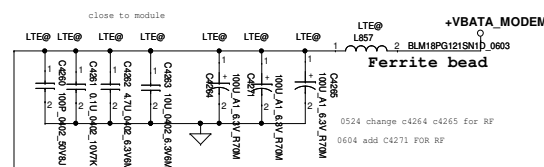
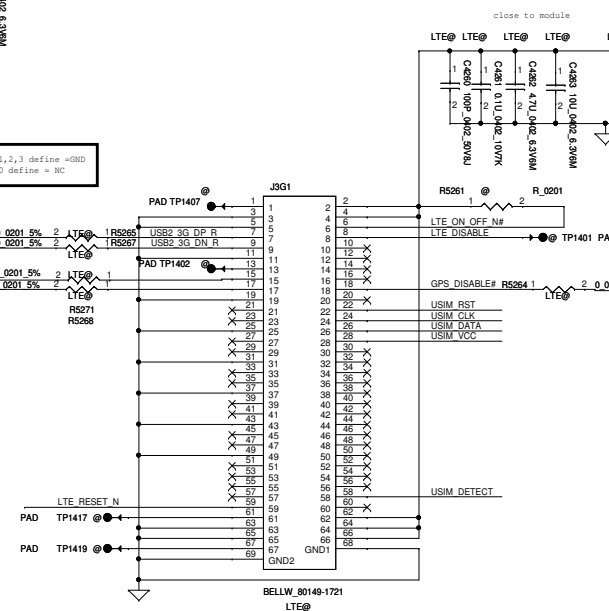
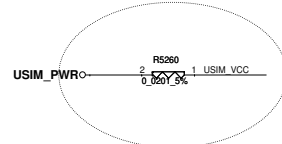
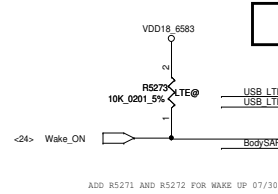
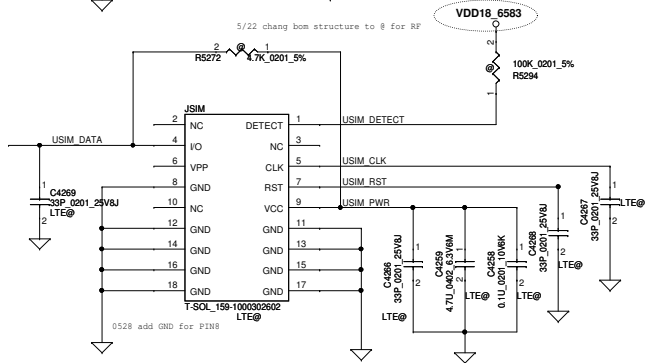
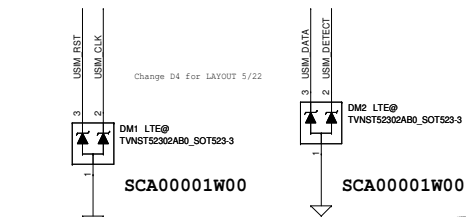
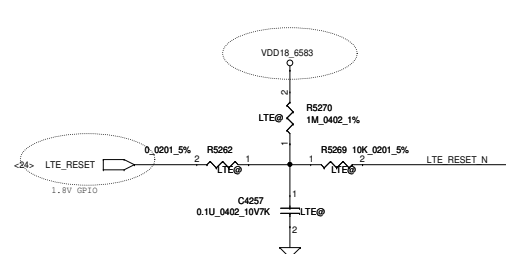
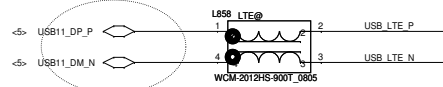
SD card



for RF request
close U201 side

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1008 Intel Review update

0527 del

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				Size		Document Number	
						ZEJ00 -LA-A791P	
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				Date:	Thursday, August 08, 2013	Sheet 19 of 28

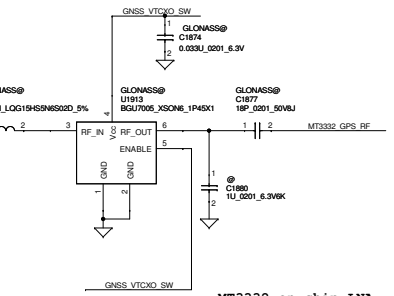
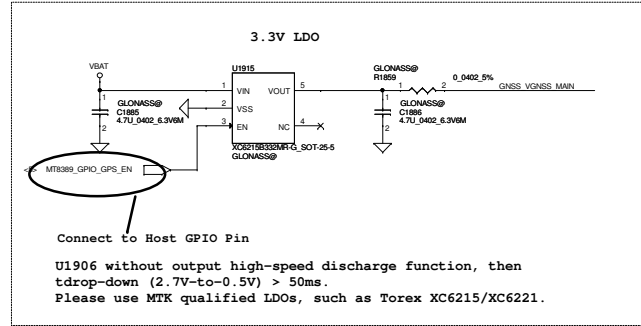


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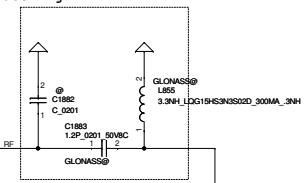
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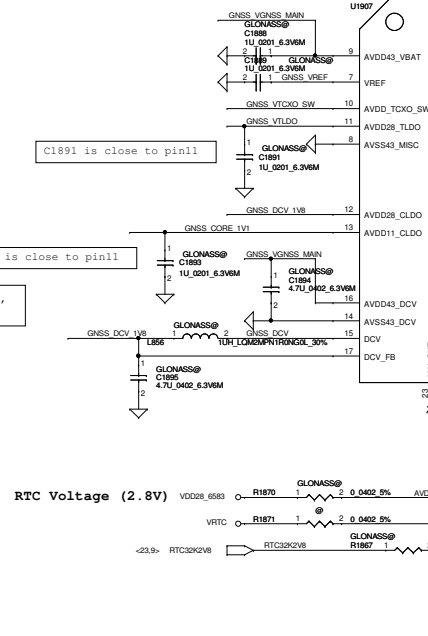


MT3332 on-chip LNA Matching



U1907
SA00006M400
S IC MT3332N QFN 48P GPS

MT3332 QFN-48pins



1.8V / 2.8V IO Voltage Selection
2.8V IO : U1907 pin30 - 42 need change to GNSS_VTLD0
1.8V IO : U1907 pin30 - 42 (IO voltage is SMP5_V8)

Reserved for GPS HRST from host, connect to Host (MT62xx) GPIO pin

Reserved for improving GNSS Hot-Start performance.

Reference Frequency Selection
16.368MHz TCXO : R1864 = NC, PIN_41 = NC
26MHz TCXO : R1864 = 10K, PIN_41 = NC
26MHz XTAL : R1864 = 10K, PIN_41 contact 10K

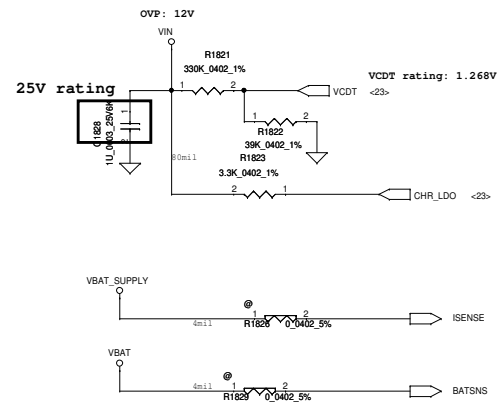
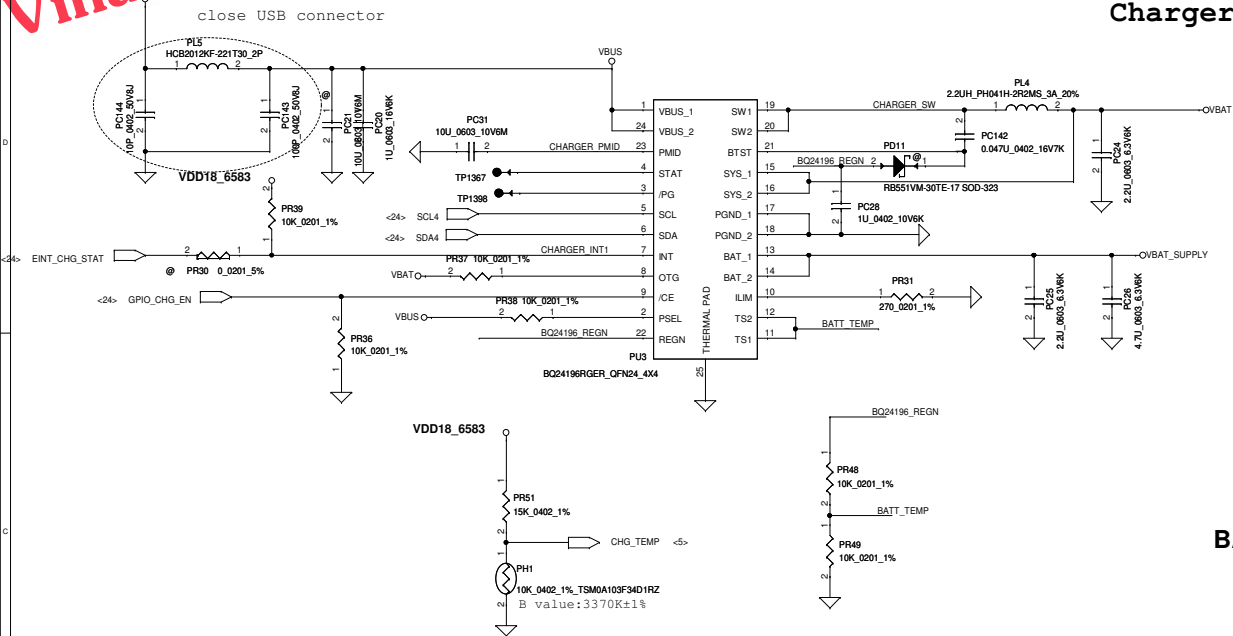
As EINT to Host

Connect to HOST UART (TX/RX) interface

Connect GNSS_HOST_VRTC to always alive voltage source, and keep the voltage swing of GNSS_HOST_32K RTC clock same as GNSS_HOST_VRTC.

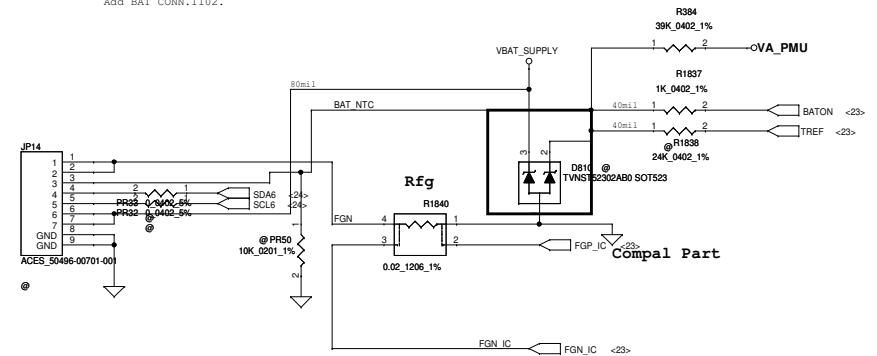
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Issued Date	2014/1/09	Deciphered Date	ZE100-LA-A791P	
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Charger

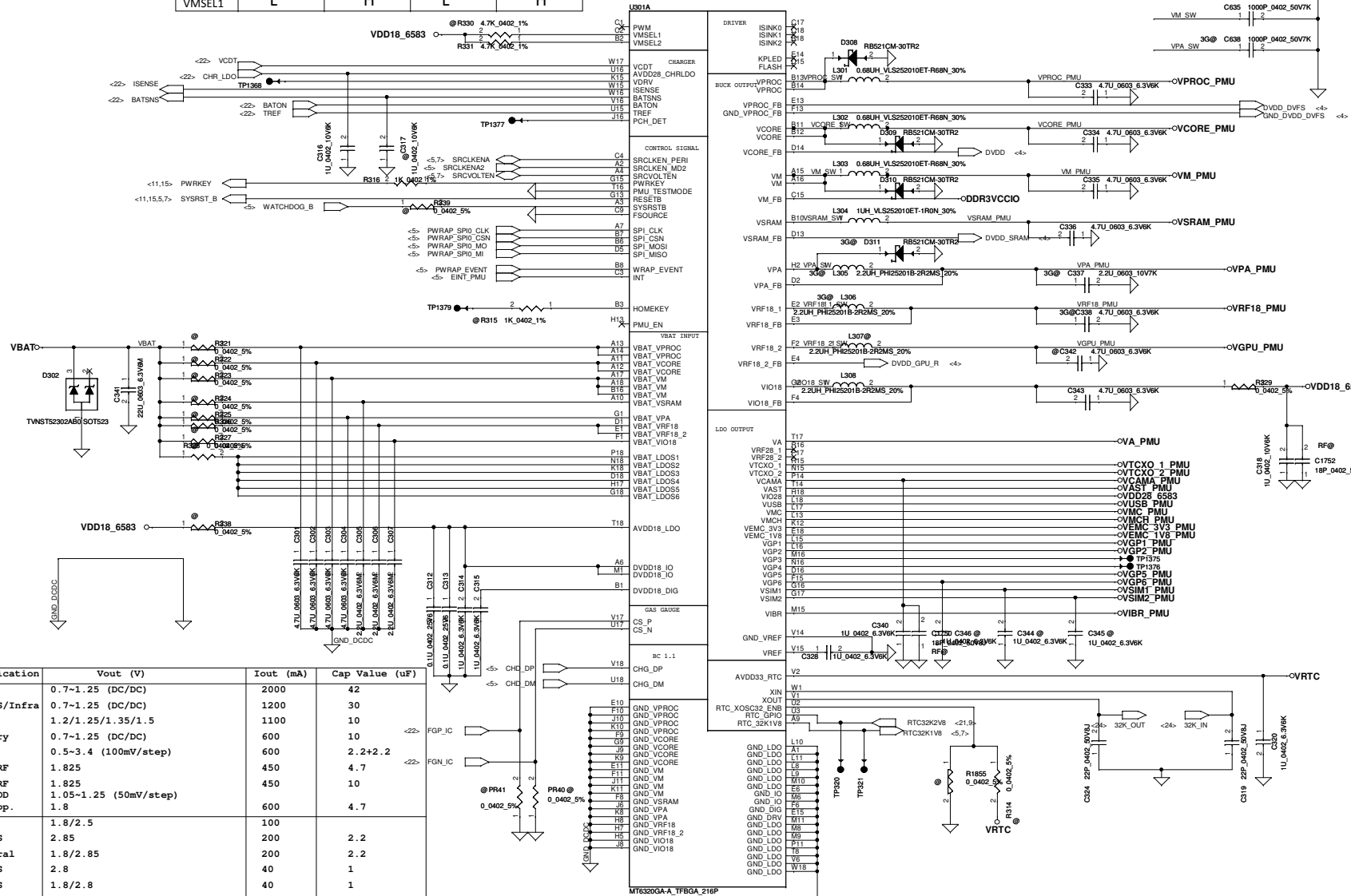


BATTERY CONNECTOR

Add BAT_CONN.1102.



	LPDDR2/1.2V	DDR3U/1.25V	DDR3L/1.35V	DDR3/1.5V
VMSEL2	L	L	H	H
VMSEL1	L	H	L	H



Audio





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www.qdzbwx.com

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PVT_Phase
2013/7/8
Add C1752,C1750 for RF request
change PD11 reserve
change PC31 0805 to 0603 for ME request.

PVT_Phase
2013/8/6
PR30, R339, R321, R322, R323, R324, R325, R326,
R327,R338, R1855, R329 change short pad.
PR31 change 270 ohm. setting input current limit 1.8A

www.qdzbw.com

http://www.winfix.com

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Vinafix

P4 ADD C1757 C1756 C1686 C1687 FOR RF requirement.
P5 ADD C1611 C1754 FOR RF requirement.
P6 DEL Test point for RF -- TP04 TP05 TP07 TP08 TP09 TP10 TP11 TP12 TP1374 TP14 TP15 TP16 TP17 TP24 TP26 TP27 TP28 TP31 TP32 TP33
P7 ADD C1755 FOR RF requirement.
Change U8 /C2650 Package for ME requirement.
P8 ADD C6 for EMI/ESD requirement.
P12 ADD MPU-6515(U4032)/C4177/R4915/R4914 FOR reserve MPU-6515
P13 ADD C1751 FOR RF requirement.
P15 ADD D806 FOR SW2 (ESD requirement.)
P17 ADD C1684 FOR RF requirement.
P23 ADD C1750 C1752 FOR RF requirement.
P24 ADD C1753 FOR RF requirement.

Q1 Q2 QL1 Change BOM from SB000002X00 to SB00000S700
C1890 C4175 Change PN From SE00000V600 to SE00000V680

C TEST

P5 DEL R1872 ADN R1869
P5 ADD TP1378
P7 Change R5227 BOM Structure to @
P8 ADD C55 FOR EMI ESD
P12 ADD D312 R1269 R1270 C1416 C1420 FOR Audio
P15 Change SW1 SW2 PCB Footprint
P18 ADD R5271 AND R5272 FOR WAKE UP

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