

Compal Confidential

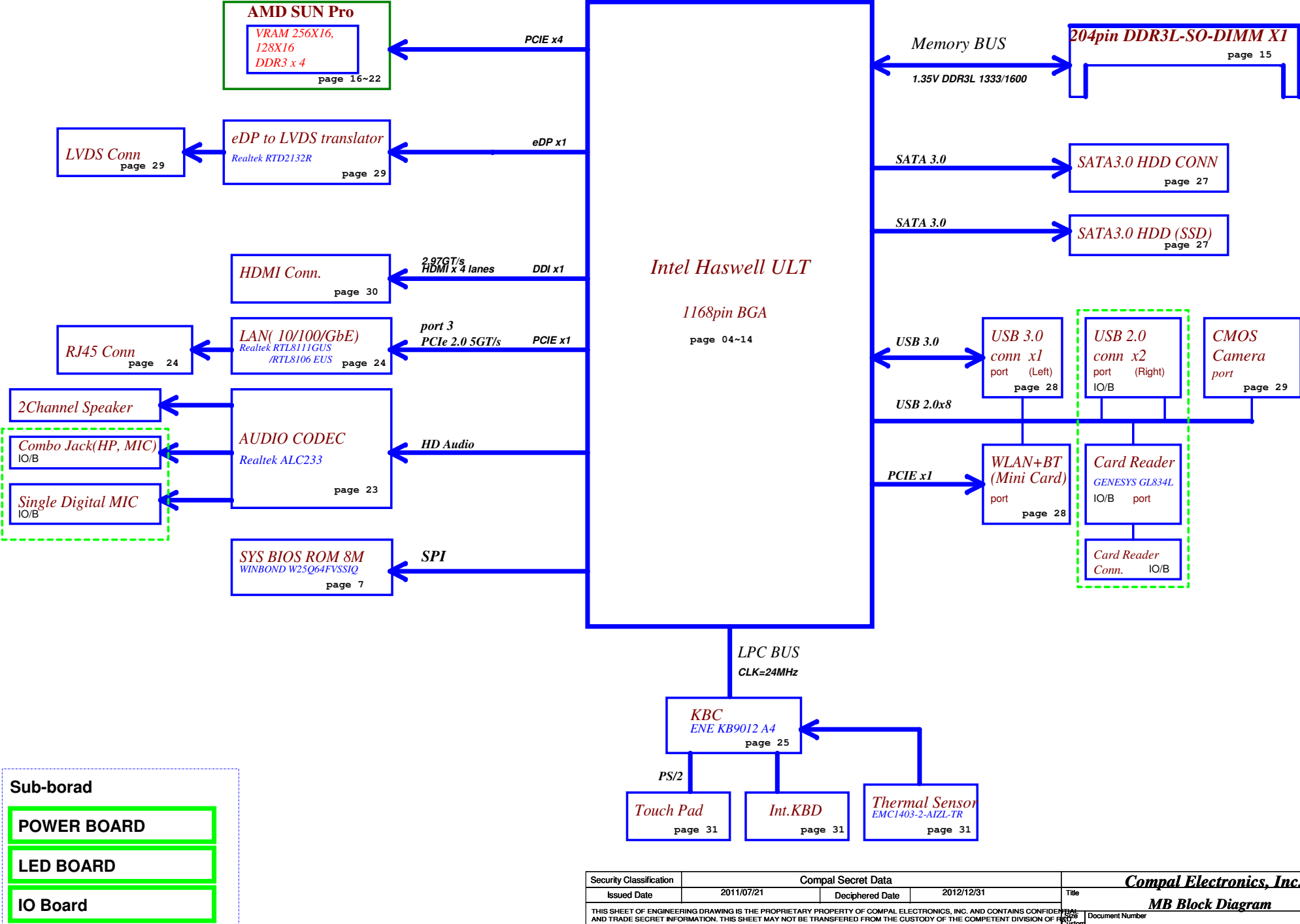
Model Name : ZIUS6/S7
File Name : LA-A321PR01
BOM P/N:4319OP38L5x -- ZIUS7
4319OP38L0x -- ZIUS6

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ZIUS6/S7 M/B Schematics Document
Intel SharyBay ULT Processor + AMD SUN Pro

2013-03-14
REV: 0.2

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2011/06/24	Deciphered Date	2012/07/12	Cover Page	
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Voltage Rails

power plane State	B+	+5VALW +3VALW	+1.35V	+5VS +3VS +1.5VS +1.05VS_VTT +CPU_CORE +0.675VS +VGA_CORE (PX) +0.95VS_VGA (PX) +3.3VS_VGA (PX) +1.8VS_VGA (PX) +1.5VS_VGA (PX)
S0	O	O	O	O
S3	O	O	O	X
S5 S4/AC	O	O	X	X
S5 S4/ Battery only	X	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

STATE	SIGNAL	SLP_S0#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

BOARD ID Table

Board ID	PCB Revision
0	
1	
2	0.2
3	0.1
4	
5	
6	
7	

BOM Structure Table

BTO Item	BOM Structure
Connector	ME@
45 LEVEL	45@
Unpop	@
CPU OPTION	CPU1@
INTEL UMA	UMA@
AMD SUN Pro	PX@
VRAM Option	S1G@ M1G@ H1G@ S2G@ M2G@
LAN RTL8111GUS	8111@ SWR@
LAN RTL8106EUS	8106@ SWR@
USB3.0	USB3@
EMI PART	EMI@
ESD PART	ESD@

EC SM Bus1 address

Device	Address	Device	Address
Smart Battery Charger		Thermal Sensor EMC1403-2-AIZL-TR Panel - eDP to LVDS translator SharkBay ULT AMD SUN Pro	1001_101xb

EC SM Bus2 address

CPU SM Bus address

Device	Address	Device	Address
DDR DIMM0 WALN Touch Pad	0XA0		

CPU SML0 Bus address

USB 2.0 Port Table

USB 2.0 Port	3 External USB Port
0	USB 2.0 Port (I/O Board)
1	USB 3.0/2.0 Port (MB)
2	USB 2.0 Port (I/O Board)
3	Card Reader
4	Touch Screen (reserve)
5	Camera
6	Mini Card (WLAN/BT)
7	

USB 3.0 Port Table

Port	
1	
2	USB 3.0 Port (MB)
3	
4	

PCIe Port Table

Port	Lane	
1		
2		LAN
3		WLAN
4		
5	0	
	1	
	2	GPU
	3	
6	0	
	1	
	2	
	3	

SATA Port Table

Port	
3	
2	
1	mSATA SSD
0	HDD

SMBUS Control Table

	HOST	VGA	BATT	KB9012	SODIMM	WLAN	PANEL sensor ADM1032	Thermal sensor EMC1403	VGA sensor ADM1032	CPU Sensor	Touch Pad	Security ROM	NFC
EC_SMB_CK1 EC_SMB_DA1	KB9012 +3VLP	X	+3VLP	X	X	X	X	X	X	X	X	X	X
EC_SMB_CK2 EC_SMB_DA2	KB9012 +3VS	+3VS	X	X	X	X	+3VS	+3VS	+3VS	+3VALW	X	X	X
SMBCLK SMBDATA	CPU +3VALW	X	X	X	+3VS	+3VS	X	X	X	X	+3VS	+3VS	+3VS
SMLCLK SML0DATA	CPU +3VALW	X	X	X	X	X	X	X	X	X	X	X	X
SML1CLK SML1DATA	KB9012 +3VS	X	X	+3VS	X	X	X	X	X	+3VALW	X	X	X

CPU part

U1 CPU1@	U1 CPU2@	U1 CPU3@	U1 CPU4@	U1 CPU5@
I5-3317U 1.7G SA00006FY20	I3-4010U 1.7G SA00006SX20	I3-4100U 1.8G SA00006SU20	I5-4250U 1.3G SA00006NM40	I5-4200U 1.6G SA00006SM20
U1 CPU6@	U1 CPU7@			
I7-4550U 1.5G SA00006SJ30	I7-4500U 1.8G SA00006SL20			

PCB part

ZZZ5
PCB OR LA-A321P REV0 M/B DA600104000

VRAM

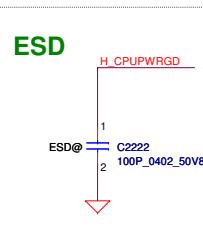
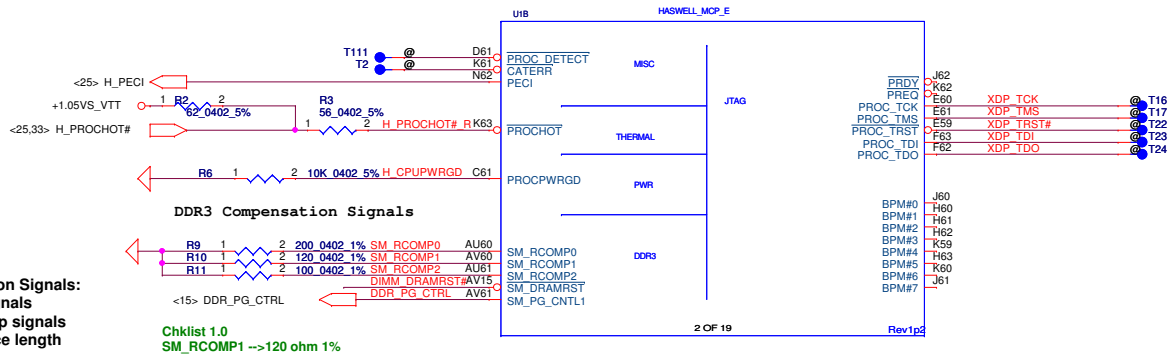
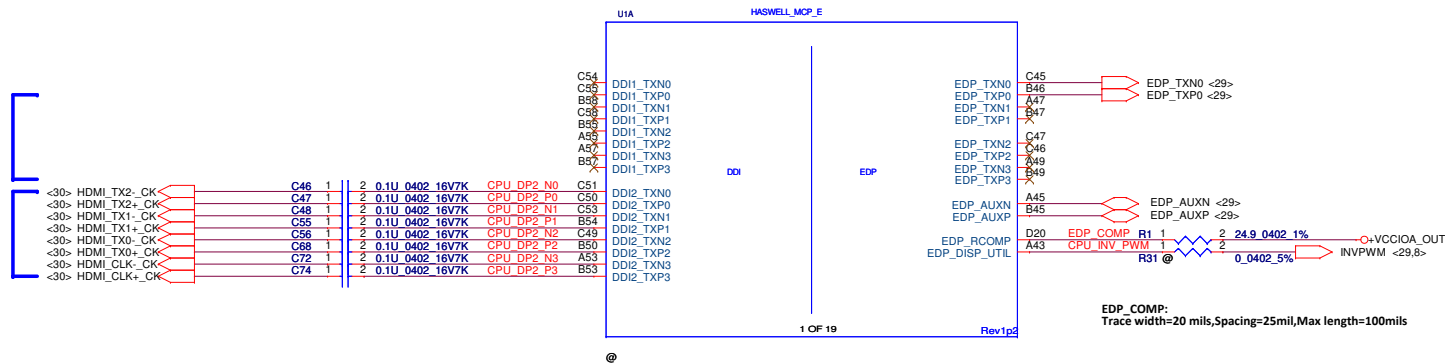
ZZZ3	ZZZ7	ZZZ6	ZZZA	ZZZ8
Samsung S2G@ X7641338L34 K4W401646B-HC11	MICRON M2G@ X7641338L35 MT41K256M168A-1070:E	Samsung S1G@ X7641338L31 K4W201646B-BC1A	MICRON M1G@ X7641338L32 MT41K256M168A-1070:E	Hynix H1G@ X7641338L33 K4W201646B-BC1A

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Issued Date	2011/07/12	Deciphered Date	2012/07/01	Notes List	
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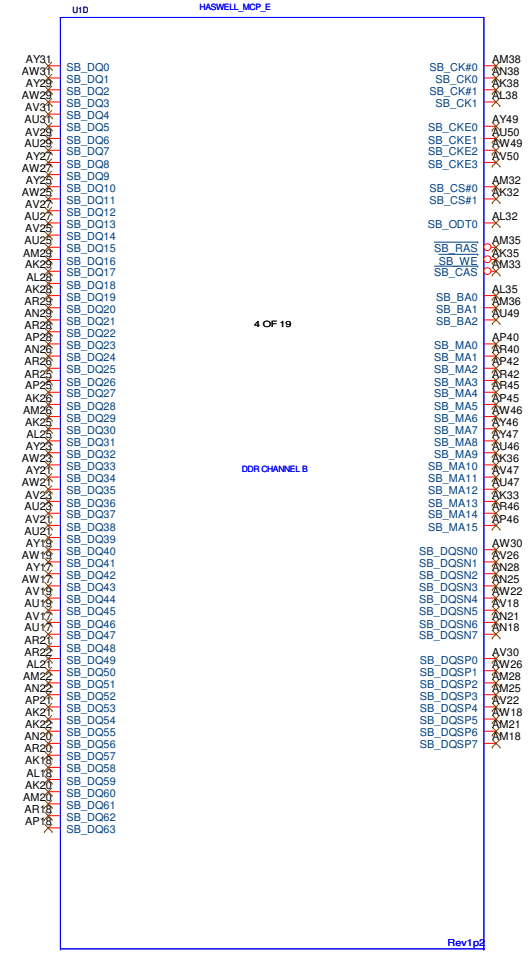
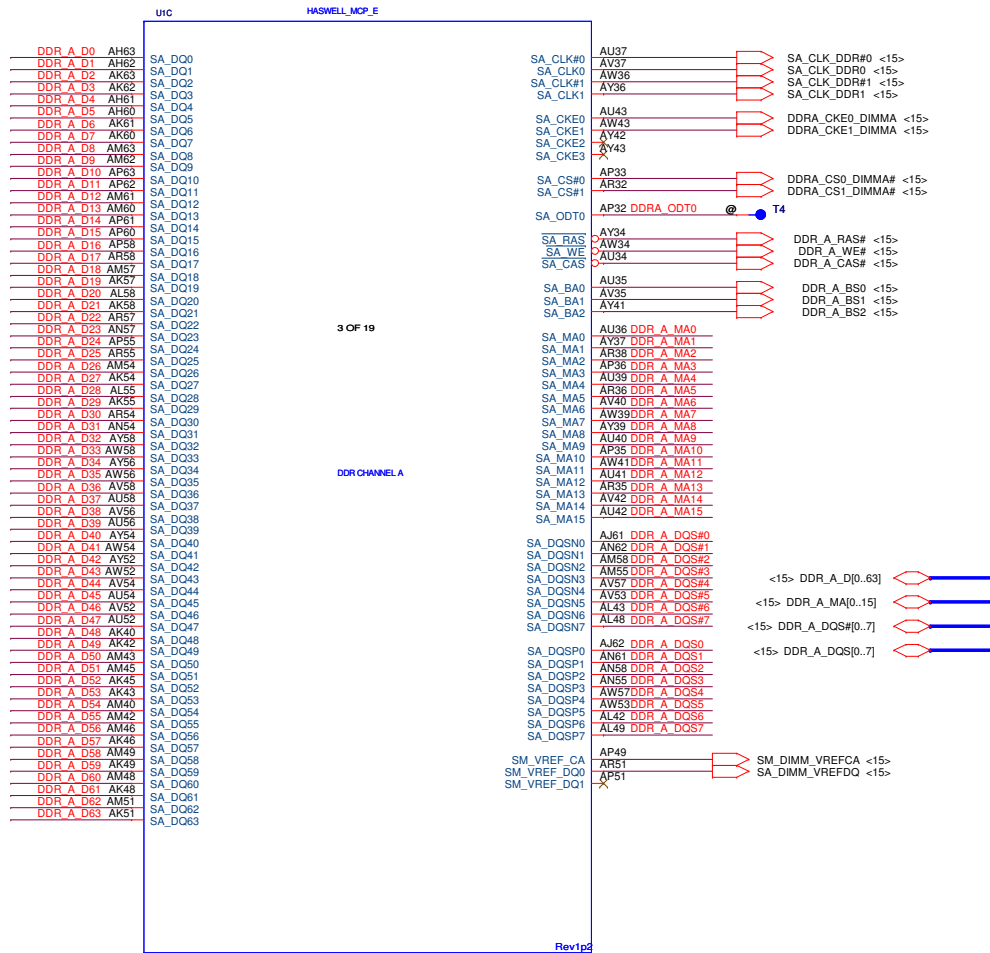
CRT

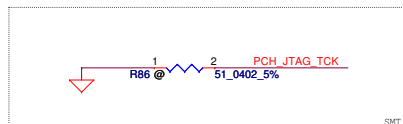
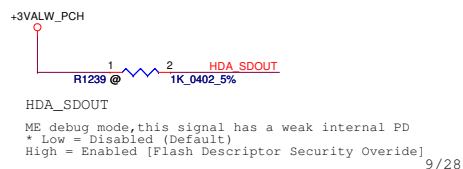
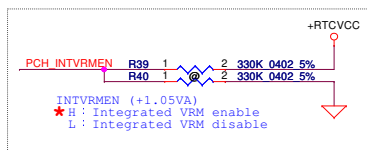
HDMI

eDP

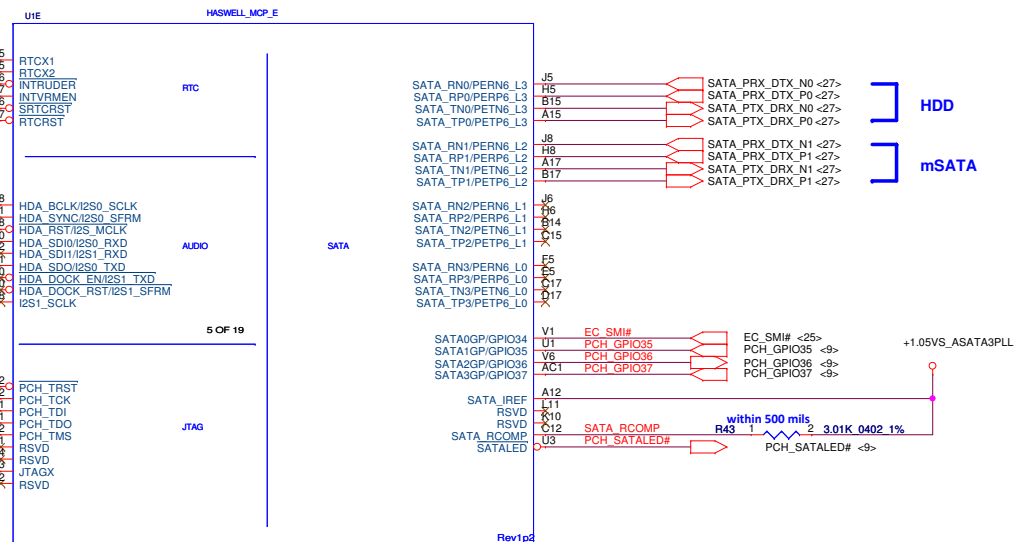
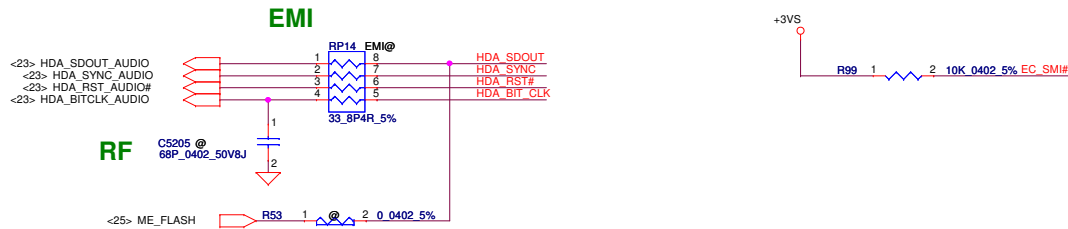
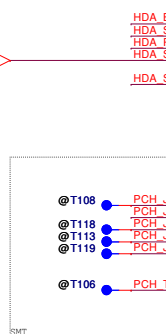


Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title	HSW MCP(1/11) DDI,MSIC,XDP	
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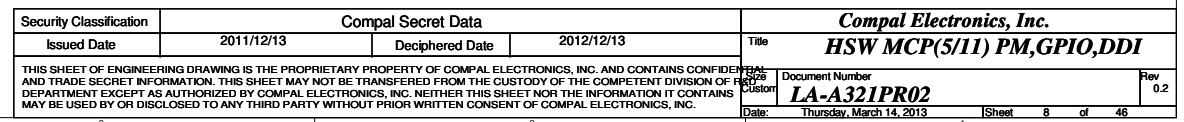


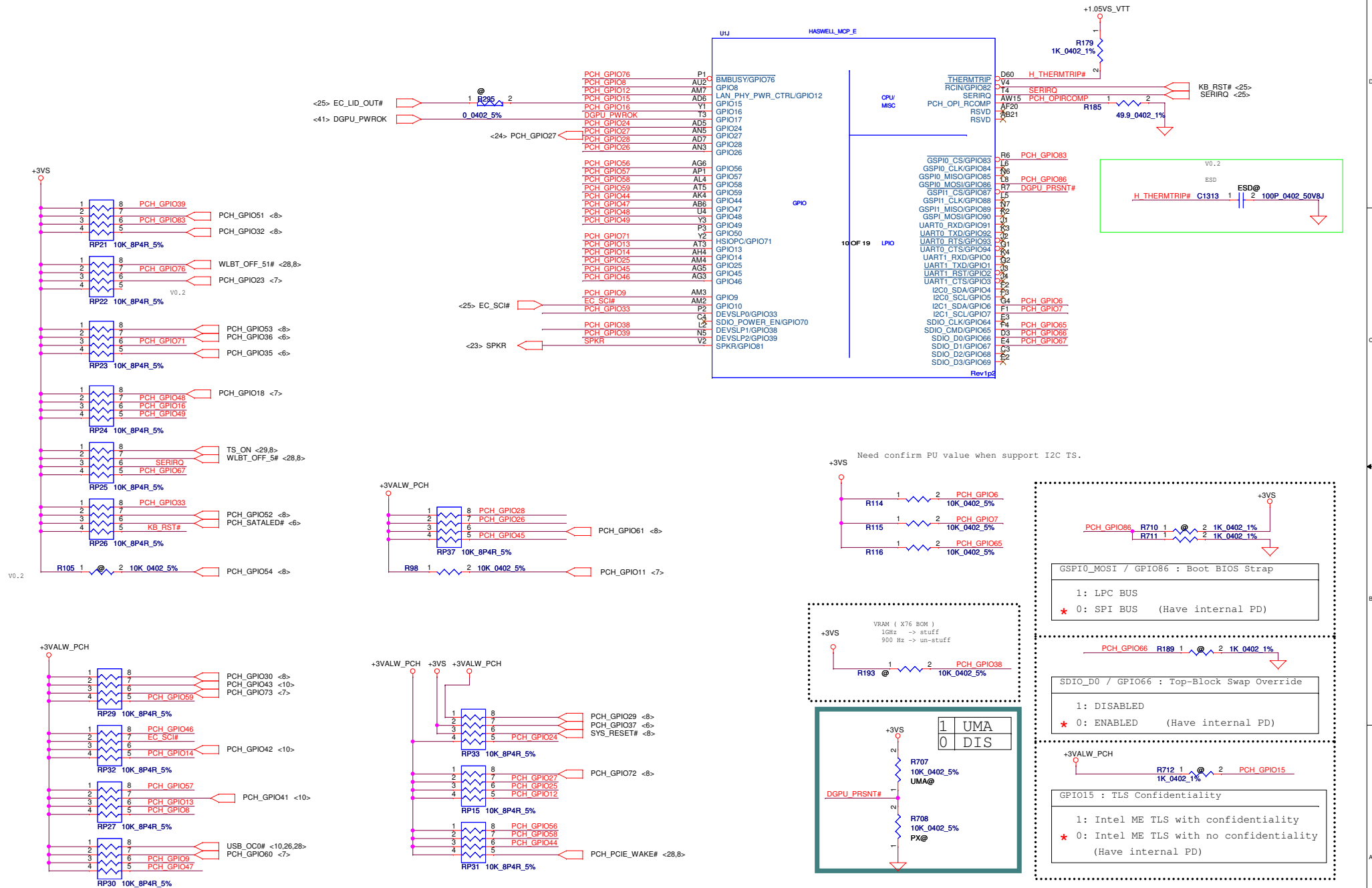


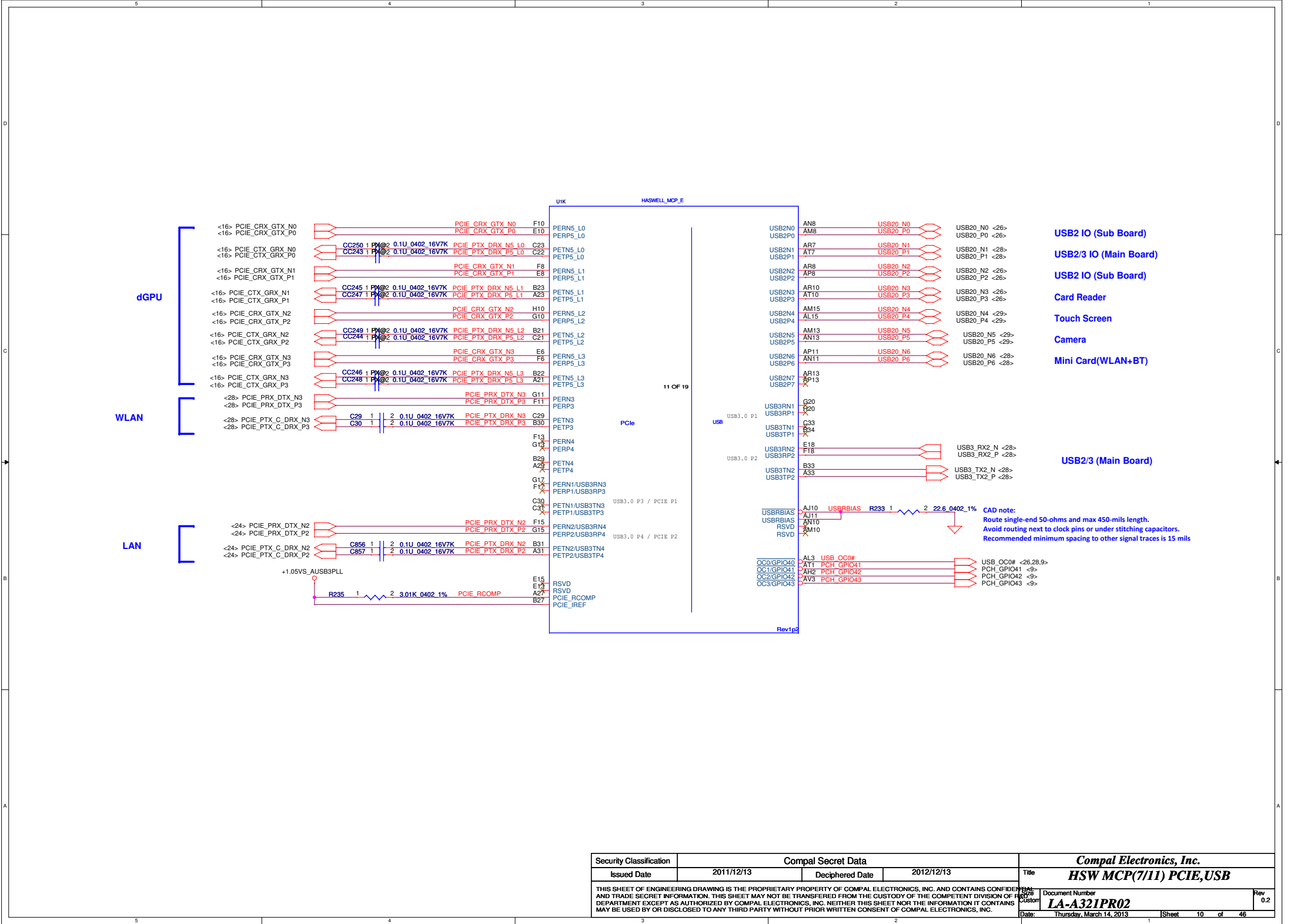
The schematic diagram illustrates the RTC circuit. It features a +RTCVCC supply connected to a network of resistors (R36, R37, R35) and capacitors (C2, C5). The network is connected to the JME1 and JME2 pins of the NE CMOS device. The JME1 pin is also connected to a 1MΩ resistor (R35) and a 10V capacitor (C5). The JME2 pin is connected to a 10V capacitor (C2). The circuit is labeled with component values and pin numbers.



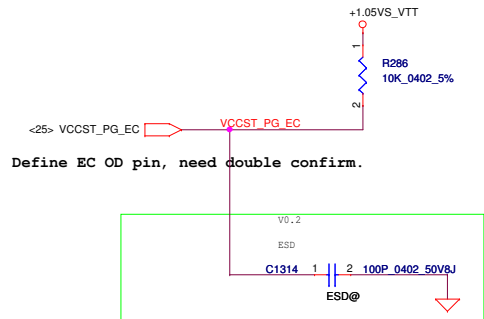
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Issued Date	2011/12/13	Deciphered Date	2012/12/13	Title		
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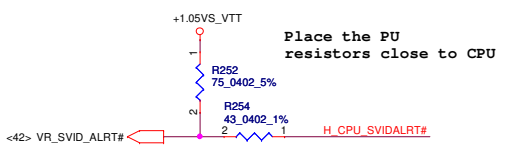




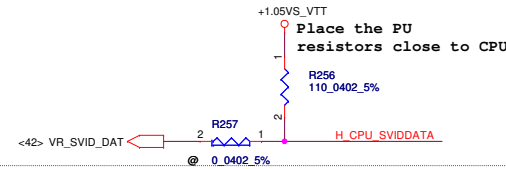
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Issued Date		2011/12/13		Deciphered Date		2012/12/13		Title		HSW MCP(7/11) PCIE,USB					
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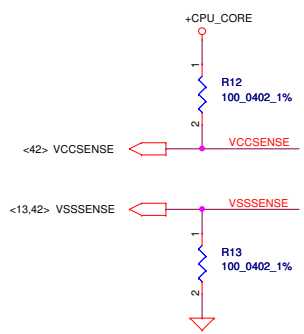
SVID ALERT



SVID DATA

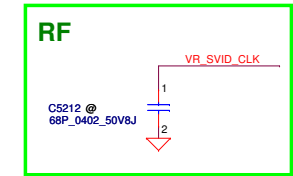


R256:
CRB r0.7 changed from 130 Ohms to 110 Ohms

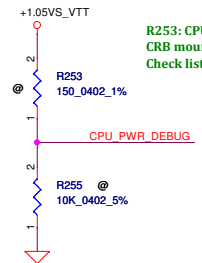


CAD Note: PU resistor should be close to CPU

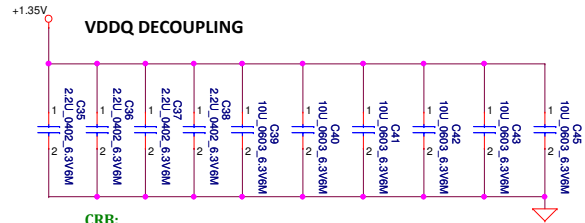
CAD Note: PD resistor should be close to CPU



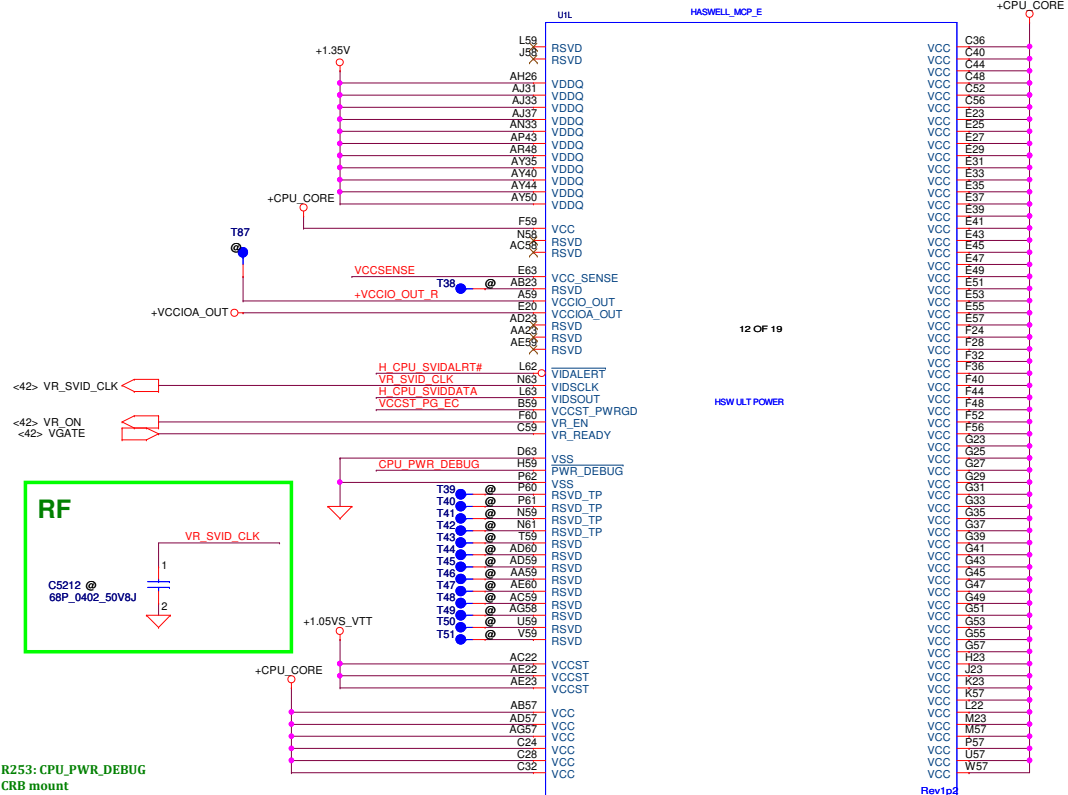
R253: CPU_PWR_DEBUG
CRB mount
Check list_XDP use only



VDDQ DECOUPLING

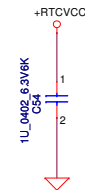


CRB:
+1.35V : 470UF/2V/7343 *2 (Un-mount)
10UF/6.3V/0603 *6
2.2UF/6.3V/0402 *4

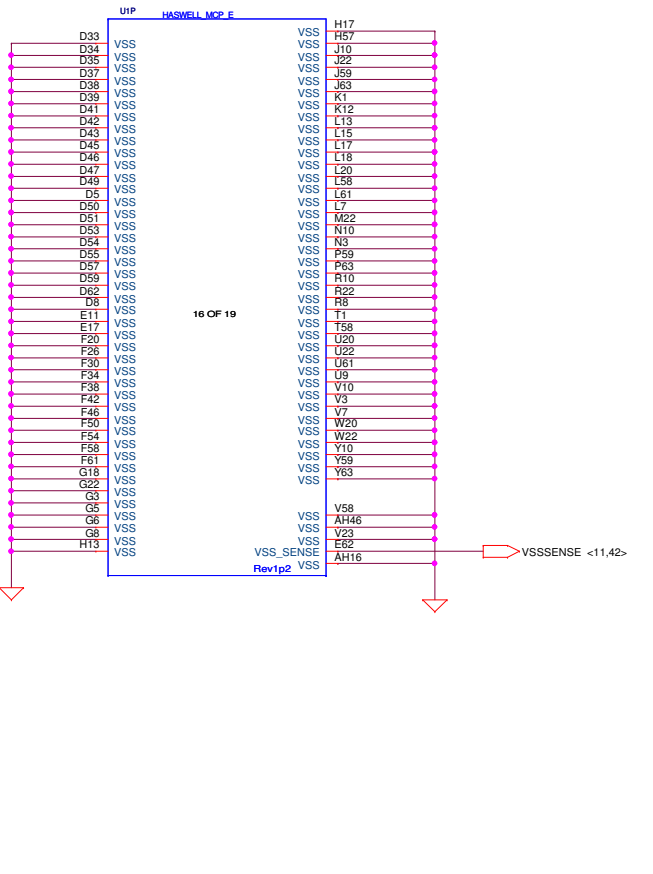
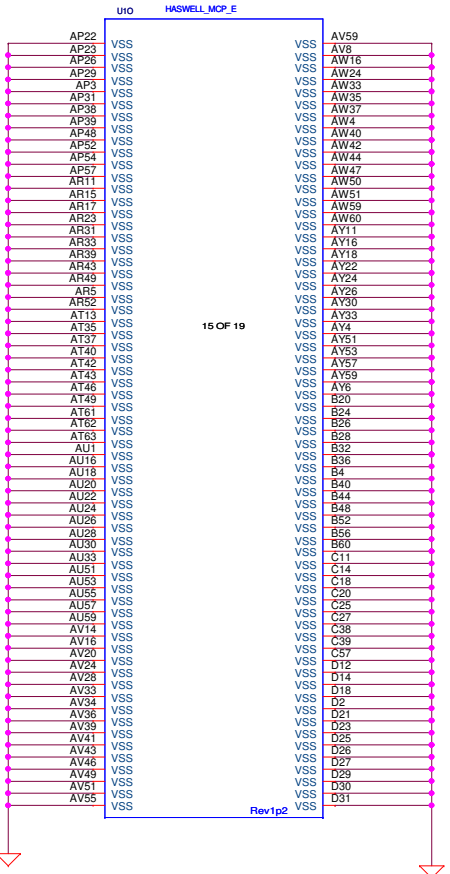
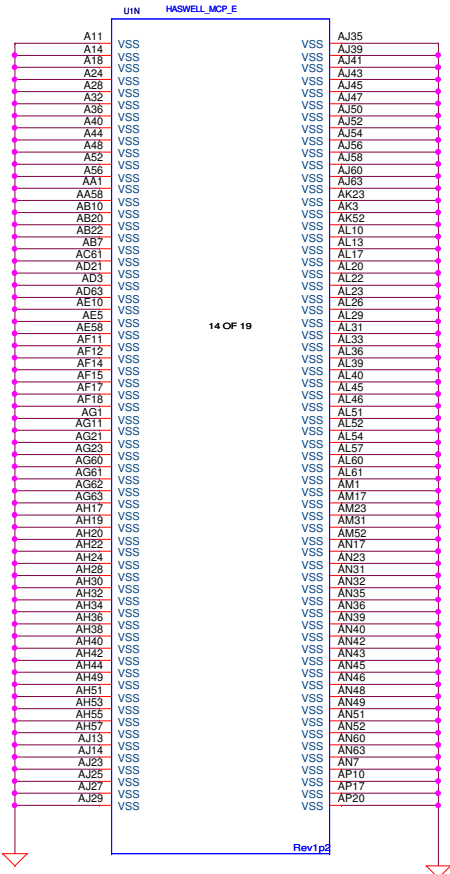


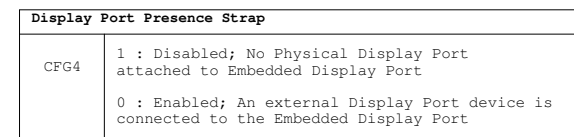
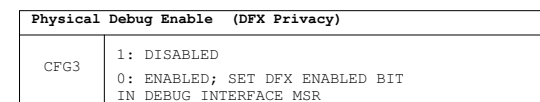
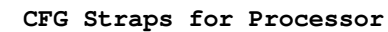
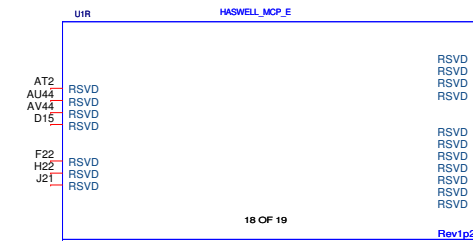
Close to AH14

+3VALW_PCH C75 2 1 1U 0402 6.3V4Z



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All VREF traces should have 10 mil trace width

[illegible]

RB1.0.0.1uF*1 / 2.2uF*1

CRB1.0 10uF *1 / 1uF *4

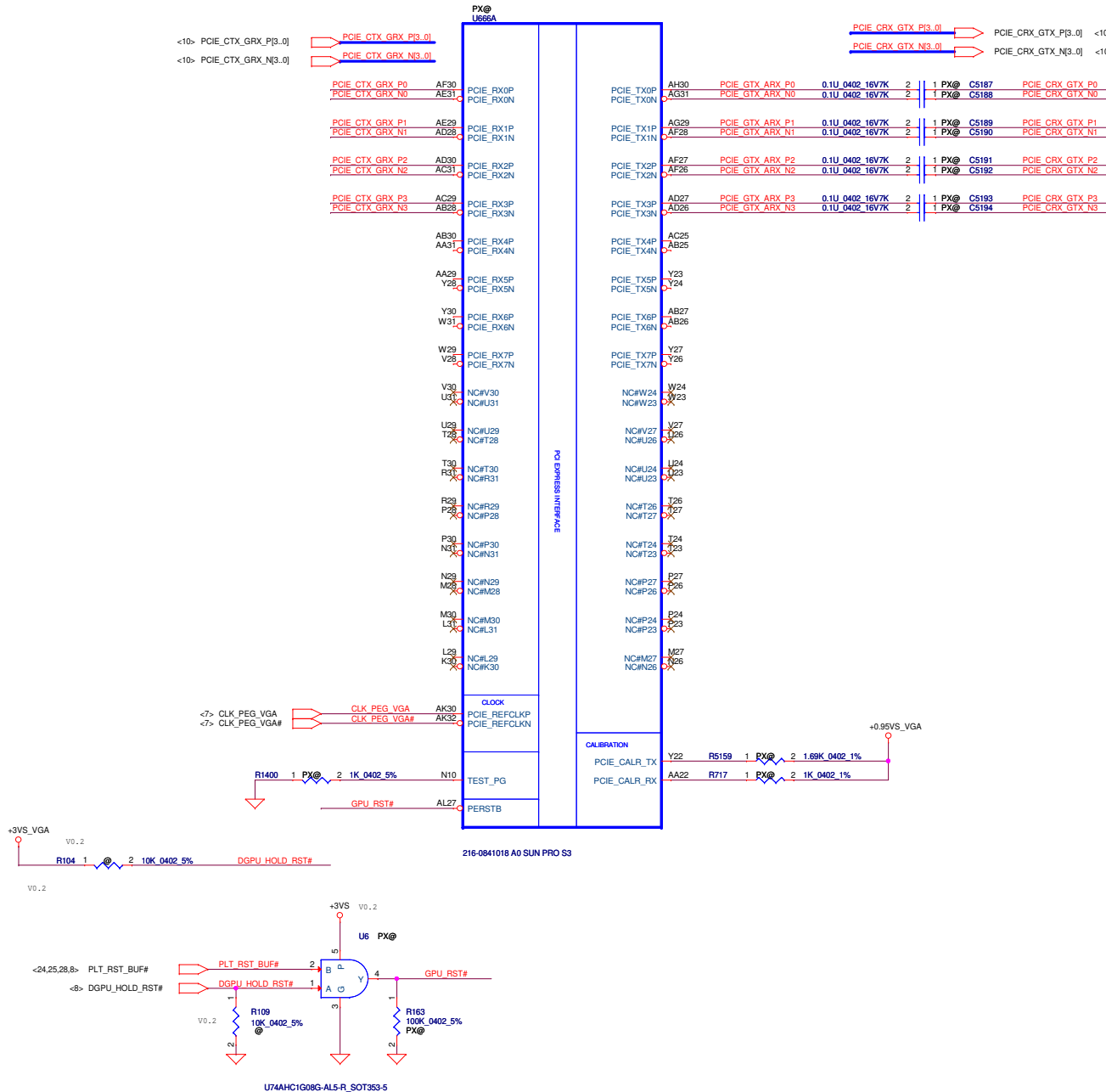
CHANNEL A /TYPE :Reverse / H:4mm

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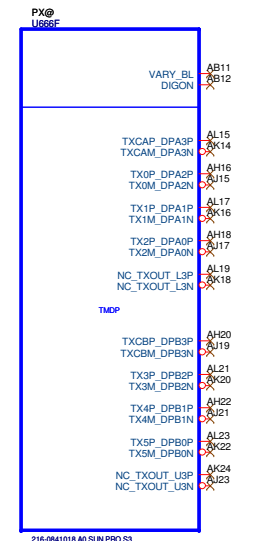
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BN 05070001 T00

PN:SP07000LT00

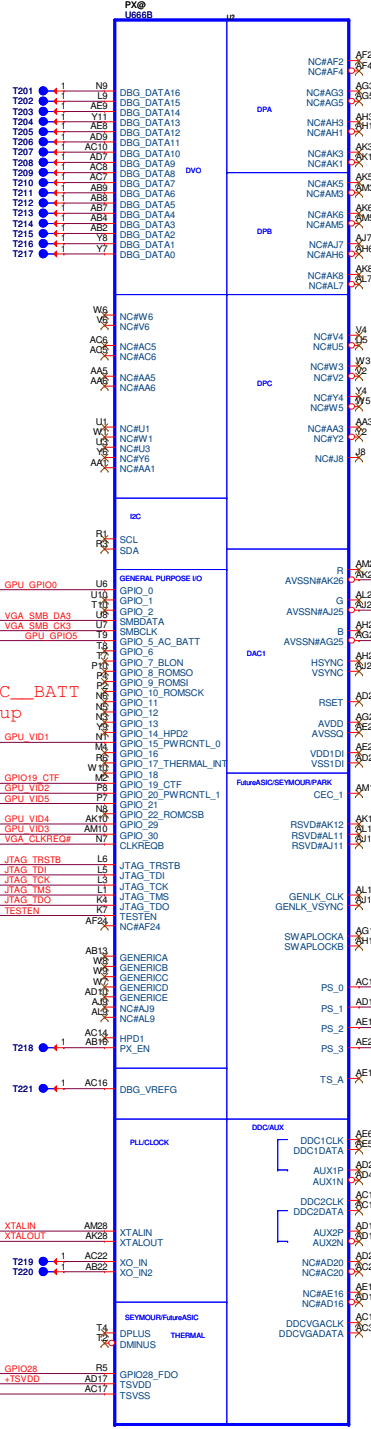
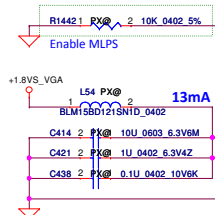
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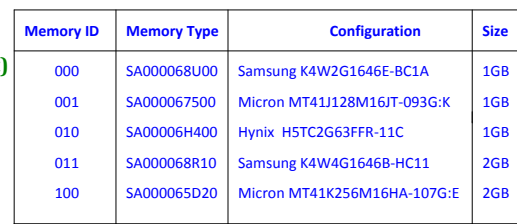
No Use GPU Display Port output



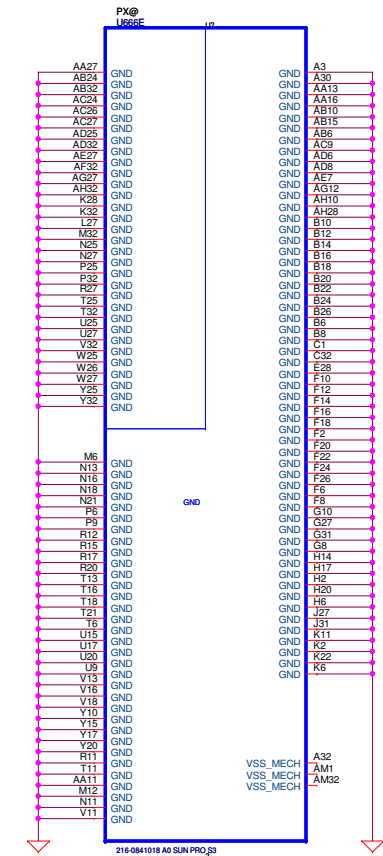
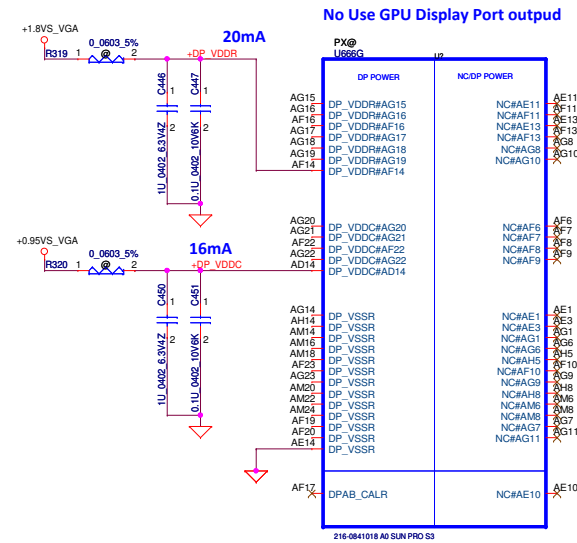
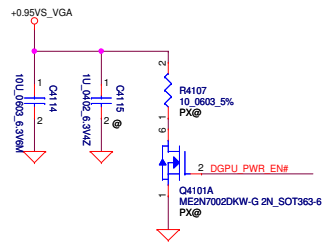
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Issued Date	2013/01/11	Deciphered Date	2013/12/31	Title	SUN_PCIE/DP
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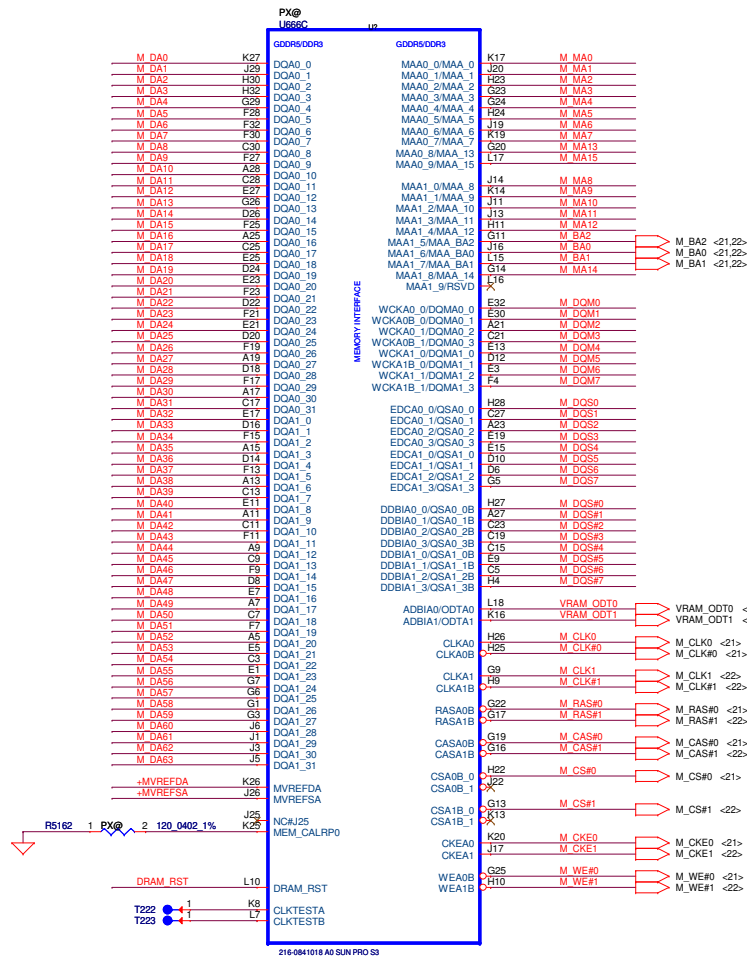
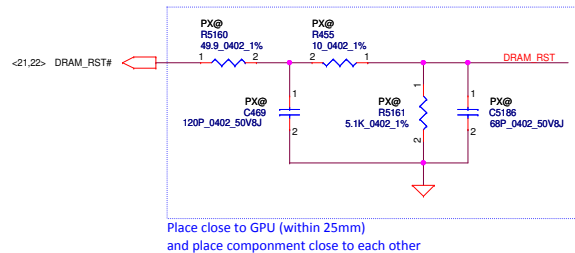
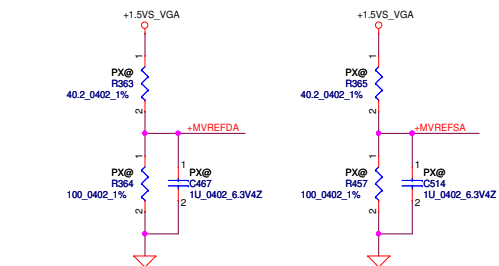
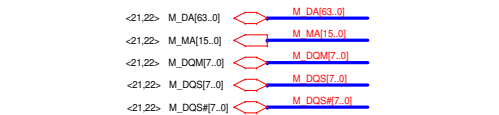


Cap (nF)	Bitd [5:4]
680nF	00
82nF	01
10nF	10
NC	11



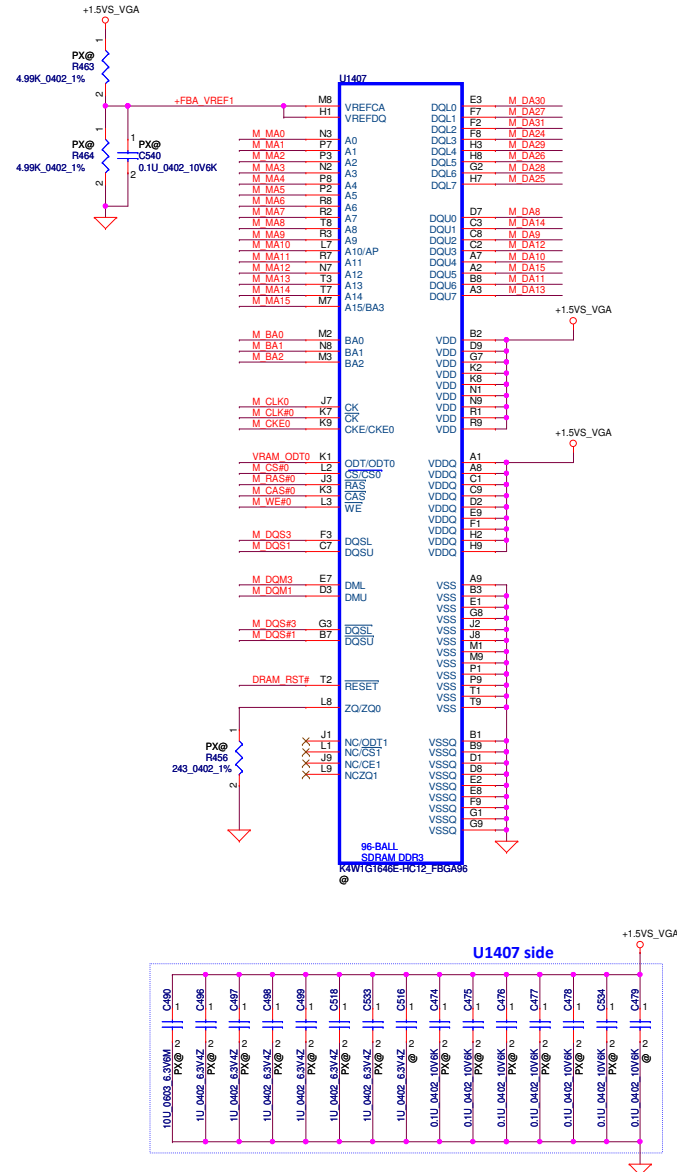
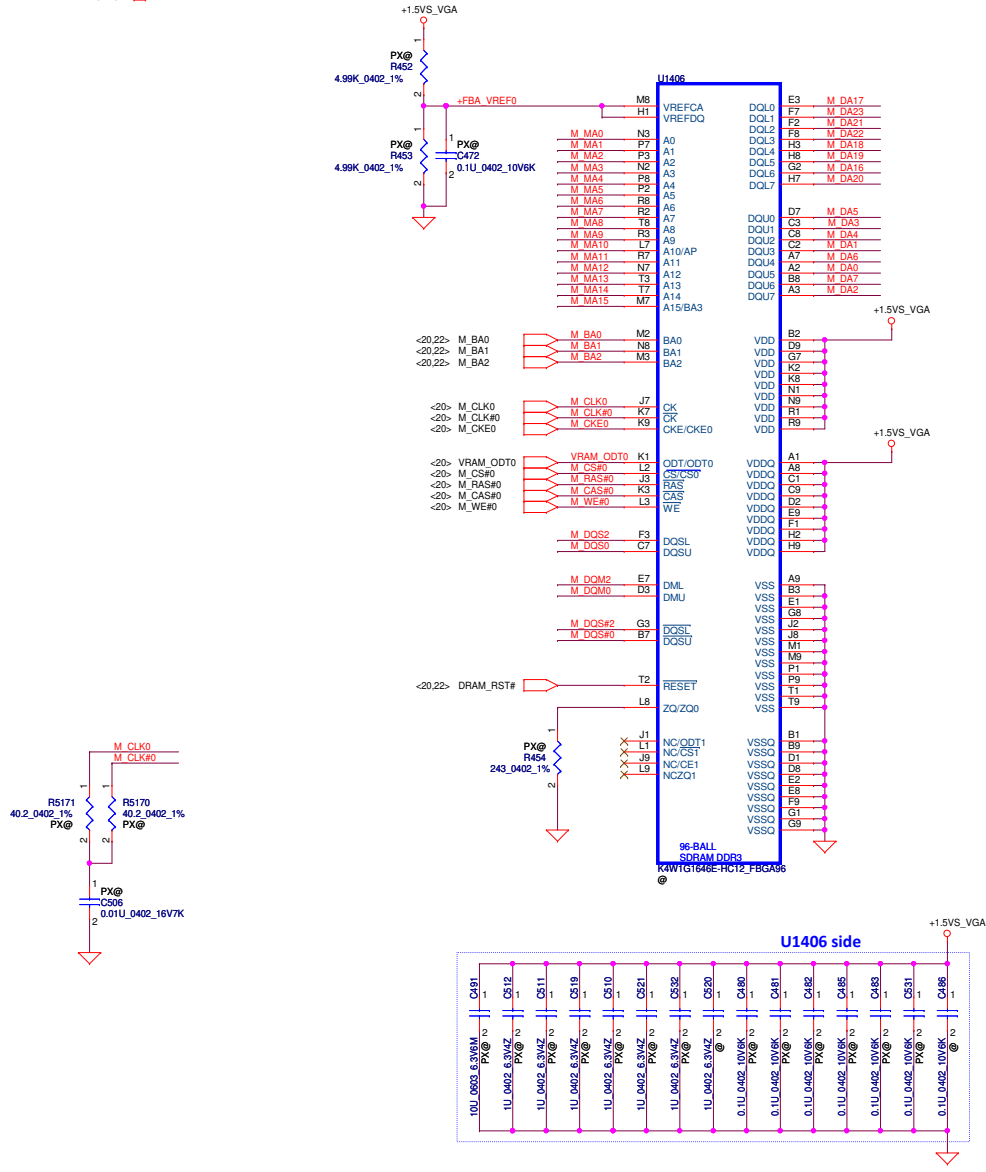
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010	SA00006H400	Hynix H5TC2G63FFR-11C	1GB
011	SA000068R10	Samsung K4W4G1646B-HC11	2GB
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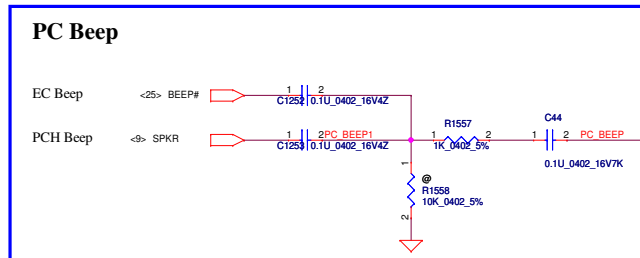
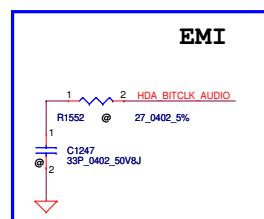
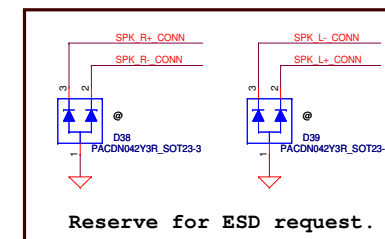
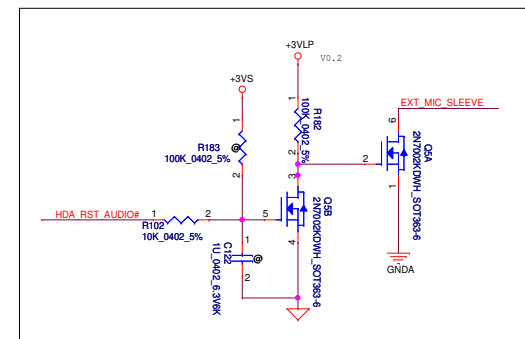
Memory Partition A - Lower 32 bits

- <20,22> M_DA[63..0] M_DA[63..0]
- <20,22> M_MA[15..0] M_MA[15..0]
- <20,22> M_DQM[7..0] M_DQM[7..0]
- <20,22> M_DQS[7..0] M_DQS[7..0]
- <20,22> M_DQS# [7..0] M_DQS# [7..0]

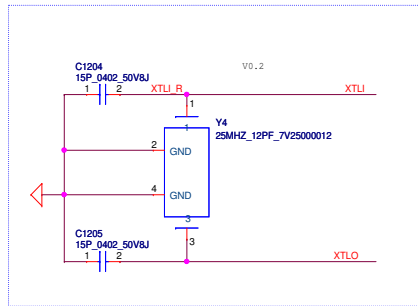
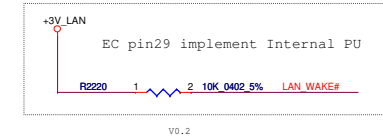
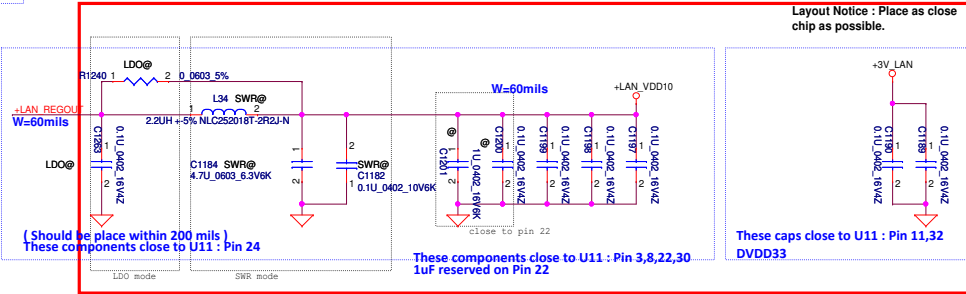
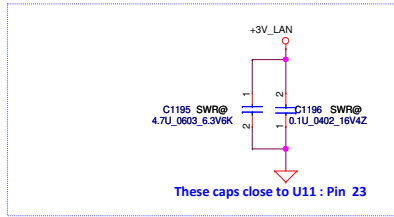
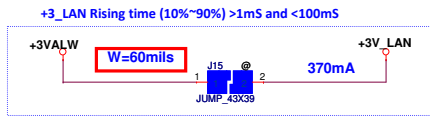


[illegible]

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				Custom	0.2
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					LA-A321PR02	0.2
				Date: Thursday, March 14, 2013	Sheet 23 of 46	

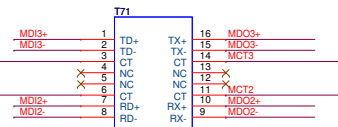
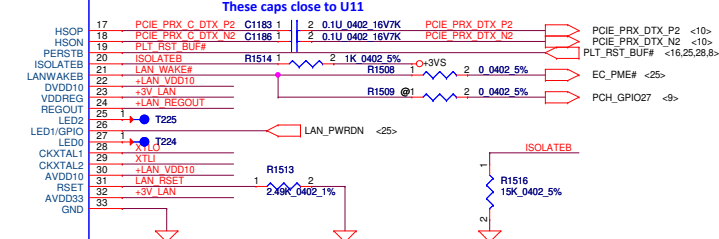


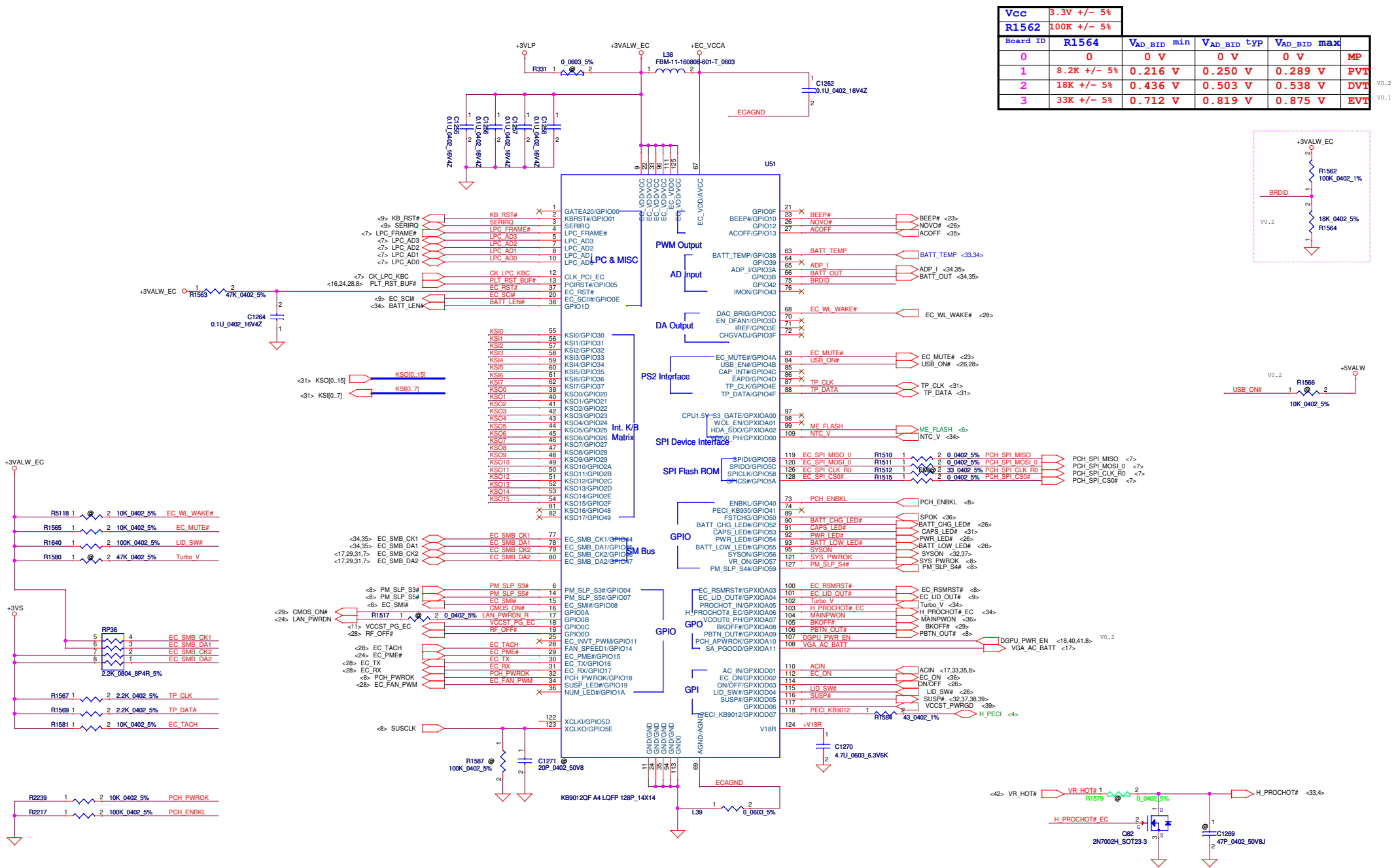
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<10> PCIE_PTX_C_DRX_P2
<10> PCIE_PTX_C_DRX_N2
<7> CLK_PCIE_LAN
<7> CLK_PCIE_LAN#

MDIO+ 1 MDIP0
MDIO- 2 MDIN0
+LAN_VDDIO 3 AVDD10
MDI1+ 4 MDIP1
MDI1- 5 MDIN1
MDI2+ 6 MDIP2
MDI2- 7 MDIN2
+LAN_VDDIO 8 AVDD10
MDI3+ 9 MDIP3
MDI3- 10 MDIN3
+3V_LAN 11 CLKREQB
LAN_CLKREQ# 12 HSIP
LAN_CLKREQ# 13 HSIN
REFCLK_P 14 RSET
REFCLK_N 15 AVDD33
GND 16

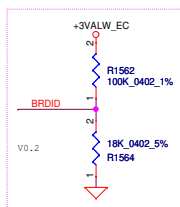
U11
8106@
RTL8106EUS
SA00006ML00

U11
RTL8111GUS-QF_N32_4X4
SA00006ML00



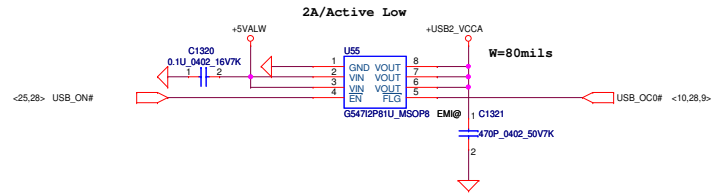
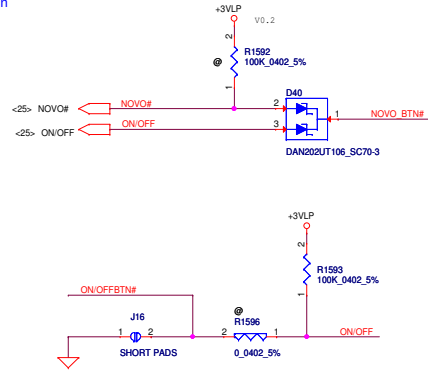


Vcc	3.3V +/- 5%
R1562	100K +/- 5%
Board ID	R1564
VAD_BID min	0 V
VAD_BID typ	0 V
VAD_BID max	0 V
0	0
1	8.2K +/- 5%
2	18K +/- 5%
3	33K +/- 5%
	0.216 V
	0.250 V
	0.289 V
	0.436 V
	0.503 V
	0.538 V
	0.712 V
	0.819 V
	0.875 V
	MP
	PVT
	DVT
	EVT

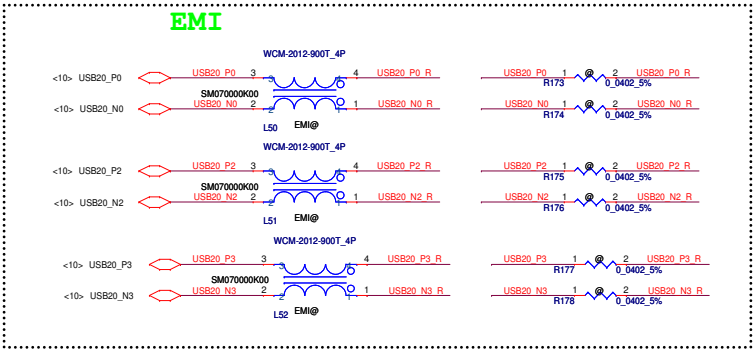
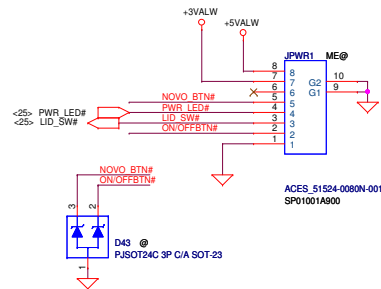


I/O Board CONN.

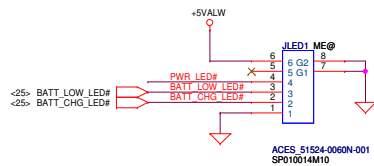
Connector: 0.3A / pin



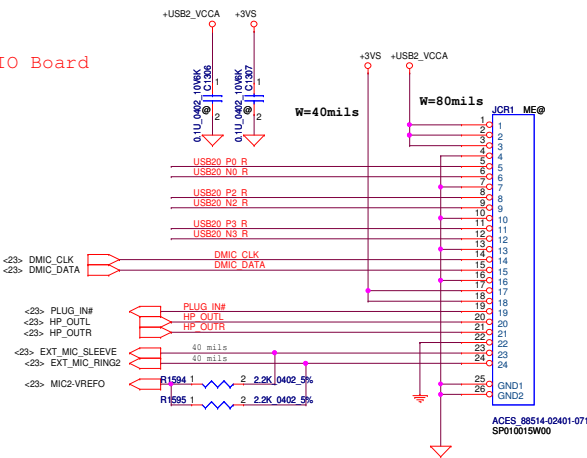
Power Board



LED Board

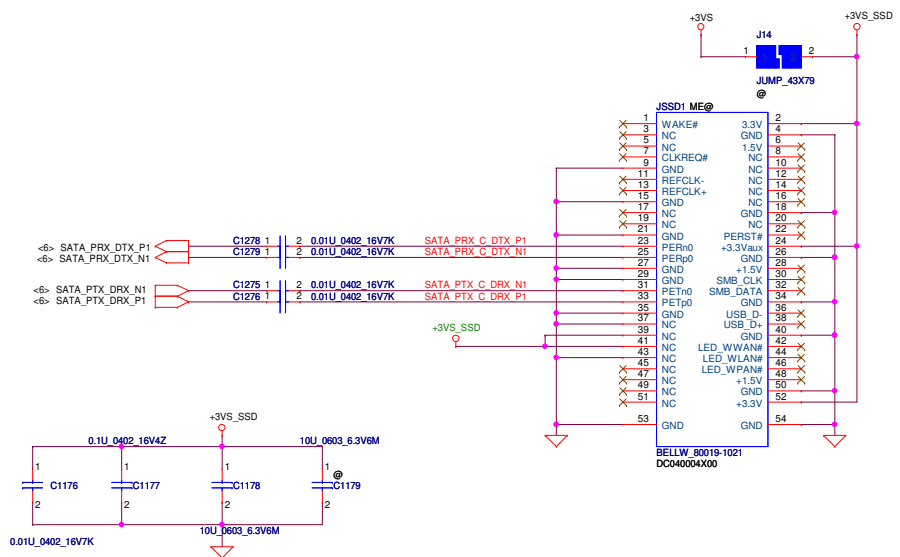


IO Board

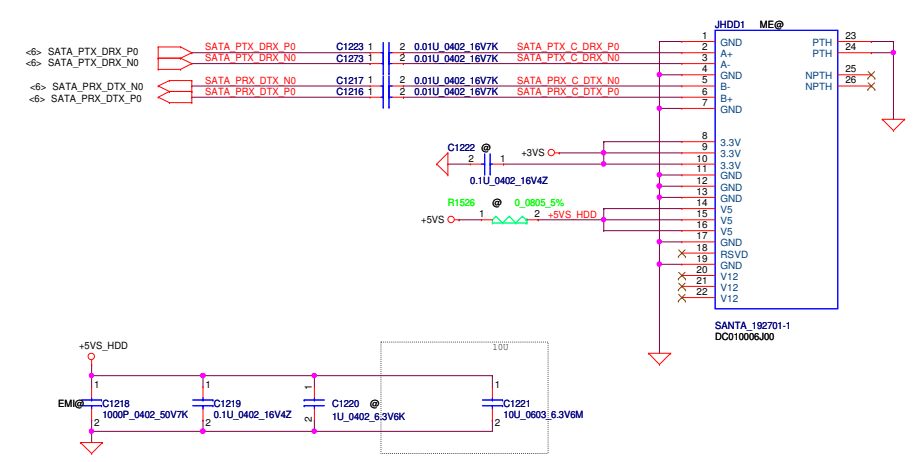


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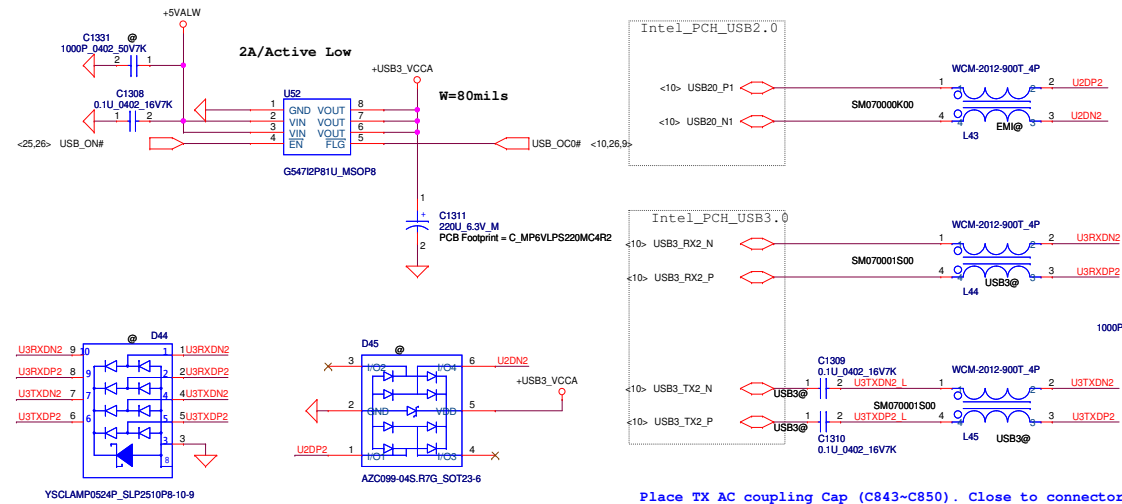
mSATA CONN.



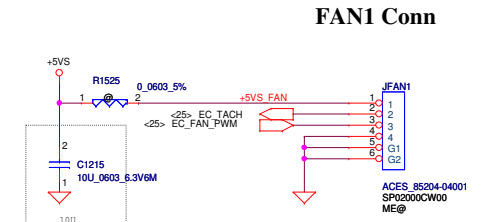
SATA HDD CONN.



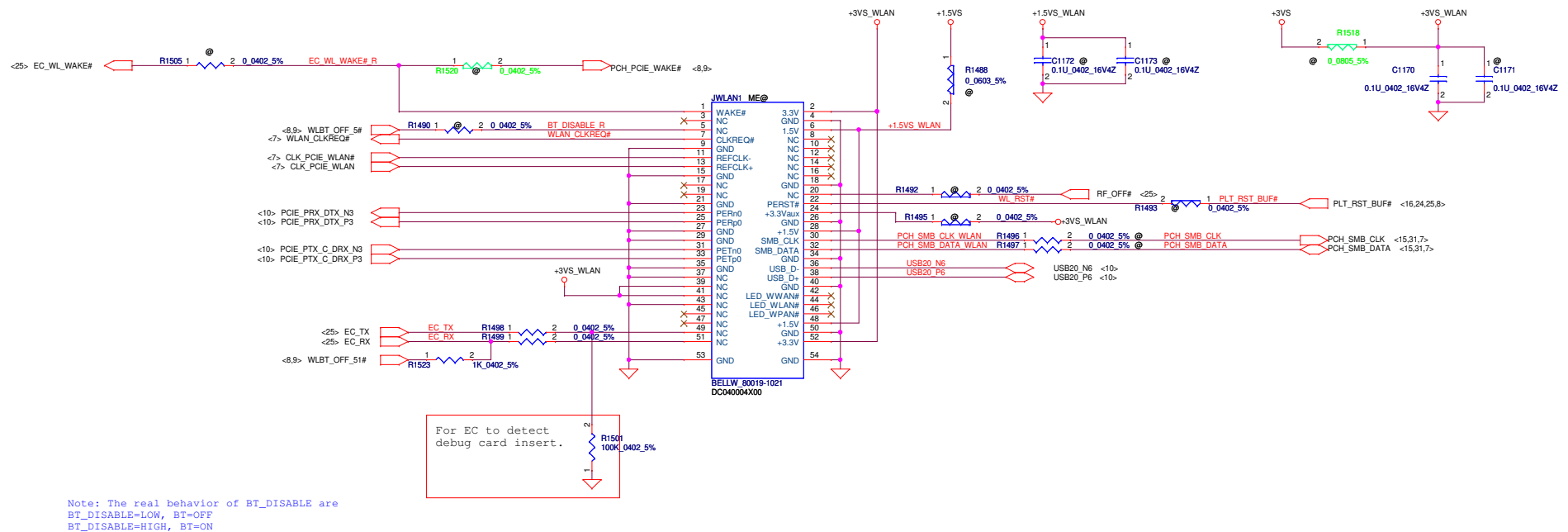
USB 3.0 Charger & Conn.



FAN CONN.

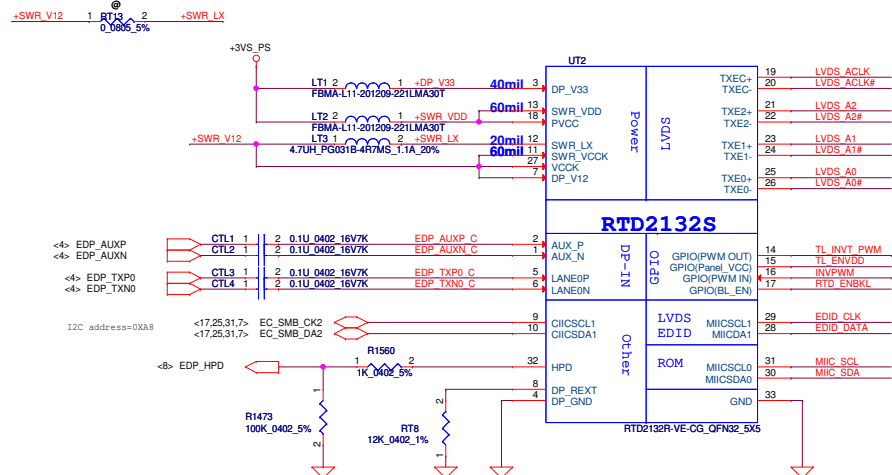


Mini-Express Card for WLAN(Half)



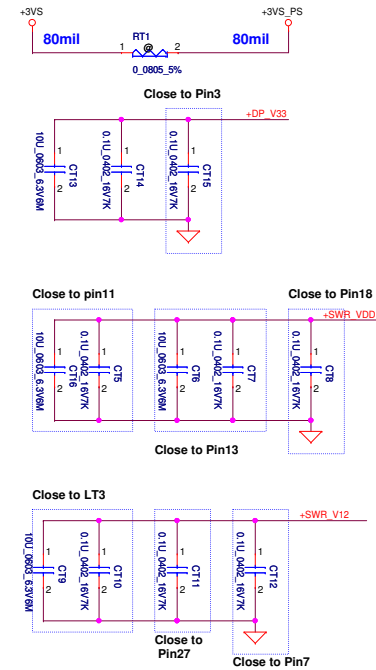
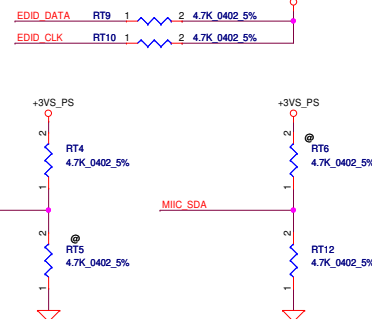
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Issued Date	2011/07/21	Deciphered Date	2012/12/31	Mini-Card	
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RTD2132R LDO MODE

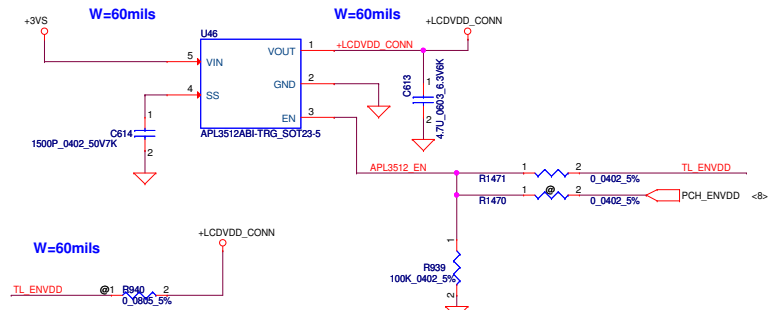


MIIC_SDA		0	1
MIIC_SCL		0	1
		X	EC CODE
		1	Internal ROM
			EEPROM

TL_ENVDD need 60 mil if use for LVDS power on R version

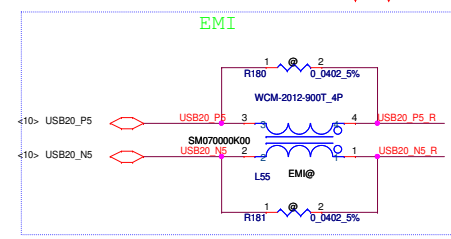
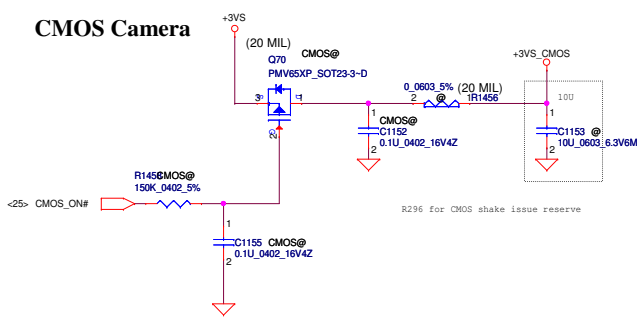


LCD POWER CIRCUIT

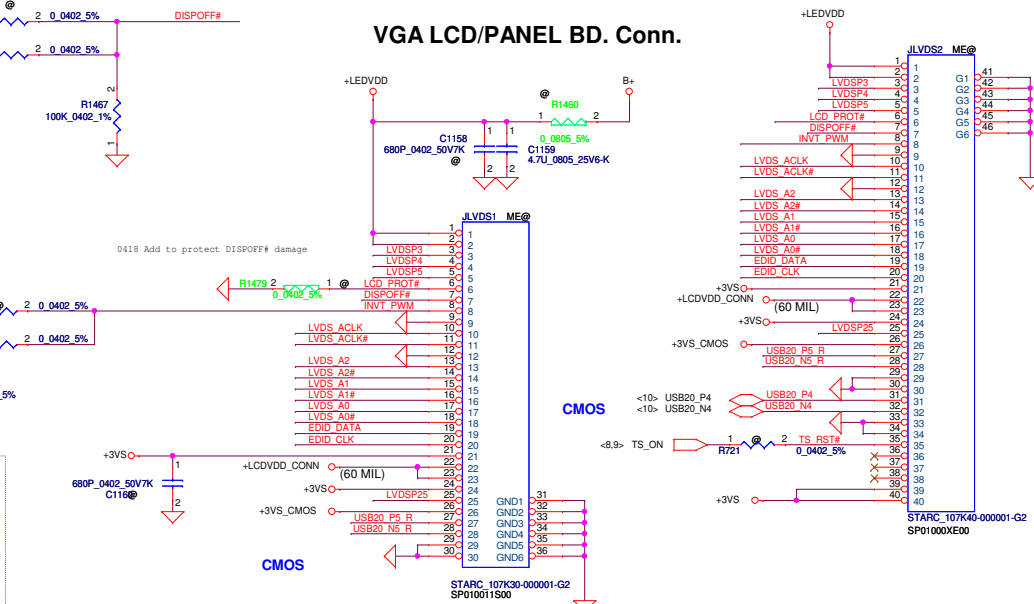


RTD2132R Internal load switch for +LCD_VCC

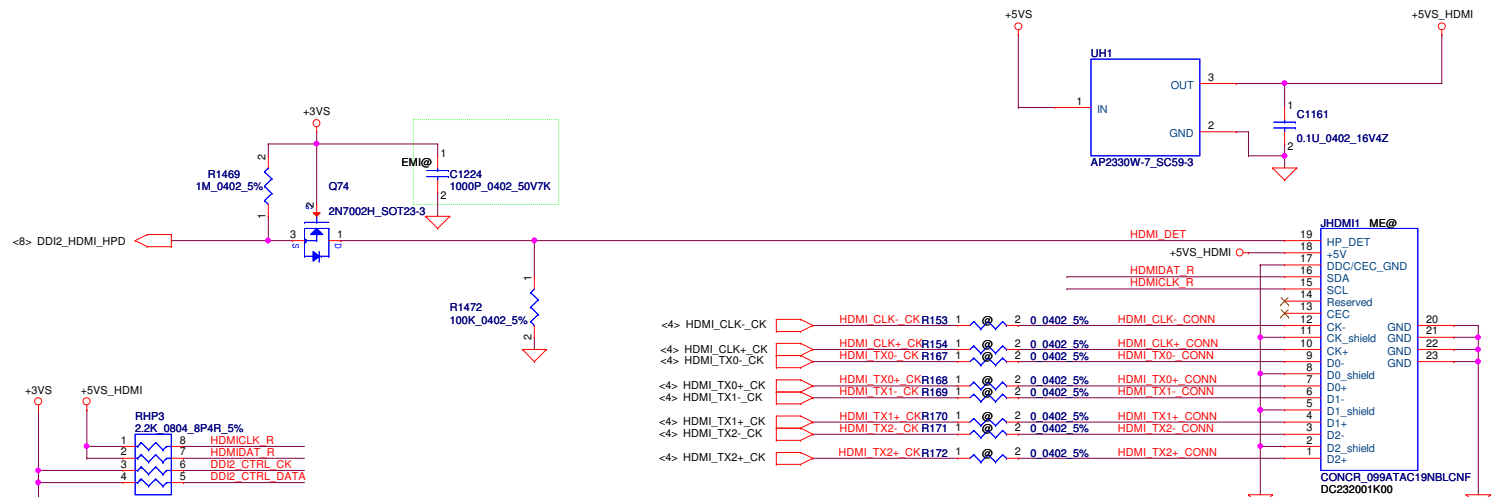
CMOS Camera



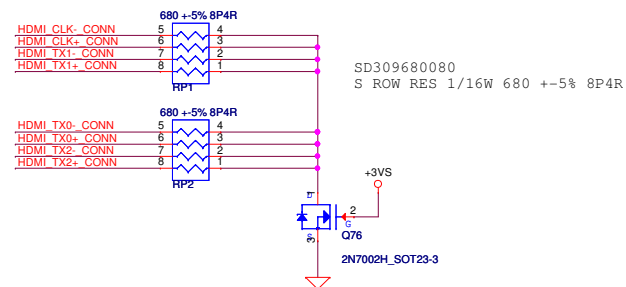
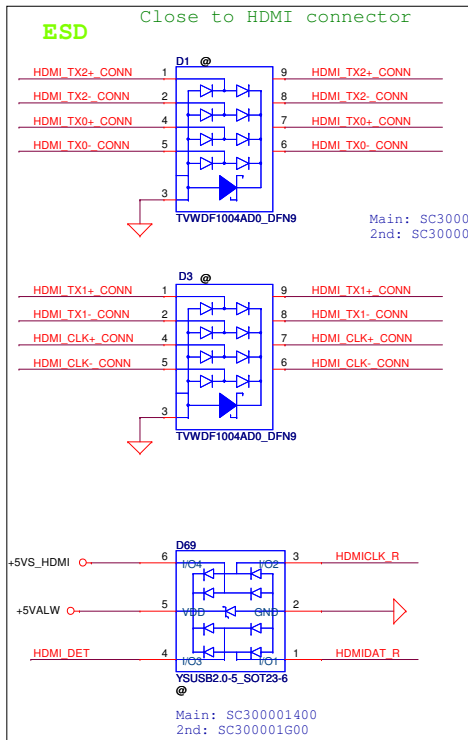
VGA LCD/PANEL BD. Conn.



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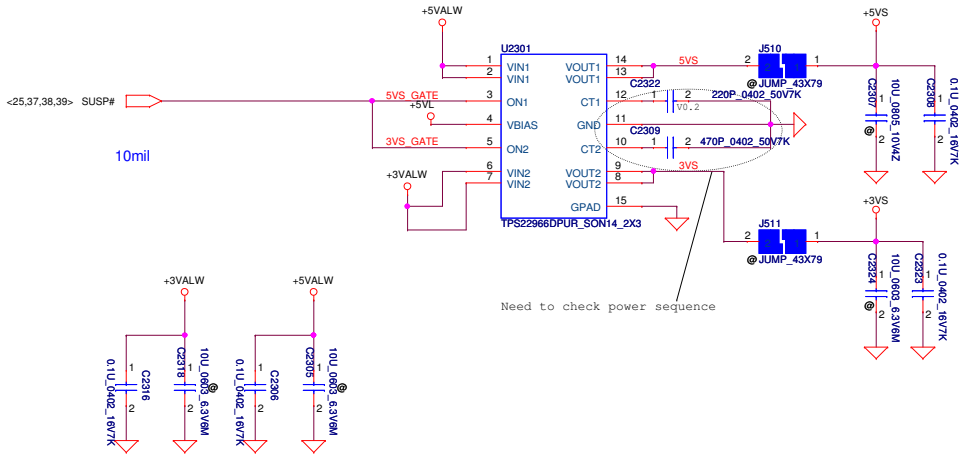


Change Symbol to main source 02/24

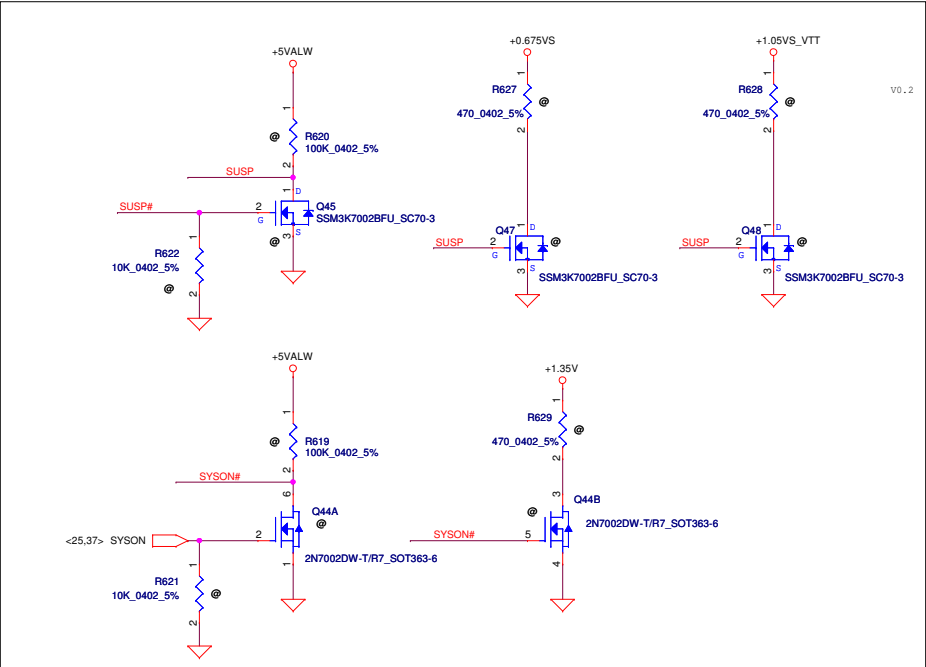
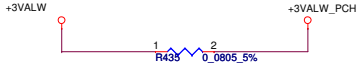


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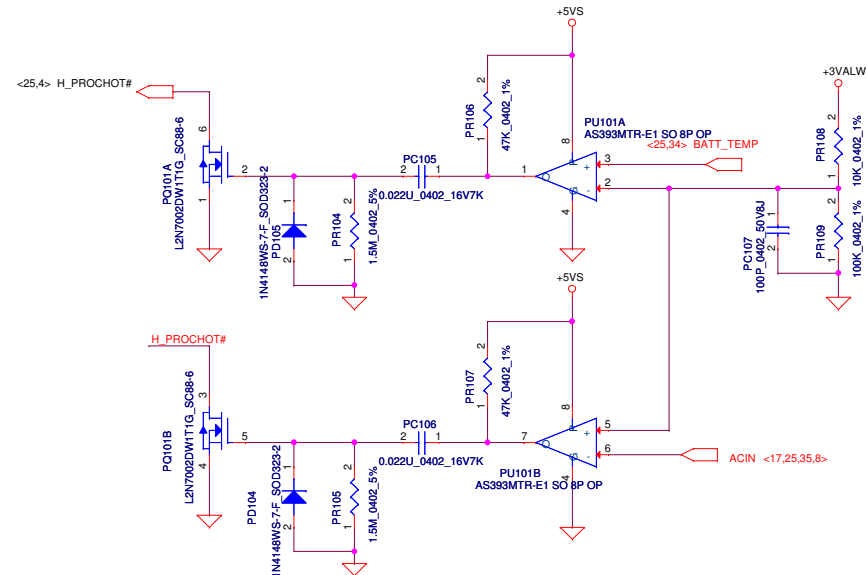
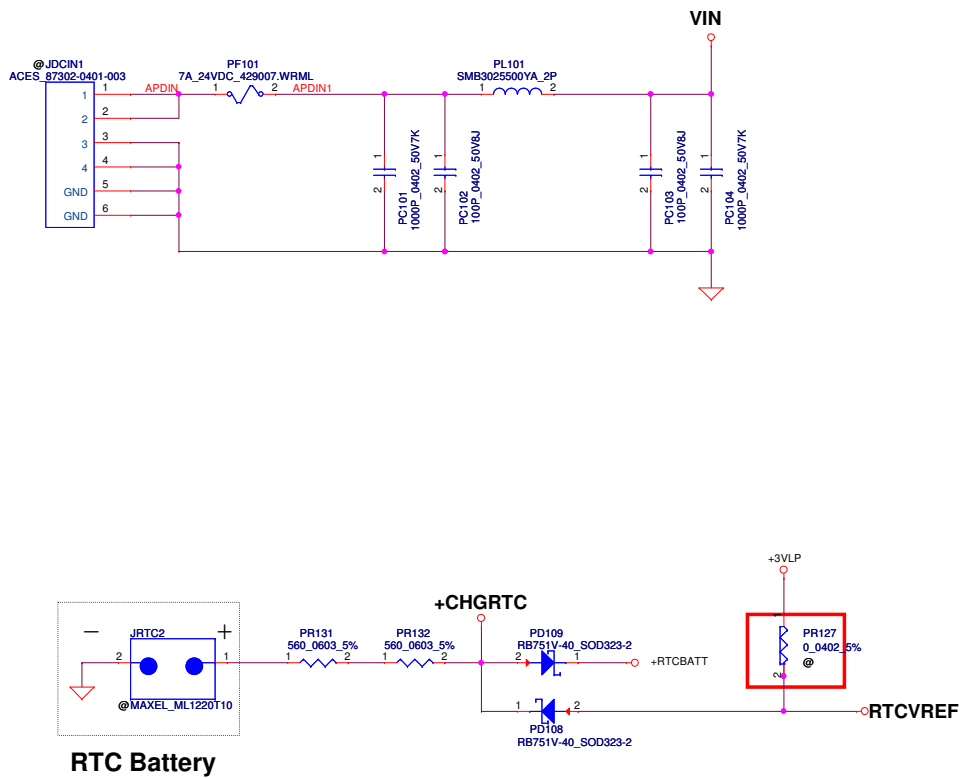
+5VALW TO +5VS
+3VALW TO +3VS



+3VALW TO +3VALW(PCH AUX Power)
+5VALW TO +5VALW(PCH AUX Power)



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Power Supply Section Schematic

Inputs:

- JBATT1:** 9-pin connector (1-9). Pin 1 is connected to VMB2. Pin 2 is connected to the positive terminal of the 12A_65V_451012MRL capacitor.
- JBATT2:** 9-pin connector (1-9). Pin 1 is connected to the positive terminal of the 6.49K_0402_1% resistor.

Components:

- Resistors:**
 - R201: 100_0402_1%
 - R202: 100_0402_1%
 - R203: 6.49K_0402_1%
 - R204: 6.49K_0402_1%
 - R205: 10K_0402_5%
- Capacitors:**
 - PC201: 1000P_0402_50V7K
 - PC202: 0.01U_0402_50V7K

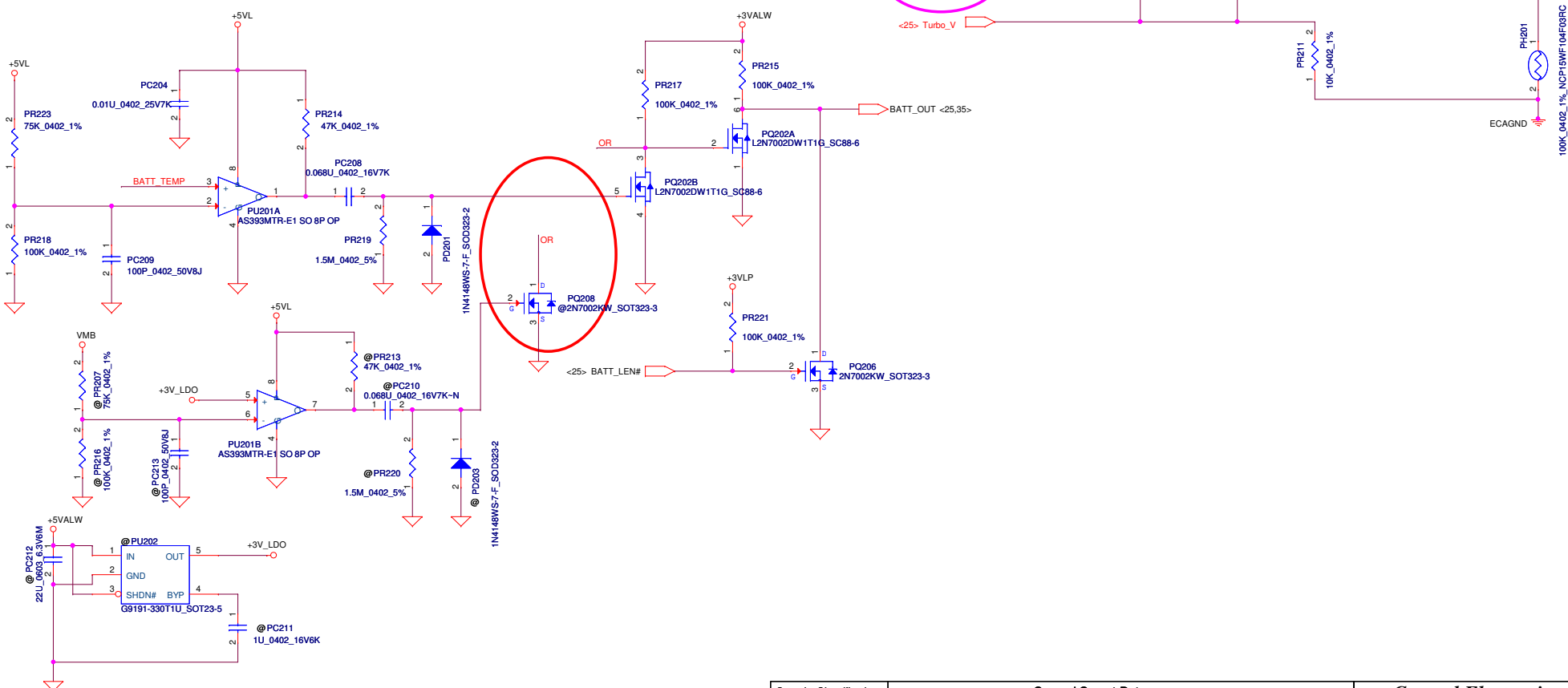
Outputs:

- BATT+:** Positive terminal of the 0.01U_0402_50V7K capacitor.
- EC_SMB_CK1:** ~25,35
- EC_SMB_DA1:** ~25,35
- +3VALW:** ~3VALW
- +3VLP:** ~3VLP
- BATT_TEMP:** ~25,33

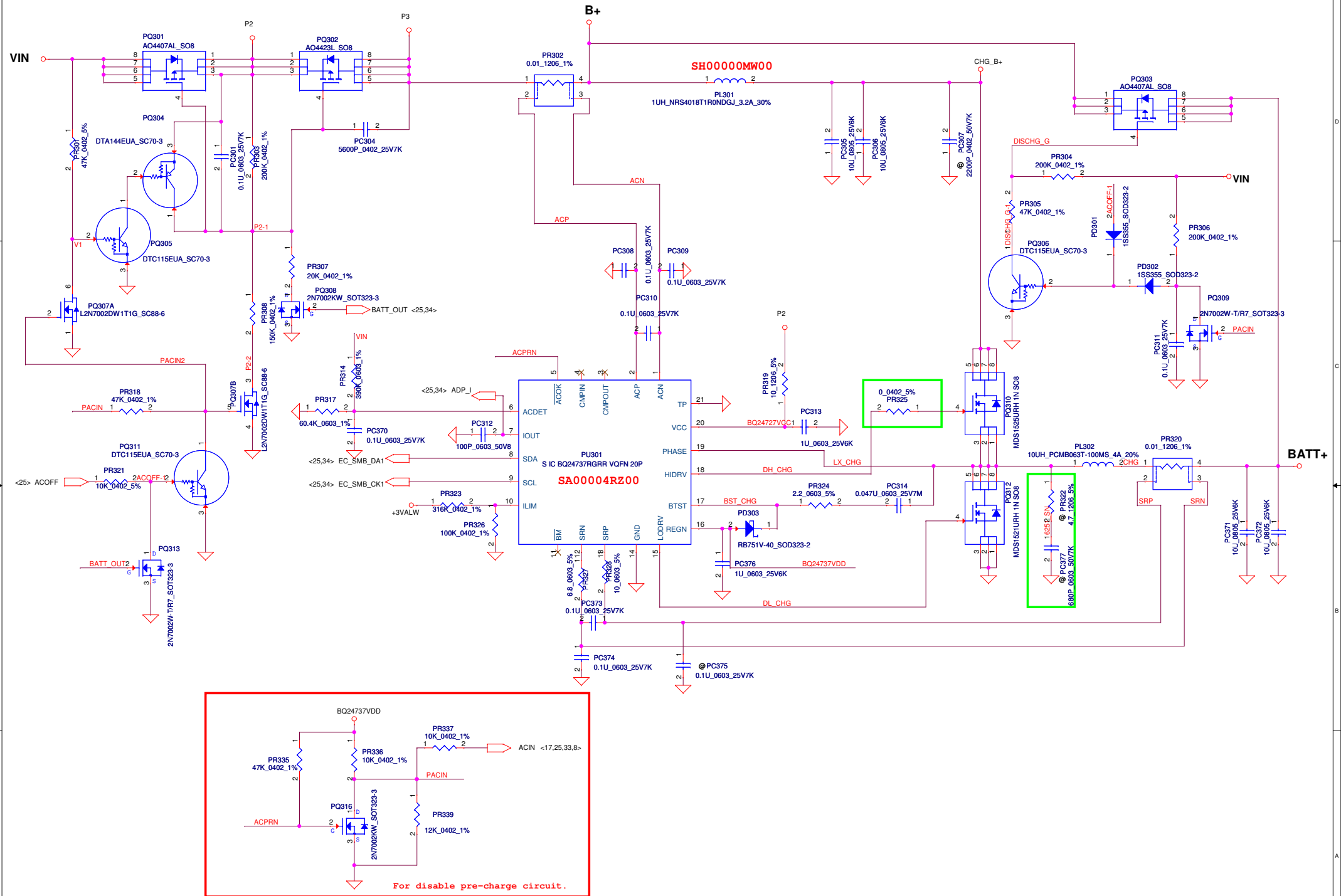
Notes:

- A/D**

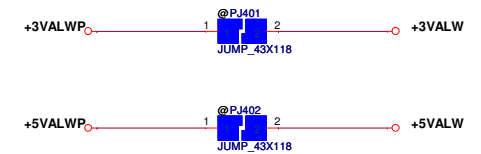
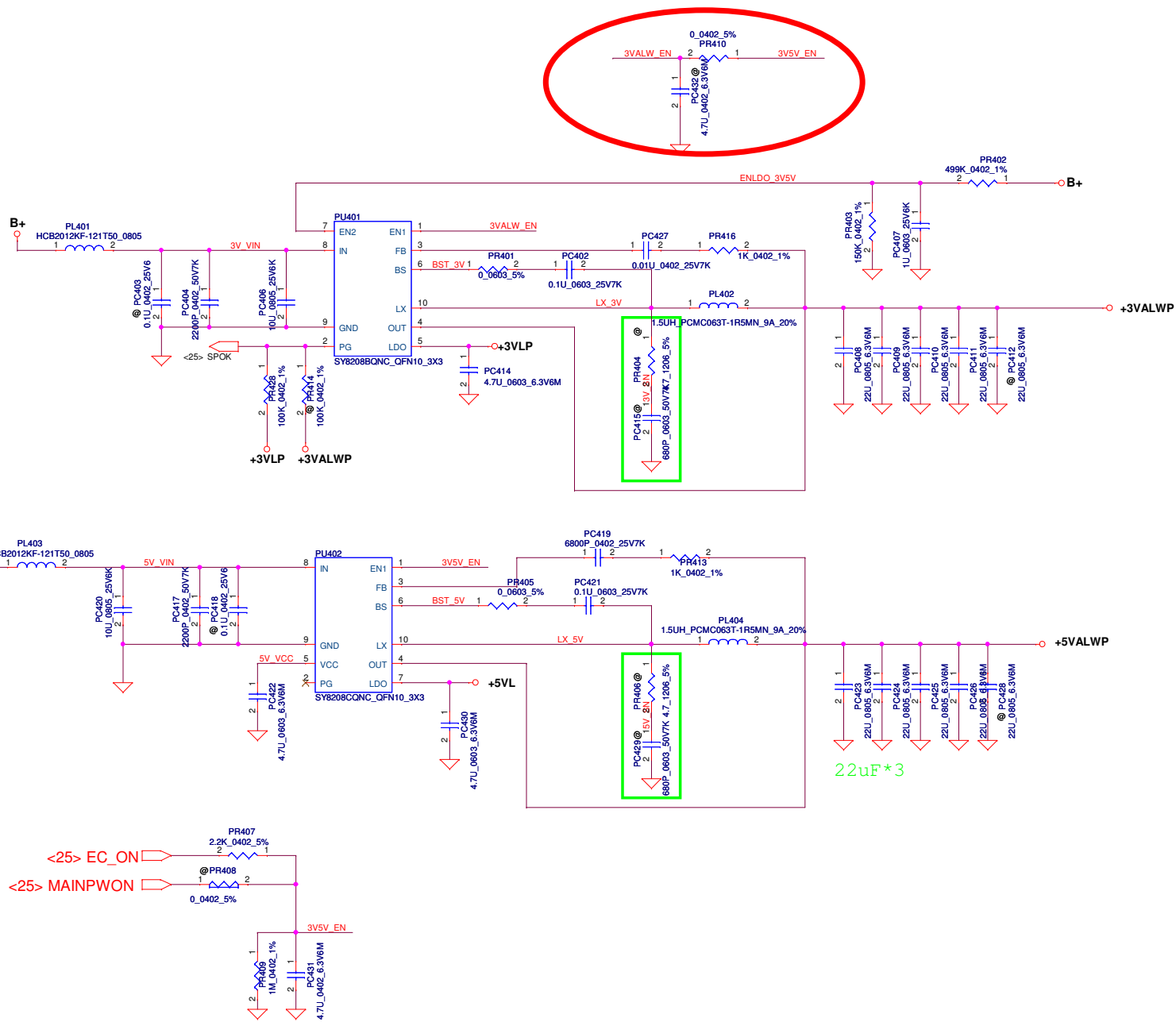
65W(UMA) : PR205=1.65K(SD034165180)



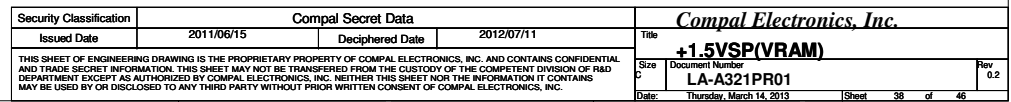
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Issued Date	2010/01/25	Deciphered Date	2012/07/11	Title		
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					LA-A321PR01
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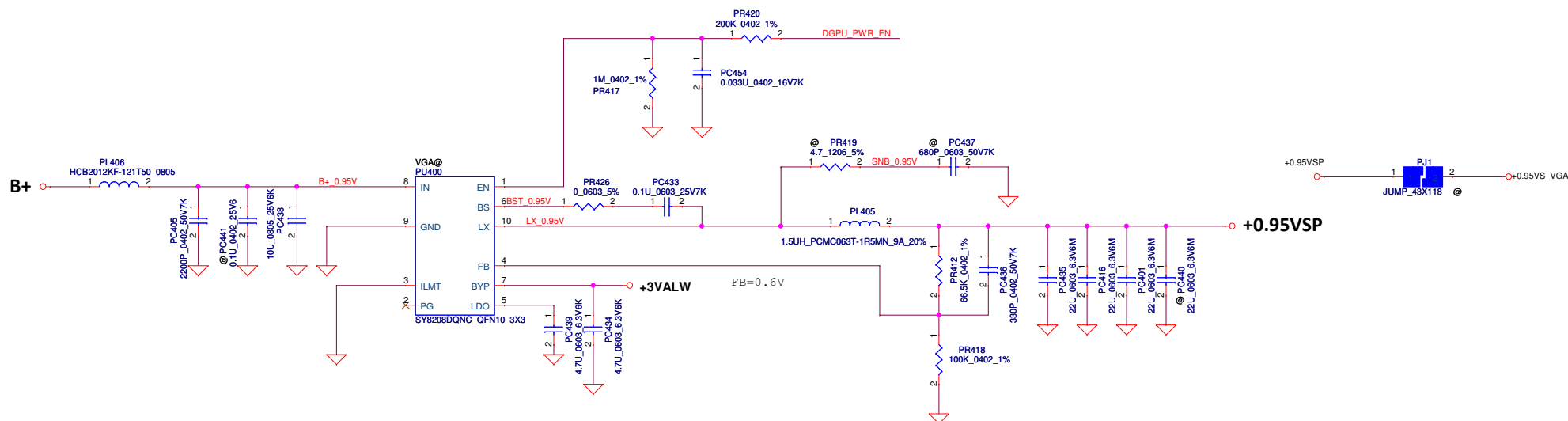
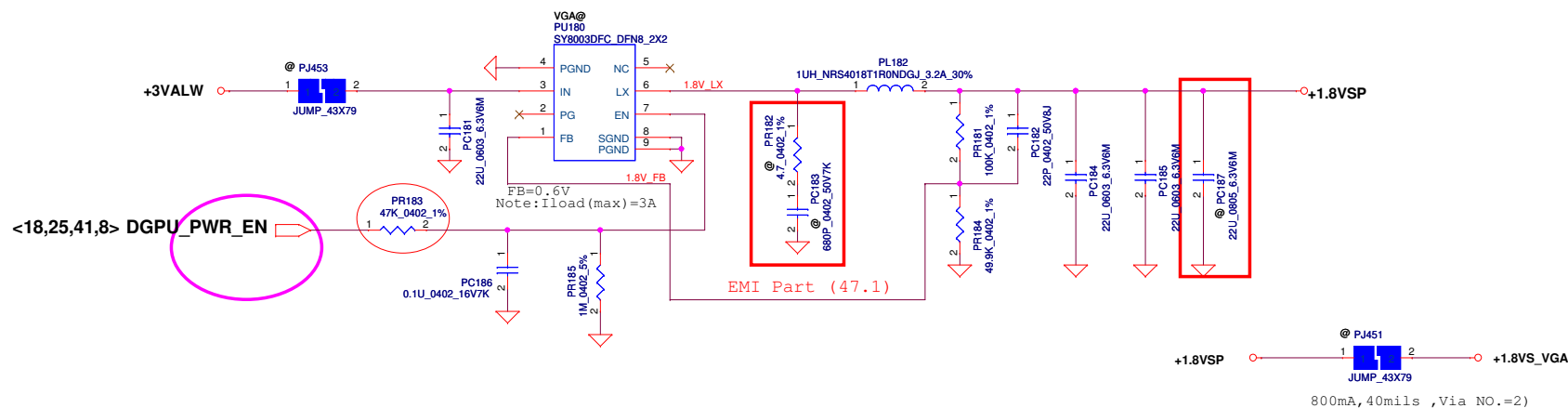


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				Date:	Thursday, March 14, 2013	Sheet	36 of 46

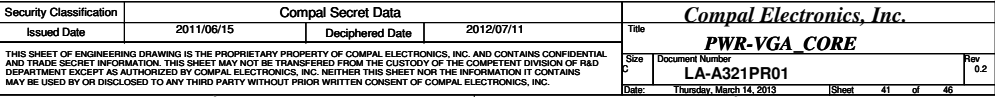


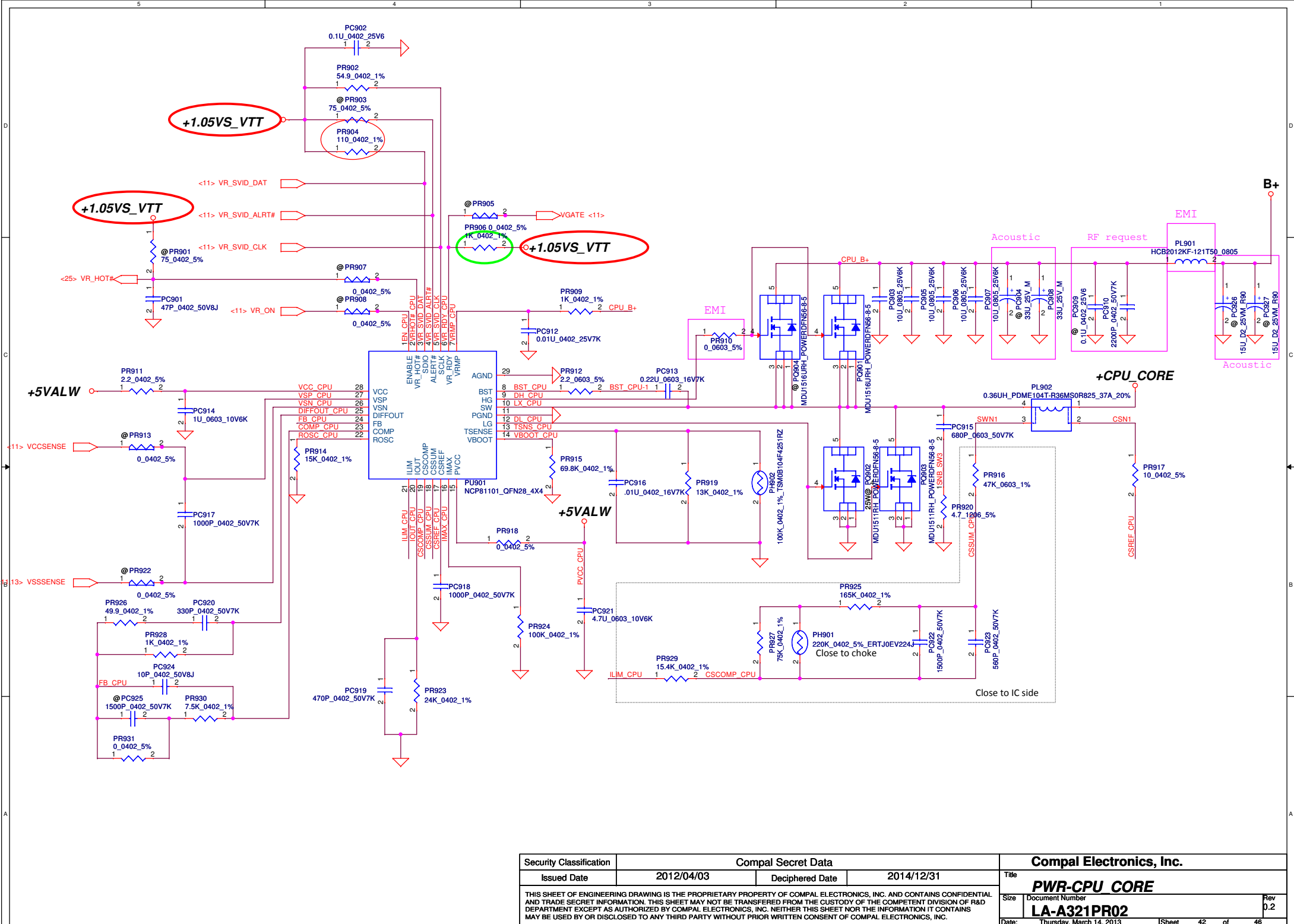
EMI

1.8V controller (35.15), Support component (35.16)



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+CPU_CORE

30 X 22u/0805

RF request

Reserve

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Version change list (P.I.R. List)

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for PWR

Item	Modify List	PG#	Reason for change	Date	Phase
1	For HW sequence	44	ADD PR821 100K ohm pull high +VS_VGA	3/11	SDV
2	For HW sequence	37	Remove PC512 0.1U_0402	3/11	SDV
3	For HW sequence	39	Change PC443 to 0.1U_0402	3/11	SDV
4	For HW sequence	40	Change PC186 to 0.1uf	2/7	SDV
5	For HW sequence	40	Change PC454 to 0.033uf	2/7	SDV
6	For HW sequence	40	Change PR183 to 47K ohm	2/7	SDV
7					
9					
10					
11					
12					
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				Size Custom	
Document Number				Rev	
C38-AMD KABINI Schematic				0.2	
Date: Thursday, March 14, 2013				Sheet 44 of 46	

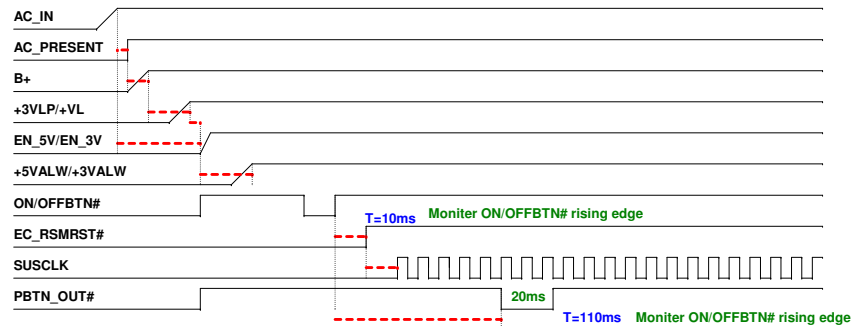
Version change list (P.I.R. List)

Page 1 of 1
for HW

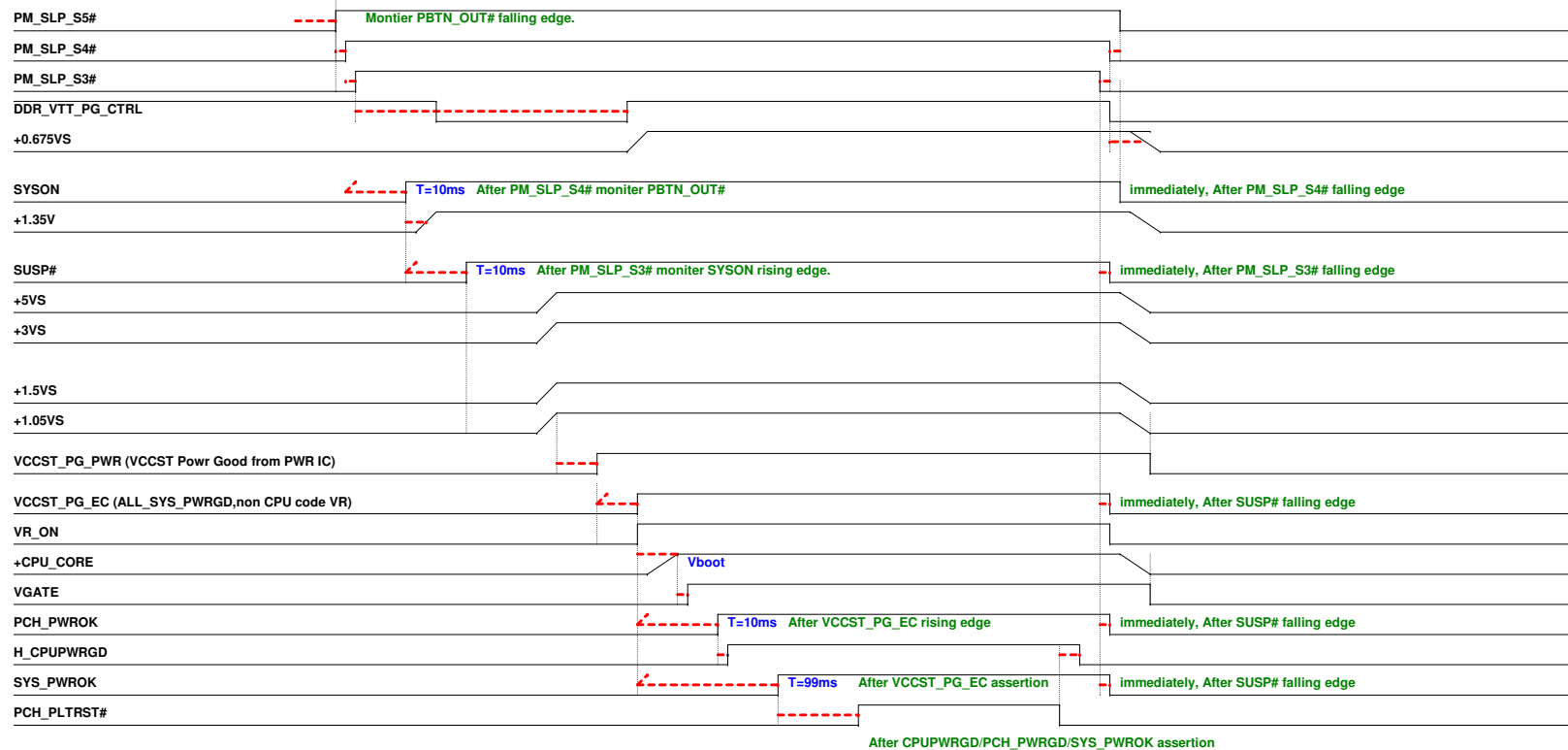
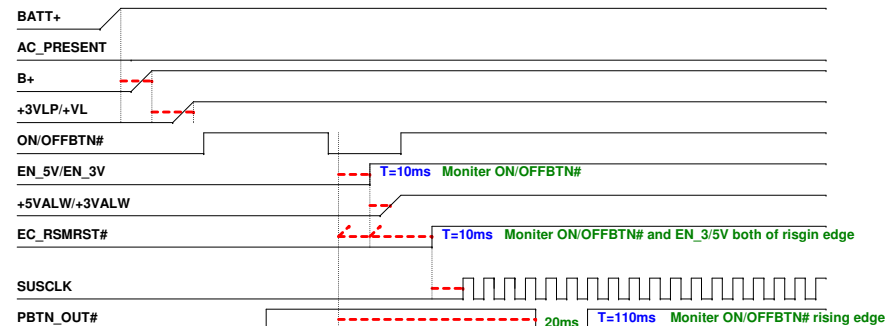
Item	Reason for change	PG#	Modify List	Date	Phase
1					
2					
3					
4					
5					
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