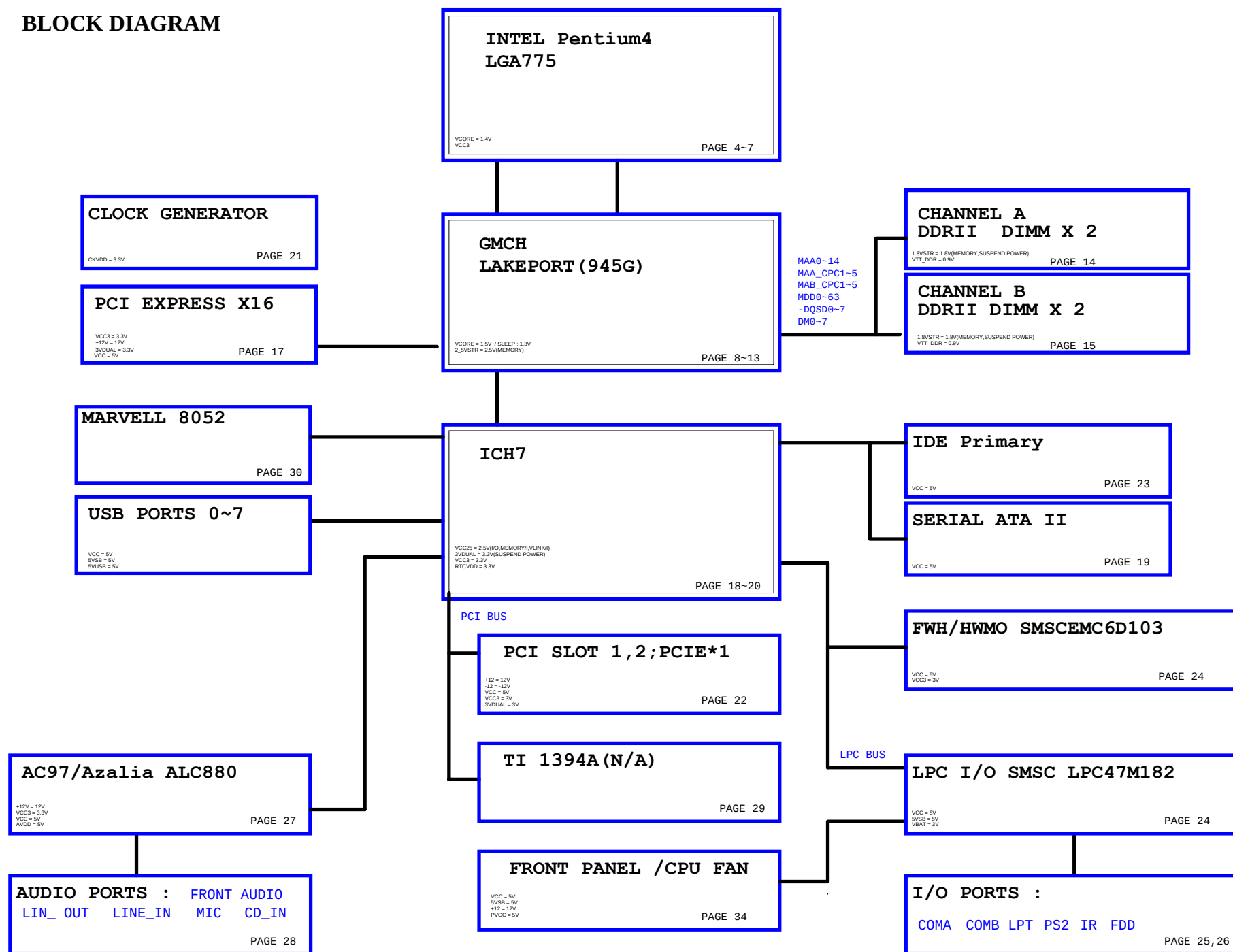


01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B
06	P4 LGA775 C
07	P4 LGA775 D,E,F,G
08	GMCH-LAKEPORT HOST
09	GMCH-LAKEPORT DDRII
10	GMCH-LAKEPORT PCI E, DMI
11	GMCH-LAKEPORT INT VGA
12	GMCH-LAKEPORT GND
13	GMCH-LAKEPORT PWR
14	DDRII CHANNEL A 1,2
15	DDRII CHANNEL B 1,2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	GB/CK410M CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY/USB
24	SMSC LPC IO
25	COM LPT
26	BIOS,CI,HWM,KB/MS
27	ALC880

SHEET	TITLE
28	REAR AUDIO JACK
29	TI TSB43AB23 1394
30	MARVELL 88E8052
31	VCORE PWM ISL6556
32	DISCRETE POWER
33	ATX, OTHERS POWER
34	FRONT PANEL

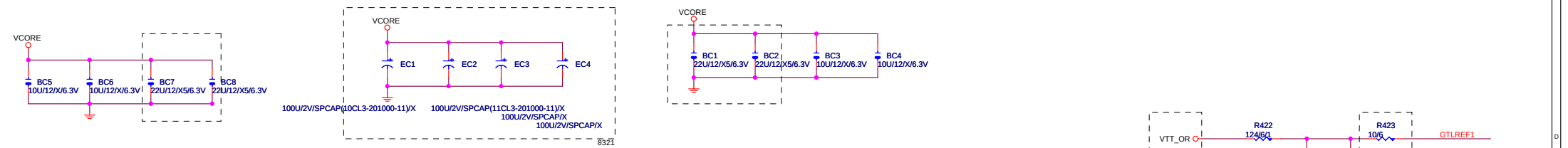
BLOCK DIAGRAM



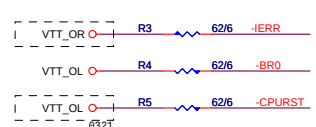
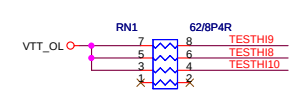
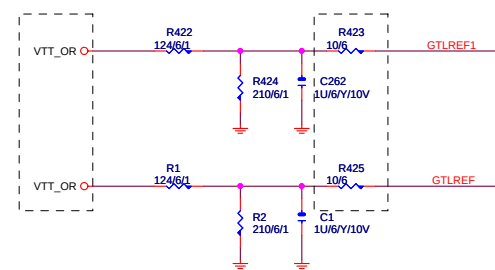
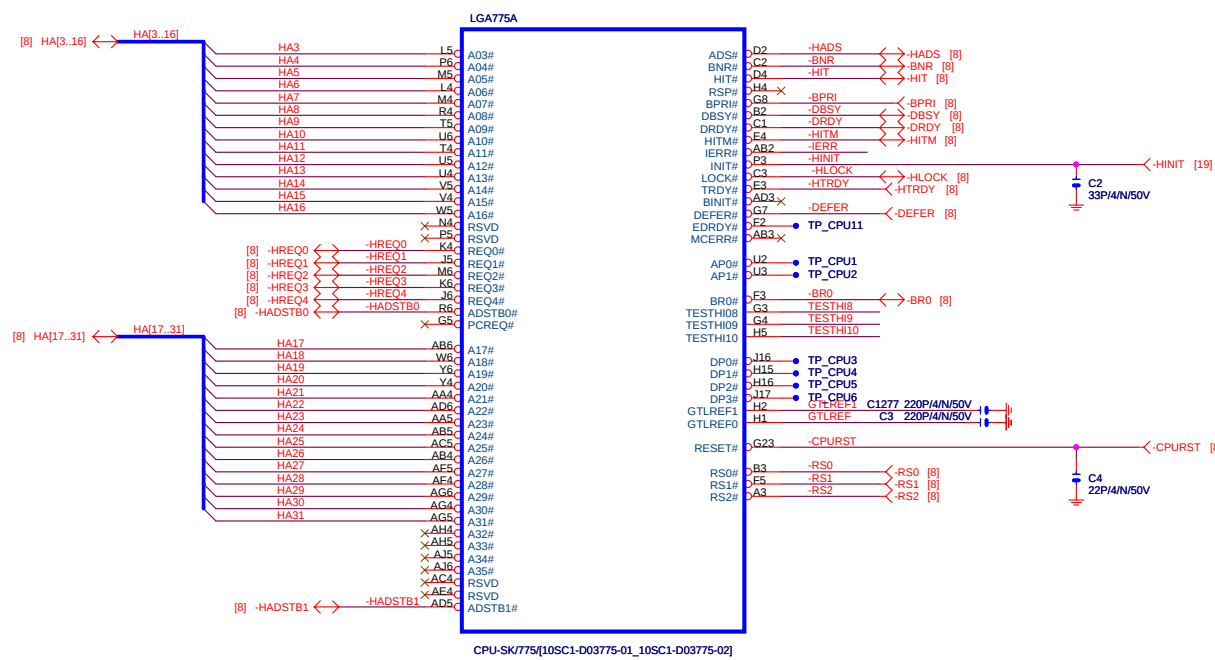
Circuit or PCB layout change
for next version

2005/06/10

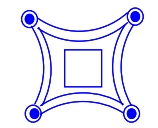
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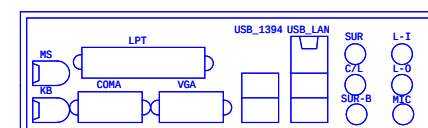
LGA775 FOOT PRINT FOR ACER:LGA775-ACER

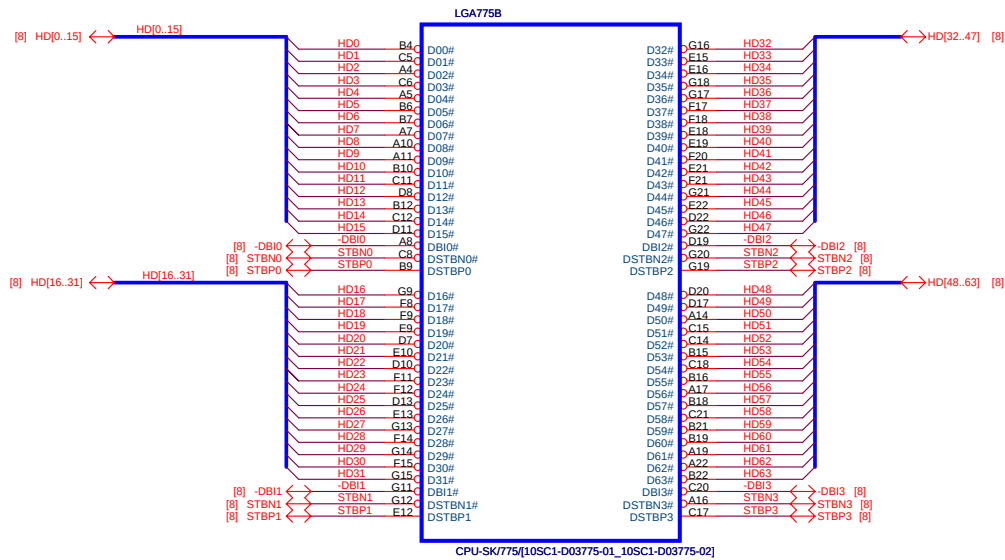


LGA775-CR
CPU_RETAINION[12KRC-040005-21]

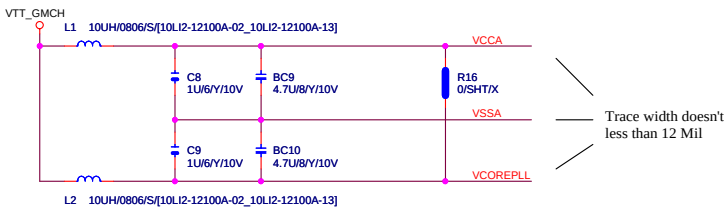


IO_SHIELD
IO_SHIELD[12AIO-AE0006-01]

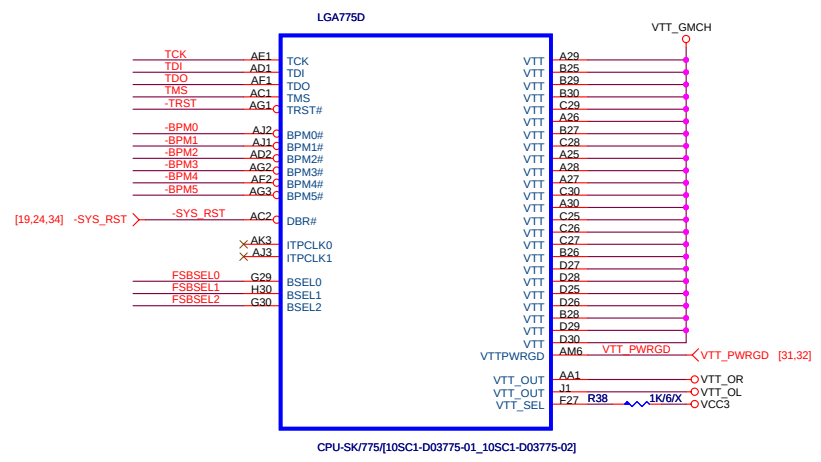
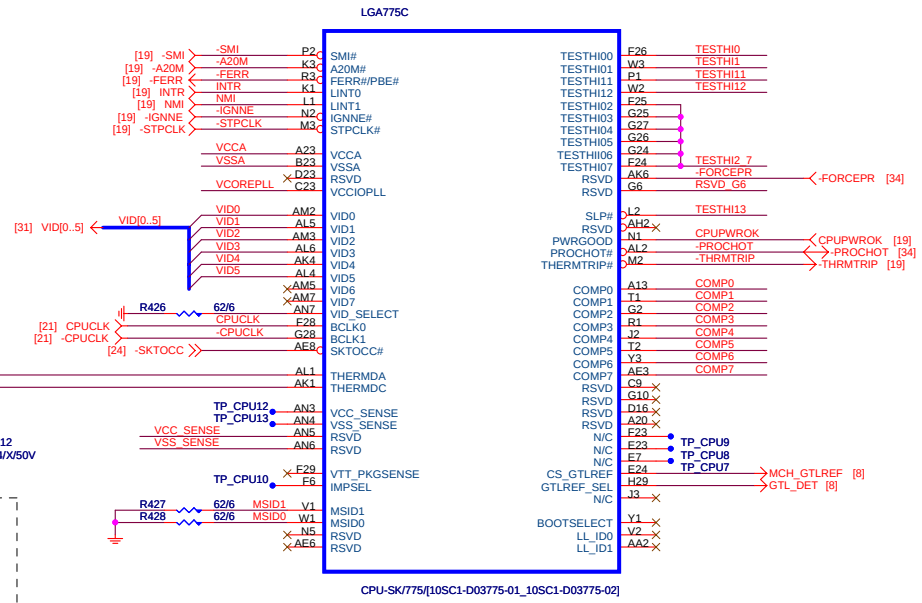
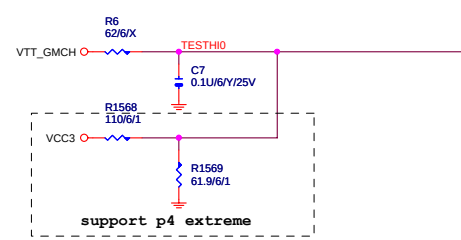
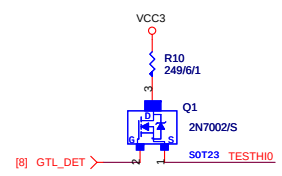
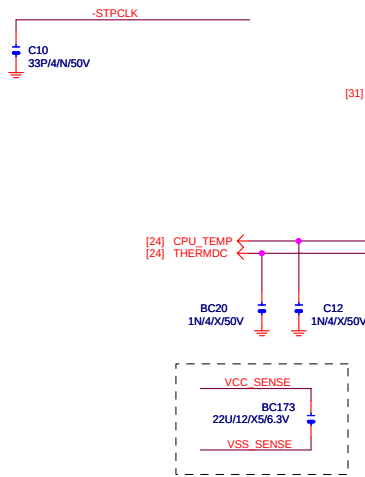




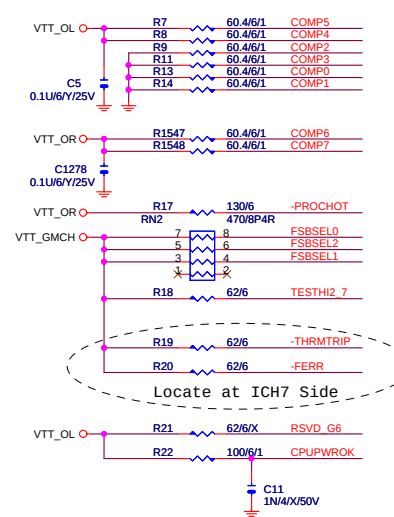
Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



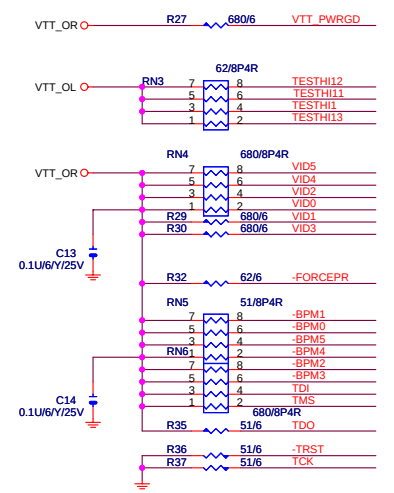
As close as possible to
CPU socket

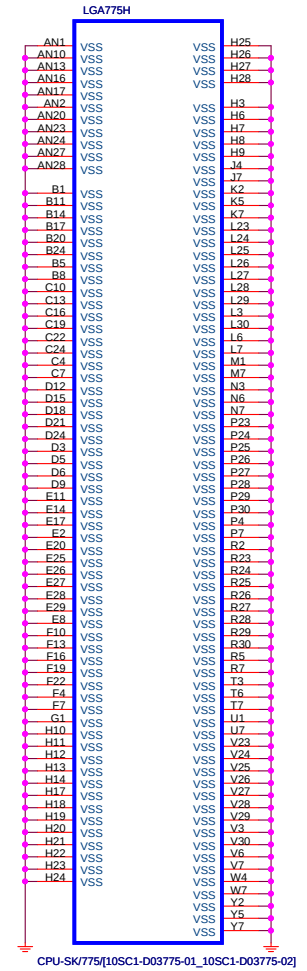
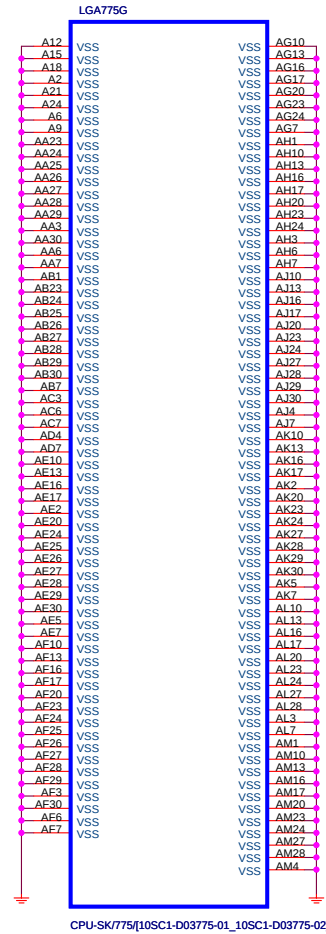
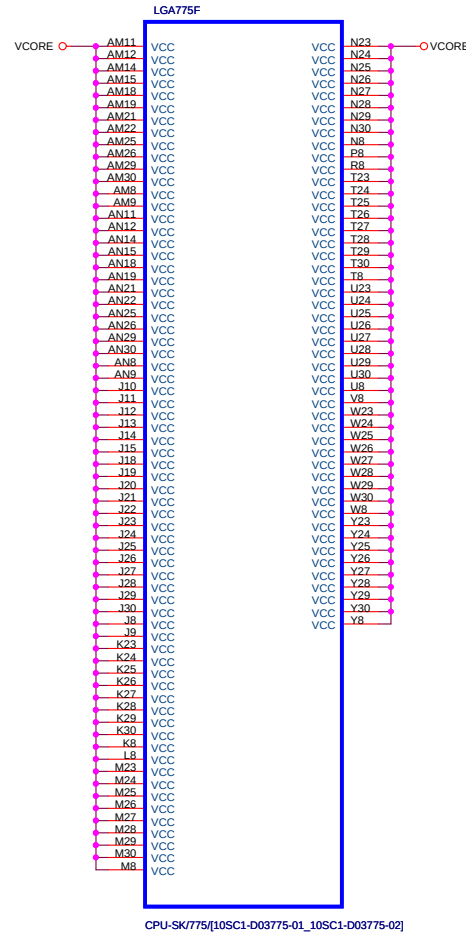
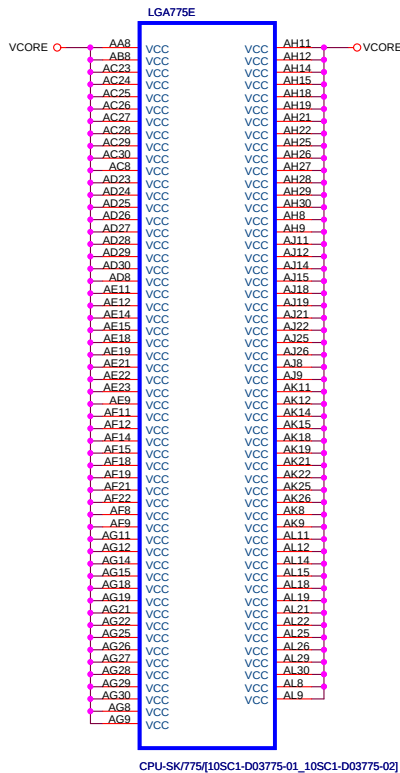


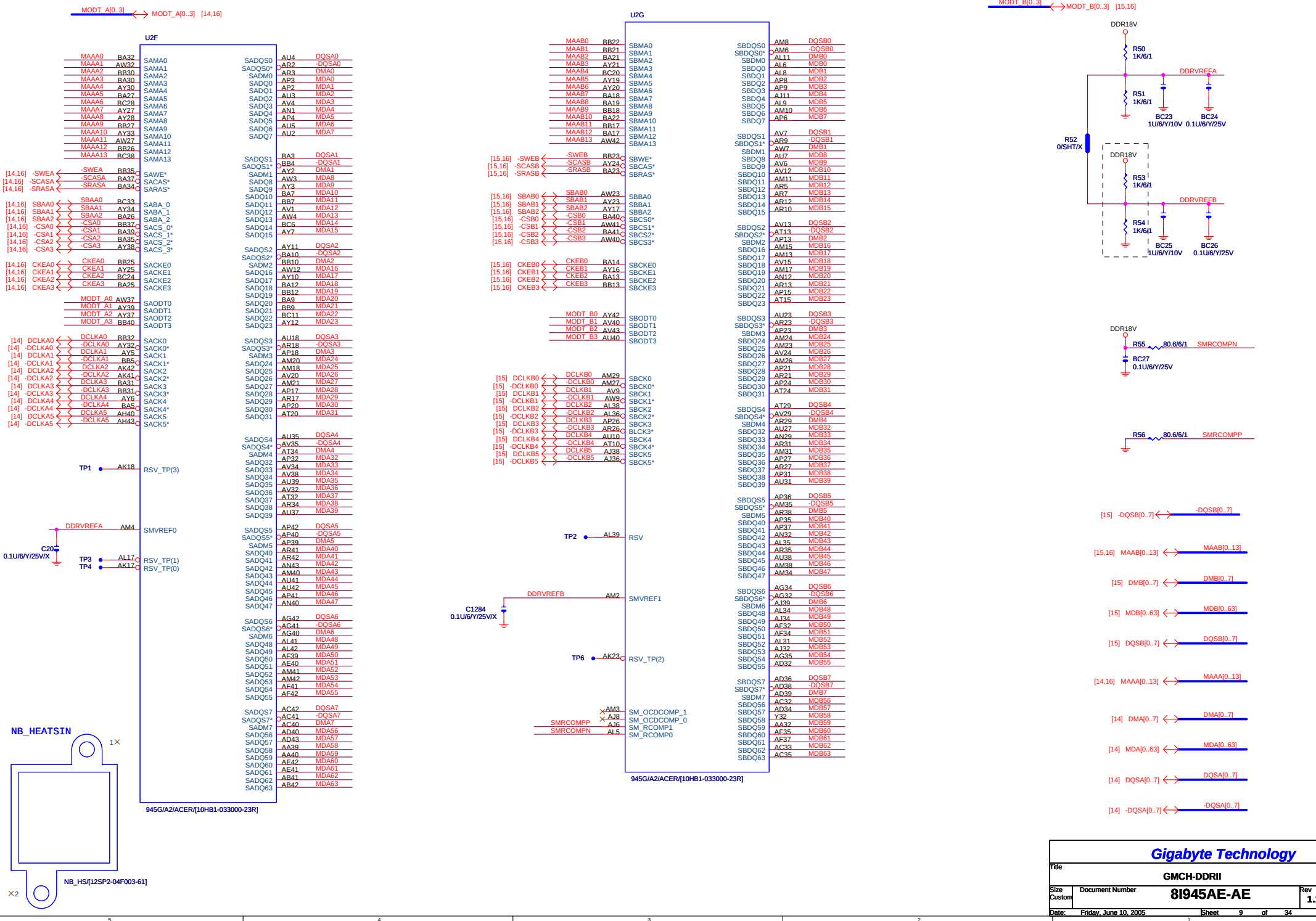
Place outside of CPU socket

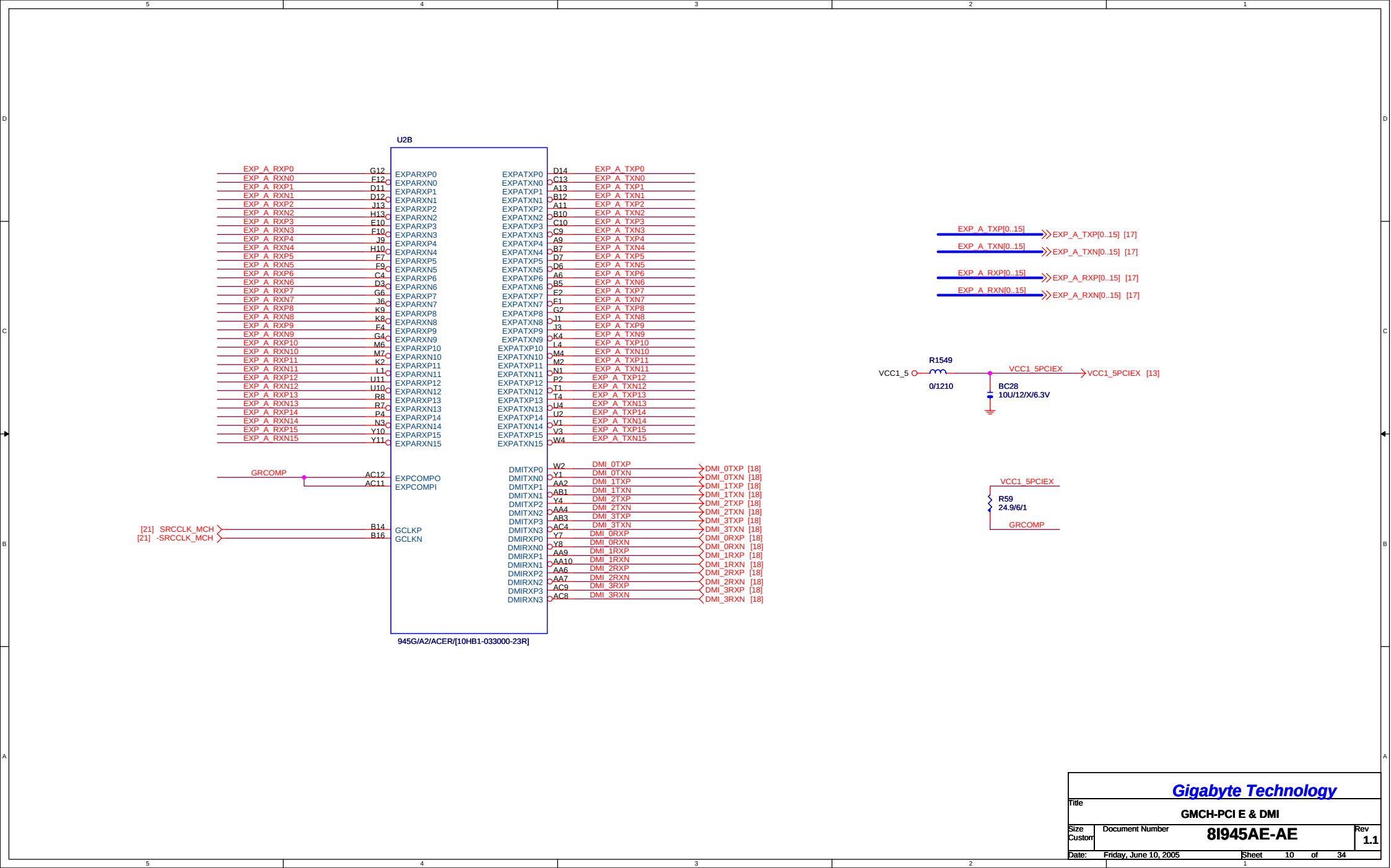


Locate at ICH7 Side









U2C

A4	VSS	N2
A16	VSS	N6
A22	VSS	N8
A26	VSS	N13
A31	VSS	N15
A35	VSS	N24
B4	VSS	N26
B6	VSS	N27
B9	VSS	N29
B11	VSS	N31
B13	VSS	N33
B21	VSS	N36
B22	VSS	N39
B28	VSS	N43
B33	VSS	P3
B38	VSS	P14
C3	VSS	P15
C5	VSS	P24
C7	VSS	P26
C12	VSS	P27
C14	VSS	P29
C22	VSS	P30
C40	VSS	R6
D2	VSS	R9
D5	VSS	R12
D10	VSS	R14
D16	VSS	R30
D20	VSS	R31
D21	VSS	R34
E3	VSS	R37
E4	VSS	R39
E7	VSS	T2
E9	VSS	T42
E12	VSS	U3
E13	VSS	U5
E17	VSS	U9
E18	VSS	U12
E20	VSS	U14
E21	VSS	U31
E32	VSS	U33
F2	VSS	U36
F6	VSS	U38
F13	VSS	V2
F18	VSS	V8
F26	VSS	V11
F34	VSS	V12
F42	VSS	V14
G3	VSS	V34
G5	VSS	V36
G7	VSS	V37
G9	VSS	V38
G10	VSS	V39
G13	VSS	V43
G15	VSS	W3
G18	VSS	Y2
G20	VSS	Y5
G21	VSS	Y6
G24	VSS	Y9
G27	VSS	Y12
G29	VSS	Y14
G31	VSS	Y31
G32	VSS	Y35
G35	VSS	Y37
G38	VSS	Y39
H12	VSS	Y42
H17	VSS	AA3
H26	VSS	AA6
H27	VSS	AA11
J2	VSS	AA12
J5	VSS	AA21
J7	VSS	AA23
J10	VSS	AA31
J12	VSS	AA33
J21	VSS	AA36
J24	VSS	AB2
J29	VSS	AB43
J38	VSS	AC2
J43	VSS	AC3
K3	VSS	AC7
K5	VSS	AC10
K6	VSS	AC14
K7	VSS	AC21
K10	VSS	AR6
K12	VSS	AR15
K13	VSS	AR20
K15	VSS	AR24
K20	VSS	AR32
K27	VSS	AR37
K32	VSS	AR39
K34	VSS	AR43
K37	VSS	AT12
K39	VSS	AT17
L2	VSS	AT18
L12	VSS	AT21
L13	VSS	AT23
L24	VSS	AT26
L26	VSS	AT27
L29	VSS	AT31
L31	VSS	AU6
L42	VSS	AU8
M3	VSS	AU12
M5	VSS	AU13
M8	VSS	AU15
M9	VSS	AU17
M10	VSS	AU20
M13	VSS	AD18
M20	VSS	AD20
M21	VSS	AD22
M35	VSS	AD24
M37	VSS	AD27

945G/A2/ACER[10HB1-033000-23R]

U2E

AD7	VSS	AU21
AD9	VSS	AU24
AD11	VSS	AU26
AD13	VSS	AU29
AD33	VSS	AU32
AD35	VSS	AU34
AD37	VSS	AV2
AD42	VSS	AV10
AF1	VSS	AV17
AF2	VSS	AV37
AF3	VSS	AW10
AF5	VSS	BA4
AF33	VSS	BA42
AF36	VSS	BB3
AF38	VSS	BB6
AF43	VSS	BB11
AG30	VSS	BB14
AG31	VSS	BB19
AG33	VSS	BB34
AG36	VSS	BB39
AG37	VSS	BB41
AG38	VSS	BC9
AG39	VSS	A40
AH42	VSS	D1
AJ7	VSS	D43
AJ10	VSS	R26
AJ30	VSS	R29
AJ31	VSS	U29
AJ33	VSS	V24
AJ35	VSS	V26
AJ37	VSS	V29
AK24	VSS	W21
AK26	VSS	W23
AK29	VSS	W25
AK30	VSS	Y20
AL1	VSS	Y22
AL2	VSS	Y24
AL3	VSS	Y26
AL7	VSS	Y29
AL10	VSS	AA25
AL12	VSS	AA27
AL13	VSS	AA29
AL15	VSS	AC19
AL18	VSS	AC25
AL21	VSS	AC29
AL23	VSS	
AL24	VSS	
AL27	VSS	
AL32	VSS	
AL33	VSS	
AL37	VSS	
AL43	VSS	
AM5	VSS	
AM7	VSS	
AM9	VSS	
AM33	VSS	
AM36	VSS	
AM37	VSS	
AM39	VSS	
AN2	VSS	
AN4	VSS	
AN13	VSS	
AN15	VSS	
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AN21	VSS	
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AN31	VSS	
AN42	VSS	
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AP7	VSS	
AP10	VSS	
AP12	VSS	
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AR32	VSS	
AR37	VSS	
AR39	VSS	
AR43	VSS	
AT12	VSS	
AT17	VSS	
AT18	VSS	
AT21	VSS	
AT23	VSS	
AT26	VSS	
AT27	VSS	
AT31	VSS	
AU6	VSS	
AU8	VSS	
AU12	VSS	
AU13	VSS	
AU15	VSS	
AU17	VSS	
AU20	VSS	
AD18	VSS	
AD20	VSS	
AD22	VSS	
AD24	VSS	
AD27	VSS	
AD29	VSS	
AE19	VSS	
AE21	VSS	
AE23	VSS	
AE25	VSS	
AE18	VSS	
AF20	VSS	
AF22	VSS	
AF24	VSS	
AV1	VSS	
BC4	VSS	

AU21	VSS
AU26	VSS
AU29	VSS
AU32	VSS
AU34	VSS
AV2	VSS
AV10	VSS
AV17	VSS
AV37	VSS
AW10	VSS
BA4	VSS
BA42	VSS
BB3	VSS
BB6	VSS
BB11	VSS
BB14	VSS
BB19	VSS
BB34	VSS
BB39	VSS
BB41	VSS
BC9	VSS
A40	VSS
D1	VSS
D43	VSS
R26	VSS
R29	VSS
U29	VSS
V24	VSS
V26	VSS
V29	VSS
W21	VSS
W23	VSS
W25	VSS
Y20	VSS
Y22	VSS
Y24	VSS
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AA27	VSS
AA29	VSS
AC19	VSS
AC25	VSS
AC29	VSS

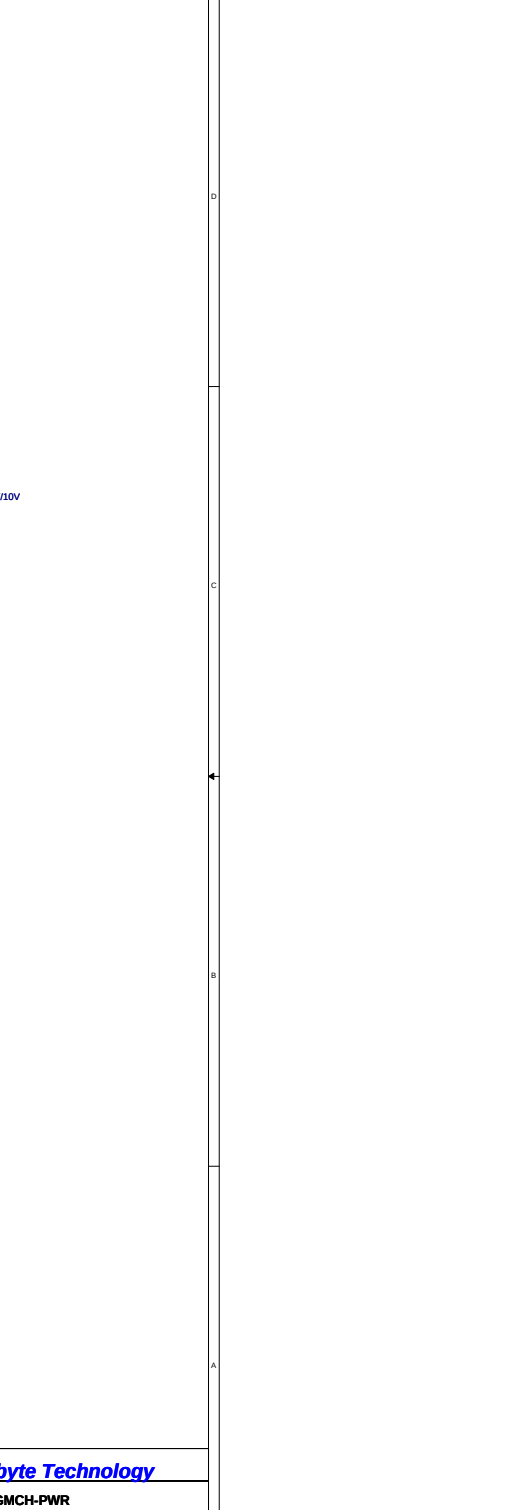
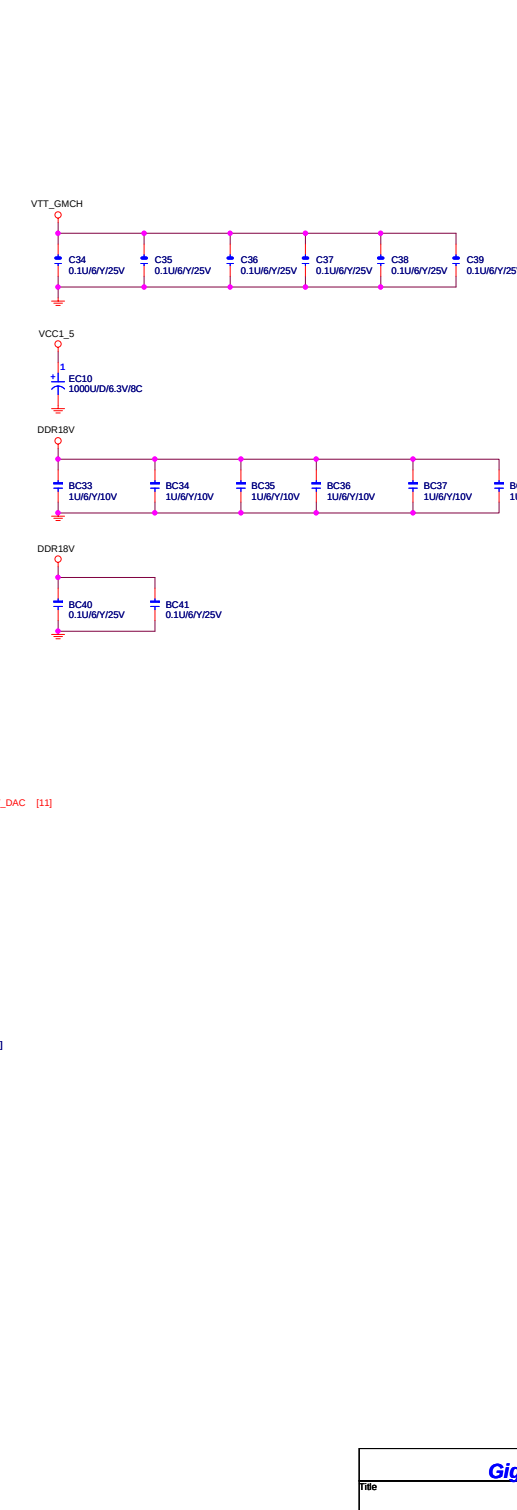
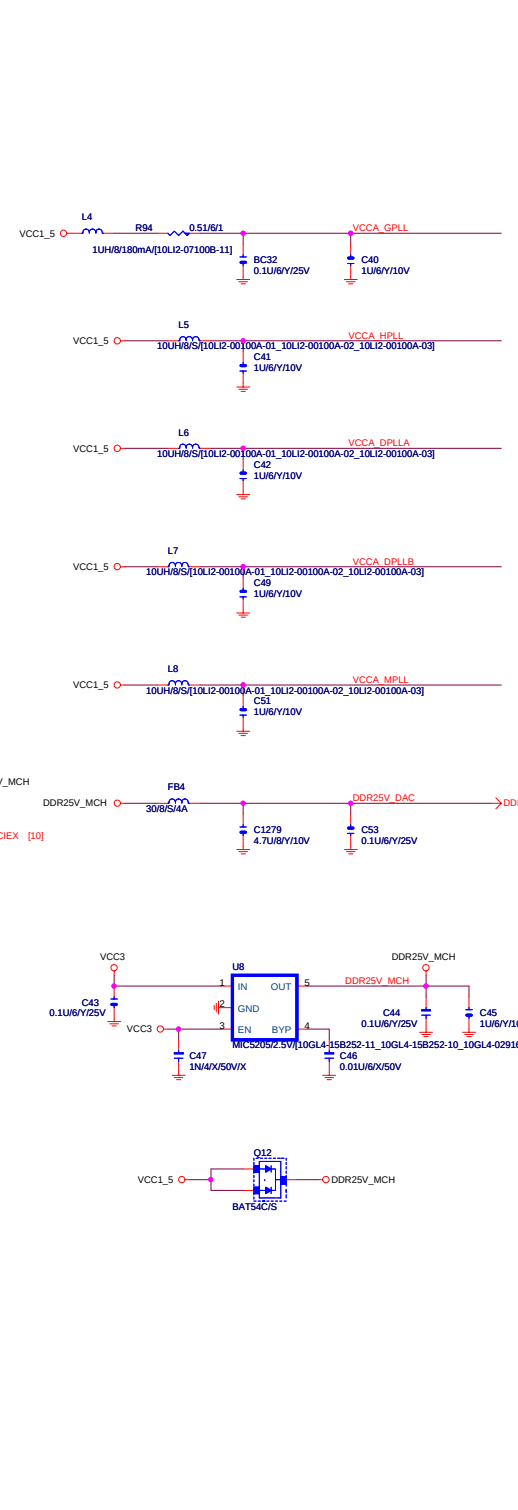
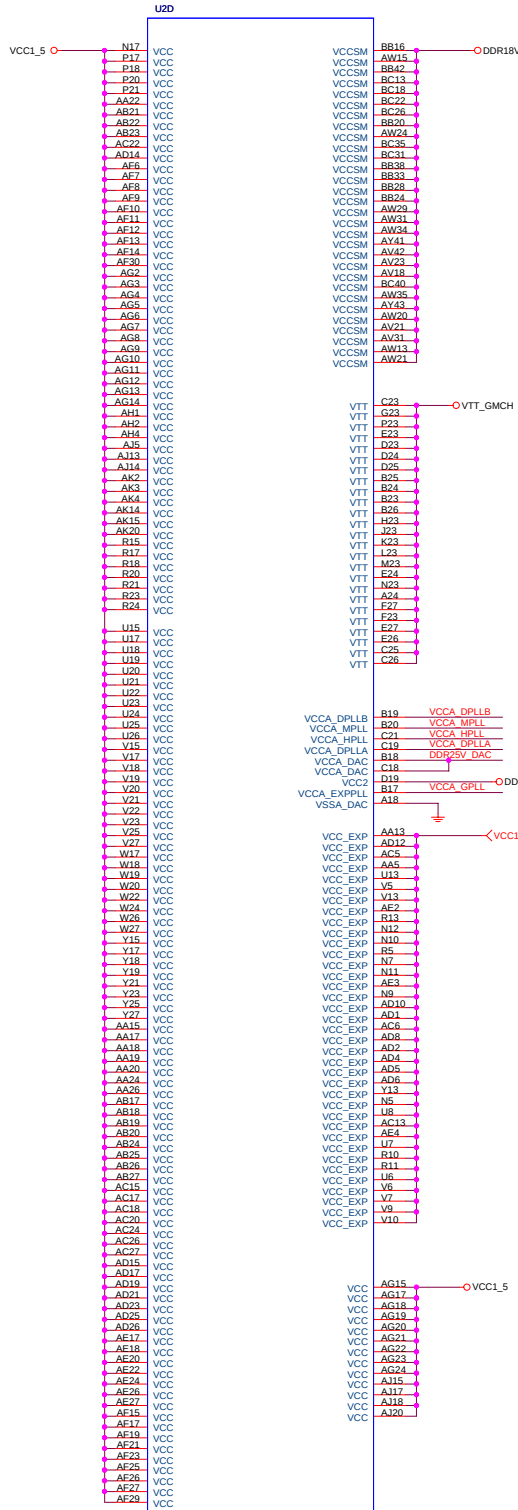
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NC	B2
NC	B3
NC	B41
NC	B42
NC	B43
NC	C2
NC	C42
NC	E35
NC	AV26
NC	AV27
NC	AW2
NC	AW29
NC	BA2
NC	BB1
NC	BB2
NC	BB43
NC	BC1
NC	BC2
NC	BC43
NC	BC45

RSVD	AK21
RSVD	AJ23
RSVD	AJ26
RSVD	AL29
RSVD	AL20
RSVD	AJ21
RSVD	AL26
RSVD	AK23
RSVD	AD36
RSVD	AC34
RSVD	Y30
RSVD	Y33
RSVD	AF31
RSVD	AD31
RSVD	U30
RSVD	V31
RSVD	AA36
RSVD	AC36
RSVD	AA43
RSVD	AG28
RSVD	AG28
RSVD	AG27
RSVD	AJ24
RSVD	AJ27
RSVD	AK46
RSVD	AW17
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RSVD	AT14
RSVD	BC16
RSVD	AJ29
RSVD	AG29

945G/A2/ACER[10HB1-033000-23R]

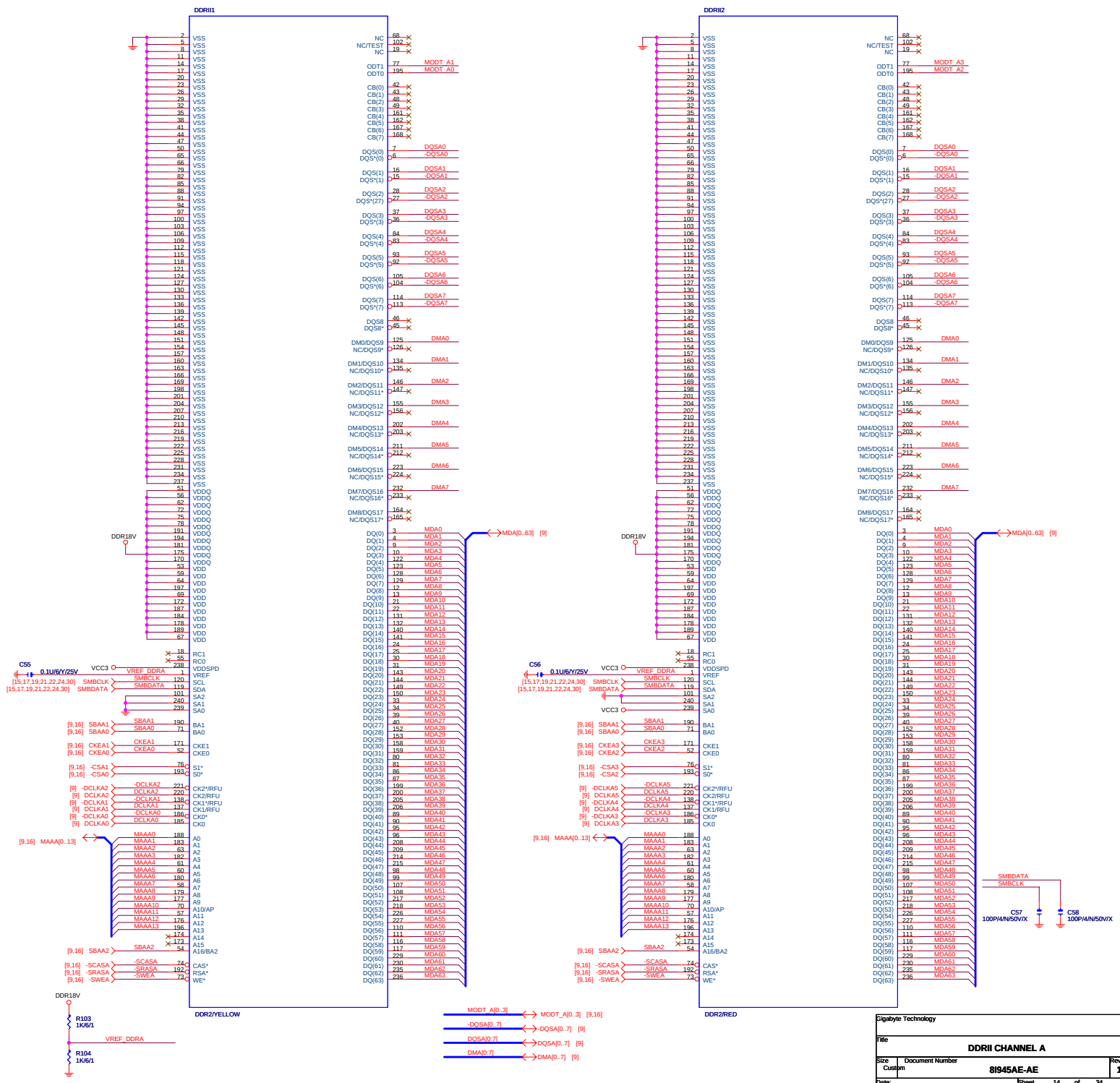
Gigabyte Technology

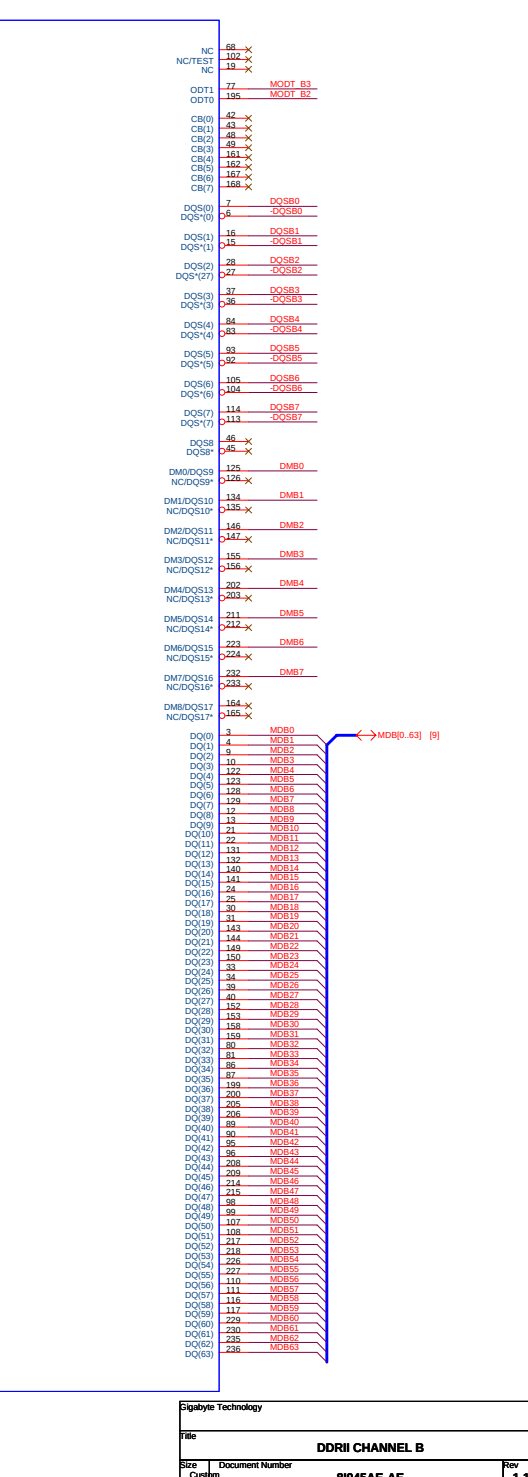
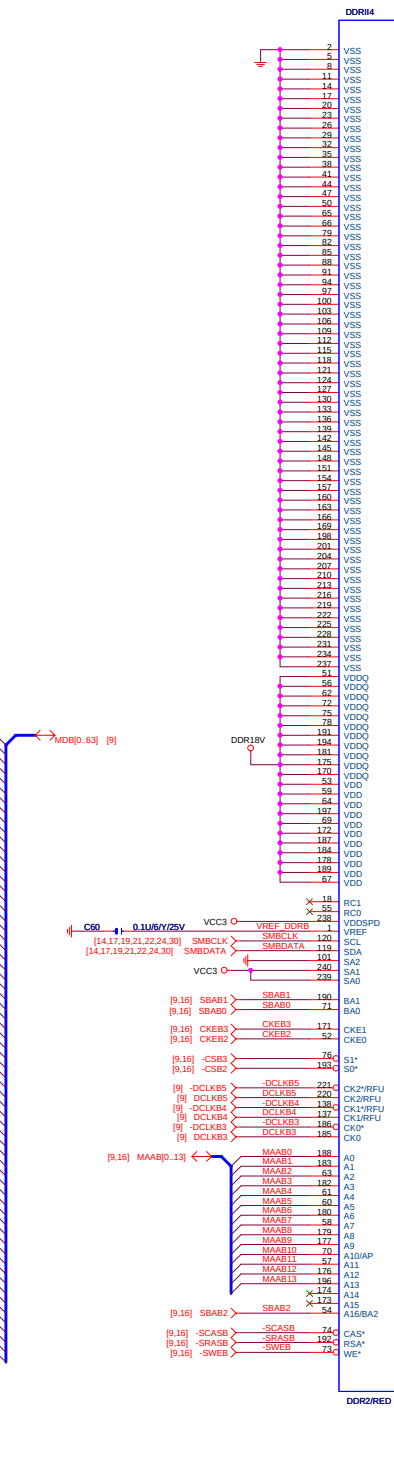
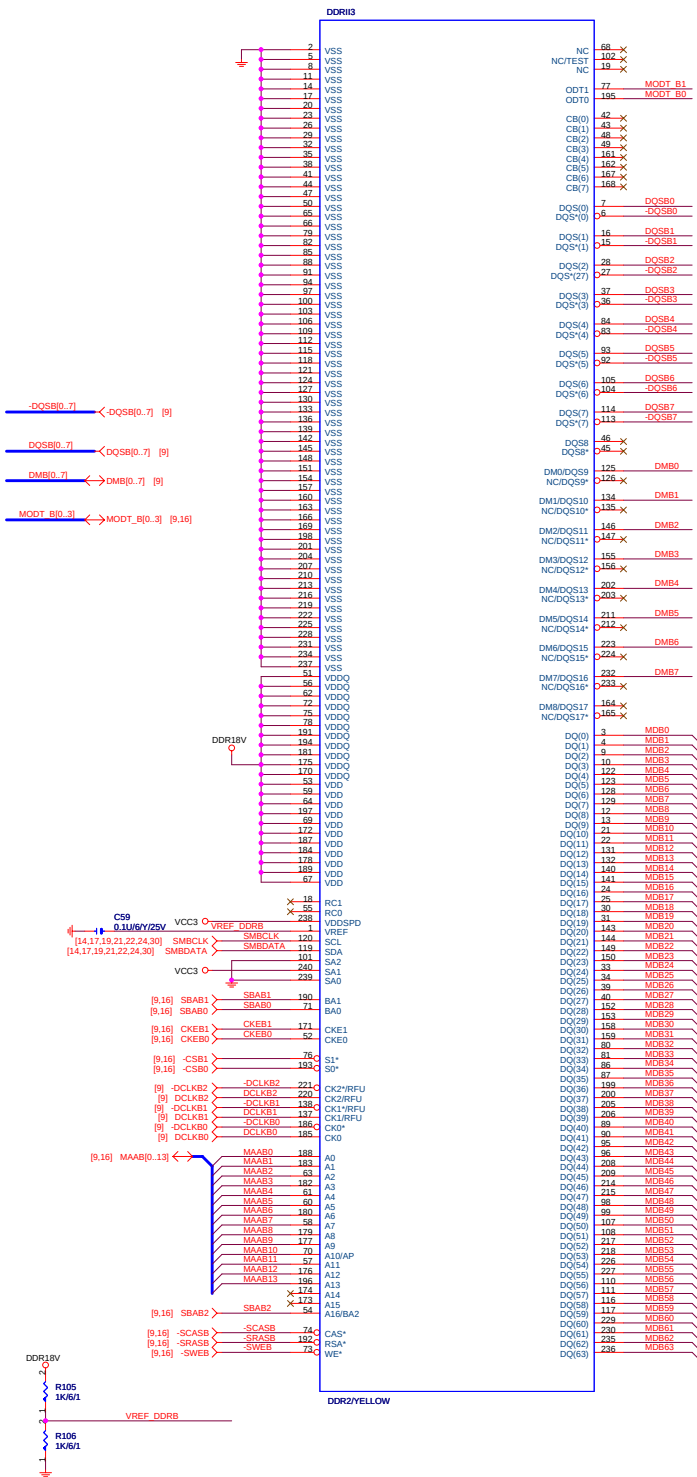
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Size			Document Number	
Custom			81945AE-AE	
Date			Friday, June 10, 2005	
Sheet			12 of 34	
Rev			1.1	



Gigabyte Technology

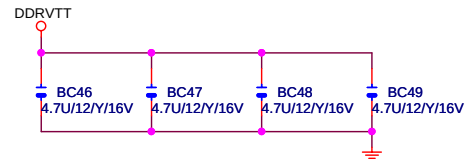
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Size			81945AE-AE
Date			Friday, June 10, 2005
Sheet			13 of 34
Rev			1.1



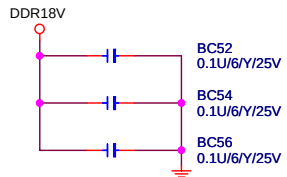


DDR TERMINATION CHANNEL A

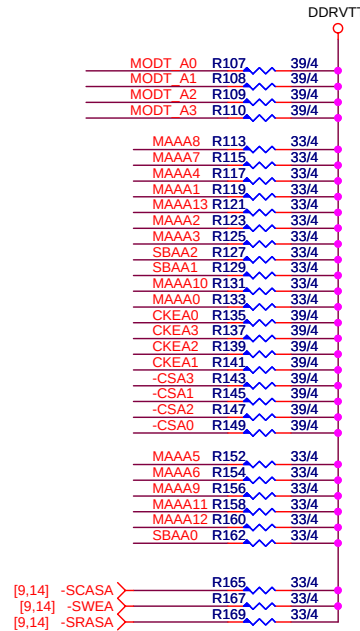
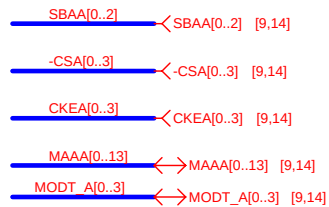
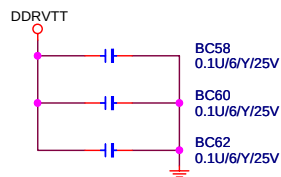
DDRVTT Decouple



DDR18V Decouple

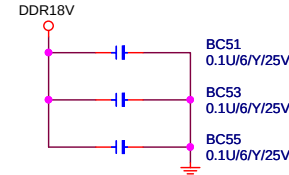


DDRVTT Decouple

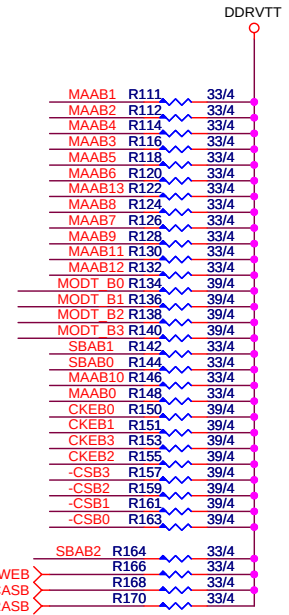
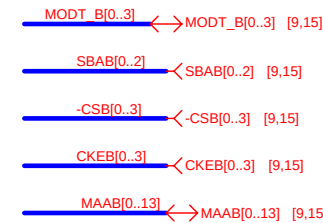
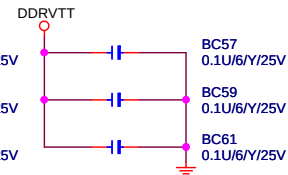


DDR TERMINATION CHANNEL B

DDR18V Decouple



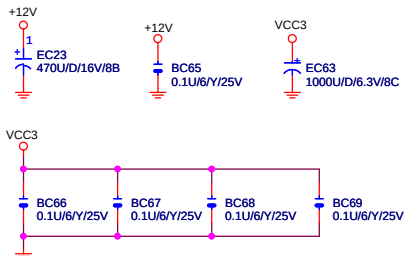
DDRVTT Decouple



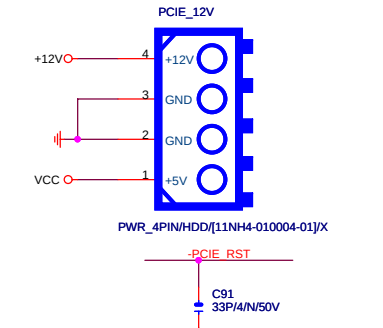
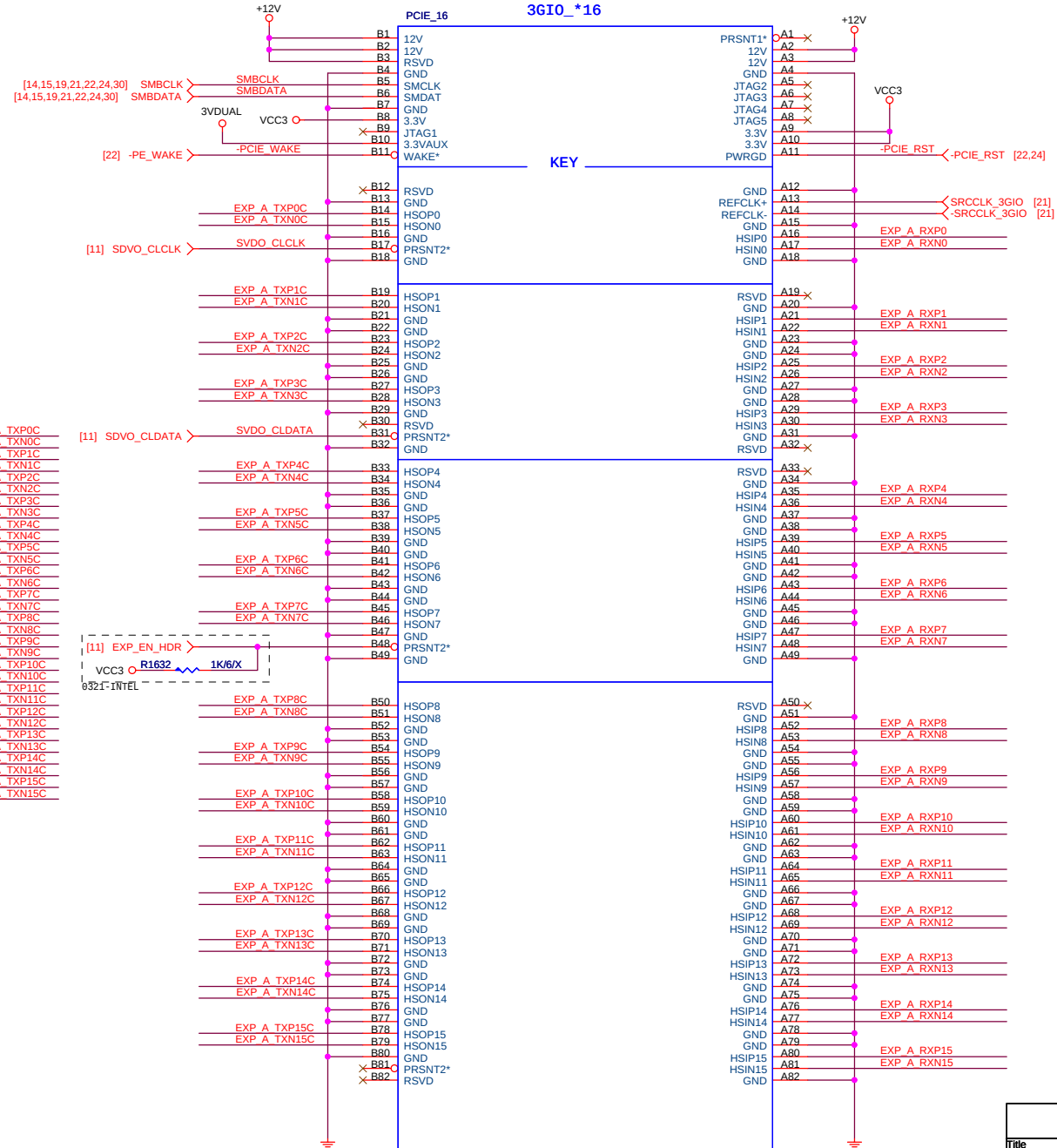
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DDRII TERMINATOR

Size Custom	Document Number	81945AE-AE	Rev 1.1
Date:	Friday, June 10, 2005	Sheet 16 of 34	



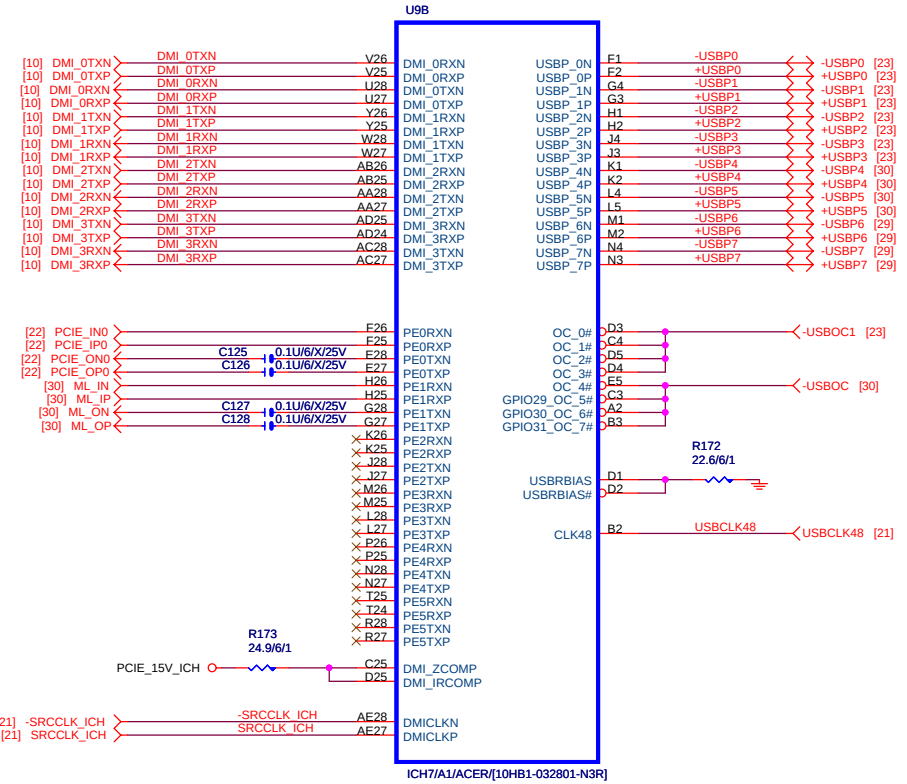
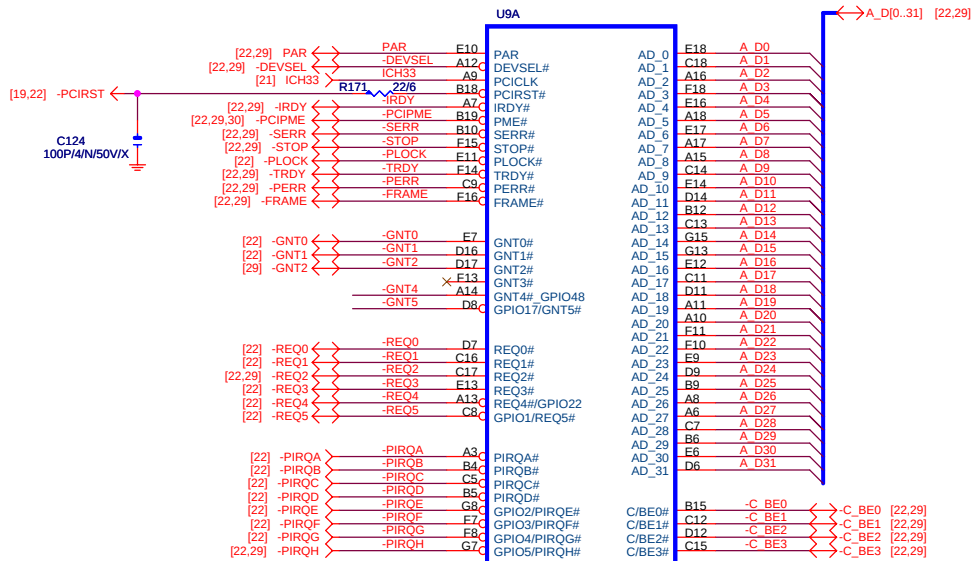
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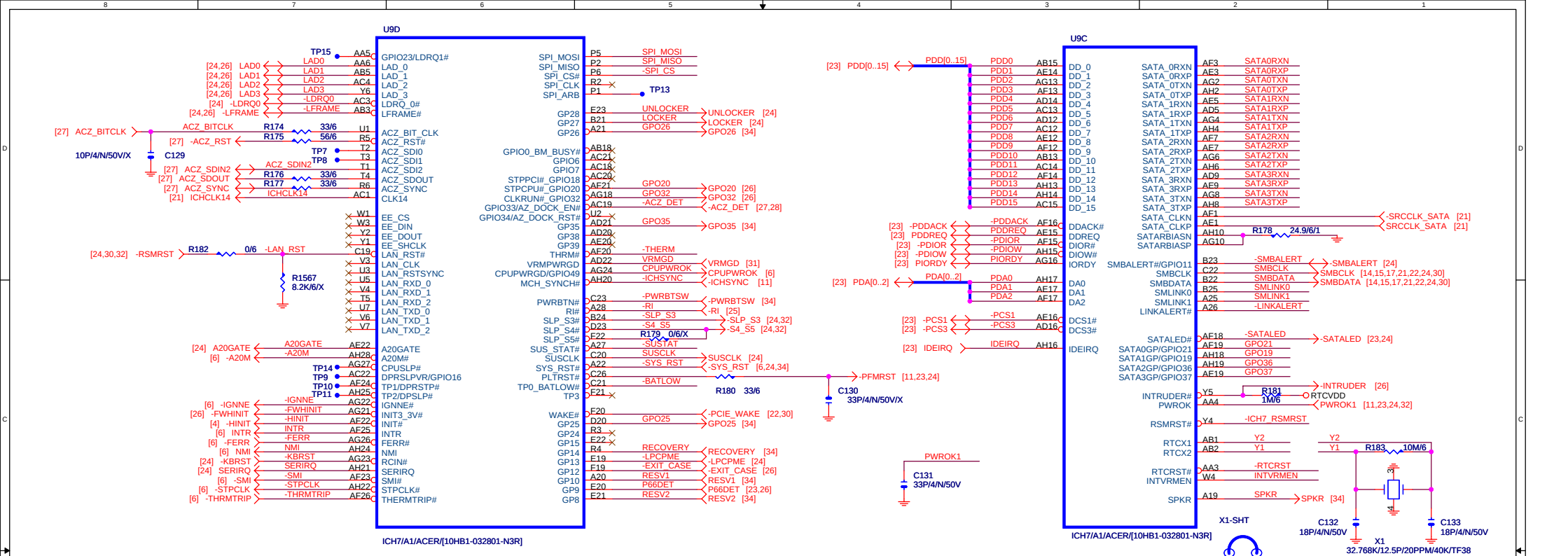


EXP_A_TXP[0..15] >>> EXP_A_TXP[0..15] [10]
EXP_A_TXN[0..15] >>> EXP_A_TXN[0..15] [10]

EXP_A_TXP0	C92	0.1U/6Y/25V	EXP_A_TXP0C
EXP_A_TXN0	C93	0.1U/6Y/25V	EXP_A_TXN0C
EXP_A_TXP1	C94	0.1U/6Y/25V	EXP_A_TXP1C
EXP_A_TXN1	C95	0.1U/6Y/25V	EXP_A_TXN1C
EXP_A_TXP2	C96	0.1U/6Y/25V	EXP_A_TXP2C
EXP_A_TXN2	C97	0.1U/6Y/25V	EXP_A_TXN2C
EXP_A_TXP3	C98	0.1U/6Y/25V	EXP_A_TXP3C
EXP_A_TXN3	C99	0.1U/6Y/25V	EXP_A_TXN3C
EXP_A_TXP4	C100	0.1U/6Y/25V	EXP_A_TXP4C
EXP_A_TXN4	C101	0.1U/6Y/25V	EXP_A_TXN4C
EXP_A_TXP5	C102	0.1U/6Y/25V	EXP_A_TXP5C
EXP_A_TXN5	C103	0.1U/6Y/25V	EXP_A_TXN5C
EXP_A_TXP6	C104	0.1U/6Y/25V	EXP_A_TXP6C
EXP_A_TXN6	C105	0.1U/6Y/25V	EXP_A_TXN6C
EXP_A_TXP7	C106	0.1U/6Y/25V	EXP_A_TXP7C
EXP_A_TXN7	C107	0.1U/6Y/25V	EXP_A_TXN7C
EXP_A_TXP8	C108	0.1U/6Y/25V	EXP_A_TXP8C
EXP_A_TXN8	C109	0.1U/6Y/25V	EXP_A_TXN8C
EXP_A_TXP9	C110	0.1U/6Y/25V	EXP_A_TXP9C
EXP_A_TXN9	C111	0.1U/6Y/25V	EXP_A_TXN9C
EXP_A_TXP10	C112	0.1U/6Y/25V	EXP_A_TXP10C
EXP_A_TXN10	C113	0.1U/6Y/25V	EXP_A_TXN10C
EXP_A_TXP11	C114	0.1U/6Y/25V	EXP_A_TXP11C
EXP_A_TXN11	C115	0.1U/6Y/25V	EXP_A_TXN11C
EXP_A_TXP12	C116	0.1U/6Y/25V	EXP_A_TXP12C
EXP_A_TXN12	C117	0.1U/6Y/25V	EXP_A_TXN12C
EXP_A_TXP13	C118	0.1U/6Y/25V	EXP_A_TXP13C
EXP_A_TXN13	C119	0.1U/6Y/25V	EXP_A_TXN13C
EXP_A_TXP14	C120	0.1U/6Y/25V	EXP_A_TXP14C
EXP_A_TXN14	C121	0.1U/6Y/25V	EXP_A_TXN14C
EXP_A_TXP15	C122	0.1U/6Y/25V	EXP_A_TXP15C
EXP_A_TXN15	C123	0.1U/6Y/25V	EXP_A_TXN15C

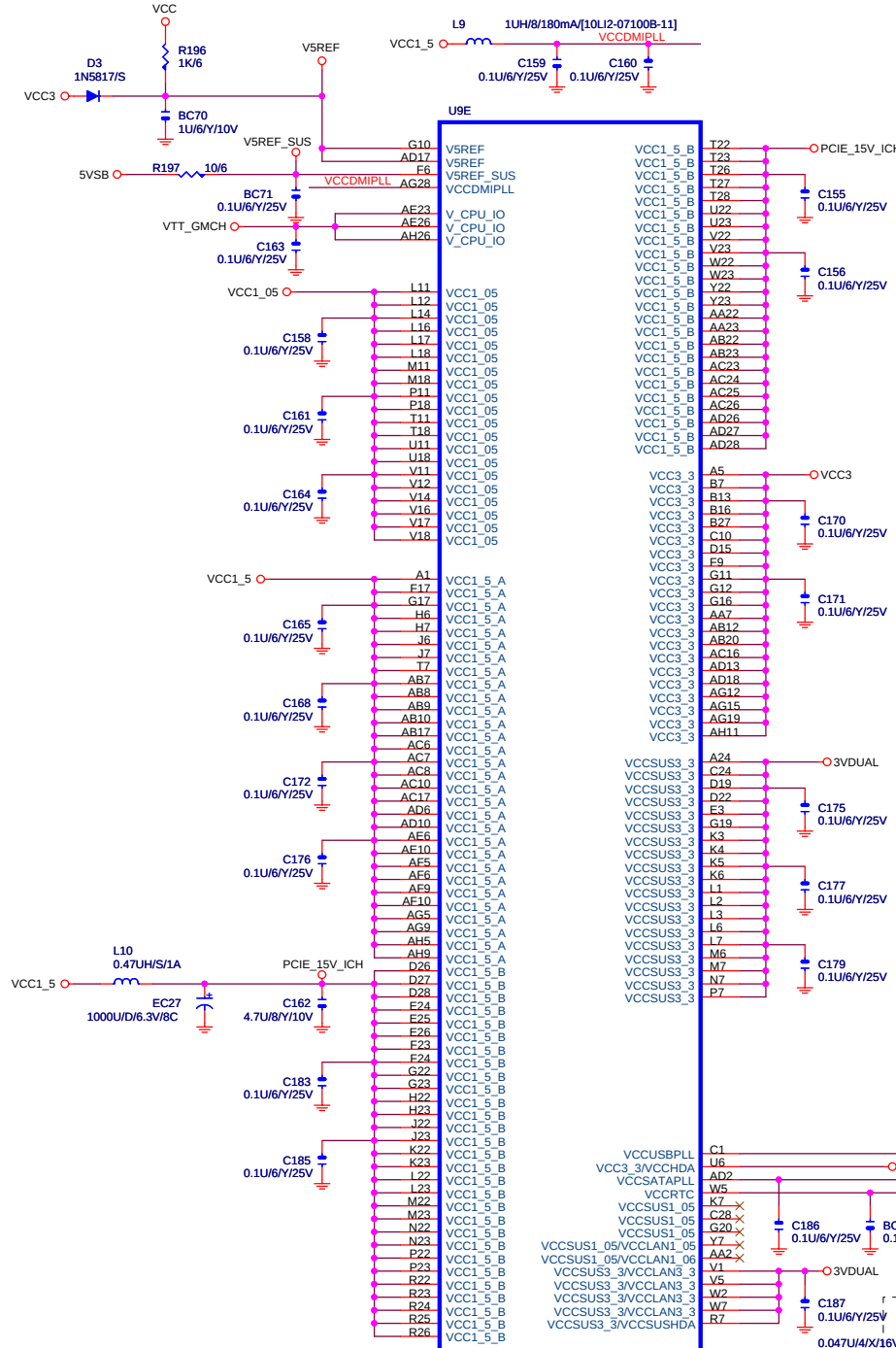
EXP_A_RXP[0..15] >>> EXP_A_RXP[0..15] [10]
EXP_A_RXN[0..15] >>> EXP_A_RXN[0..15] [10]





A4	VSS1	VSS101	R14
A23	VSS2	VSS102	R15
B1	VSS3	VSS103	R16
B8	VSS4	VSS104	R17
B11	VSS5	VSS105	R18
B14	VSS6	VSS106	T6
B17	VSS7	VSS107	T12
B20	VSS8	VSS108	T13
B26	VSS9	VSS109	T14
B28	VSS10	VSS110	T15
C2	VSS11	VSS111	T16
C6	VSS12	VSS112	T17
D10	VSS13	VSS113	T18
D13	VSS14	VSS114	U12
D18	VSS15	VSS115	U13
D21	VSS16	VSS116	U14
D24	VSS17	VSS117	U15
E1	VSS18	VSS118	U16
E2	VSS19	VSS119	U17
E8	VSS20	VSS120	U24
E15	VSS21	VSS121	U25
F3	VSS22	VSS122	U26
F4	VSS23	VSS123	V2
F5	VSS24	VSS124	V13
F12	VSS25	VSS125	V15
F27	VSS26	VSS126	V24
F28	VSS27	VSS127	V27
G1	VSS28	VSS128	V28
G2	VSS29	VSS129	W6
G5	VSS30	VSS130	W24
G6	VSS31	VSS131	W25
G9	VSS32	VSS132	W26
G14	VSS33	VSS133	Y3
G18	VSS34	VSS134	Y24
G21	VSS35	VSS135	Y27
G24	VSS36	VSS136	Y28
G25	VSS37	VSS137	AA1
G26	VSS38	VSS138	AA24
H3	VSS39	VSS139	AA25
H4	VSS40	VSS140	AA26
H5	VSS41	VSS141	AB4
H24	VSS42	VSS142	AB6
H27	VSS43	VSS143	AB11
H28	VSS44	VSS144	AB14
J1	VSS45	VSS145	AB16
J2	VSS46	VSS146	AB19
J5	VSS47	VSS147	AB21
J24	VSS48	VSS148	AB24
J25	VSS49	VSS149	AB27
J26	VSS50	VSS150	AB28
K24	VSS51	VSS151	AC2
K27	VSS52	VSS152	AC5
K28	VSS53	VSS153	AC9
L13	VSS54	VSS154	AC11
L15	VSS55	VSS155	AD1
L24	VSS56	VSS156	AD3
L25	VSS57	VSS157	AD4
L26	VSS58	VSS158	AD7
M3	VSS59	VSS159	AD8
M4	VSS60	VSS160	AD11
M5	VSS61	VSS161	AD15
M12	VSS62	VSS162	AD19
M13	VSS63	VSS163	AD23
M14	VSS64	VSS164	AE2
M15	VSS65	VSS165	AE4
M16	VSS66	VSS166	AE8
M17	VSS67	VSS167	AE11
M24	VSS68	VSS168	AE13
M27	VSS69	VSS169	AE18
M28	VSS70	VSS170	AE21
N1	VSS71	VSS171	AE24
N2	VSS72	VSS172	AE2
N5	VSS73	VSS173	AF4
N6	VSS74	VSS174	AF8
N11	VSS75	VSS175	AF11
N12	VSS76	VSS176	AF1
N13	VSS77	VSS177	AF27
N14	VSS78	VSS178	AF28
N15	VSS79	VSS179	AG1
N16	VSS80	VSS180	AG3
N17	VSS81	VSS181	AG7
N18	VSS82	VSS182	AG14
N24	VSS83	VSS183	AG17
N25	VSS84	VSS184	AG20
N26	VSS85	VSS185	AG25
P3	VSS86	VSS186	AH1
P4	VSS87	VSS187	AH3
P12	VSS88	VSS188	AH7
P13	VSS89	VSS189	AH12
P14	VSS90	VSS190	AH23
P15	VSS91	VSS191	AH27
P16	VSS92	VSS192	C27
P17	VSS93	VSS193	E4
P24	VSS94	VSS194	AG11
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P28	VSS96		
R1	VSS97		
R11	VSS98		
R12	VSS99		
R13	VSS100		

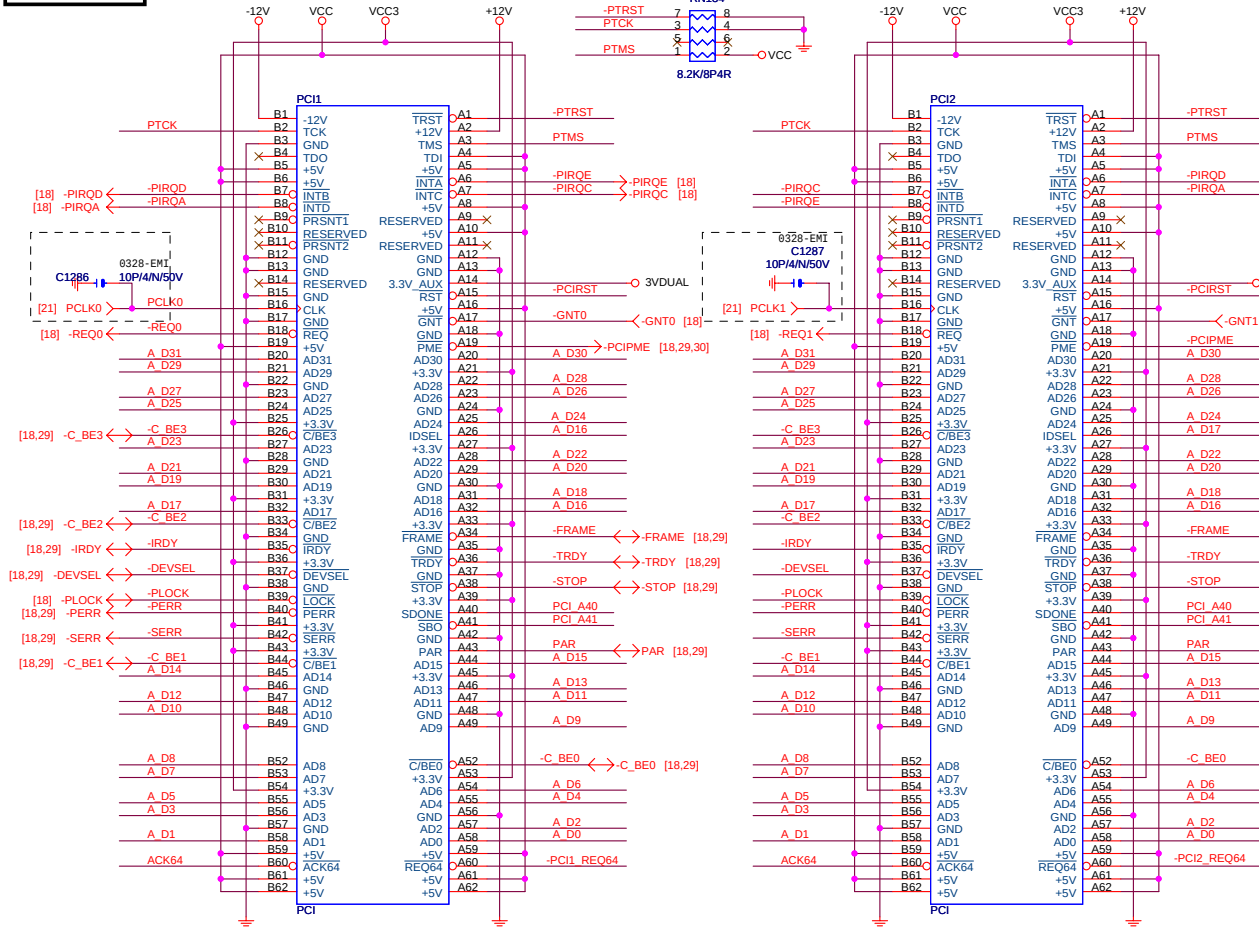
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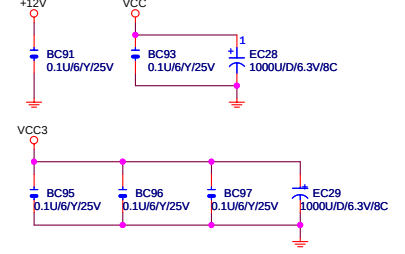
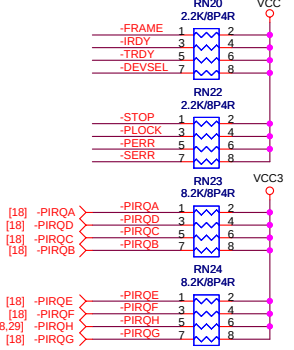
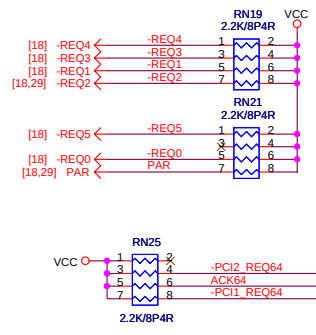
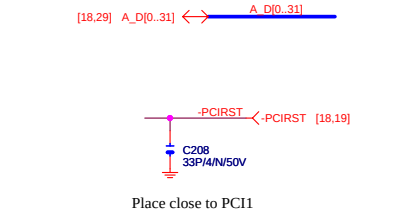
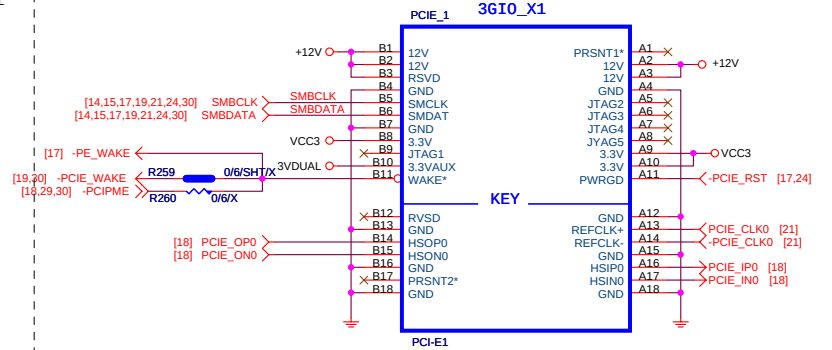
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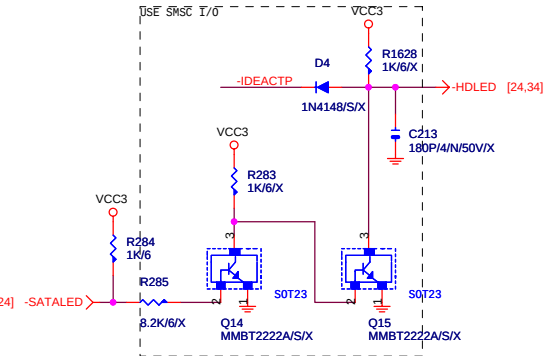
PCI1, 2 SLOT



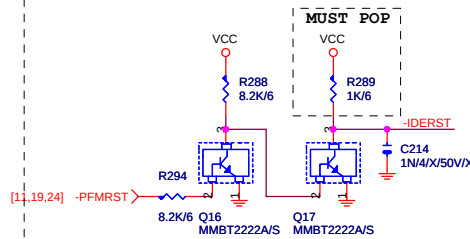
PCIE*1



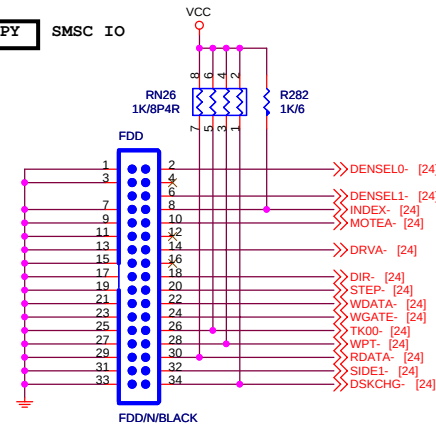
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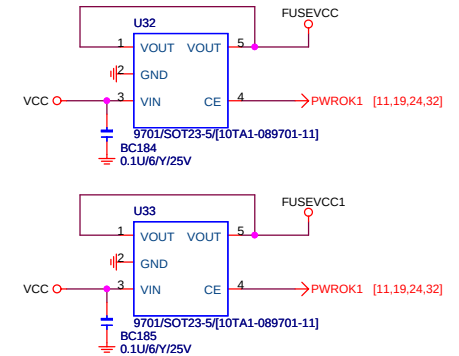
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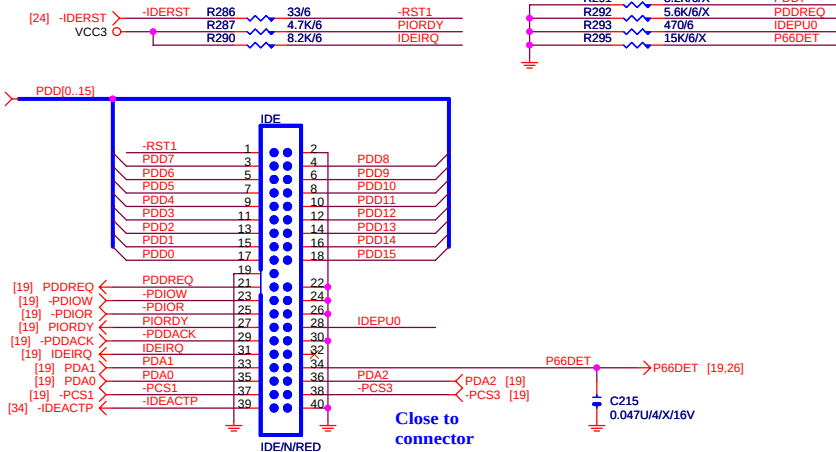
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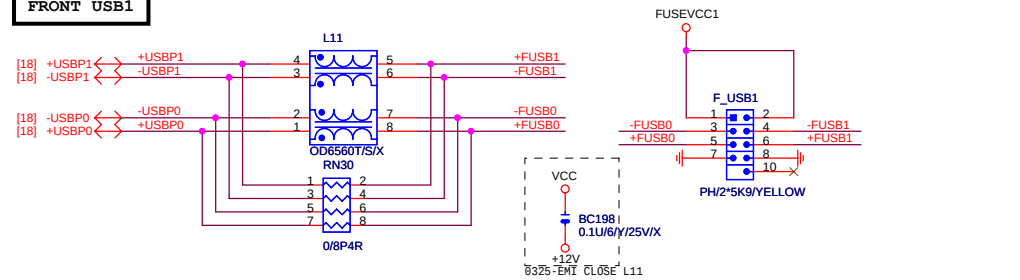
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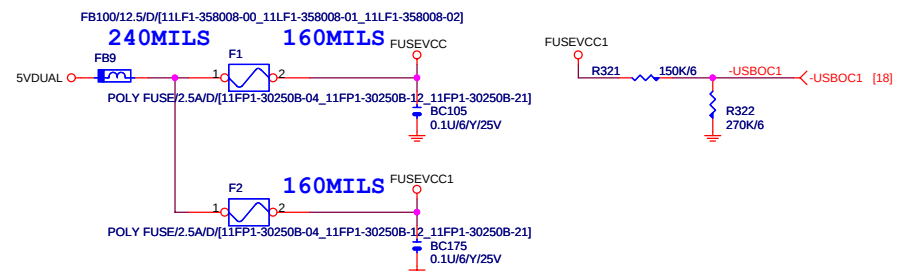
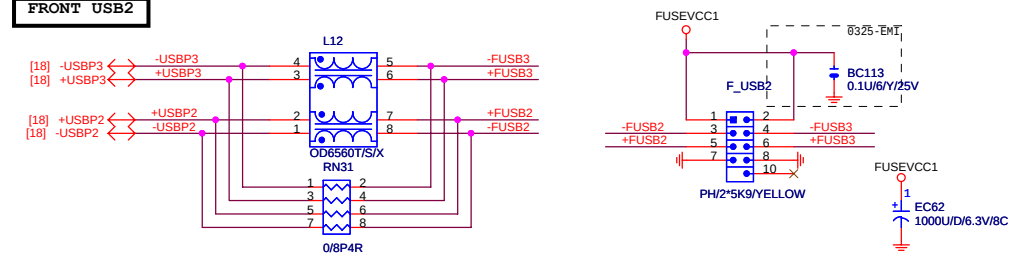
IDE



FRONT USB1



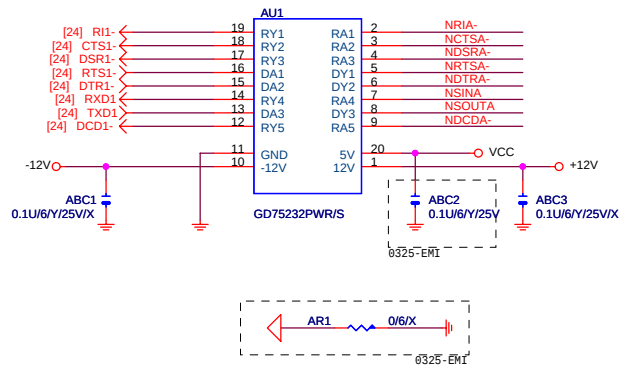
FRONT USB2



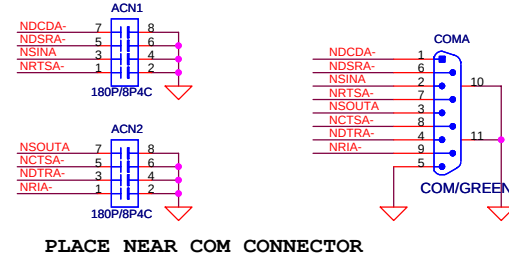
Gigabyte Technology

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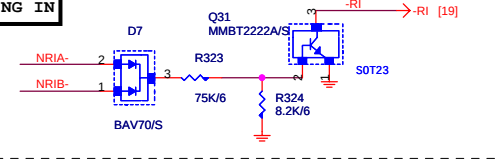
COMA



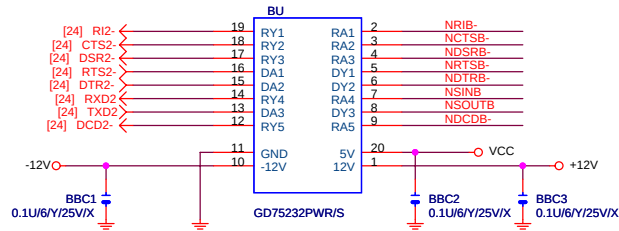
EXTERNAL COMA



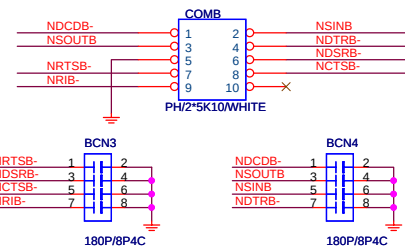
RING IN



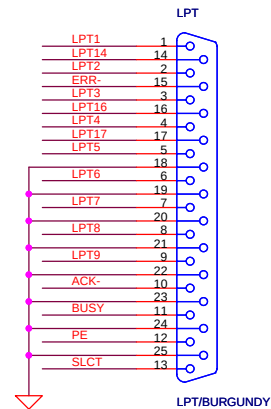
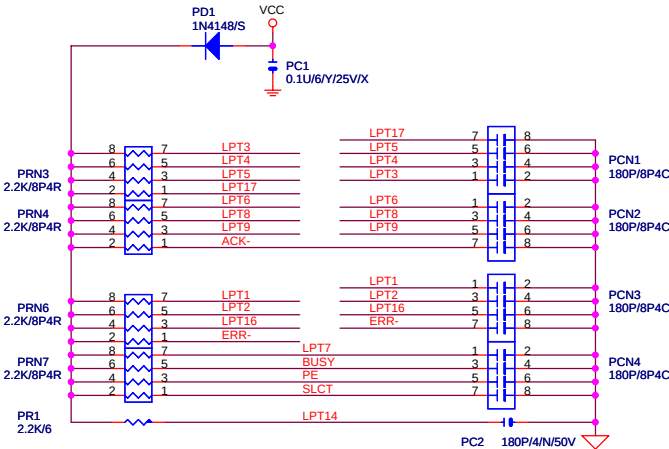
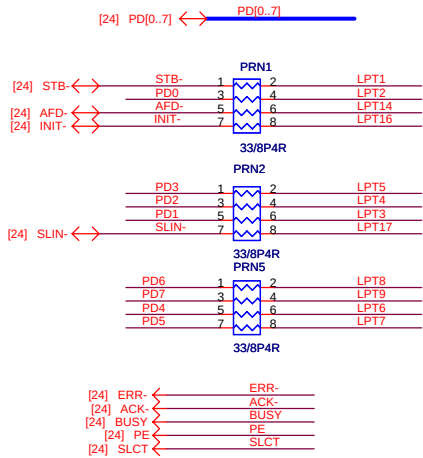
COMB



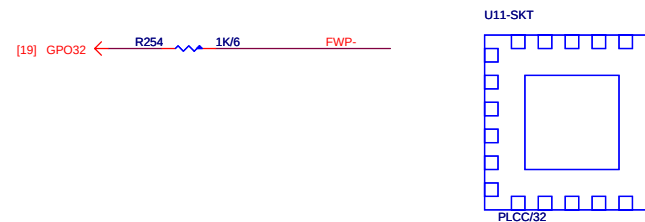
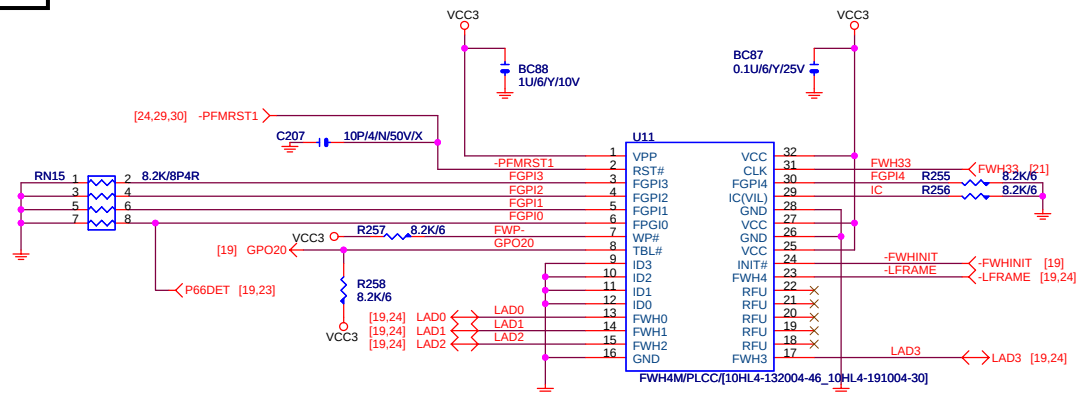
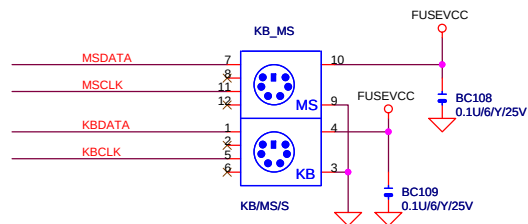
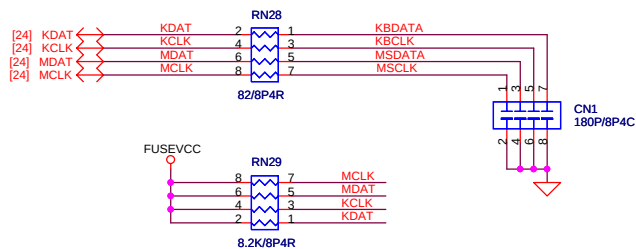
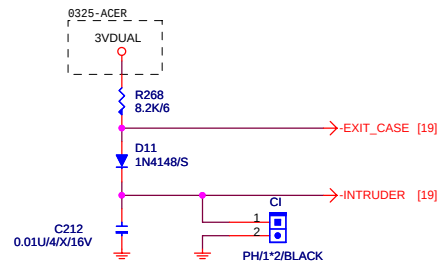
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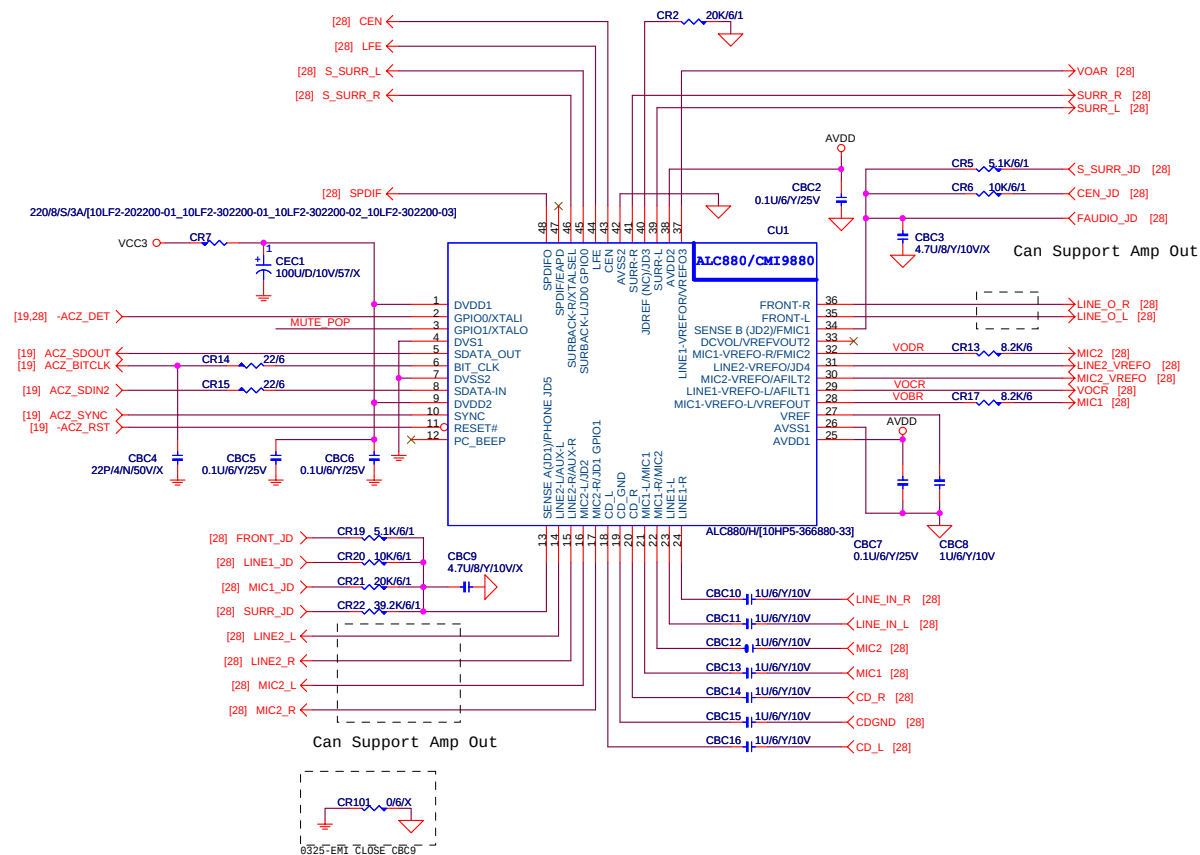
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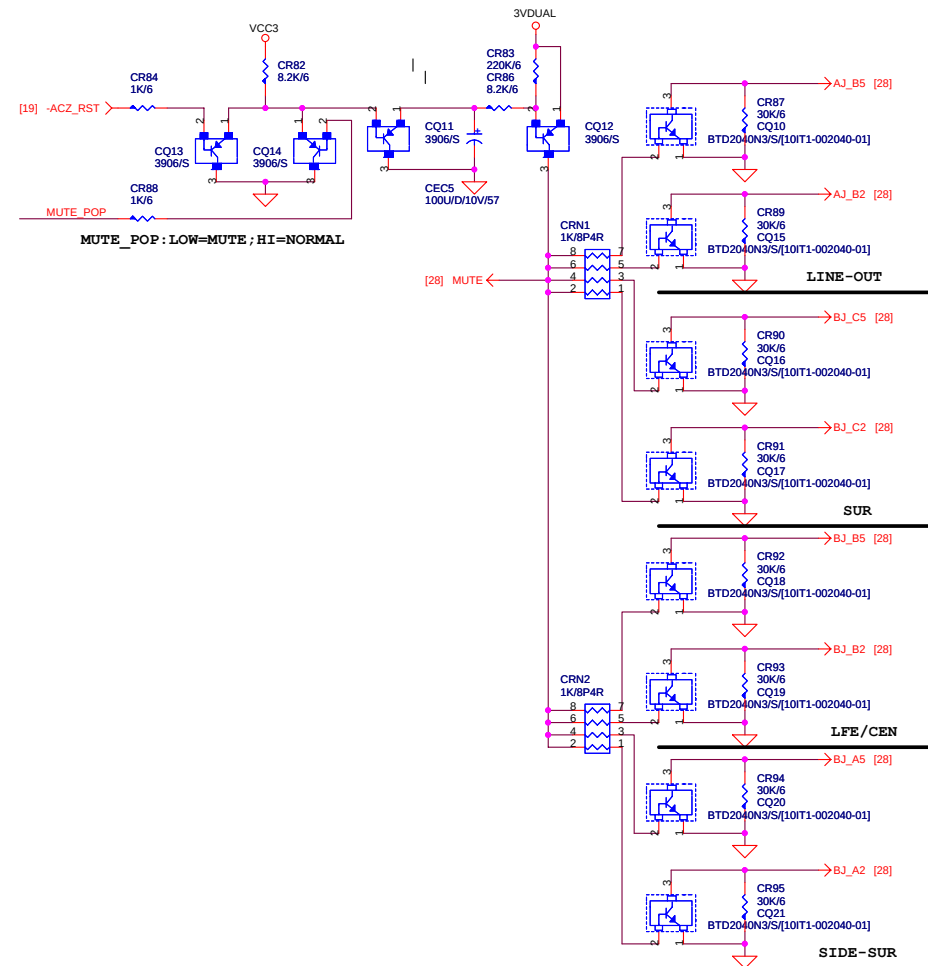
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CASE OPEN=LOW;
CASE CLOSE=HI;
```



AZALIA CODEC



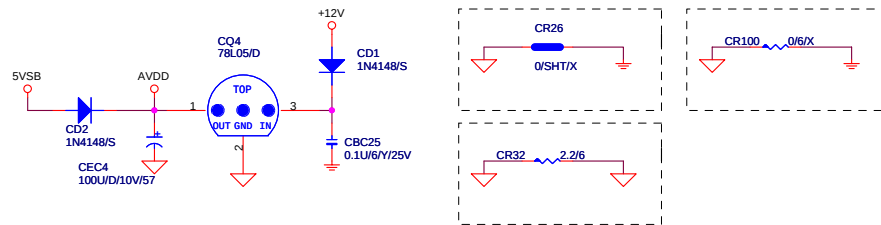
ANTI-POP NOISE



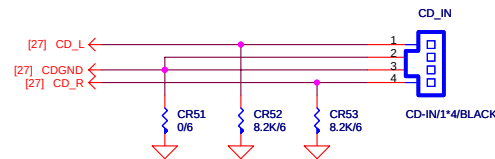
Gigabyte Technology

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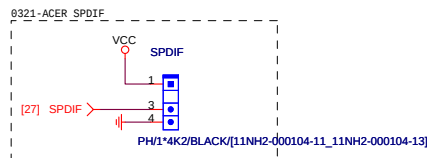
CODEC POWER/EMI PAD



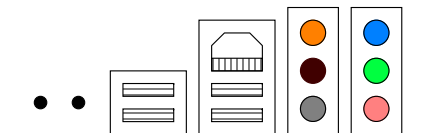
CD IN



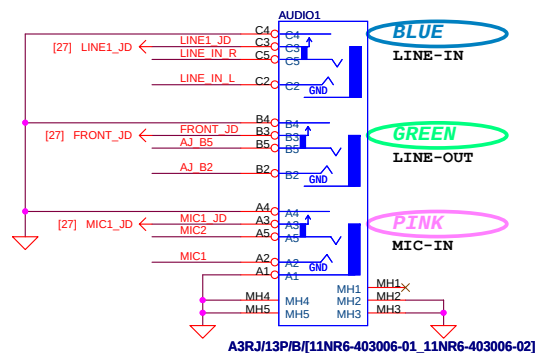
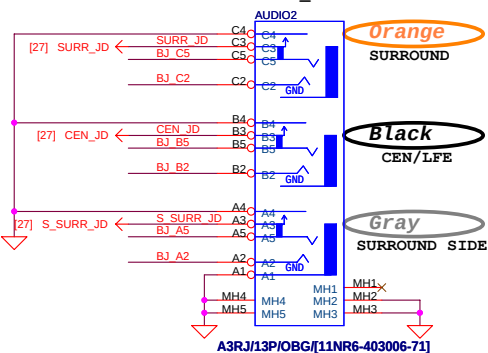
SPDIF



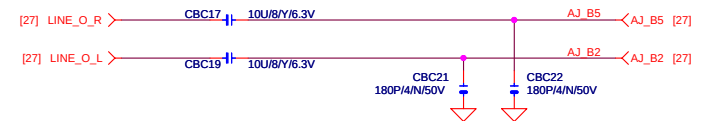
AZALIA JACK	ACER REQUEST CHANGE LOCTION
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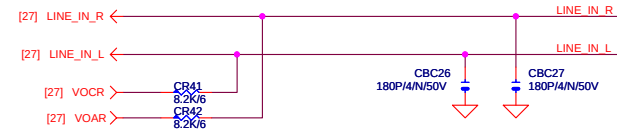
0321
NEW TYPE: IBM AUDIO-13P-1



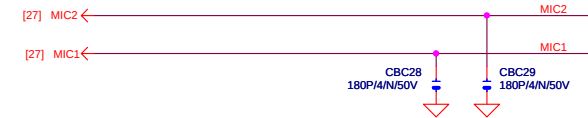
LINE-OUT



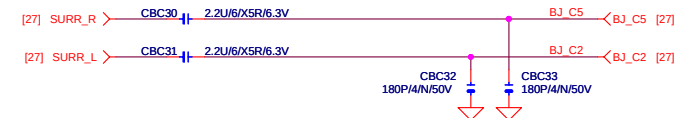
LINE-IN



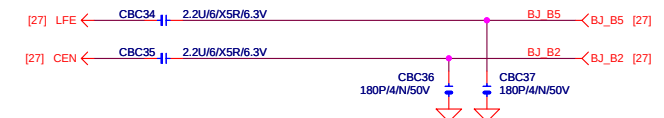
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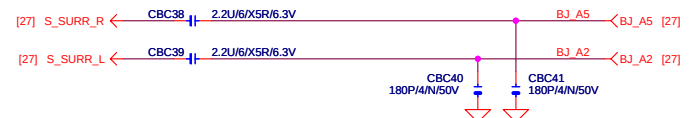
SURROUND



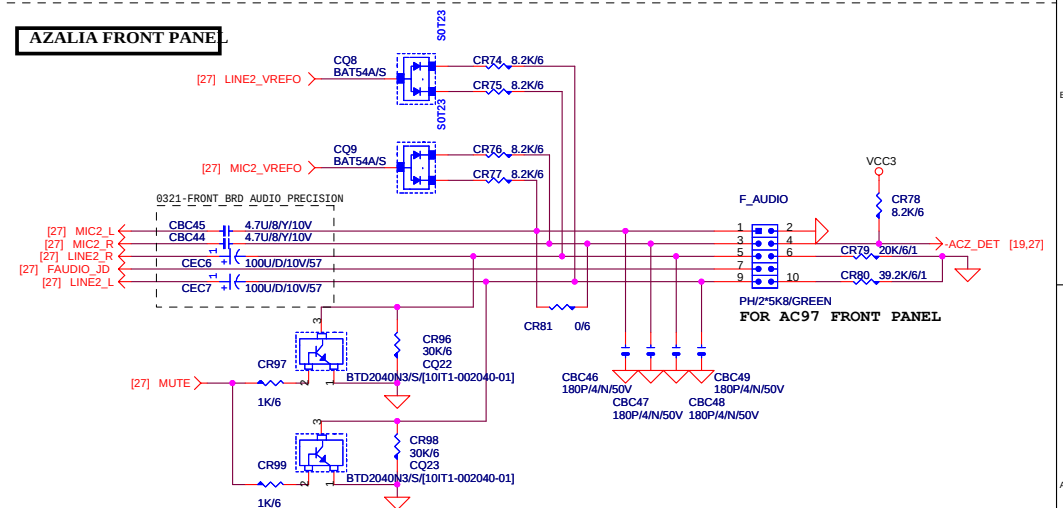
CEN/LFE

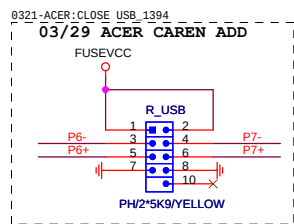
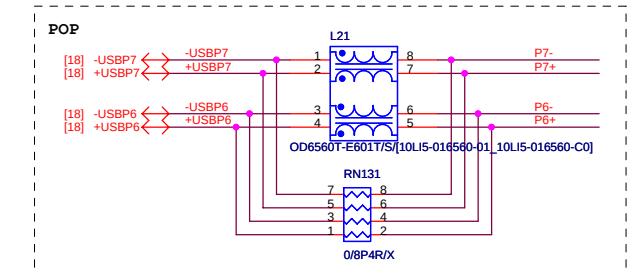
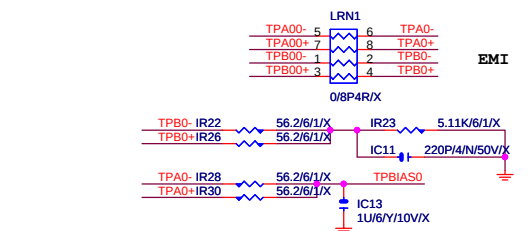
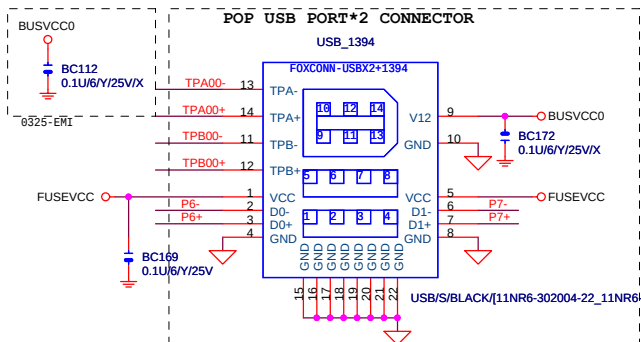
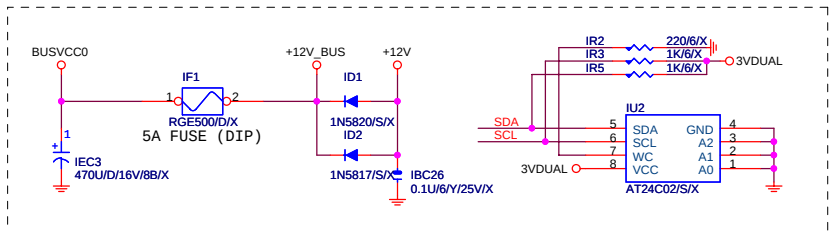
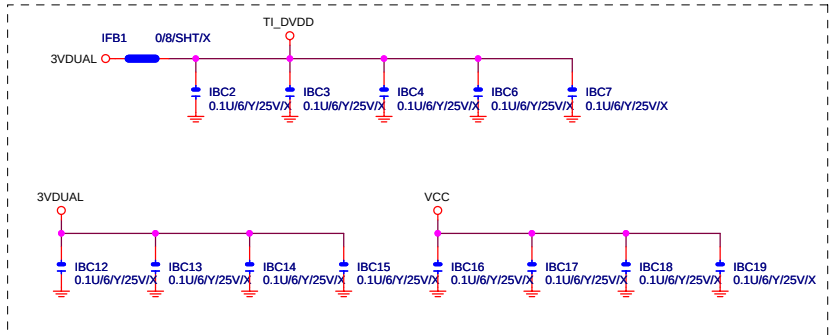


SURR BACK



AZALIA FRONT PANE

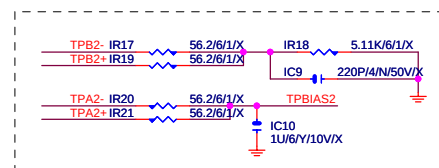
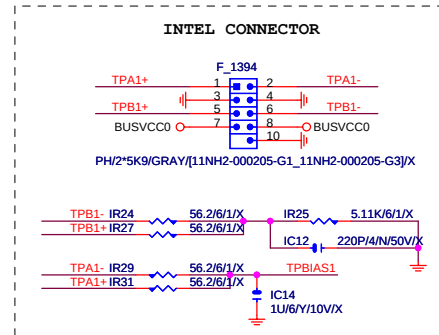
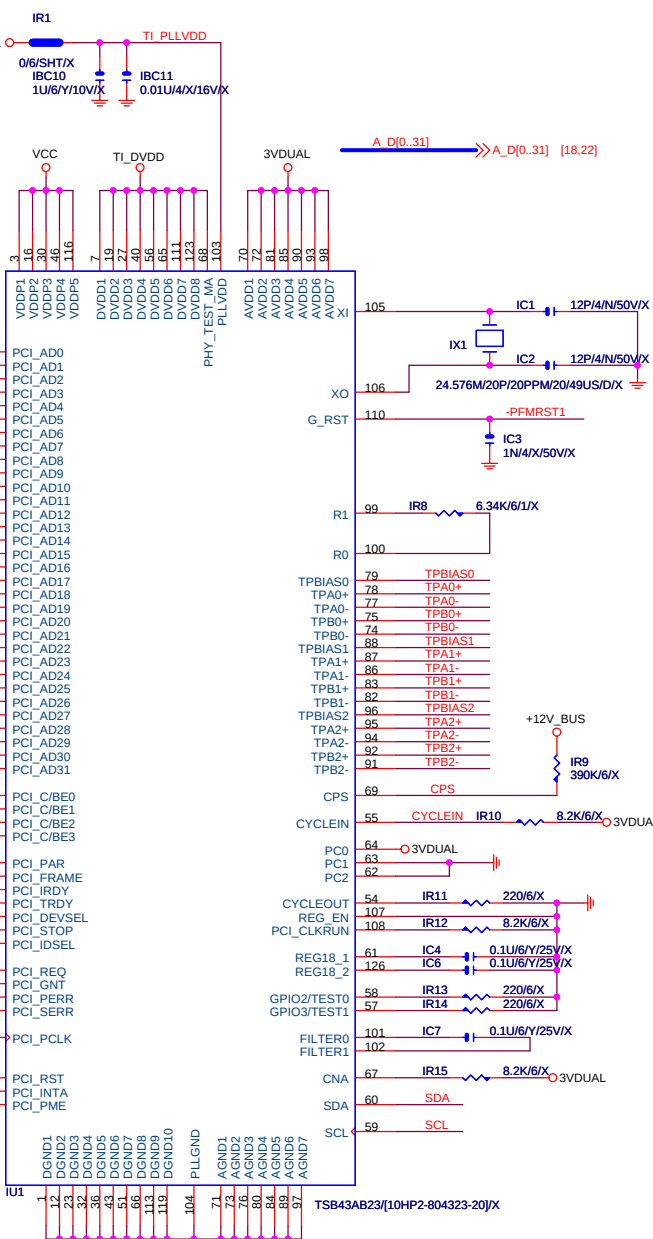




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| A D1 | 50 | PCI_AD1 |
| A D2 | 49 | PCI_AD2 |
| A D3 | 48 | PCI_AD3 |
| A D4 | 47 | PCI_AD4 |
| A D5 | 45 | PCI_AD5 |
| A D6 | 44 | PCI_AD6 |
| A D7 | 42 | PCI_AD7 |
| A D8 | 39 | PCI_AD8 |
| A D9 | 38 | PCI_AD9 |
| A D10 | 37 | PCI_AD10 |
| A D11 | 35 | PCI_AD11 |
| A D12 | 34 | PCI_AD12 |
| A D13 | 33 | PCI_AD13 |
| A D14 | 29 | PCI_AD14 |
| A D15 | 31 | PCI_AD15 |
| A D16 | 14 | PCI_AD16 |
| A D17 | 13 | PCI_AD17 |
| A D18 | 11 | PCI_AD18 |
| A D19 | 10 | PCI_AD19 |
| A D20 | 9 | PCI_AD20 |
| A D21 | 8 | PCI_AD21 |
| A D22 | 6 | PCI_AD22 |
| A D23 | 5 | PCI_AD23 |
| A D24 | 128 | PCI_AD24 |
| A D25 | 127 | PCI_AD25 |
| A D26 | 125 | PCI_AD26 |
| A D27 | 124 | PCI_AD27 |
| A D28 | 122 | PCI_AD28 |
| A D29 | 121 | PCI_AD29 |
| A D30 | 120 | PCI_AD30 |
| A D31 | 118 | PCI_AD31 |

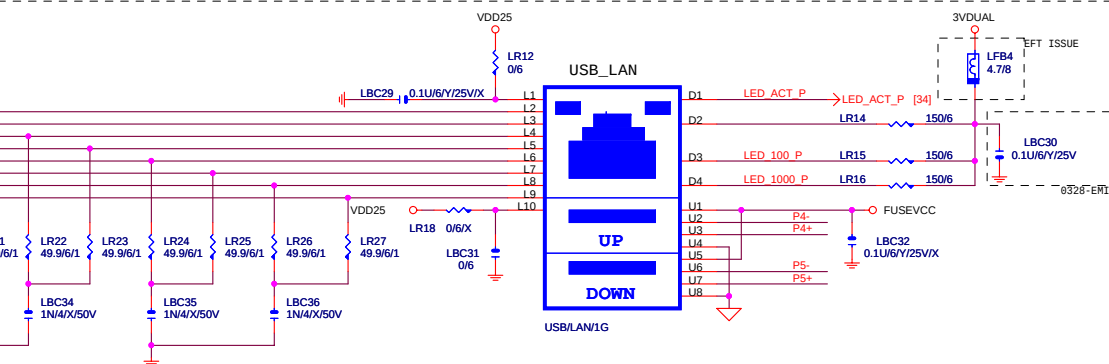
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|-----------------|----|------------|
| [18,22] -C_BE0 | 41 | PCI_C/BE0 |
| [18,22] -C_BE1 | 28 | PCI_C/BE1 |
| [18,22] -C_BE2 | 15 | PCI_C/BE2 |
| [18,22] -C_BE3 | 2 | PCI_C/BE3 |
| [18,22] PAR | 26 | PCI_PAR |
| [18,22] -FRAME | 17 | PCI_FRAME |
| [18,22] -IRDY | 18 | PCI_IRDY |
| [18,22] -TRDY | 20 | PCI_TRDY |
| [18,22] -DEVSEL | 21 | PCI_DEVSEL |
| [18,22] -STOP | 22 | PCI_STOP |
| A D23 | 4 | PCI_IDSEL |

- | | | |
|---------------------|-----|----------|
| [18,22] -REQ2 | 115 | PCI_REQ |
| [18] -GNT2 | 114 | PCI_GNT |
| [18,22] -PERR | 24 | PCI_PERR |
| [18,22] -SERR | 25 | PCI_SERR |
| [21] 1394CLK | 112 | PCI_PCLK |
| [24,26,30] -PFMRST1 | 53 | PCI_RST |
| [18,22] -PIRQH | 109 | PCI_INTA |
| [18,22,30] -PCIPME | 117 | PCI_PME |

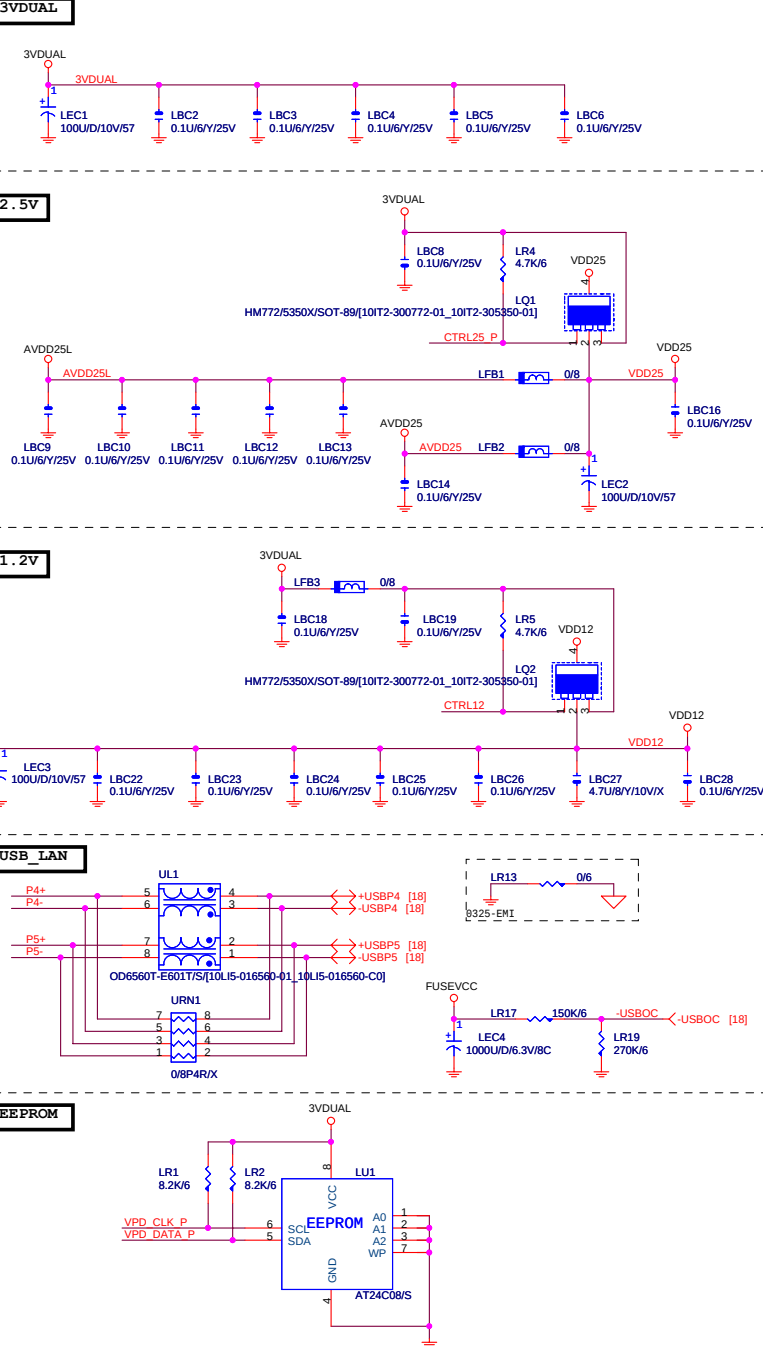


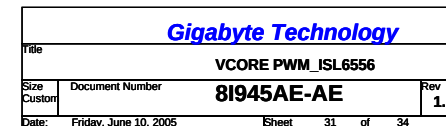
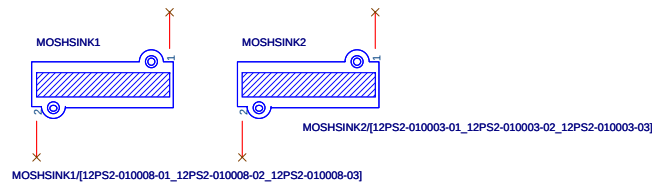
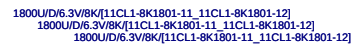
Layout Check 注意事項

- [14,15,17,19,21,22,24] SMB
[14,15,17,19,21,22,24] SMBD

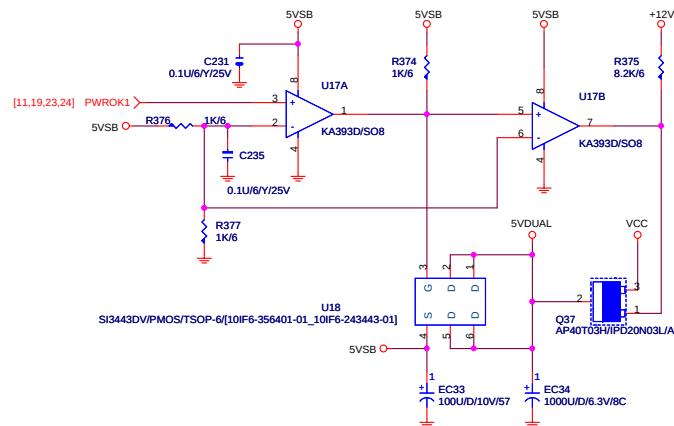


EEPROM

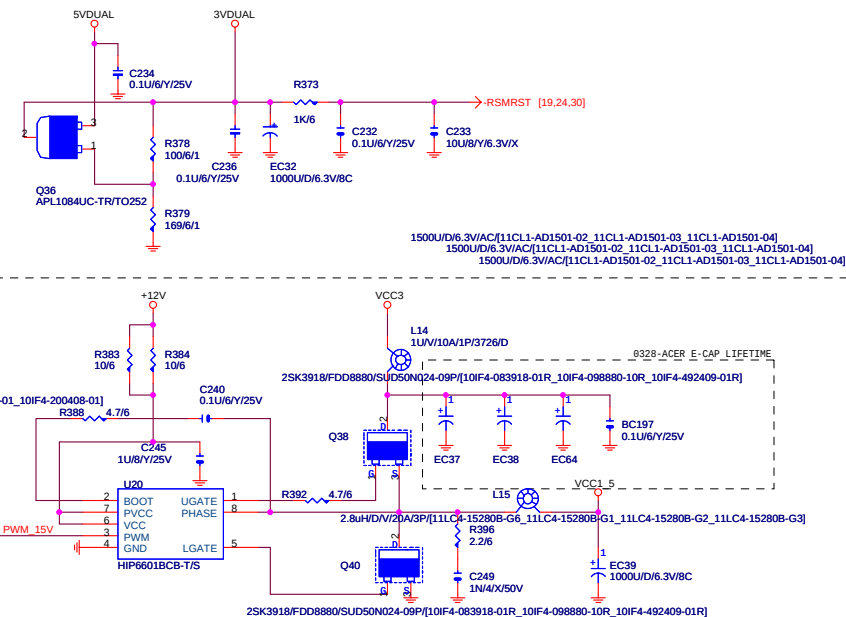




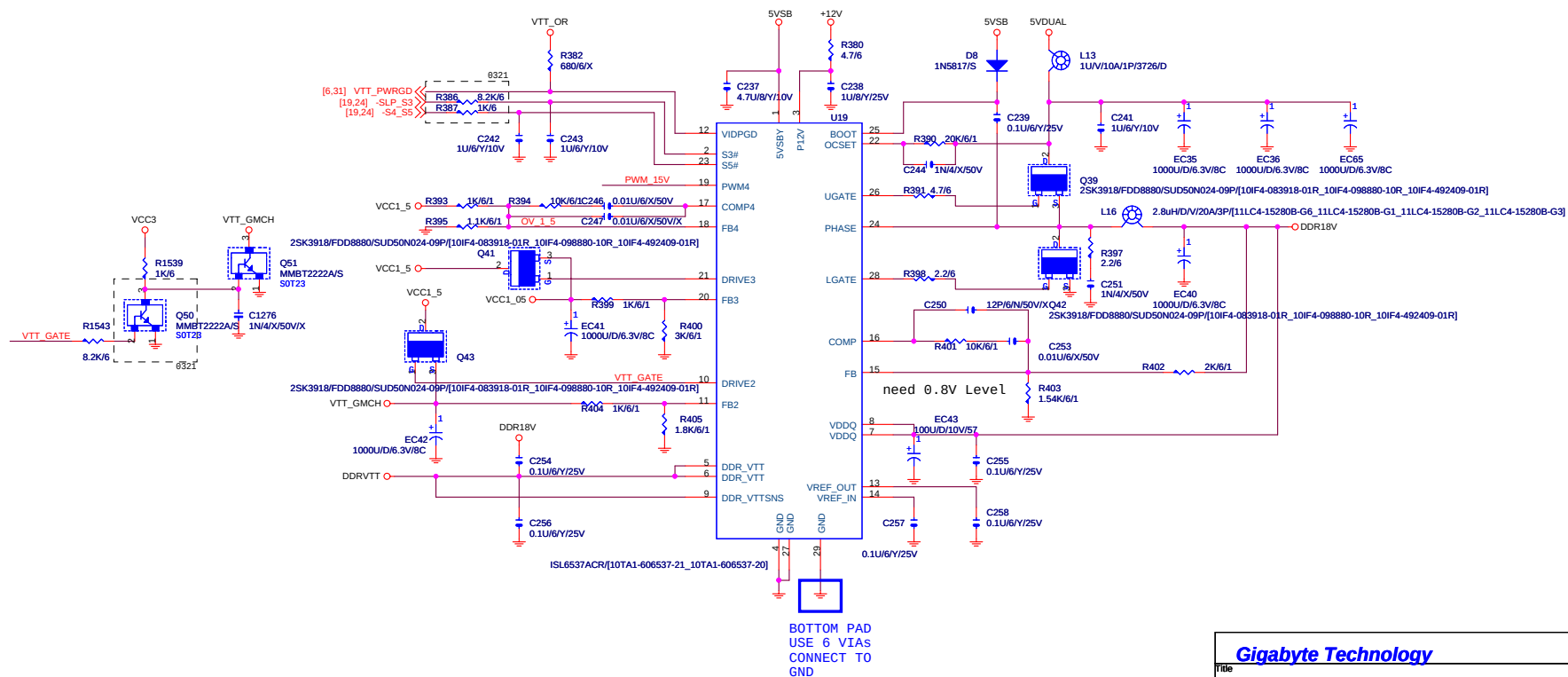
5VDUAL



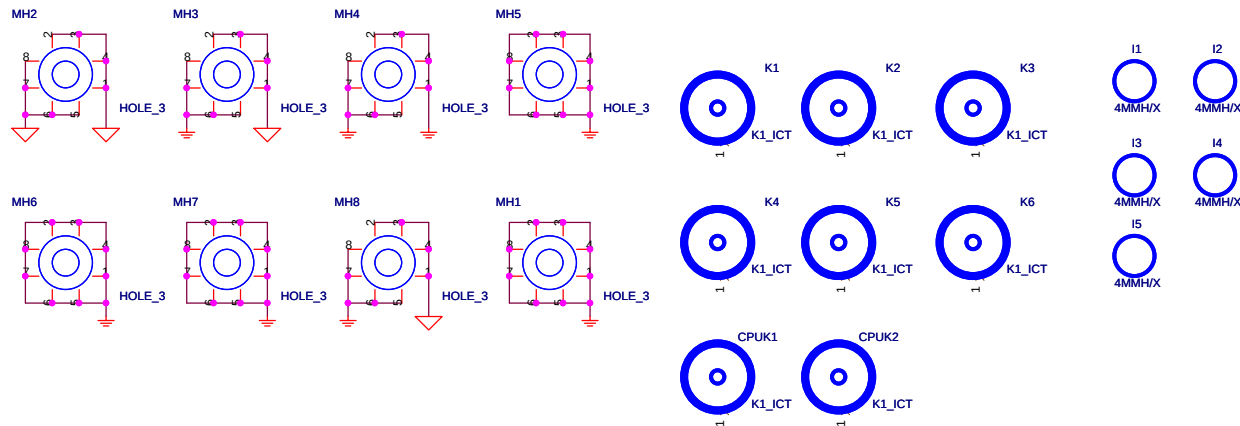
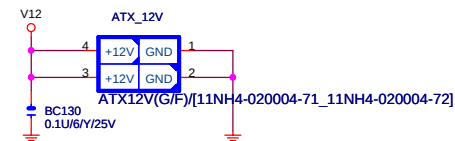
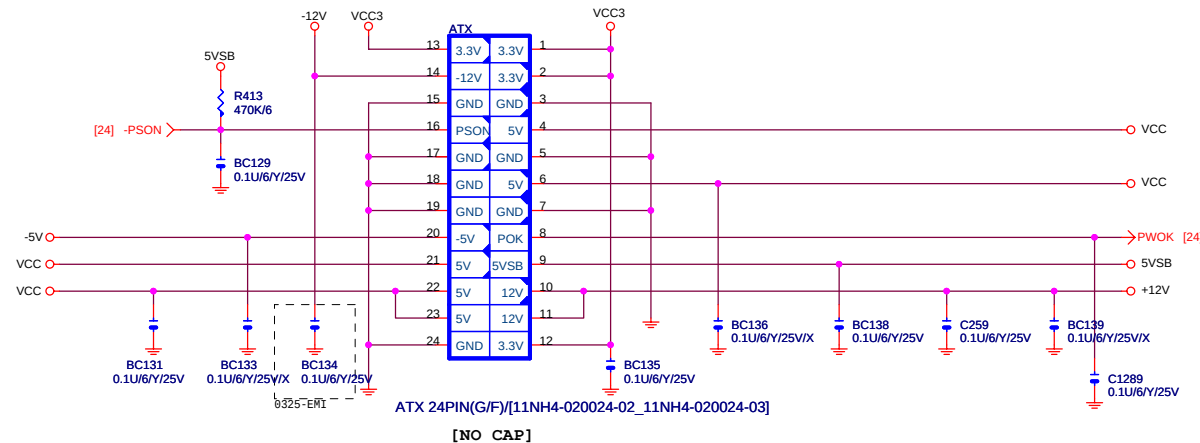
3VDUAL



VCC1 5



ATX POWER CONNECTOR

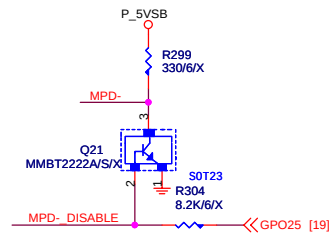
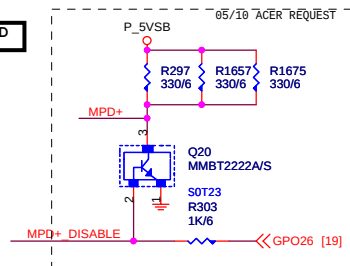


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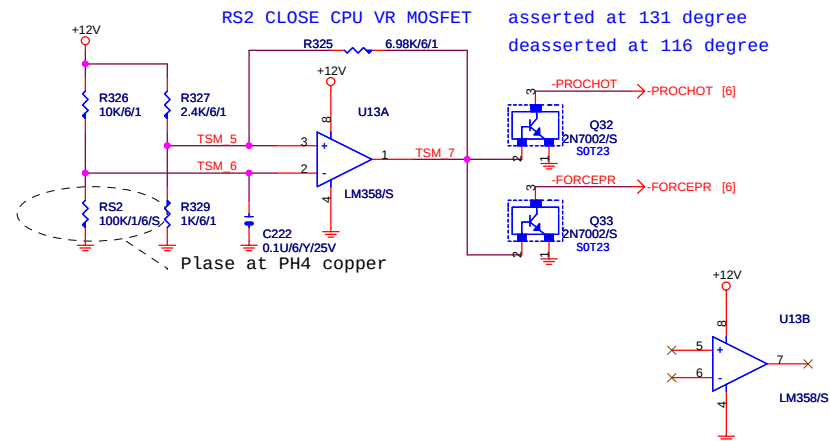
Title			
ATX POWER CONNECTOR			
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DUAL COLOR LED

0321



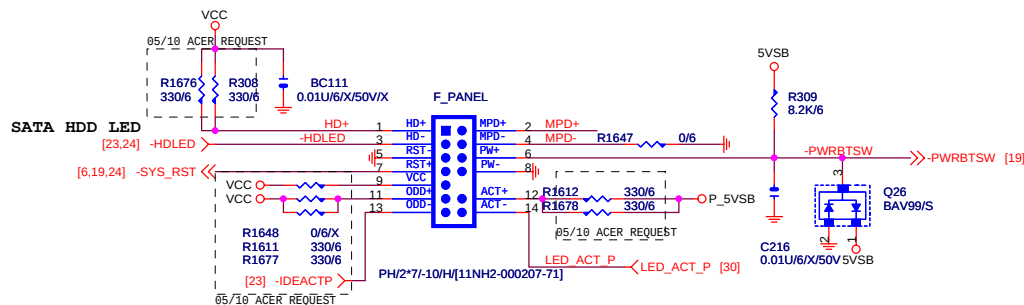
PROCESSOR HOT



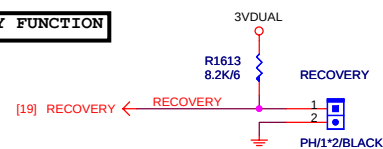
PREVENT S0 W/O CPU, GREEN IS ON.

INTEL FRONT PANEL

modify PIN2*7 cut 10pin for ACER



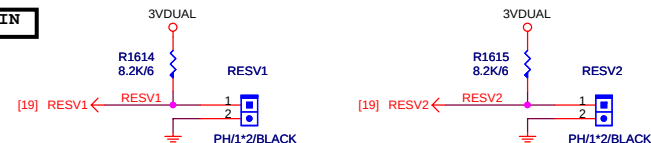
RECOVERY FUNCTION



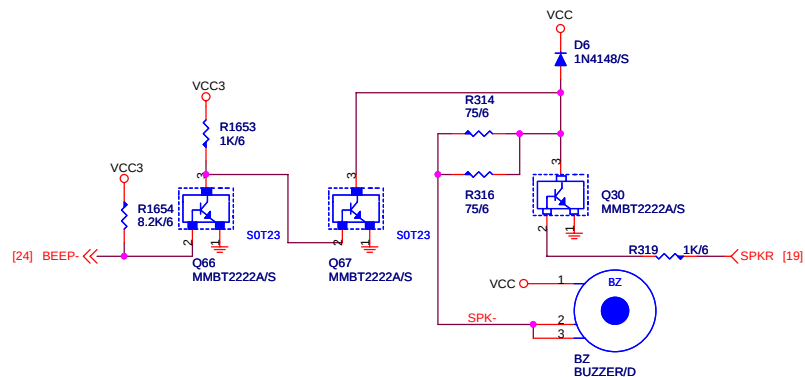
RECOVERY

OPEN	NORMAL
SHORT	RECOVERY

RESERVE PIN



BUZZER



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Custom			
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