

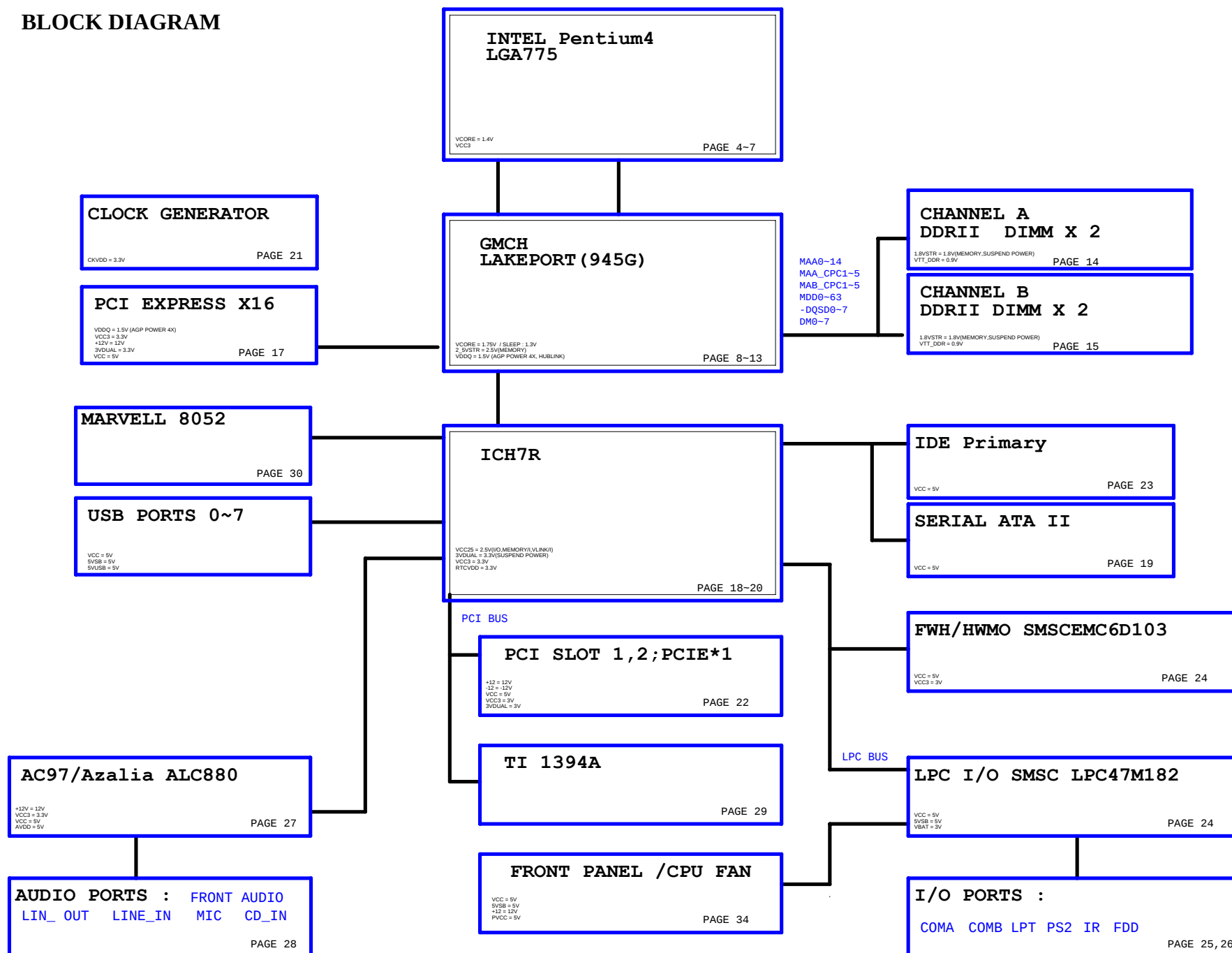
SHEET TITLE

01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4 LGA775 A
05	P4 LGA775 B
06	P4 LGA775 C
07	P4 LGA775 D,E,F,G
08	GMCH-LAKEPORT HOST
09	GMCH-LAKEPORT DDRII
10	GMCH-LAKEPORT PCI E, DMI
11	GMCH-LAKEPORT INT VGA
12	GMCH-LAKEPORT GND
13	GMCH-LAKEPORT PWR
14	DDRII CHANNEL A 1,2
15	DDRII CHANNEL B 1,2
16	DDRII TERMINATION
17	PCI EXPRESS*16 SLOT
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	GB/CK410M CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE/FLOPPY/USB
24	SMSC LPC IO
25	COM LPT
26	BIOS,CI,HWM,KB/MS
27	ALC880

SHEET TITLE

28	REAR AUDIO JACK
29	TI TSB43AB23 1394
30	MARVELL 88E8052
31	VCORE PWM ISL6556
32	DISCRETE POWER
33	ATX, OTHERS POWER
34	FRONT PANEL

BLOCK DIAGRAM



Model Name: 8I945AEF-AE

Version: 1.1

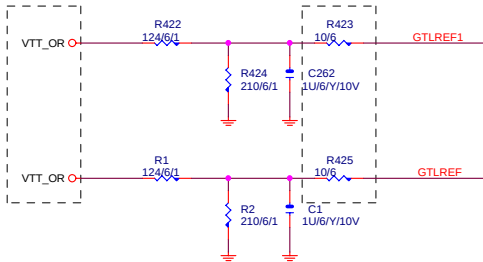
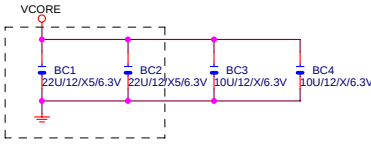
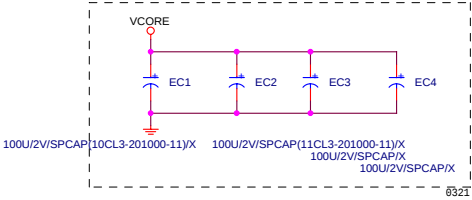
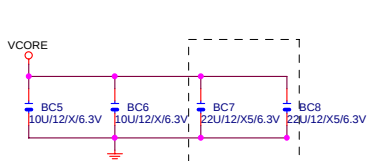
Component value change history

2005/06/30

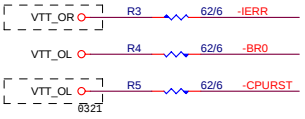
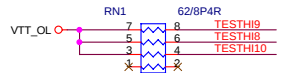
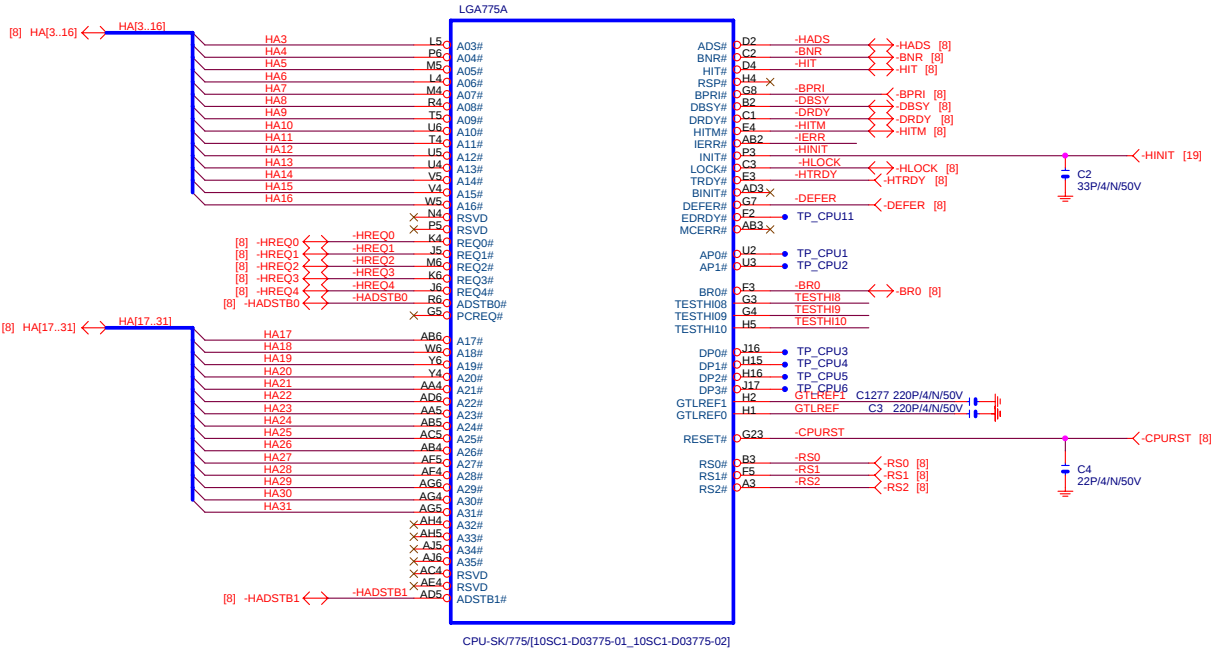
Data	Reason	Change Item
2005/02/01	PAGE 29:ACER modify USB_1394 for rear window	
	PAGE 24:add 5VDUAL LOCKER	
2005/03/21	PCB REV 0.2 MODIFY(ACER)	
	PAGE 31:LAYOUT CHANGED	1.VCORE PWM(NORTH:3 PHASE,EAST:1 PHASE)
		ADD MOSHSINK1,2
	PAGE 31:LAYOUT CHANGED	2.CPU_FAN,SYS_FAN USE OP+MOSFET
	PAGE 29:LAYOUT CHANGED	3.R USB/USB 1394 PORT 6/7 CO-LAYOUT on REAR SIDE
		4.Front 1394 connector only 1 keep
	PAGE 19:ACER ADD TPM FUNCTION	5.ADD TPM CONNECTOR
	PAGE 28:ACER MODIFY SPDIF CONNECTOR	6.SPDIF PH/1*4K2
	PAGE 26:ACER MODIFY CASEOPEN-->RTCVDD->3VDUAL	7.CACEOPEN- PULL-UP from RTCVDD-->3VDUAL
2005/03/29	9M8I945AEF-AE-02 E-BOM(ACER B2-C)	1.PAGE 4:ADD CPU RETAINTION/[12KRC-040005-21] & IO_SHIELD/[12AIO-AE0007-01]
	NB/SB CHIPSET MODIFY:945G/A2+ICH7/A1	2.NB/SB CHIPSET MODIFY:945G/A2/ACER/[10HB1-033000-23R]+ICH7/A1/ACER/[10HB1-032801-N3R]
	PAGE 11:LAYOUT BOM CHANGED(INTEL REQUEST)	3.SDVO CLDATA & SDVO_CLCLK add 220Ω(R1630,R1631)
	PAGE 24:LAYOUT BOM CHANGED-->FAN DESIGN	4.CPU FAN and SYS_FAN design OP+MOSFET circuit
	PAGE 28:LAYOUT BOM CHANGED-->F_AUDIO	5.ADD F AUDIO ACER CABLE AUDIO PRECISION(CEC6,CEC7-->100UF)
	PAGE 34:LAYOUT BOM CHANGED-->VCC1_5	6.EC37,EC38,EC64-->1500U/D/6.3V/AC/[11CL1-AD1501-02 11CL1-AD1501-03 11CL1-AD1501-04]
	PAGE 11:LAYOUT BOM CHANGED(INTEL REQUEST)-->SATAII PLL POWER ADD (LC) DECOUPLING	7.L22-->10UH/0806/S/[10LI2-12100A-02 10LI2-12100A-03];BC194-->10U/8/X/6.3V
	PAGE 34:BOM CHANGED-->PWRLD	8.USE GPO26(PWRLD S1/S3 BLINKING)
2005/04/15	PCB REV 0.3 MODIFY(ACER)	1.EMI ISSUE FOR COM PORT (VIN)
2005/04/28	BOM:9M8I945AEF-AE-10A PVT	1.更改PCB VER:1.0 2.CBC30,CBC31,CBC34,CBC35,CBC38,CBC39:1U-->2.2U 3.REMOVE U11<BIOS SK> 4.REMOVE PCIE_12V
2005/05/04	BOM:9M8I945AEF-AE-10A ECN	1.BZ加替料
2005/05/24	BOM:9M8I945AEF-AE-11A DVT	1.SB change ICH7R
2005/06/13	BOM:9M8I945AEF-AE-11B-DVT	1.C1288 ADD 10K/4 2.1394加替料
2005/06/13	BOM:9M8I945AEF-AE-11B PVT	1.轉P-BOM

Circuit or PCB layout change
for next version

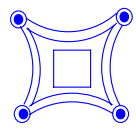
[illegible]



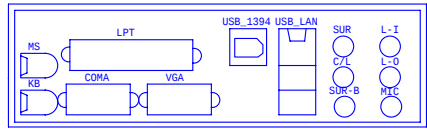
LGA775 FOOT PRINT FOR ACER:LGA775-ACER

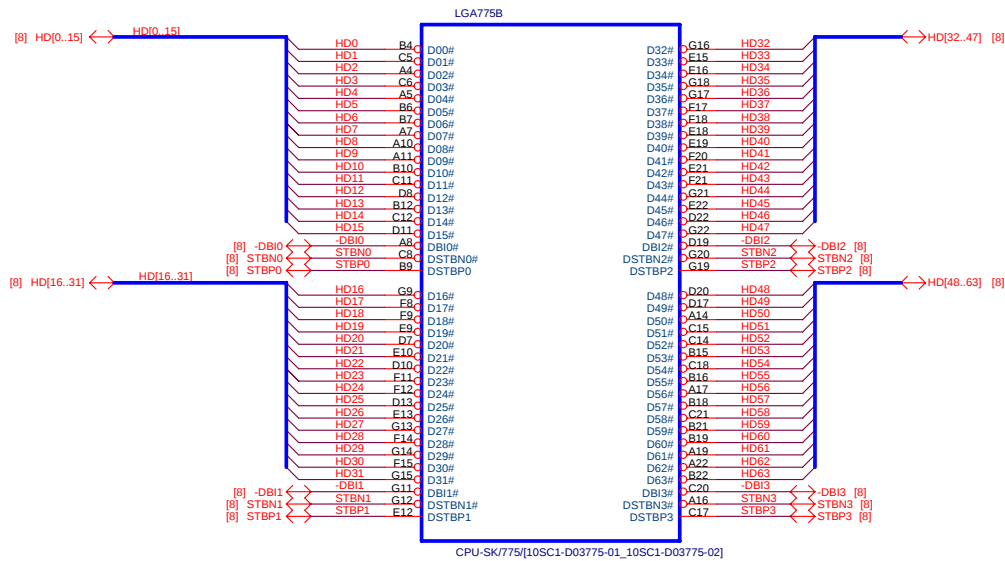


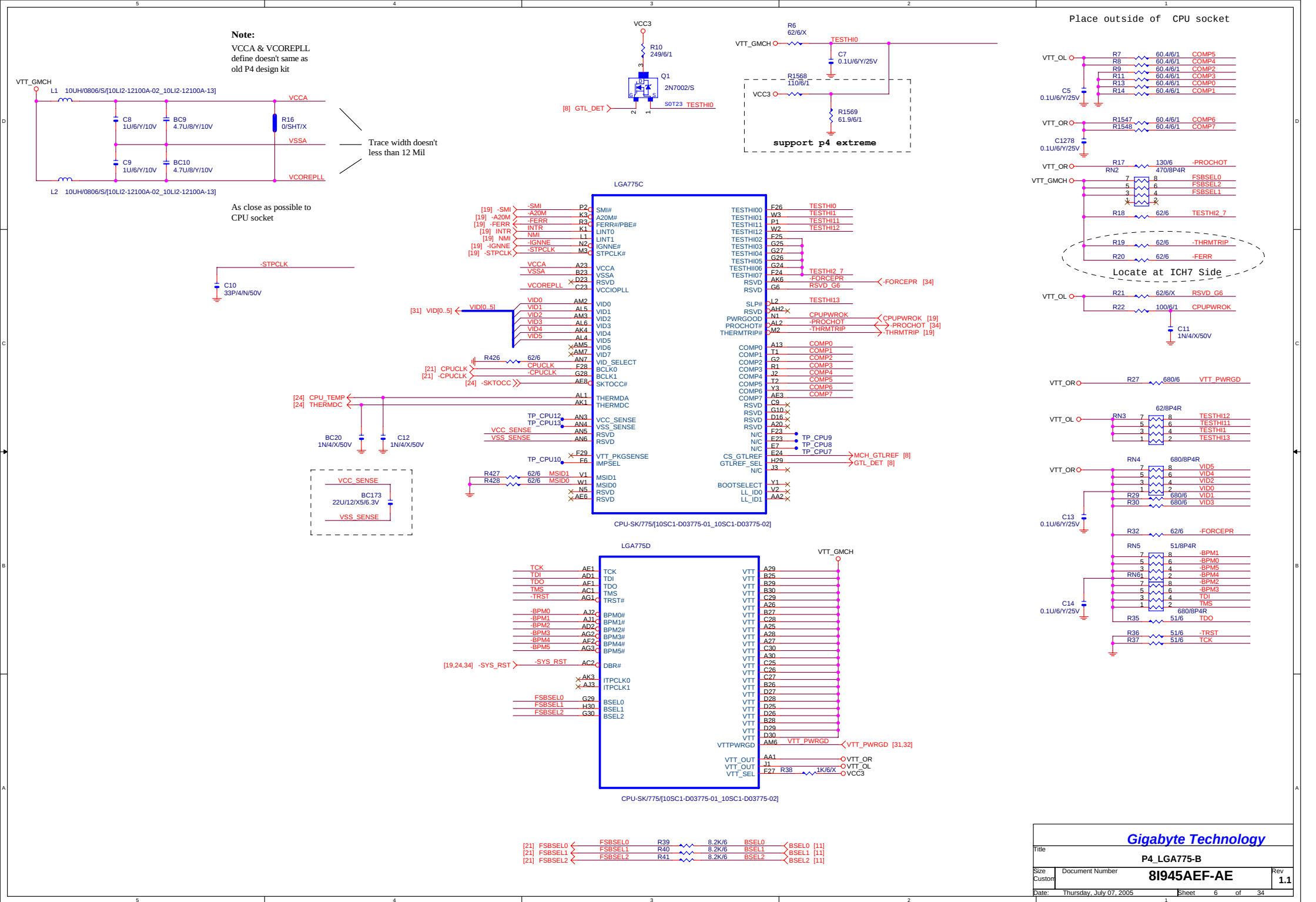
LGA775-CR
CPU_RETENTION[12KRC-040005-21]



IO_SHIELD
IO_SHIELD[12AIO-AE0007-01]



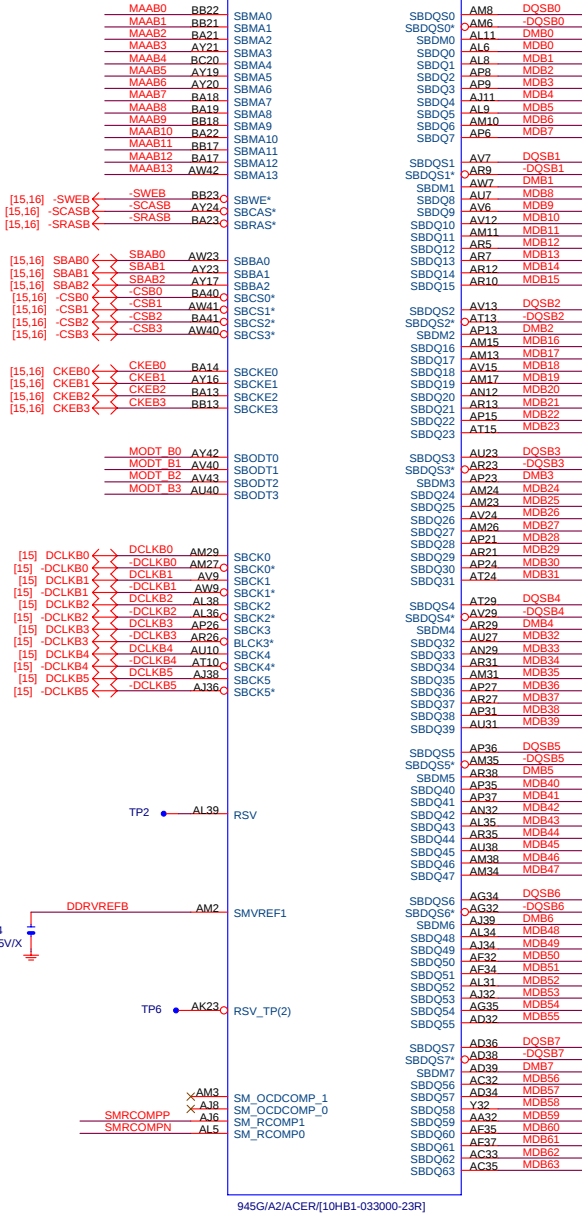




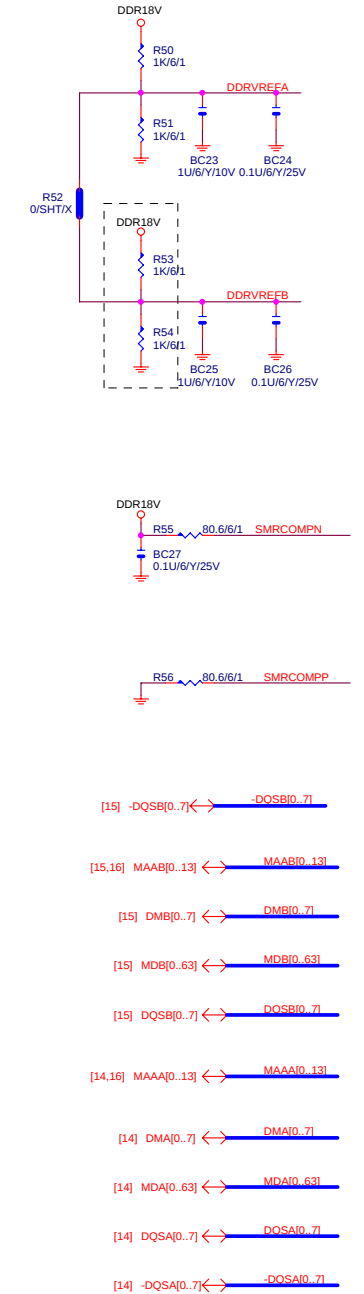
MODT_A[0..3] ↔ MODT_A[0..3] [14,16]



U2G

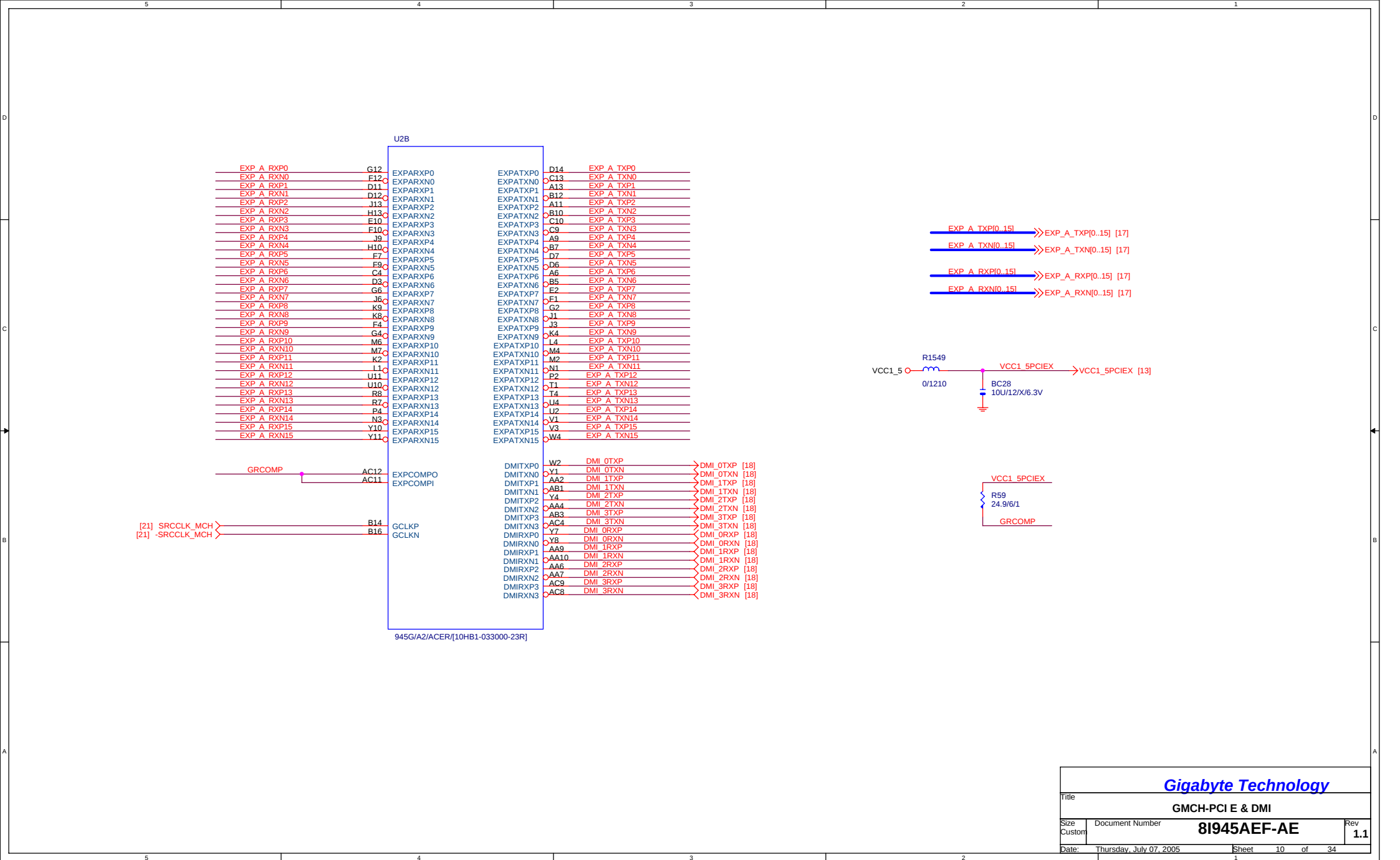


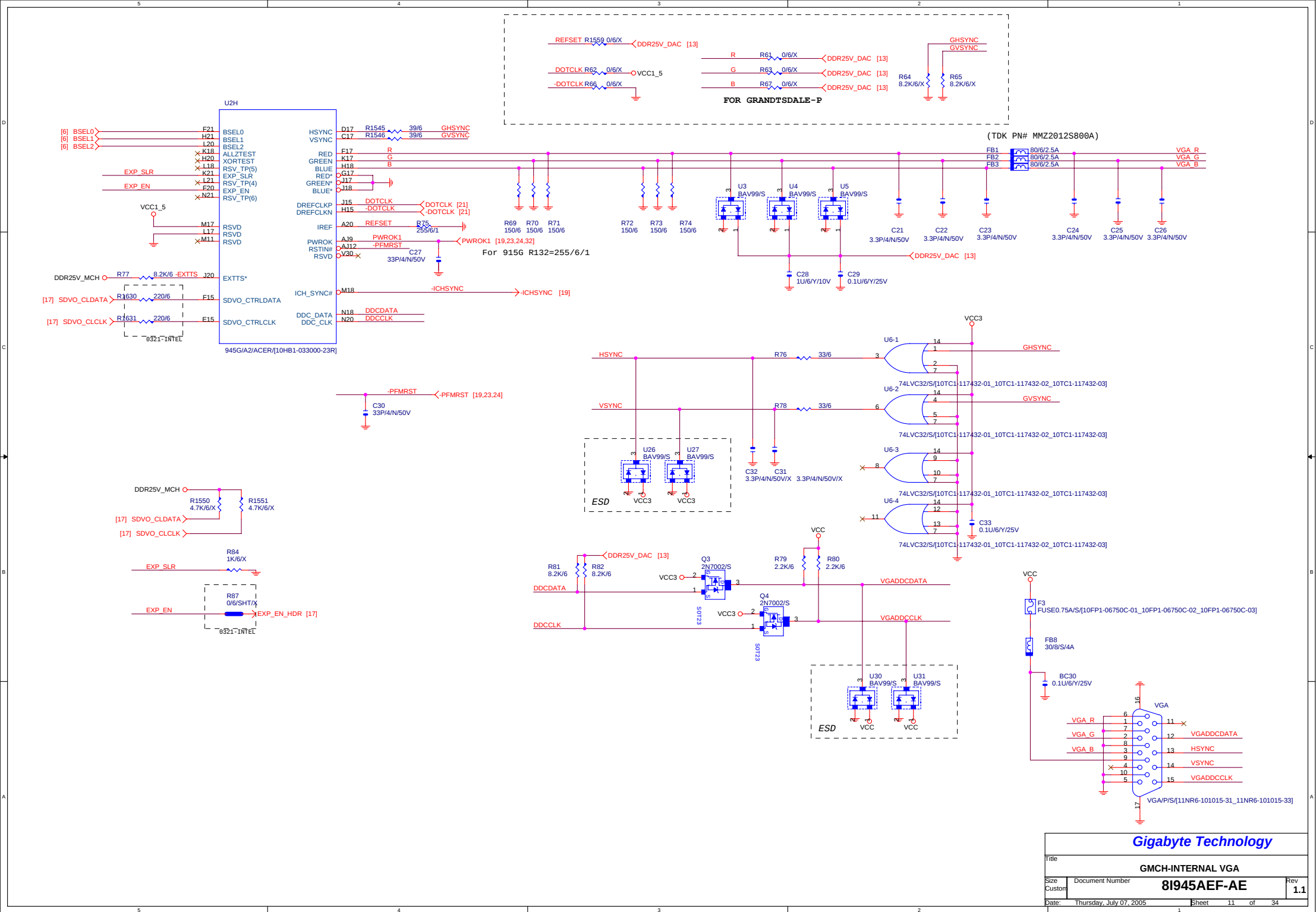
MODT_B[0..3] ↔ MODT_B[0..3] [15,16]

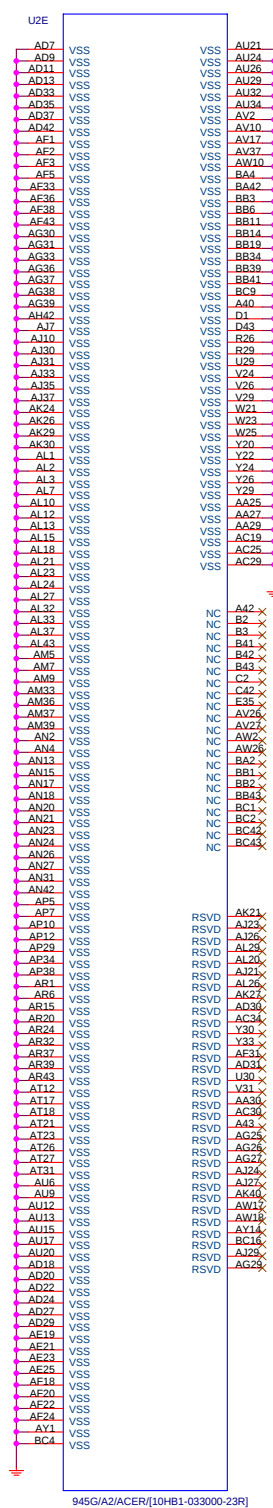
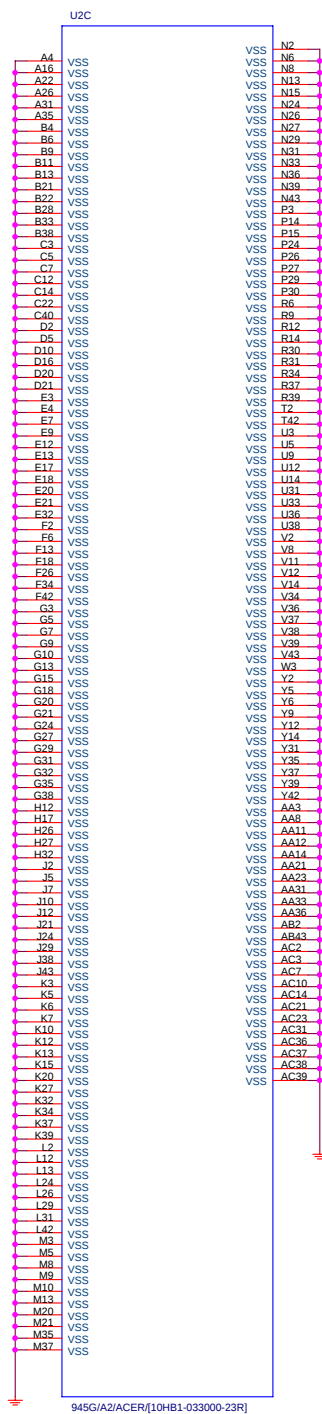


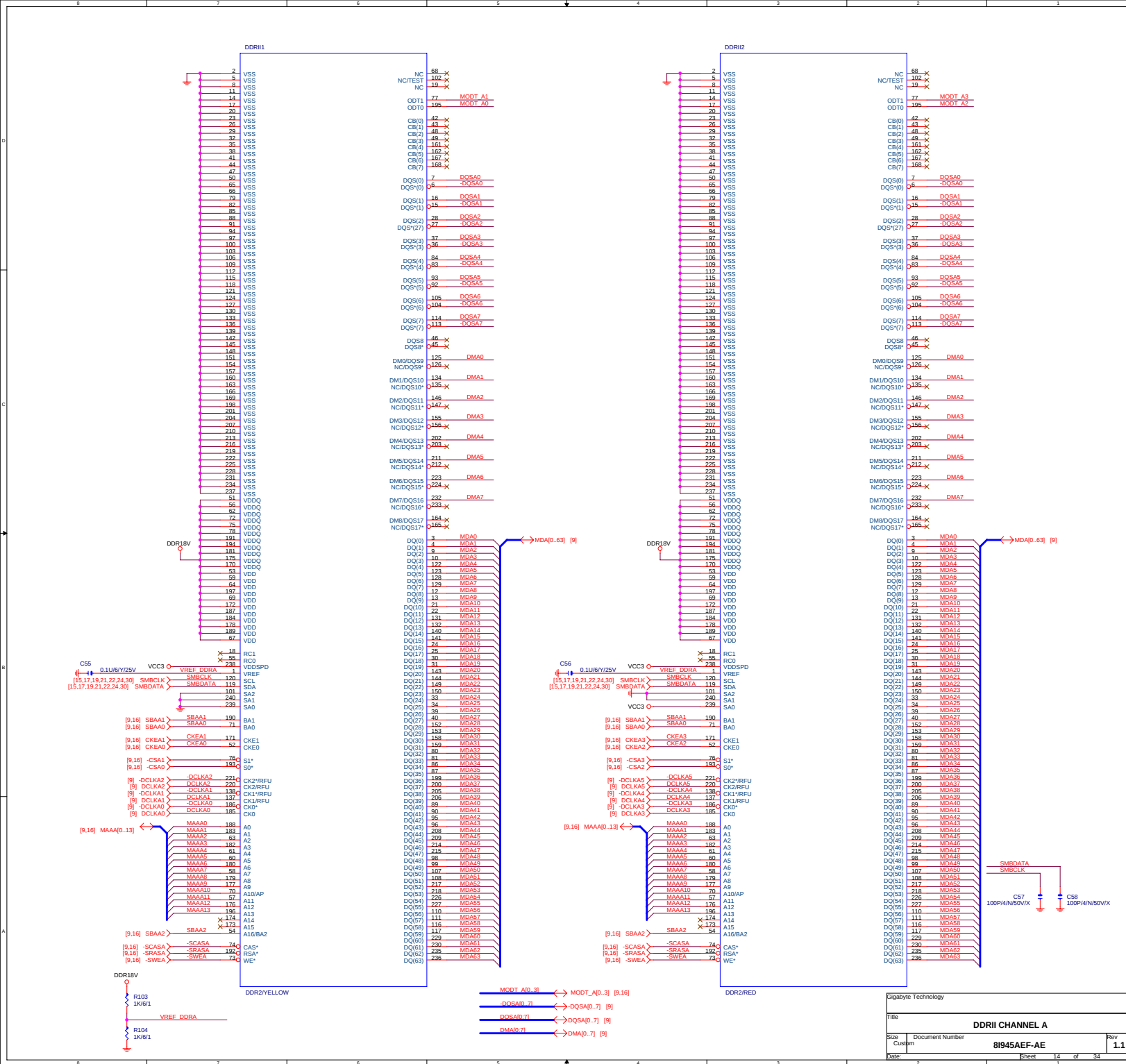
Gigabyte Technology

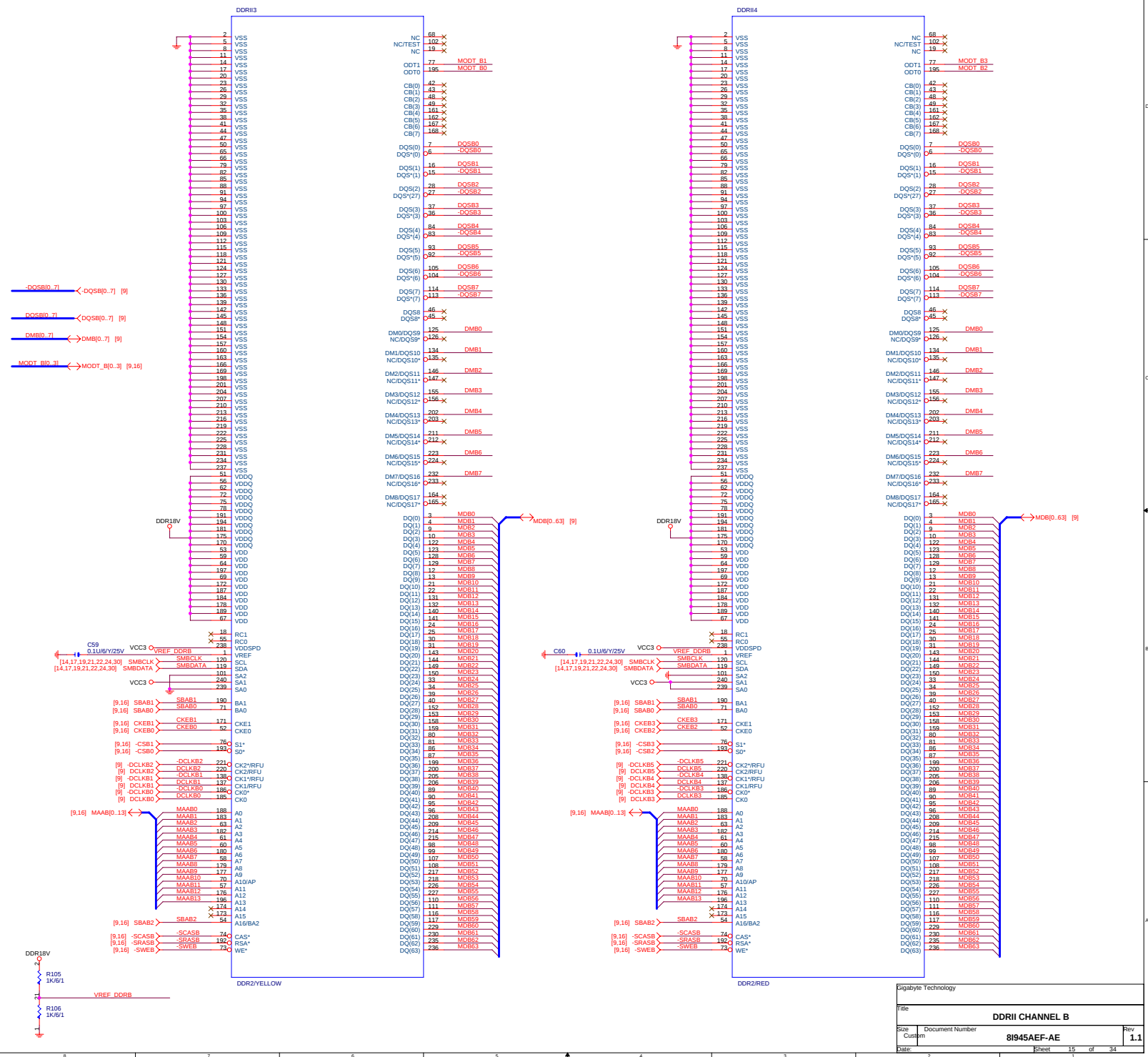
Title			GMCH-DDRII
Size	Document Number	81945AEF-AE	Rev 1.1
Custom			
Date:	Thursday, July 07, 2005	Sheet	9 of 34







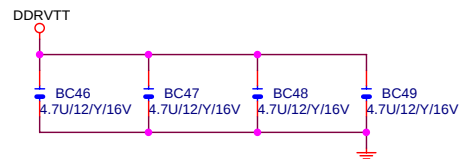




Sigabyte Technology	
File	
DDRII CHANNEL B	
Size	
Document Number	
81945AEF-AE	
Date	
Sheet 15 of 34	
Rev 1.1	

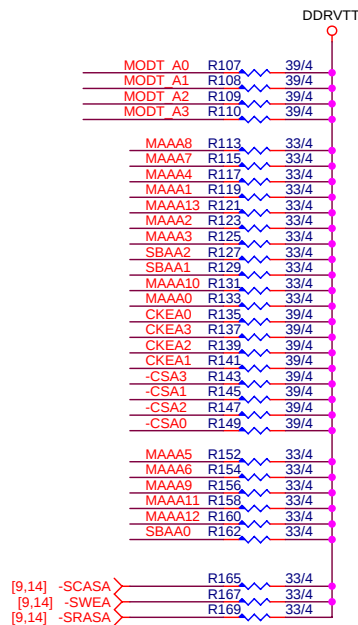
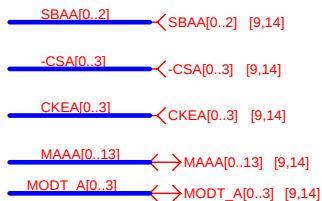
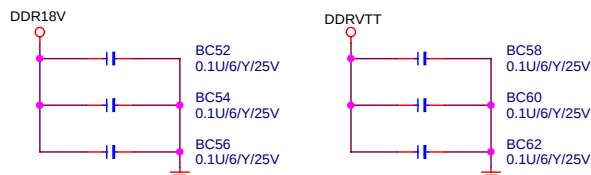
DDR TERMINATION CHANNEL A

DDRVTT Decouple



DDR18V Decouple

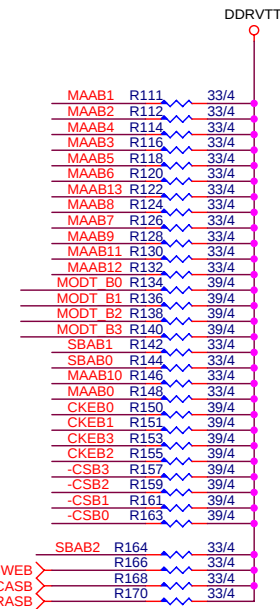
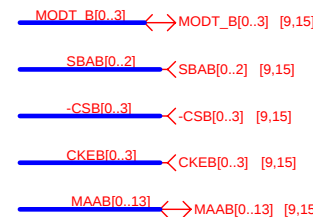
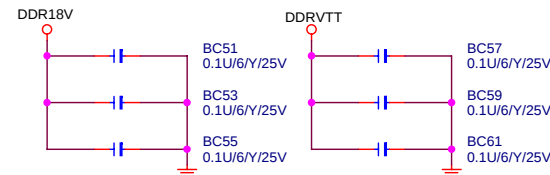
DDRVTT Decouple



DDR TERMINATION CHANNEL B

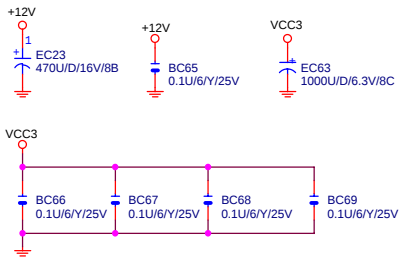
DDR18V Decouple

DDRVTT Decouple

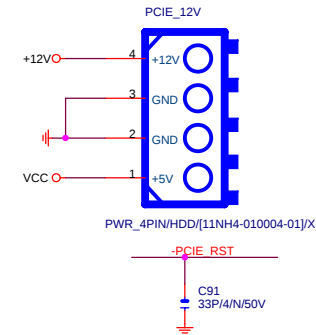
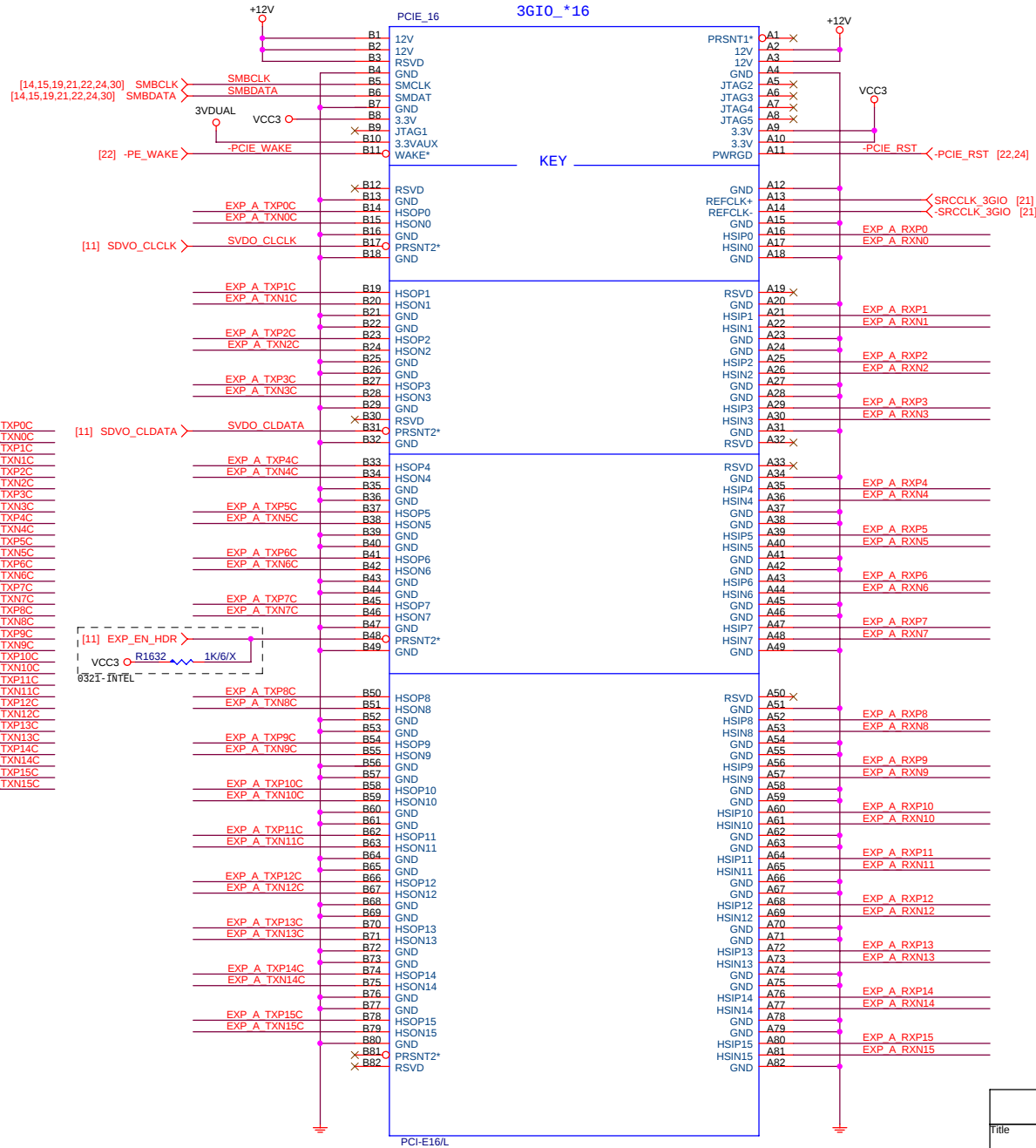


Gigabyte Technology

Title			
DDRII TERMINATOR			
Size	Document Number	Rev	
Custom	8I945AEF-AE	1.1	
Date:	Thursday, July 07, 2005	Sheet	16 of 34



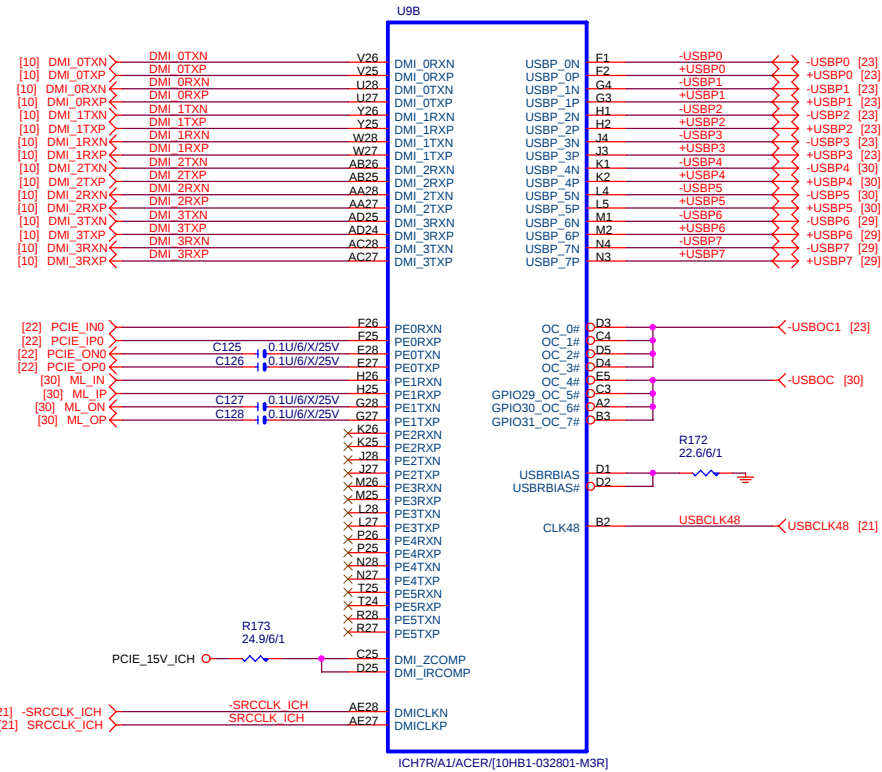
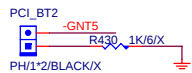
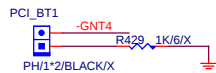
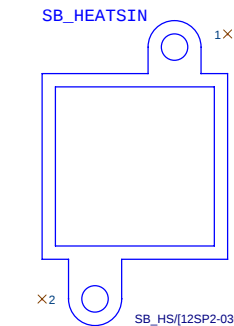
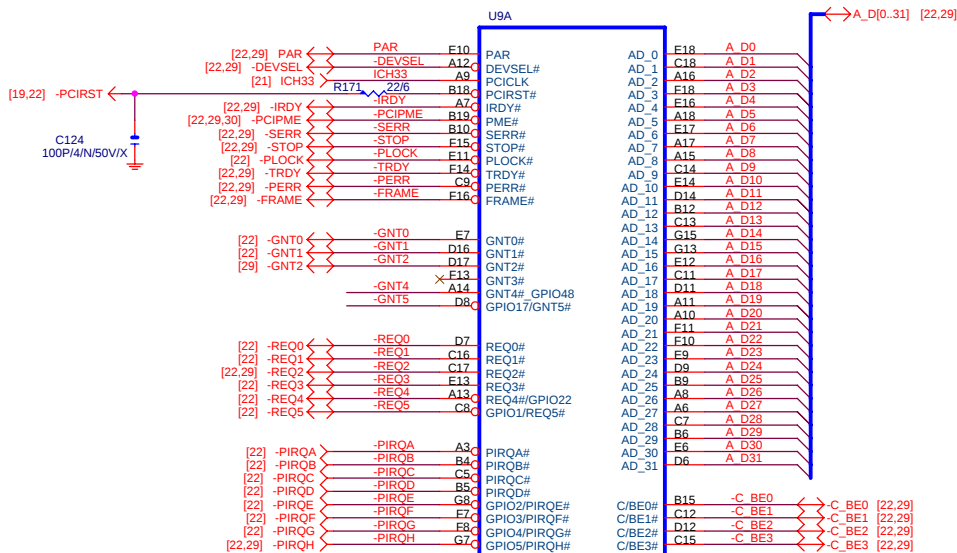
PCIESLOT-164DN-2

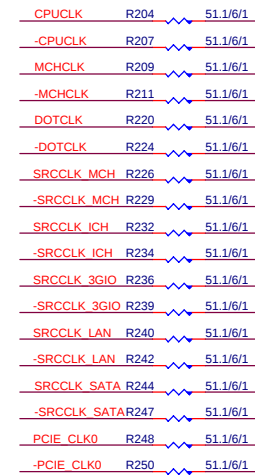
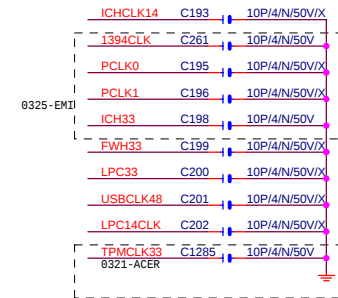
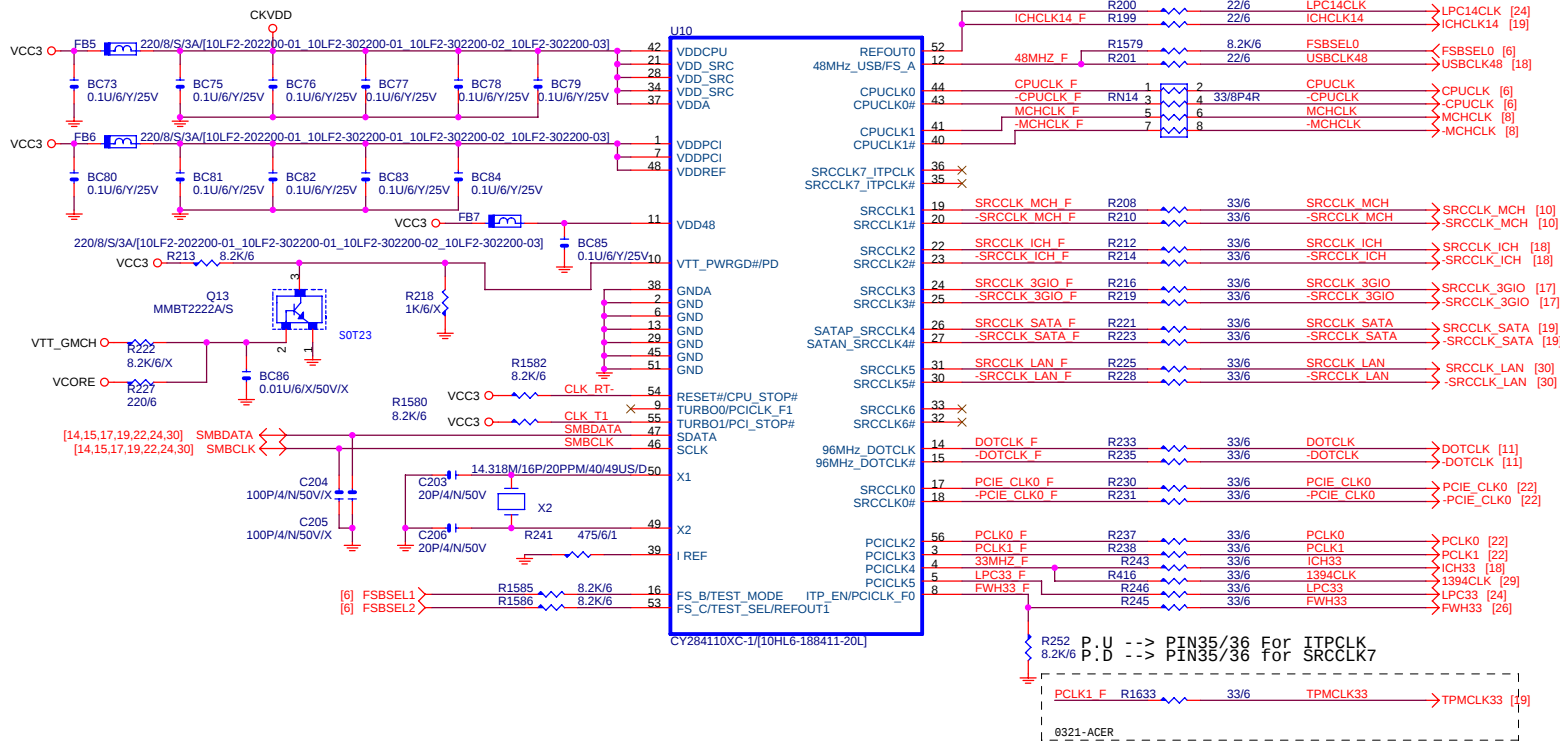


EXP A TXP0_15] >>>EXP_A_TXP0_15] [10]
EXP A TXN0_15] >>>EXP_A_TXN0_15] [10]

EXP A TXP0	C92	0.1U/6/Y/25V	EXP A TXP0C
EXP A TXN0	C93	0.1U/6/Y/25V	EXP A TXN0C
EXP A TXP1	C94	0.1U/6/Y/25V	EXP A TXP1C
EXP A TXN1	C95	0.1U/6/Y/25V	EXP A TXN1C
EXP A TXP2	C96	0.1U/6/Y/25V	EXP A TXP2C
EXP A TXN2	C97	0.1U/6/Y/25V	EXP A TXN2C
EXP A TXP3	C98	0.1U/6/Y/25V	EXP A TXP3C
EXP A TXN3	C99	0.1U/6/Y/25V	EXP A TXN3C
EXP A TXP4	C100	0.1U/6/Y/25V	EXP A TXP4C
EXP A TXN4	C101	0.1U/6/Y/25V	EXP A TXN4C
EXP A TXP5	C102	0.1U/6/Y/25V	EXP A TXP5C
EXP A TXN5	C103	0.1U/6/Y/25V	EXP A TXN5C
EXP A TXP6	C104	0.1U/6/Y/25V	EXP A TXP6C
EXP A TXN6	C105	0.1U/6/Y/25V	EXP A TXN6C
EXP A TXP7	C106	0.1U/6/Y/25V	EXP A TXP7C
EXP A TXN7	C107	0.1U/6/Y/25V	EXP A TXN7C
EXP A TXP8	C108	0.1U/6/Y/25V	EXP A TXP8C
EXP A TXN8	C109	0.1U/6/Y/25V	EXP A TXN8C
EXP A TXP9	C110	0.1U/6/Y/25V	EXP A TXP9C
EXP A TXN9	C111	0.1U/6/Y/25V	EXP A TXN9C
EXP A TXP10	C112	0.1U/6/Y/25V	EXP A TXP10C
EXP A TXN10	C113	0.1U/6/Y/25V	EXP A TXN10C
EXP A TXP11	C114	0.1U/6/Y/25V	EXP A TXP11C
EXP A TXN11	C115	0.1U/6/Y/25V	EXP A TXN11C
EXP A TXP12	C116	0.1U/6/Y/25V	EXP A TXP12C
EXP A TXN12	C117	0.1U/6/Y/25V	EXP A TXN12C
EXP A TXP13	C118	0.1U/6/Y/25V	EXP A TXP13C
EXP A TXN13	C119	0.1U/6/Y/25V	EXP A TXN13C
EXP A TXP14	C120	0.1U/6/Y/25V	EXP A TXP14C
EXP A TXN14	C121	0.1U/6/Y/25V	EXP A TXN14C
EXP A TXP15	C122	0.1U/6/Y/25V	EXP A TXP15C
EXP A TXN15	C123	0.1U/6/Y/25V	EXP A TXN15C

EXP A RXP0_15] >>>EXP_A_RXP0_15] [10]
EXP A RXN0_15] >>>EXP_A_RXN0_15] [10]



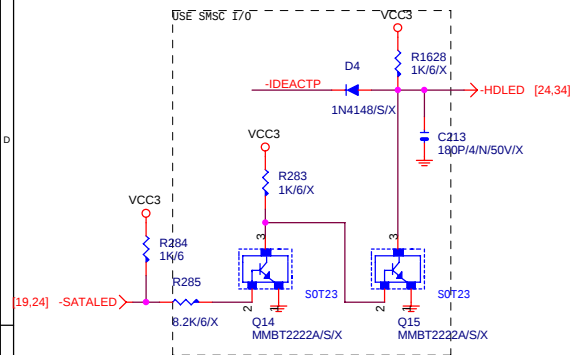


Gigabyte Technology

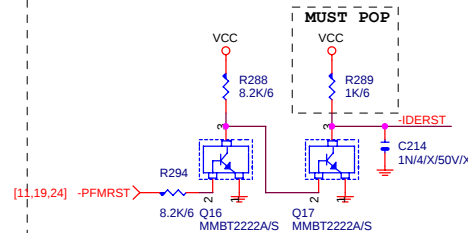
Title			
CK410M			
Size			
Custom			
Document Number			
8I945AEF-AE			
Date:			
Thursday, July 07, 2005			
Sheet			
21 of 34			
Rev			
1.1			

IDE/SATA LED

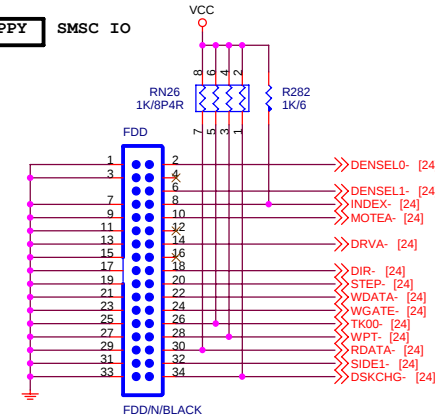
0321



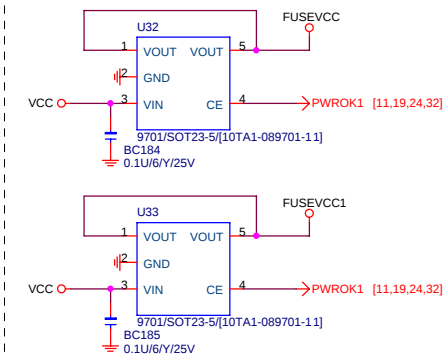
IDE RESET



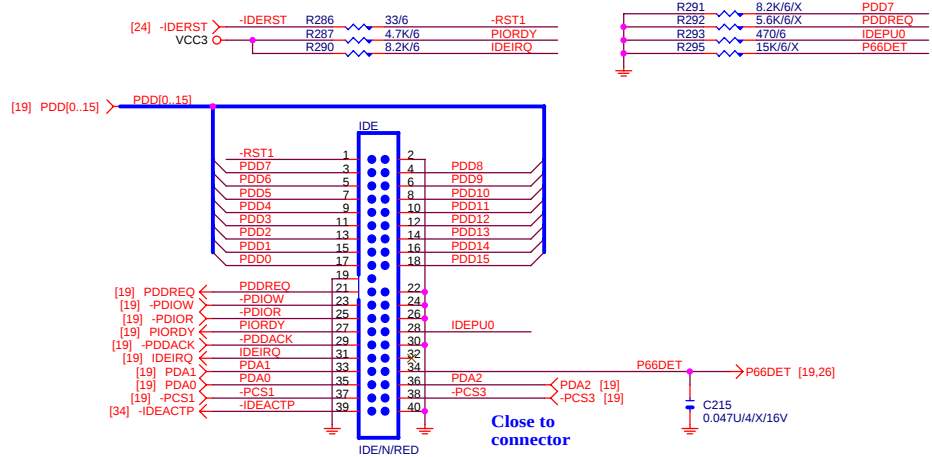
FLOPPY SMC IO



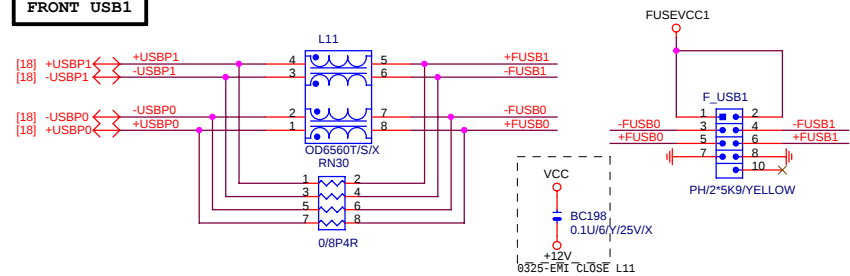
USB DROP



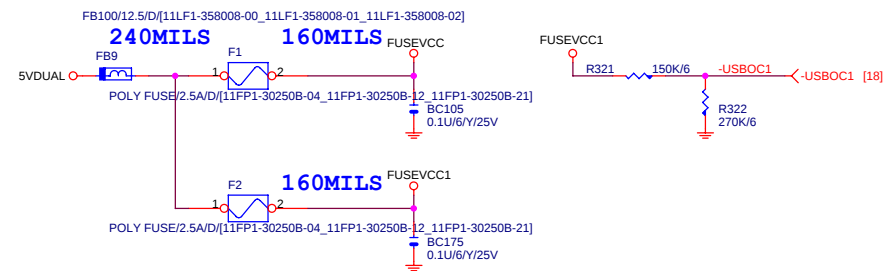
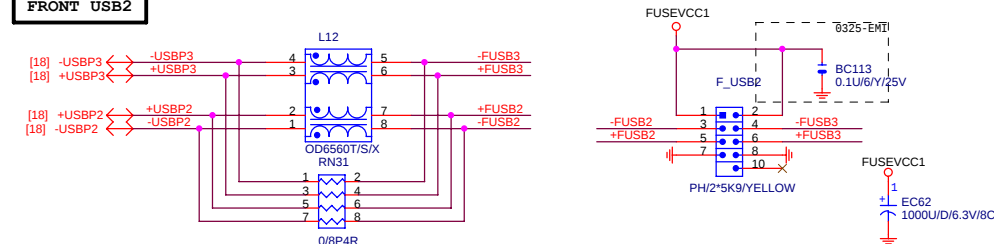
IDE



FRONT USB1



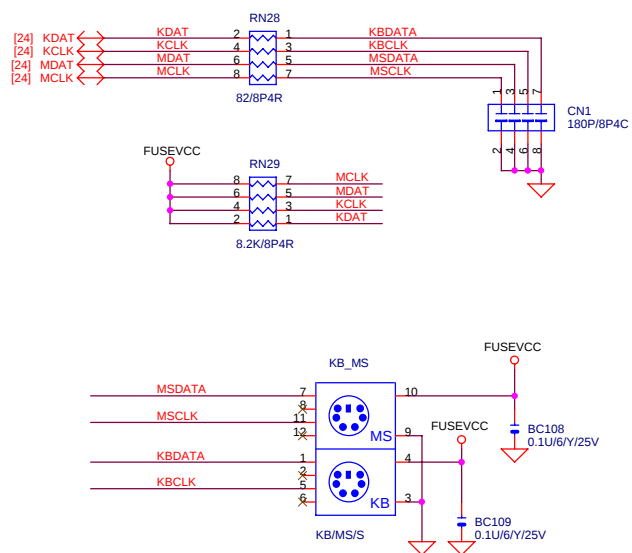
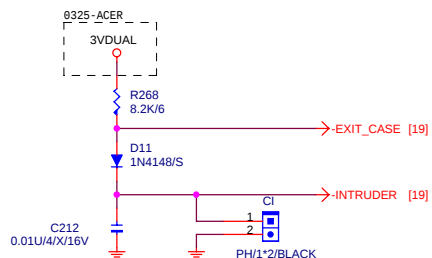
FRONT USB2



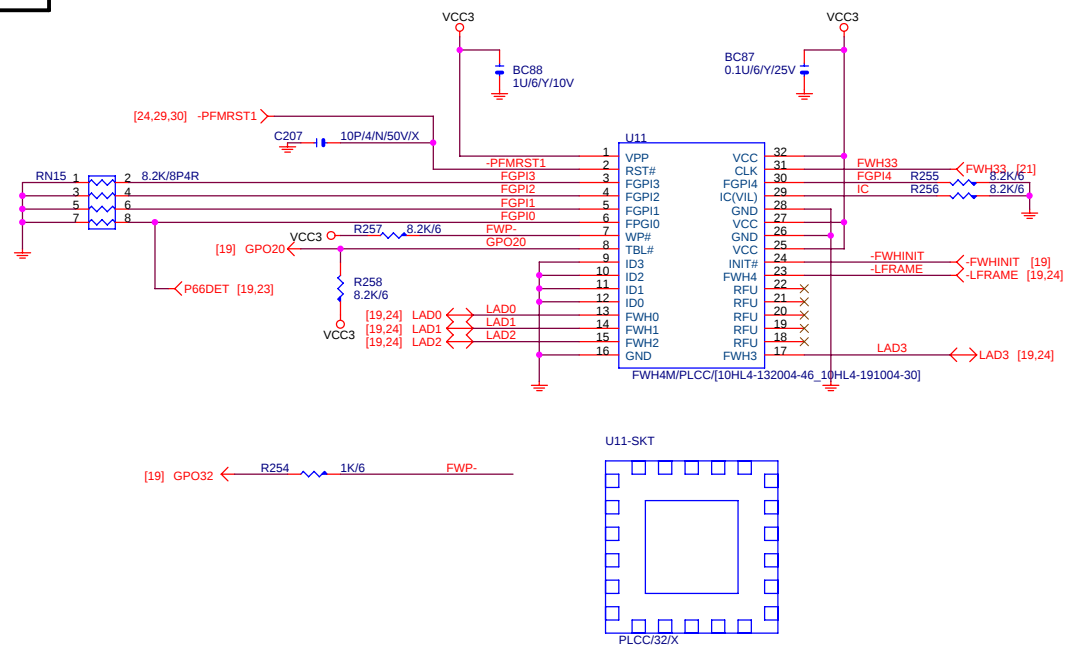
Gigabyte Technology

Title				
IDE				
Size B	Document Number			Rev 1.
	81945AEF-AE			
Date:	Thursday, July 07, 2005		Sheet	23 of 34


```
CASE OPEN=LOW;
CASE CLOSE=HI;
```



11



220/8/S/3A/[10LF2-302200-01_10LF2-302200-01_10LF2-302200-02_10LF2-302200-03]

[28] CEN ←

[28] LFE ←

[28] S_SURR_L ←

[28] S_SURR_R ←

[28] SPDIF ←

0.419V --> EMI

VCC3

CR7

CEC1

100U/D/10V/57/X

[19,28] -ACZ_DET >

MUTE_POP

[19] ACZ_SDOUT <

[19] ACZ_BITCLK <

CR14 22/6

[19] ACZ_SDIN2 <

CR15 22/6

[19] ACZ_SYNC <

[19] -ACZ_RST <

CBC4 22P/4/N/50V/X

CBC5 0.1U/6/V/25V

CBC6 0.1U/6/V/25V

[28] FRONT_ID > CR19 5.1K/6/1

[28] LINE1_ID > CR20 10K/6/1

[28] MIC1_ID > CR21 20K/6/1

[28] SURRE_ID > CR22 39.2K/6/1

[28] LINE2_L <

[28] LINE2_R <

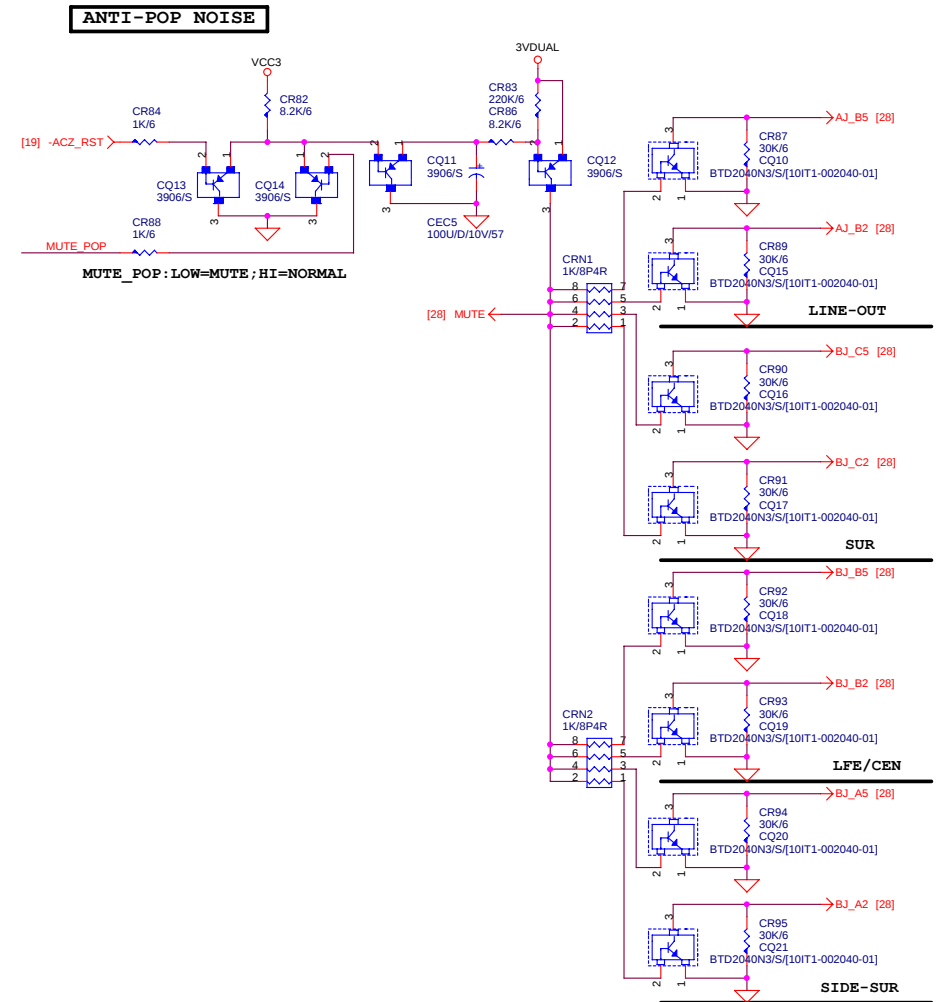
[28] MIC2_L <

[28] MIC2_R <

Can Support

CR101 0/6/X

0.325V EMI T10SE CBC



CD-IN

1 2 3 4

[27] CD_L ←

[27] CDGND ←

[27] CD_R ←

CR51 0/6

CR52 8.2K/6

CR53 8.2K/6

CD-IN1*4/BLACK

8321
NEW TYPE: IBM_AUDIO-13P-1

Orange
SURROUND

Black
CEN/LFE

Gray
SURROUND SIDE

Blue
LINE-IN

Green
LINE-OUT

Pink
MIC-IN

A3RJ/13P/OBG/[11NR6-403006-71]

A3RJ/13P/B/[11NR6-403006-01_11NR6-403006-02]

[27] LINE_O_R > CBC17 10U/8/Y/6.3V < AJ_B5 [27]

[27] LINE_O_L > CBC19 10U/8/Y/6.3V < AJ_B2 [27]

CBC21 180P/4/N/50V

CBC22 180P/4/N/50V

[27] S_SURR_L ← CBC39 2.2U/6/X5R/6.3V → BJA2 [27]

CBC40 180P/4/N/50V

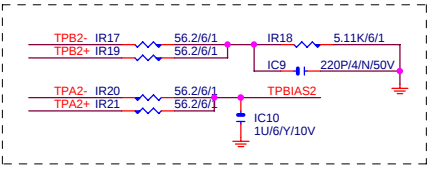
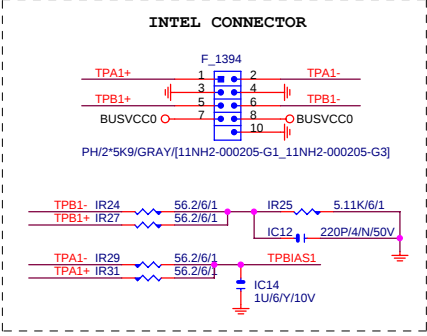
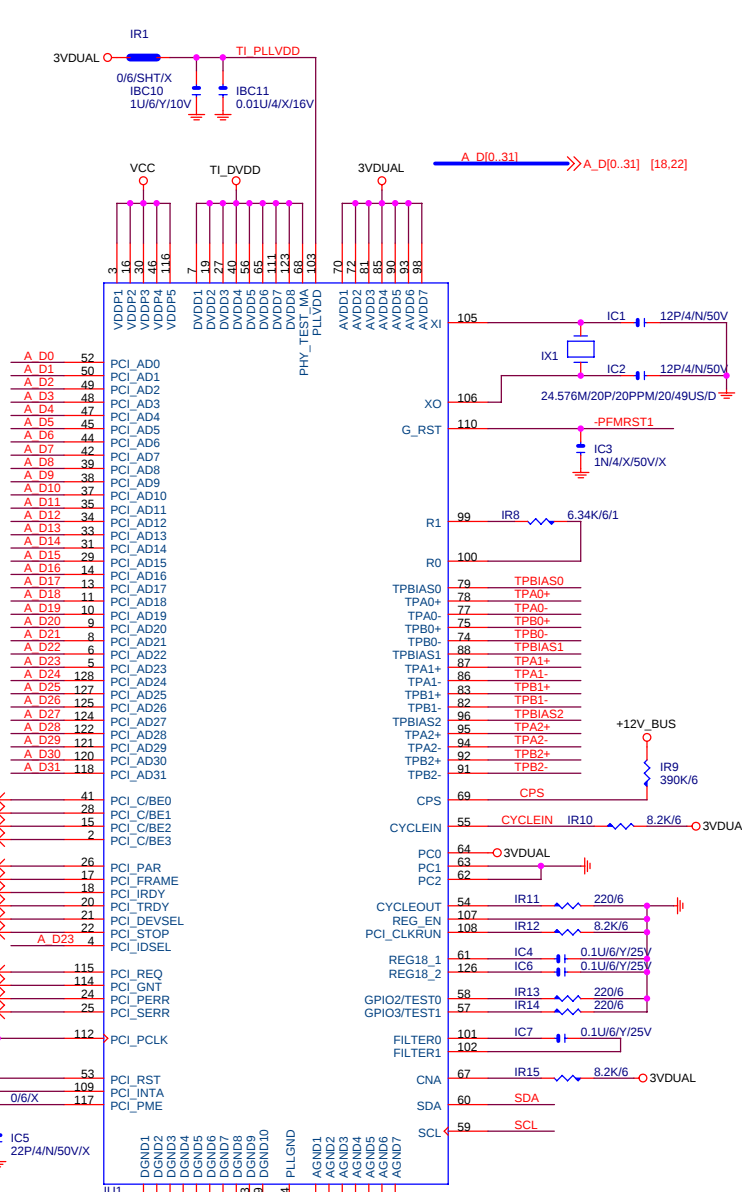
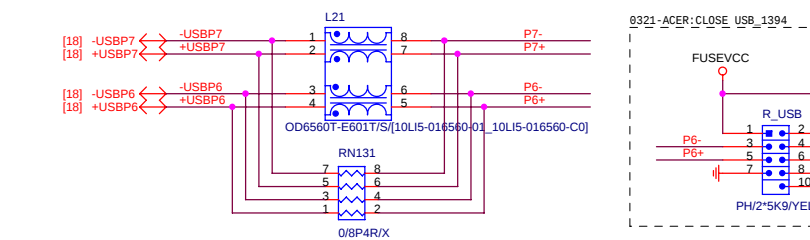
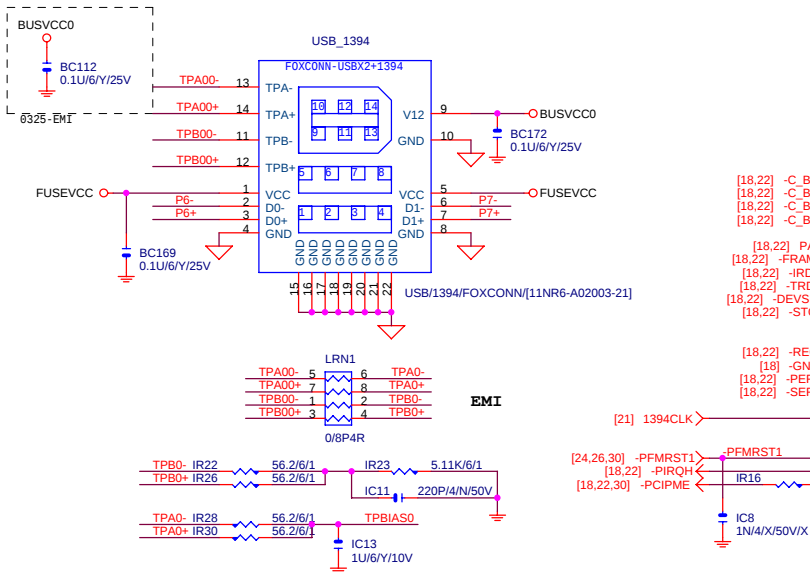
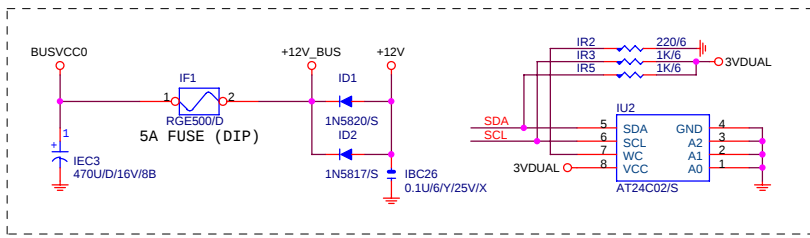
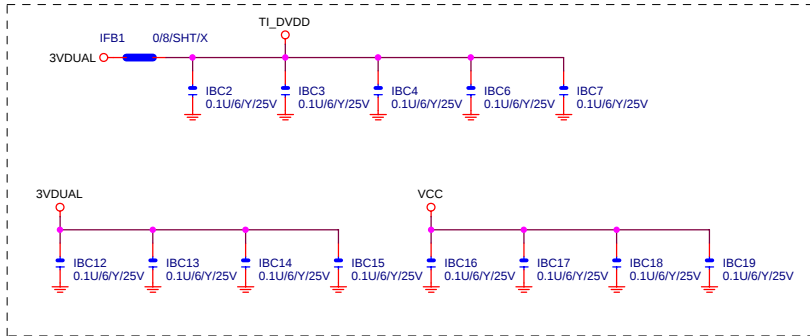
CBC40 180P/4/N/50V

Gigabyte Technology

AUDIO JACK

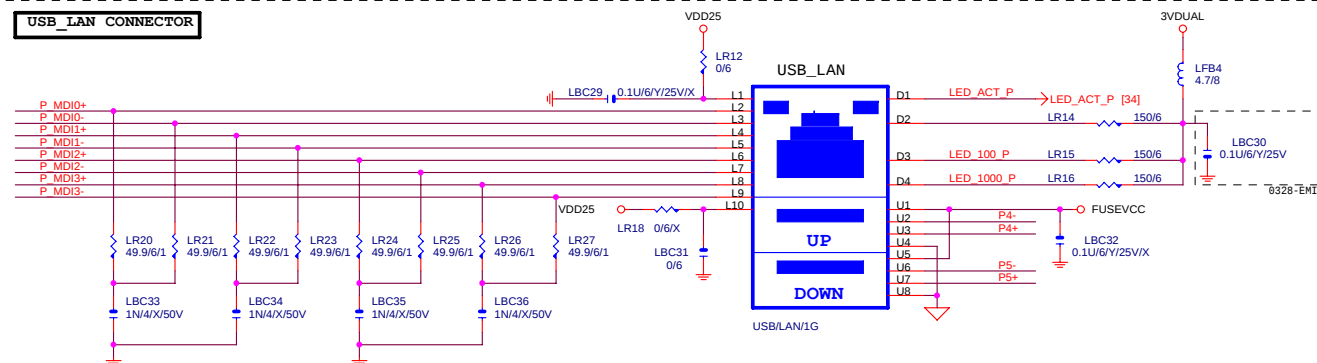
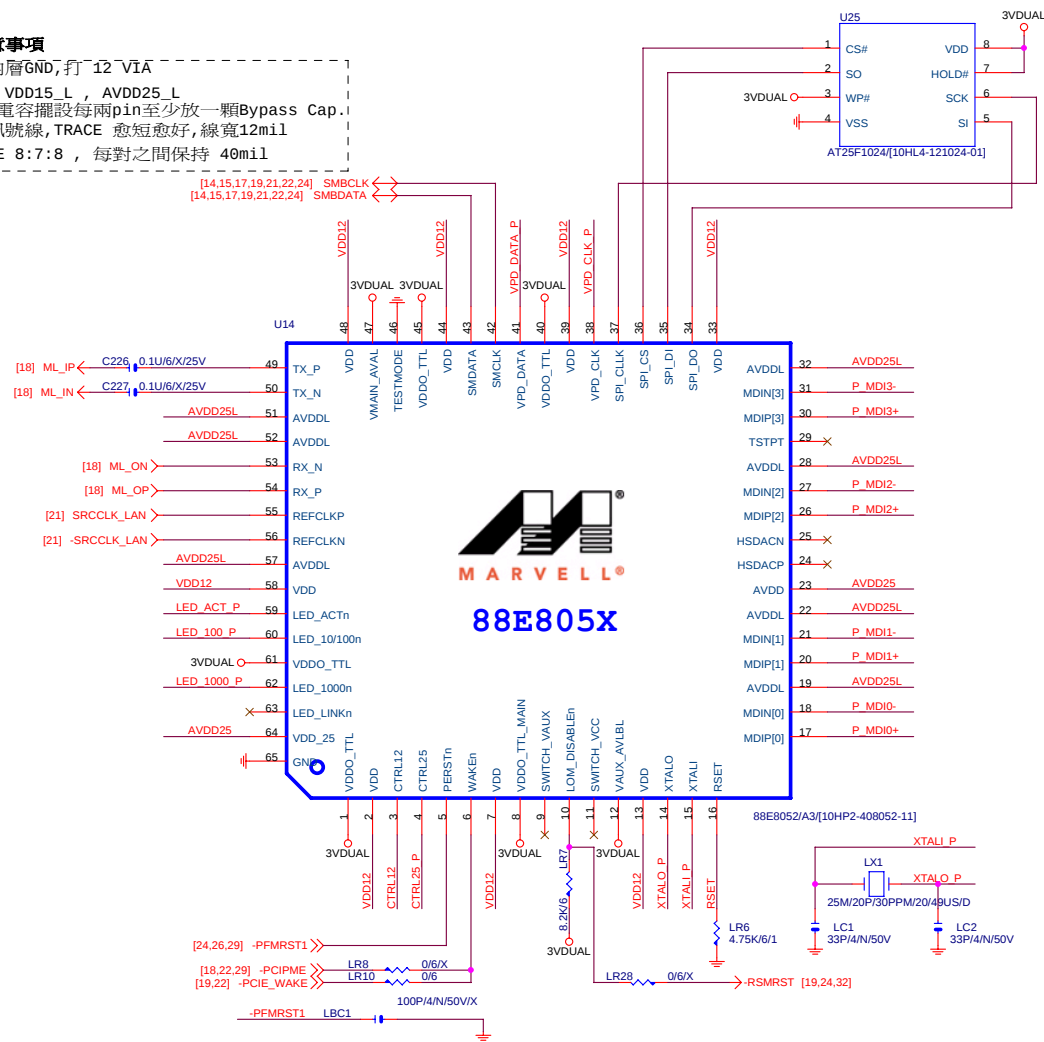
81945AEF-AE

Size	Document Number	Rev
Custom	81945AEF-AE	1.1
Date:	Thursday, July 07, 2005	Sheet 28 of 34

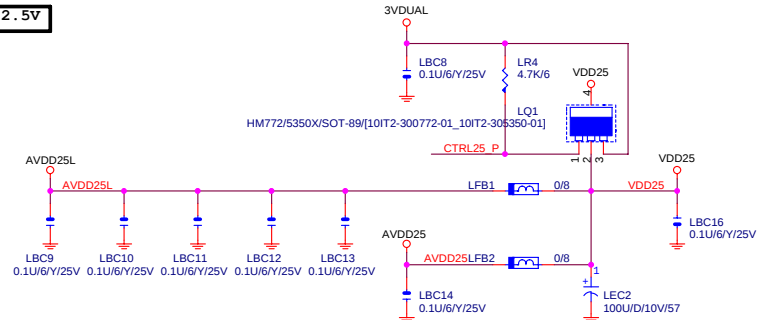
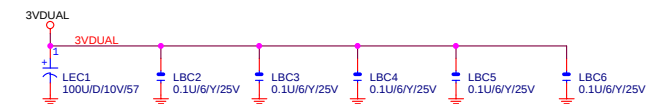


Layout Check 注意事項

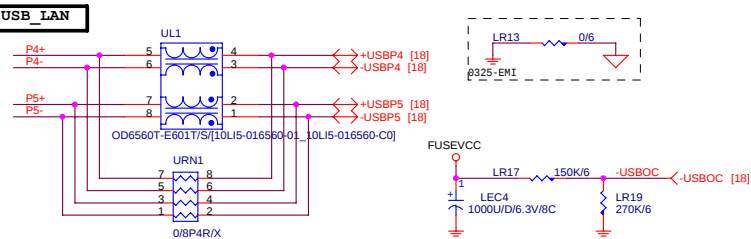
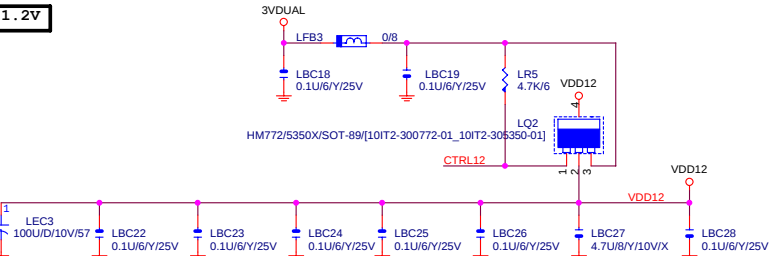
1. LUI PIN65 需下內層GND,打 12 VIA
2. 3VDUAL , VCC3 , VDD15 L , AVDD25 L
至少走20mil寬,並且電容擺設每pin至少放一顆Bypass Cap.
3. X'TAL 25MHZ 兩訊號線,TRACE 愈短愈好,線寬12mil
4. MDI正負0-3, TRACE 8:7:8 , 每對之間保持 40mil



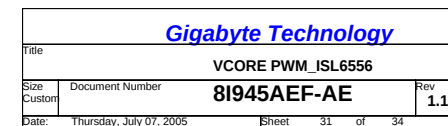
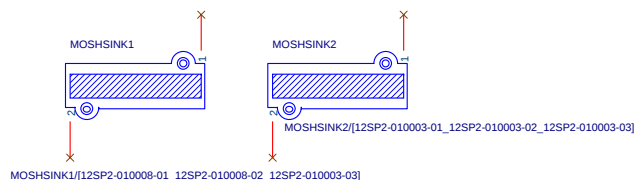
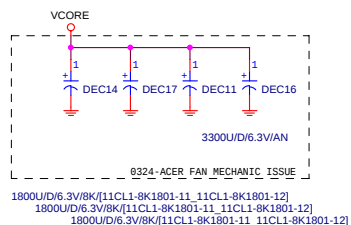
2.5V



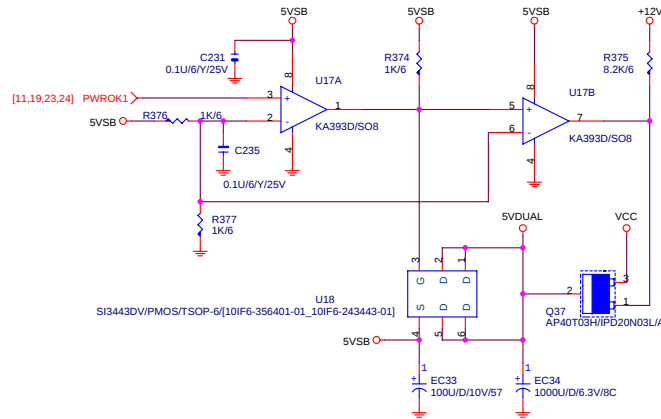
USB_LAN



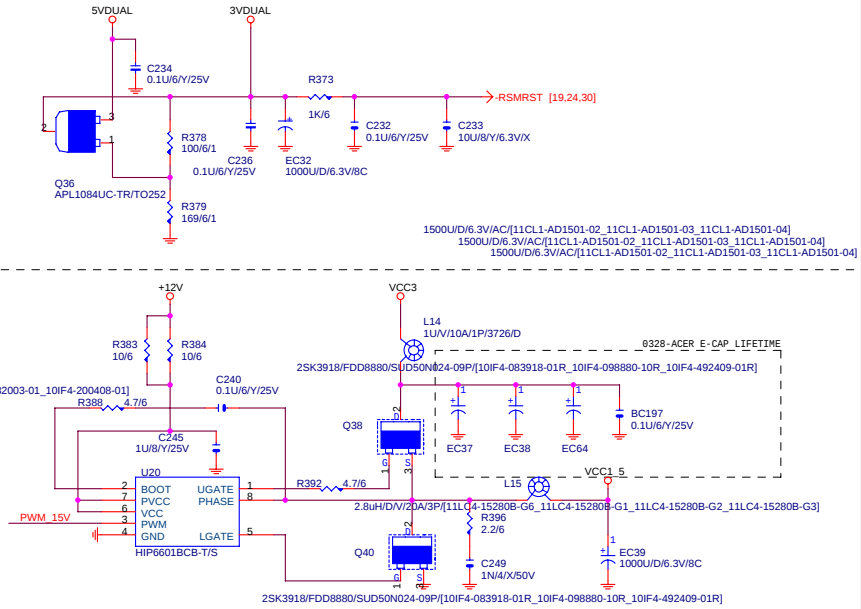
Gigabyte Technology			
Title			
MARVELL 88E8052			
Size	Document Number		Rev
Custom	81945AEF-AE		1.1
Date:	Thursday, July 07, 2005	Sheet	30 of 34



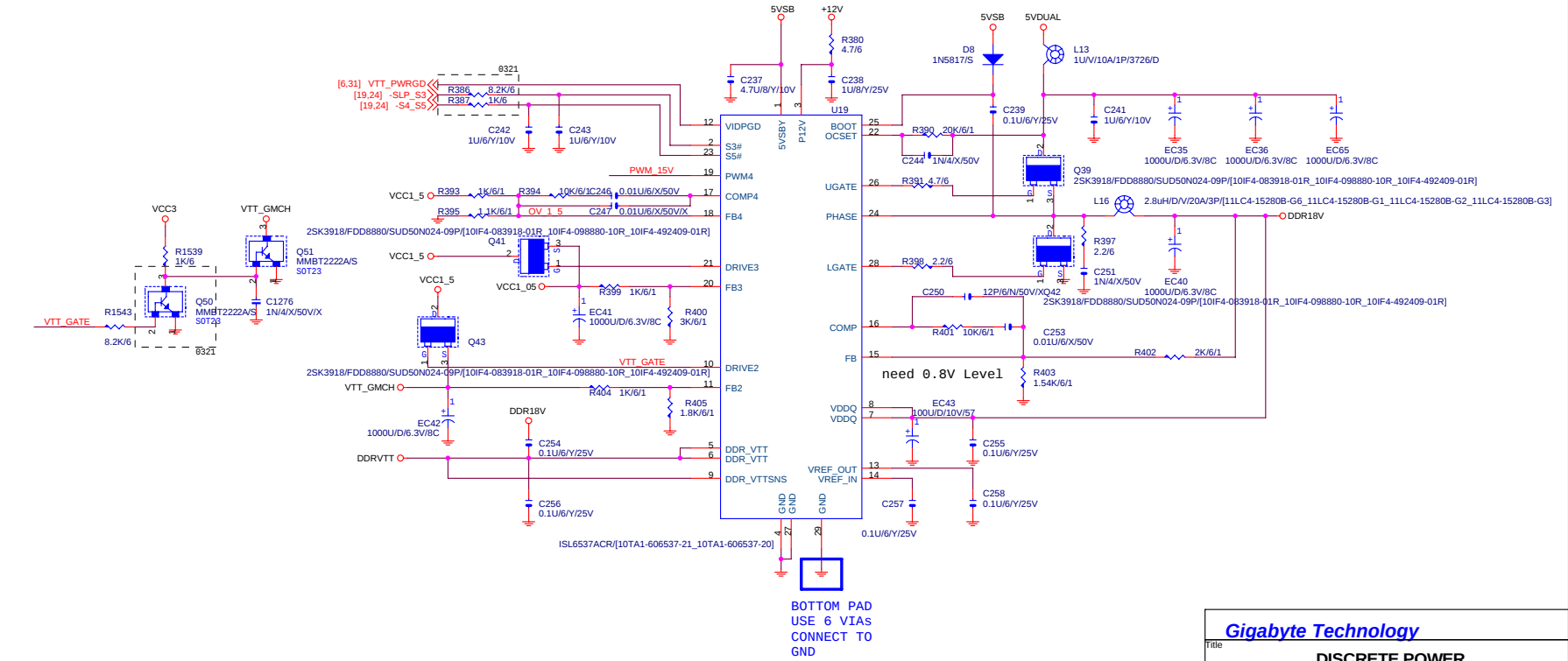
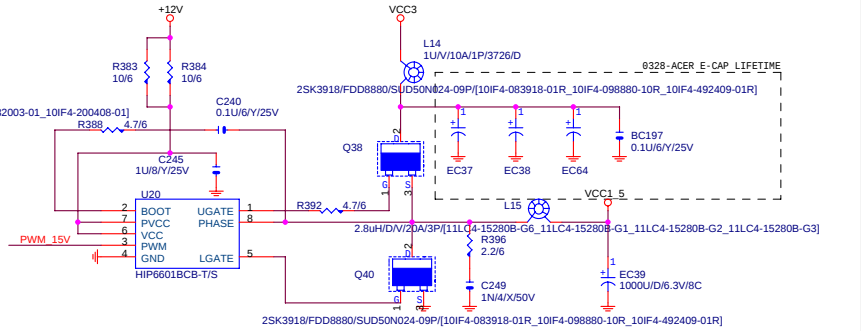
5VDUAL



3VDUAL



VCC1_5

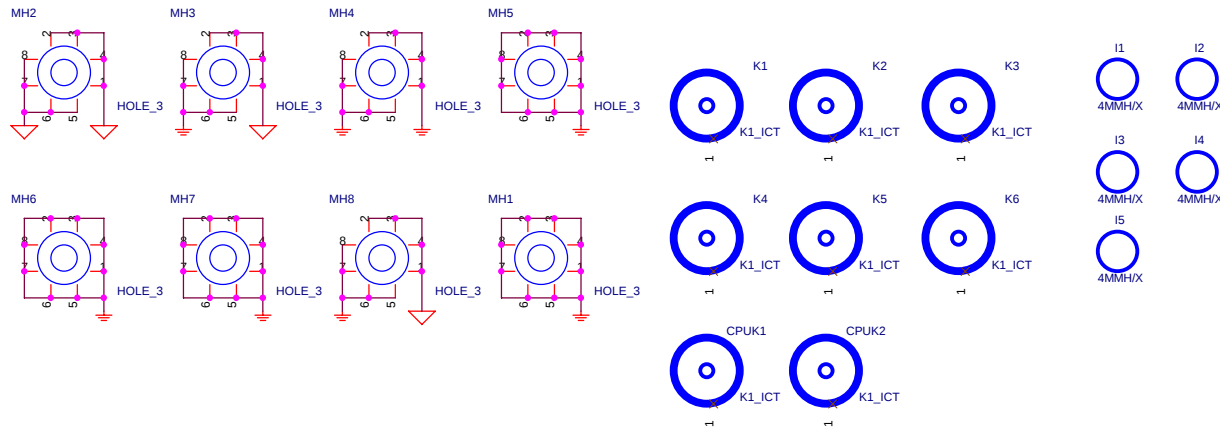
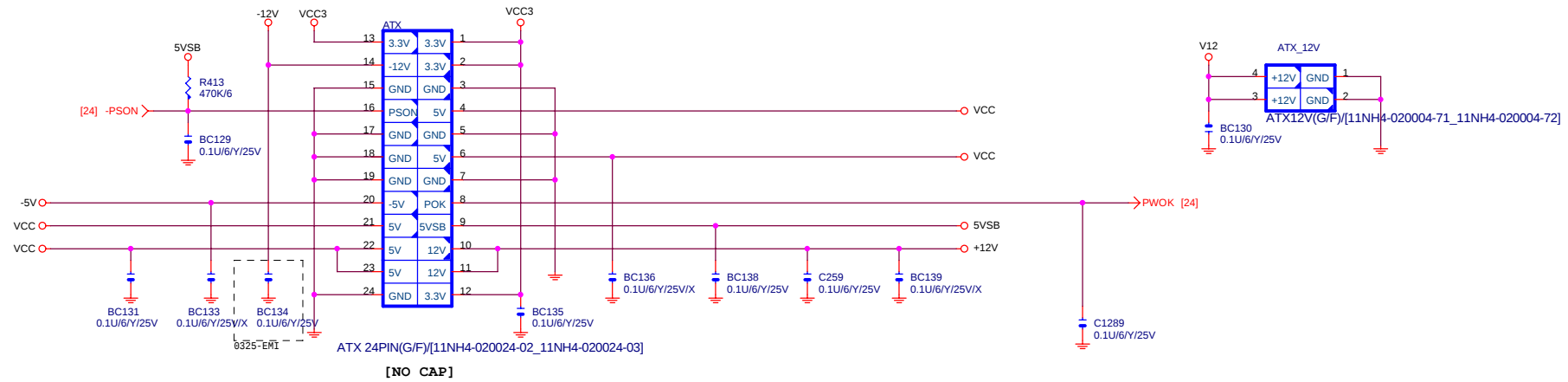


BOTTOM PAD
USE 6 VIAS
CONNECT TO
GND

Gigabyte Technology

Title			DISCRETE POWER
Size	Document Number	8I945AEF-AE	
Custom		Rev	1.1
Date:	Thursday, July 07, 2005	Sheet	32 of 34

ATX POWER CONNECTOR

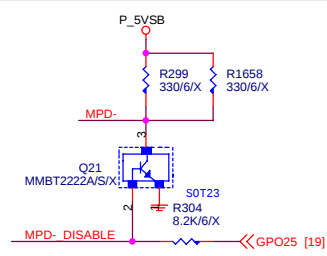
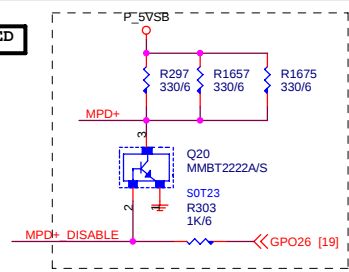


Gigabyte Technology

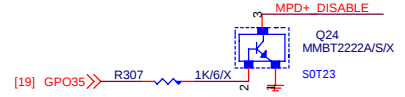
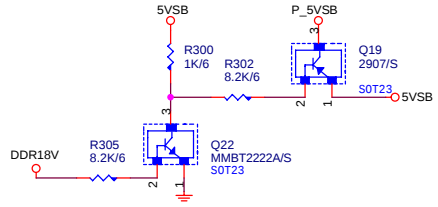
Title			
ATX POWER CONNECTOR			
Size	Document Number	8I945AEF-AE	
B			Rev 1.1
Date:	Thursday, July 07, 2005	Sheet	33 of 34
	2	1	

DUAL COLOR LED

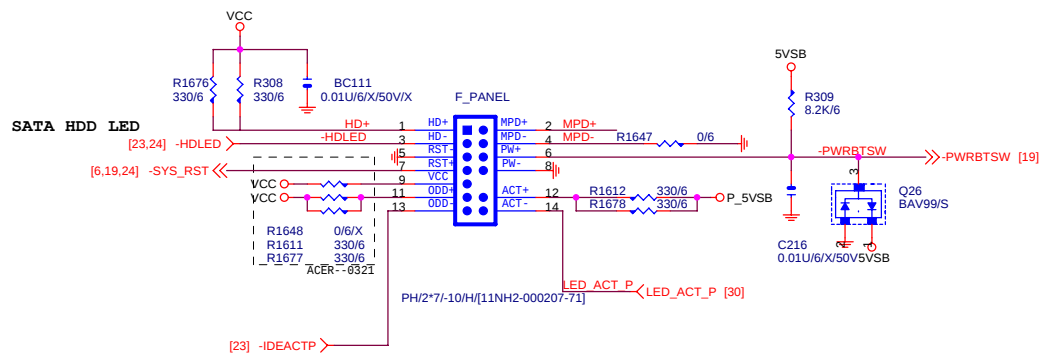
0321



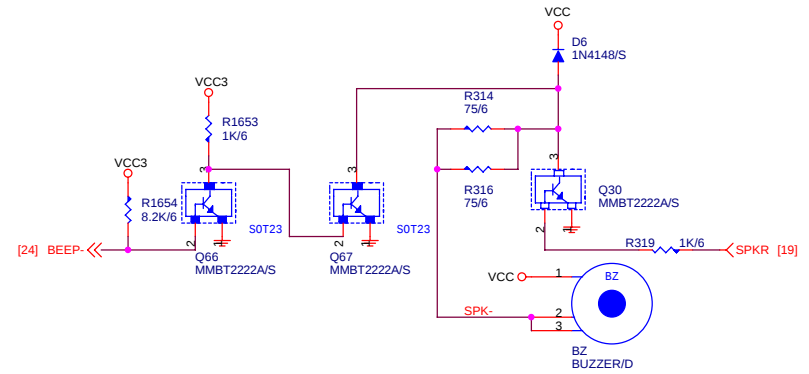
PREVENT S0 W/O CPU, GREEN IS ON.



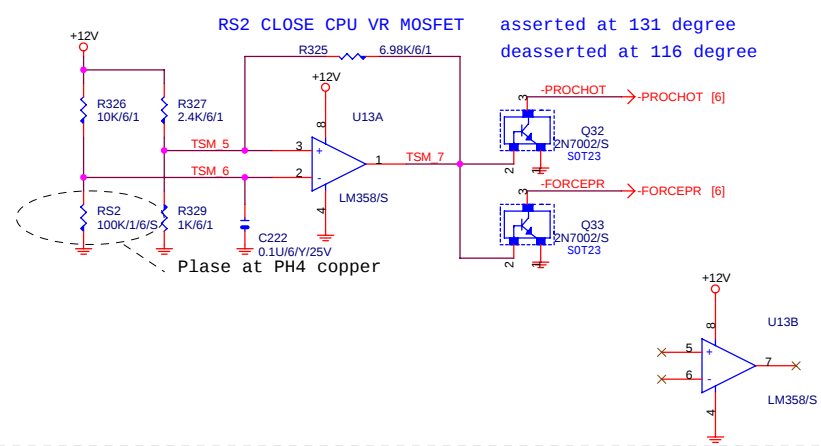
INTEL FRONT PANEL modify PIN2*7 cut 10pin for ACER



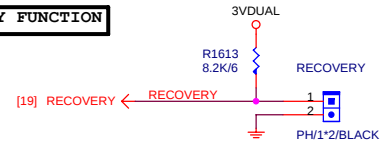
BUZZER



PROCESSOR HOT



RECOVERY FUNCTION



RECOVERY	
OPEN	NORMAL
SHORT	RECOVERY

RESERVE PIN

