

Model Name: 8I945G-PRO Revision 1.0

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27	KB_PS2,S.P.R

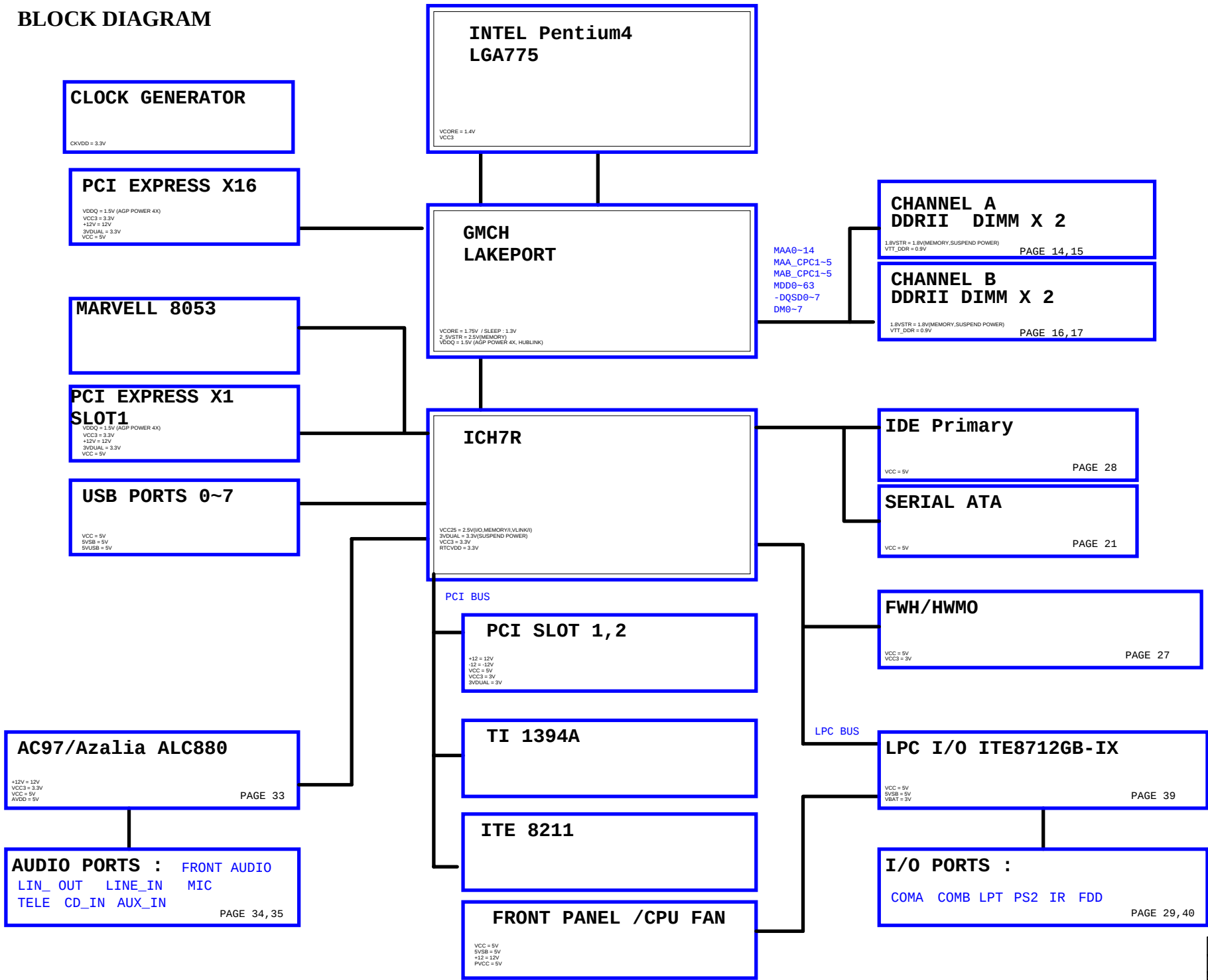
SHEET TITLE

28	FRONT PANEL
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39	VCORE PWM
40	DISCRETE POWER
41	ITE8212 RAID
42	ITE8212 RAID
43	GPIO DEFINE
44	GPIO DEFINE

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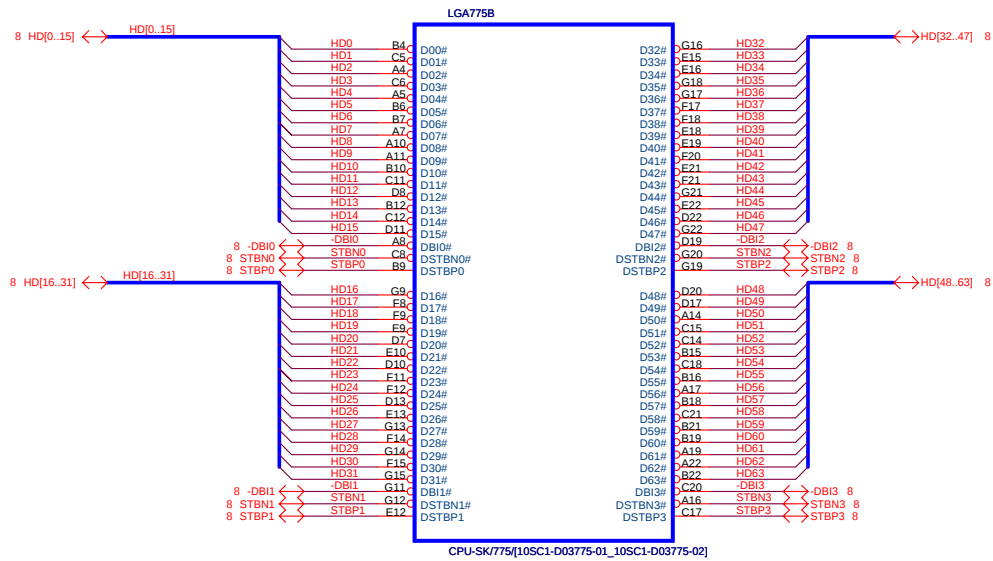
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Size	Document Number	8I945G-PRO	Rev
Custom			1.0
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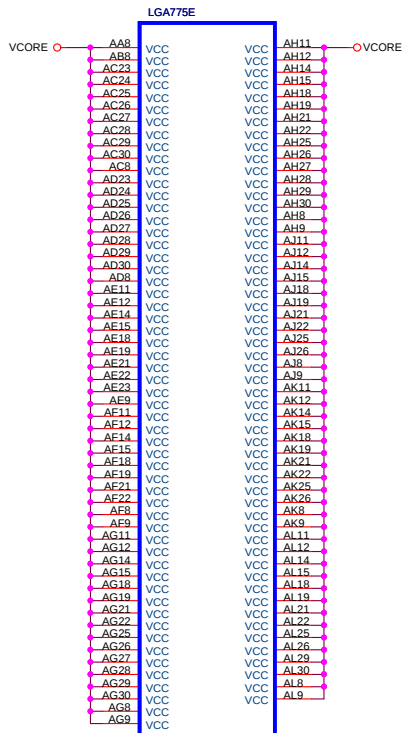
BLOCK DIAGRAM



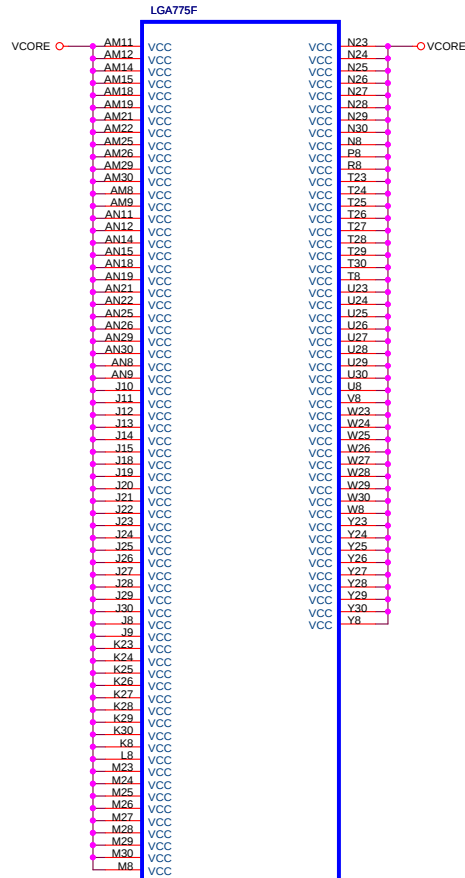
Version: 1.0

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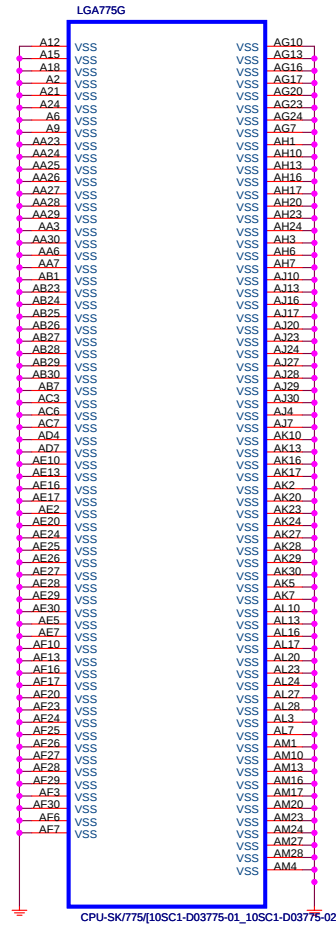


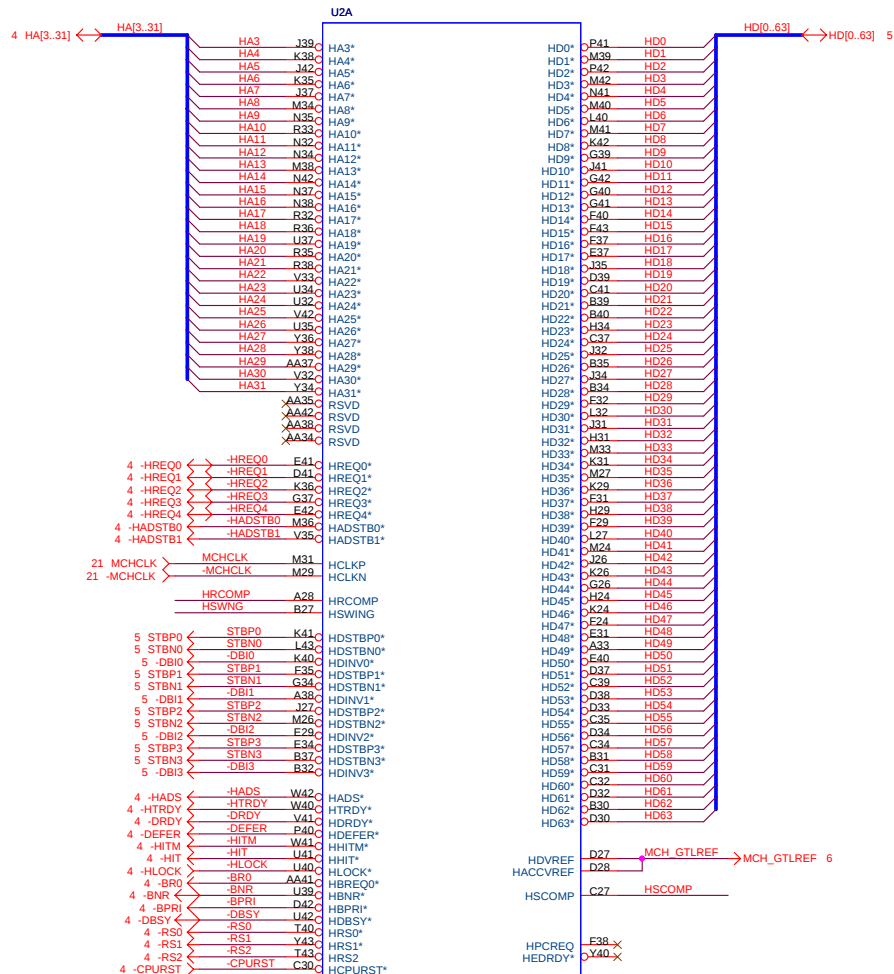


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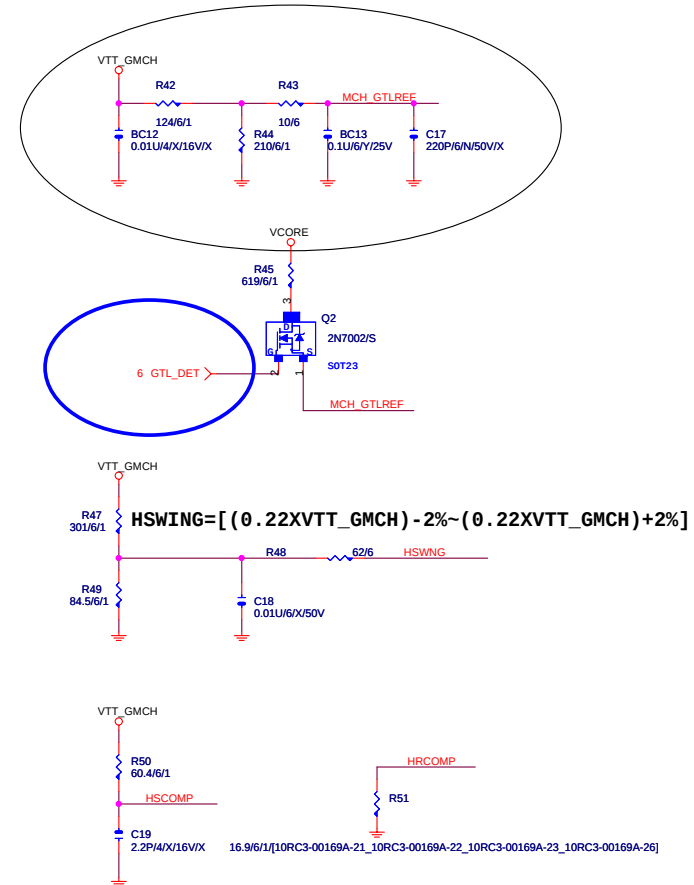
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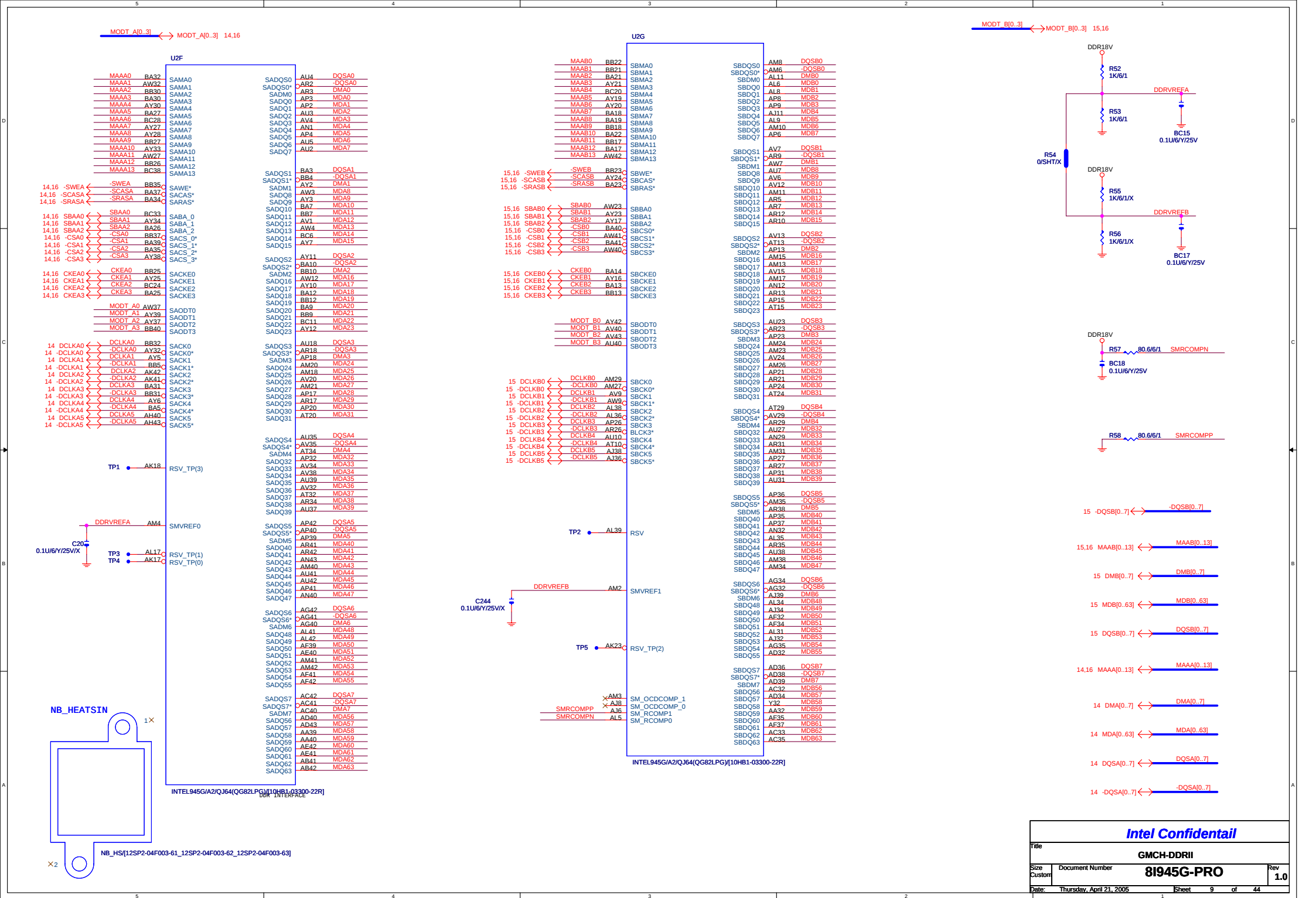


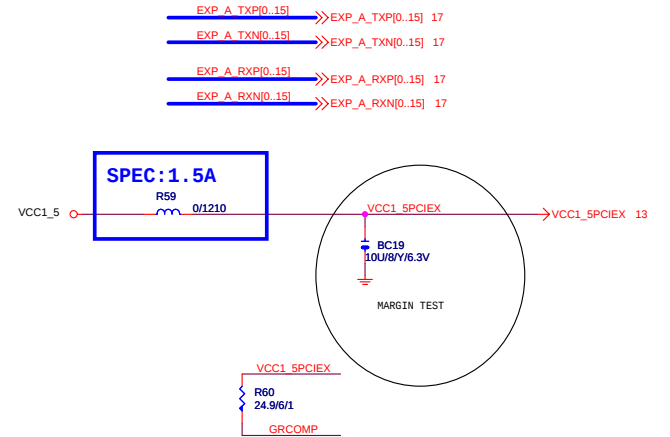
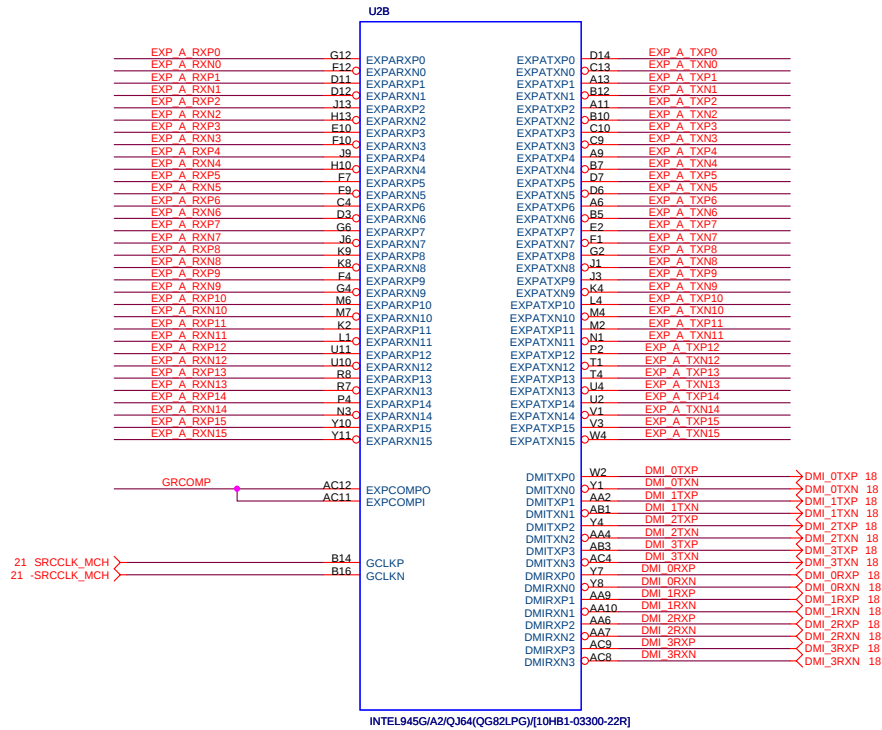


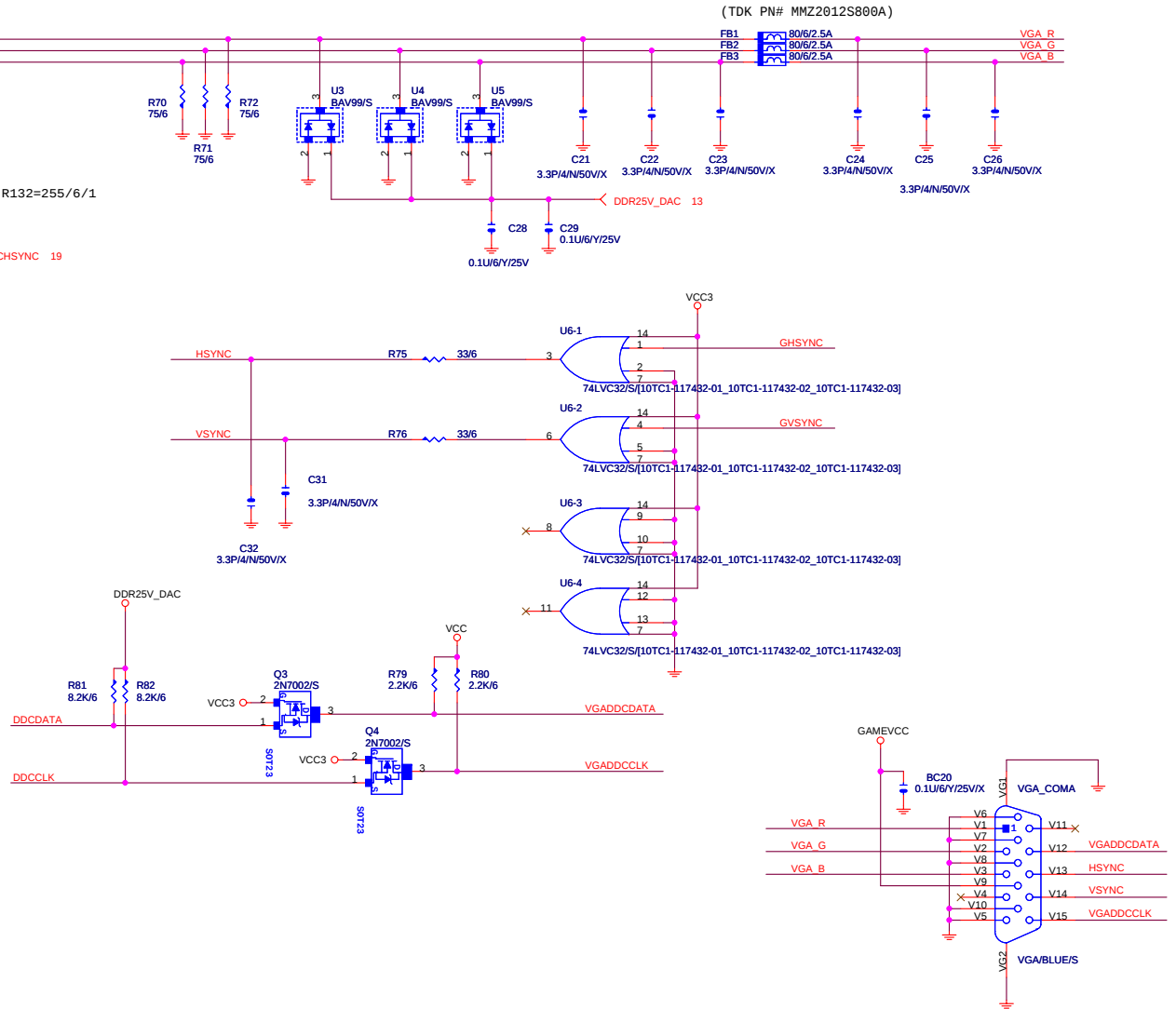
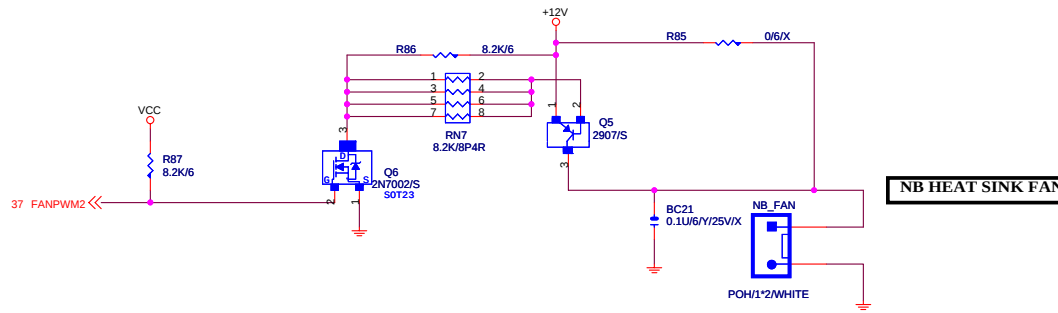
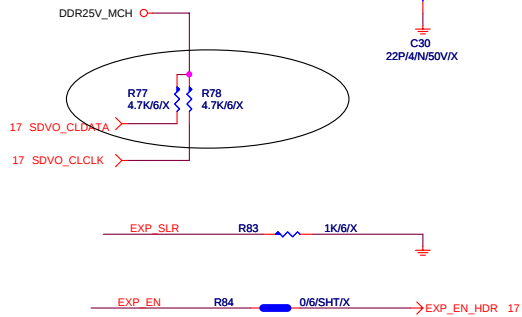
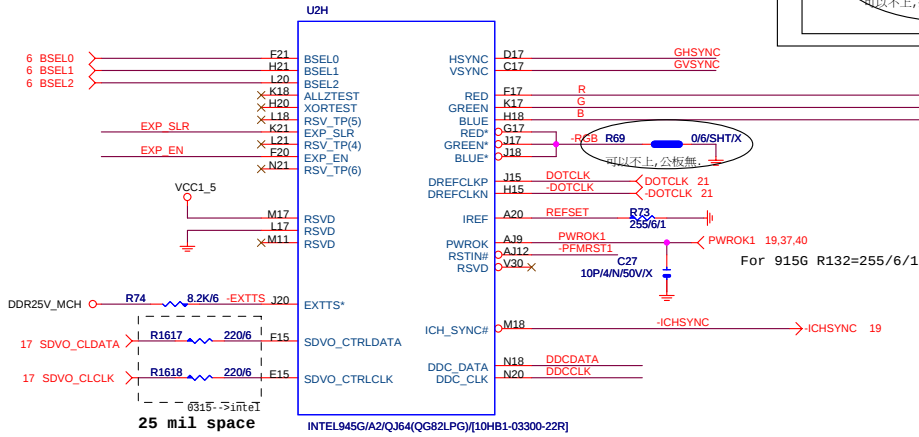
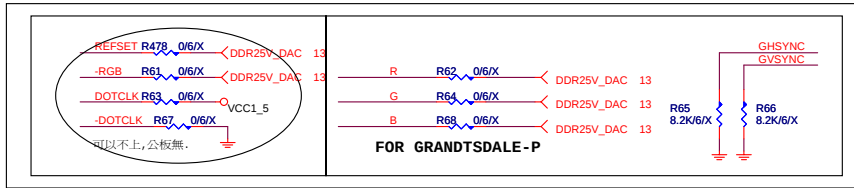
INTEL945G/A2/Q364(QG82LPG)/10H B10HB1-033000-11-REV:A1

10HB1-033000-21-REV:A2 FOR ACER









U2C

A4	VSS	N2
A16	VSS	N6
A22	VSS	N8
A26	VSS	N13
A31	VSS	N15
A35	VSS	N24
B4	VSS	N26
B6	VSS	N27
B9	VSS	N29
B11	VSS	N31
B13	VSS	N33
B21	VSS	N36
B22	VSS	N39
B28	VSS	P30
B33	VSS	P3
B38	VSS	P14
C3	VSS	P15
C5	VSS	P24
C7	VSS	P26
C12	VSS	P27
C14	VSS	P29
C22	VSS	P30
C40	VSS	R6
D2	VSS	R9
D5	VSS	R12
D10	VSS	R14
D16	VSS	R30
D20	VSS	R31
D21	VSS	R34
E3	VSS	R37
E4	VSS	R39
E7	VSS	T2
E9	VSS	T42
E12	VSS	U3
E13	VSS	U5
E17	VSS	U9
E18	VSS	U12
E20	VSS	U14
E21	VSS	U31
E32	VSS	U33
F2	VSS	U36
F6	VSS	U38
F13	VSS	V2
F18	VSS	V8
F26	VSS	V11
F34	VSS	V12
F42	VSS	V14
G3	VSS	V34
G5	VSS	V36
G7	VSS	V37
G9	VSS	V38
G10	VSS	V39
G13	VSS	V43
G15	VSS	W3
G18	VSS	Y2
G20	VSS	Y5
G21	VSS	Y6
G24	VSS	Y9
G27	VSS	Y12
G29	VSS	Y14
G31	VSS	Y31
G32	VSS	Y35
G35	VSS	Y37
G38	VSS	Y39
H12	VSS	Y42
H17	VSS	AA3
H26	VSS	AA6
H27	VSS	AA11
J2	VSS	AA12
J5	VSS	AA14
J7	VSS	AA21
J10	VSS	AA23
J12	VSS	AA31
J21	VSS	AA33
J24	VSS	AA36
J29	VSS	AB2
J38	VSS	AB43
J43	VSS	AC2
K3	VSS	AC3
K5	VSS	AC7
K6	VSS	AC10
K7	VSS	AC14
K10	VSS	AC21
K12	VSS	AR6
K13	VSS	AR15
K15	VSS	AR20
K20	VSS	AR24
K27	VSS	AR32
K32	VSS	AR37
K34	VSS	AR39
K37	VSS	AR43
K39	VSS	AT12
L2	VSS	AT17
L12	VSS	AT18
L13	VSS	AT21
L24	VSS	AT23
L26	VSS	AT26
L29	VSS	AT27
L31	VSS	AT31
L42	VSS	AU6
M3	VSS	AU8
M5	VSS	AU12
M8	VSS	AU13
M9	VSS	AU15
M10	VSS	AU17
M13	VSS	AU20
M20	VSS	AD18
M21	VSS	AD20
M35	VSS	AD22
M37	VSS	AD24

INTEL945G/A2/Q364(QG82LPG)[10HB1-03300-22R]

U2E

AD7	VSS	AU21
AD9	VSS	AU24
AD11	VSS	AU26
AD13	VSS	AU29
AD33	VSS	AU32
AD35	VSS	AU34
AD37	VSS	AV2
AD42	VSS	AV10
AF1	VSS	AV17
AF2	VSS	AV27
AF3	VSS	AW10
AF5	VSS	BA4
AF33	VSS	BA42
AF36	VSS	BB3
AF38	VSS	BB6
AF43	VSS	BB11
AG30	VSS	BB14
AG31	VSS	BB19
AG33	VSS	BB34
AG36	VSS	BB39
AG37	VSS	BB41
AG38	VSS	BC9
AG39	VSS	A40
AH42	VSS	D1
AJ7	VSS	D43
AJ10	VSS	R26
AJ30	VSS	R29
AJ31	VSS	U29
AJ33	VSS	V24
AJ35	VSS	V26
AJ37	VSS	V29
AK24	VSS	W21
AK26	VSS	W23
AK29	VSS	W25
AK30	VSS	Y20
AL1	VSS	Y22
AL2	VSS	Y24
AL3	VSS	Y26
AL7	VSS	Y29
AL10	VSS	AA25
AL12	VSS	AA27
AL13	VSS	AA29
AL15	VSS	AC19
AL18	VSS	AC25
AL21	VSS	AC29
AL23	VSS	
AL24	VSS	
AL27	VSS	
AL32	VSS	NC A42
AL33	VSS	NC B2
AL37	VSS	NC B3
AL43	VSS	NC B41
AM5	VSS	NC B42
AM7	VSS	NC B43
AM9	VSS	NC C2
AM33	VSS	NC C42
AM36	VSS	NC E35
AM37	VSS	NC AV26
AM39	VSS	NC AV27
AN2	VSS	NC AW2
AN4	VSS	NC AW29
AN13	VSS	NC B42
AN15	VSS	NC BB1
AN17	VSS	NC BB2
AN18	VSS	NC BB43
AN20	VSS	NC BC1
AN21	VSS	NC BC2
AN23	VSS	NC BC43
AN24	VSS	NC BC45
AN26	VSS	
AN27	VSS	
AN31	VSS	
AN42	VSS	
AP5	VSS	
AP7	VSS	RSVD AK21
AP10	VSS	RSVD AJ23
AP12	VSS	RSVD AJ26
AP29	VSS	RSVD AL29
AP34	VSS	RSVD AL20
AP38	VSS	RSVD AJ21
AR1	VSS	RSVD AL26
AR15	VSS	RSVD AK23
AR20	VSS	RSVD AD36
AR24	VSS	RSVD AC34
AR32	VSS	RSVD Y30
AR37	VSS	RSVD Y33
AR39	VSS	RSVD AF31
AR43	VSS	RSVD AD31
AT12	VSS	RSVD U30
AT17	VSS	RSVD V31
AT18	VSS	RSVD AA36
AT21	VSS	RSVD AC36
AT23	VSS	RSVD AA43
AT26	VSS	RSVD AG28
AT27	VSS	RSVD AG28
AT31	VSS	RSVD AG27
AU6	VSS	RSVD AJ24
AU8	VSS	RSVD AJ27
AU12	VSS	RSVD AK46
AU13	VSS	RSVD AW17
AU15	VSS	RSVD AW18
AU17	VSS	RSVD AT14
AU20	VSS	RSVD BC16
AD18	VSS	RSVD AJ29
AD20	VSS	RSVD AG29
AD22	VSS	
AD24	VSS	
AD27	VSS	
AD29	VSS	
AE19	VSS	
AE21	VSS	
AE23	VSS	
AE25	VSS	
AE18	VSS	
AE20	VSS	
AE22	VSS	
AE24	VSS	
AV1	VSS	
BC4	VSS	

INTEL945G/A2/Q364(QG82LPG)[10HB1-03300-22R]

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GMCH-GND			
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1.425~1.575V

VCC1_5
N17
P17
P18
P20
P21
AA22
AB21
AB22
AB23
AC22
AD14
AE7
AF6
AF8
AF9
AF10
AF11
AF12
AF13
AF14
AF30
AG2
AG3
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AG5
AG6
AG7
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AH1
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AC26
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AD19
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AD23
AD25
AD26
AE17
AE18
AE20
AE22
AE24
AE26
AE27
AE15
AE17
AE19
AE21
AE23
AE25
AE26
AE27
AE29

INTEL945G/A2/Q364/Q382/LPG/[10H81-03300-22R]

U2D

VCCSM
BB16
AW15
BB42
BC13
BC18
VCCSM
BC22
BC26
BB20
VCCSM
AW24
BC35
BC31
BB38
BB32
BB28
VCCSM
BB24
AW31
VCCSM
AW34
VCCSM
AV41
VCCSM
AV42
VCCSM
AV23
VCCSM
AV18
VCCSM
BC40
VCCSM
AW35
VCCSM
AV43
VCCSM
AW20
VCCSM
AV21
VCCSM
AV31
VCCSM
AW13
VCCSM
AW21

1.14~1.26V
VTT_GMCH
C23
G23
P23
E23
D23
D24
D25
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E26
C25
C26

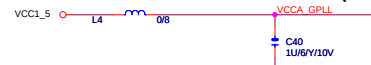
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VCCA_MPLL
VCCA_HPLL
VCCA_DPLLA
VCCA_DAC
VCCA_DAC
VCCA_DAC
VCCA_EXPPLL
VSSA_DAC
B19
B20
C21
C19
B18
C18
D18
B17
A18

VCC1_SPOEX 10
1.425~1.575V

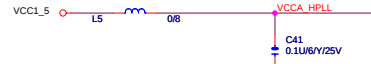
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A119
A120

1.7~1.9V
DDR18V

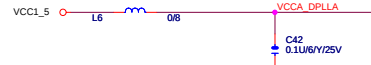
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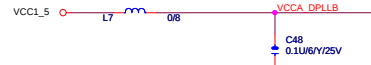
VCCA_HPLL>50mA 公板爲200mA(1.425V~1.575V)



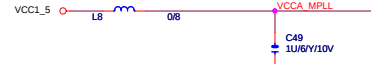
VCCA_DPLLA=65mA(1.425V~1.575V)



VCCA_DPLLB=65mA(1.425V~1.575V)



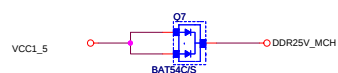
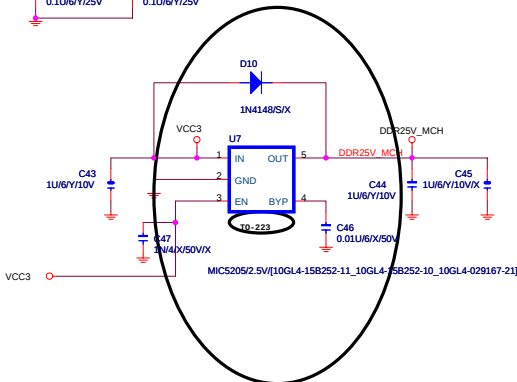
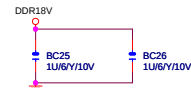
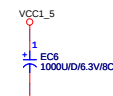
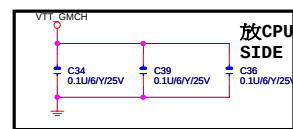
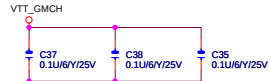
VCCA_MPLL>50mA(1.425V~1.575V)



VCCA_DAC=DDR25V_DAC=70mA(2.375~2.625V)

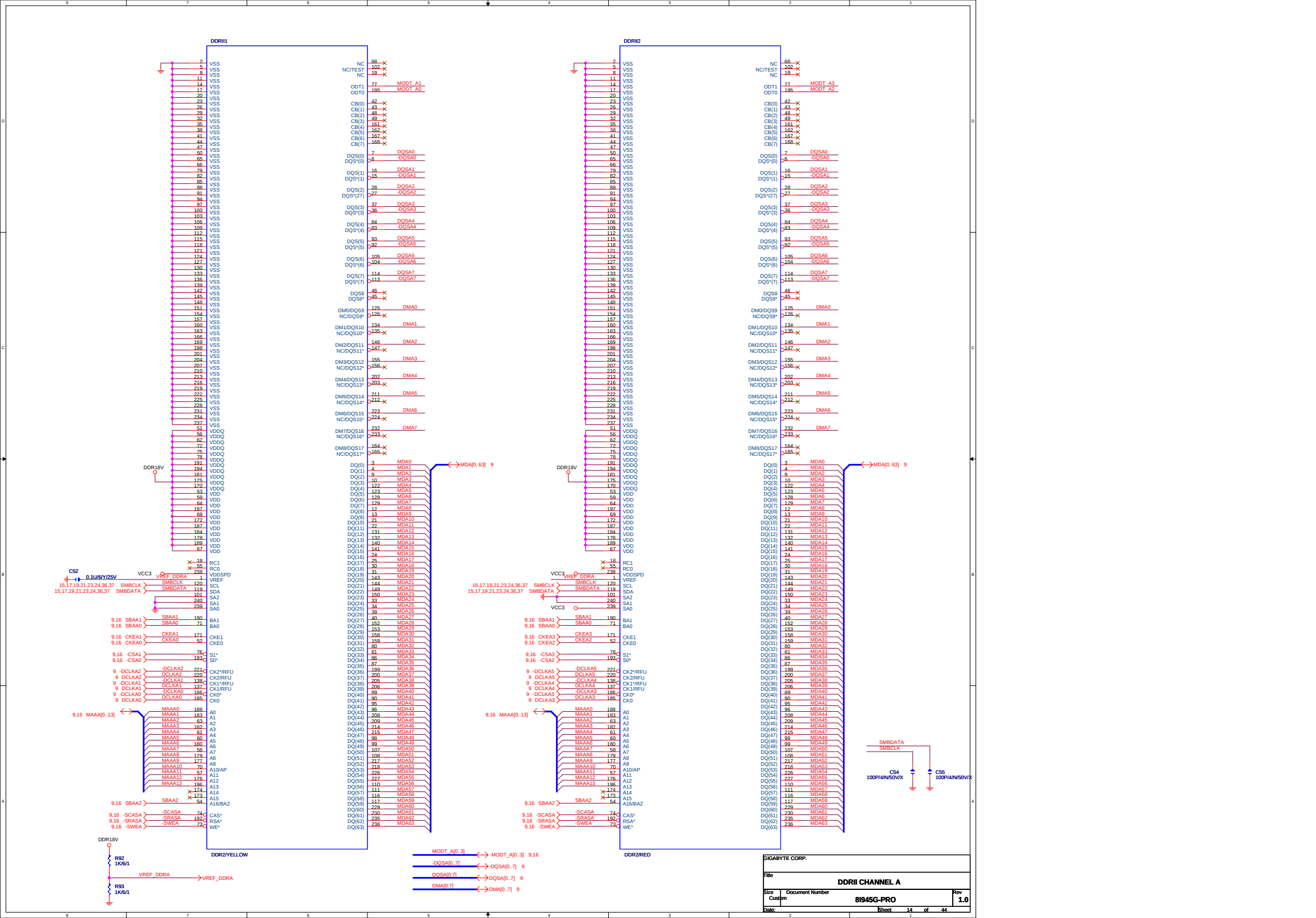


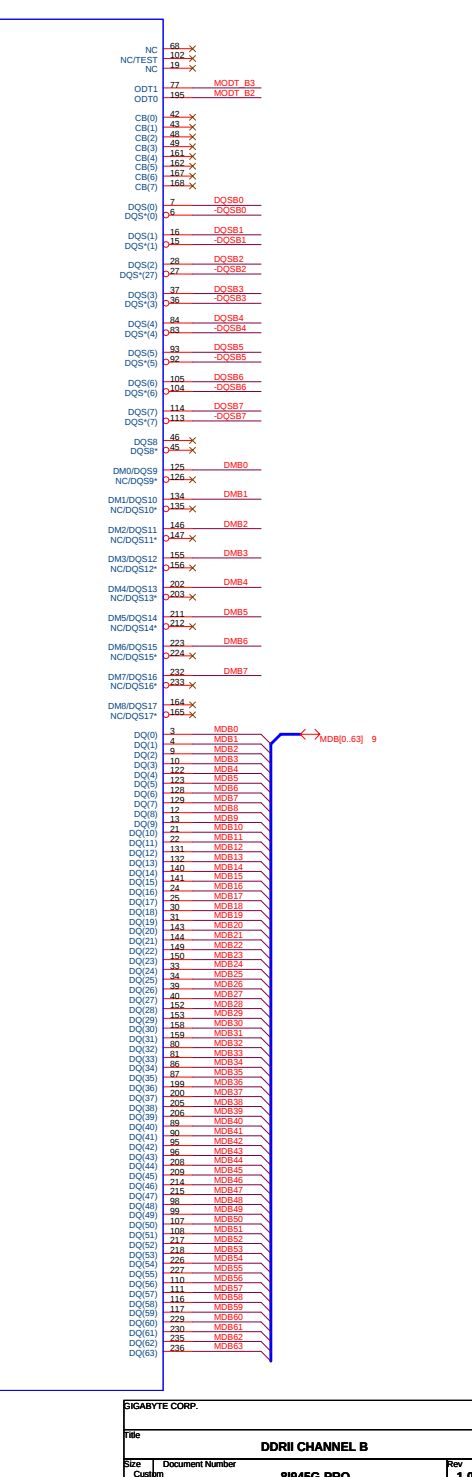
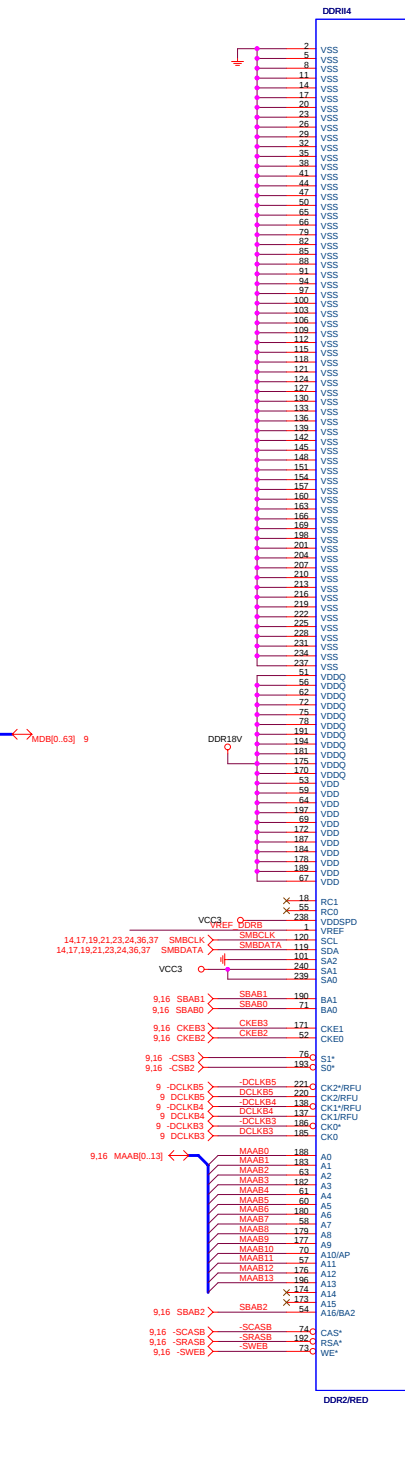
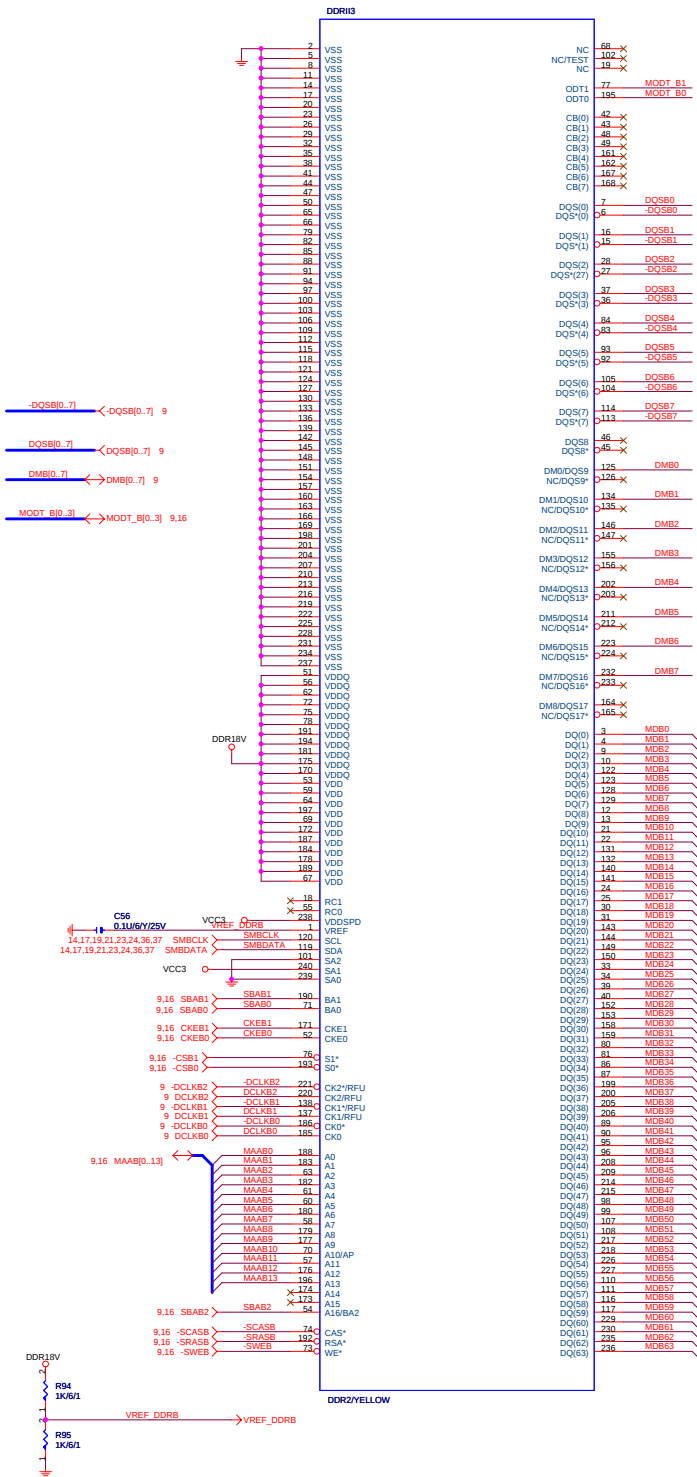
945 Design Guide rev1.5 spec.
VCCA_EXPPLL=VCCA_GPLL=45mA(1.425V~1.575V)
VCCA_HPLL>50mA 公板爲200mA(1.425V~1.575V)
VCCA_DPLLA=65mA(1.425V~1.575V)
VCCA_DPLLB=65mA(1.425V~1.575V)
VCCA_MPLL>50mA(1.425V~1.575V)
VCCA_DAC=DDR25V_DAC=70mA(2.375~2.625V)



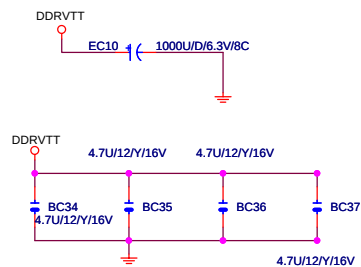
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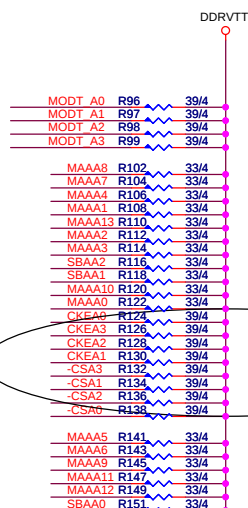
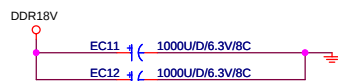




DDRVTT Decouple

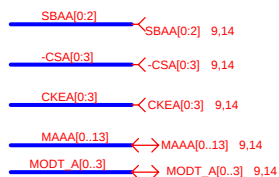
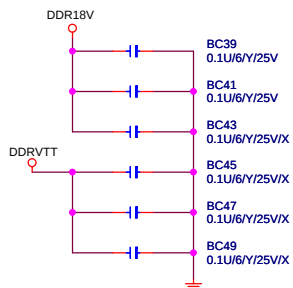


DDR TERMINATION CHANNEL A

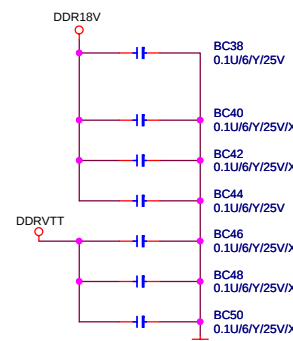


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DDR18V Decouple

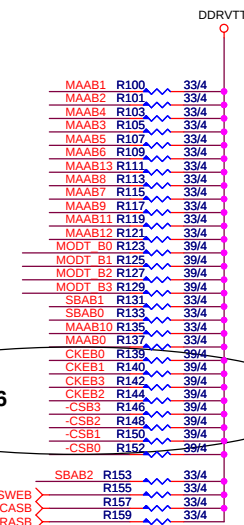


DDRVTT Decouple

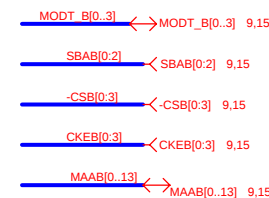


DDR18V Decouple

CHANNEL B



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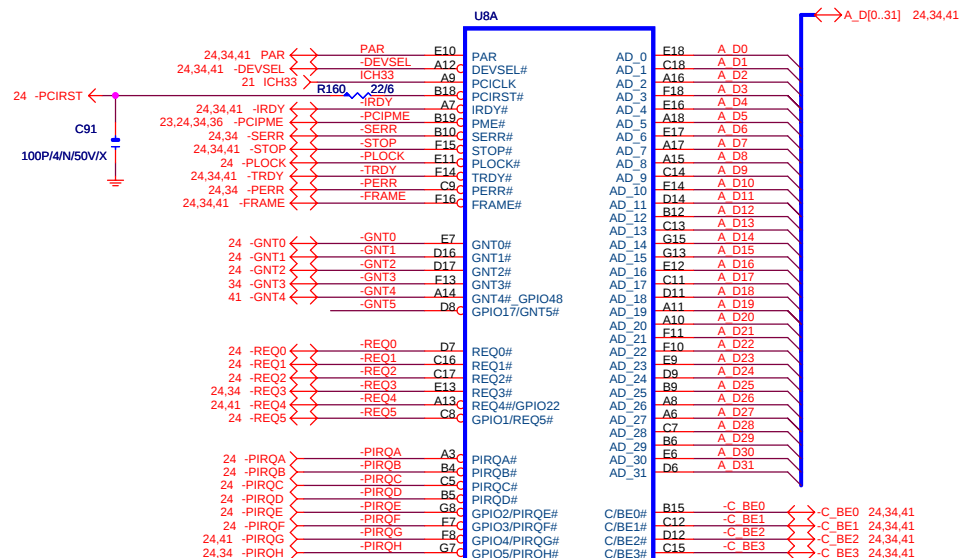
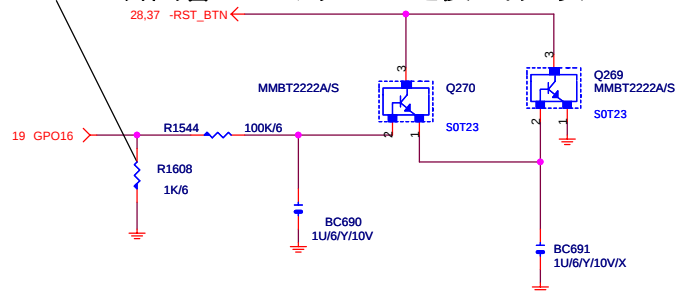


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Title			
DDRII TERMINATOR			
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FOR ICH7R POWER ON 瞬間會HIGH 到1.8V 之後0V,必須PULL DOWN 1K/6

H/W
RESET



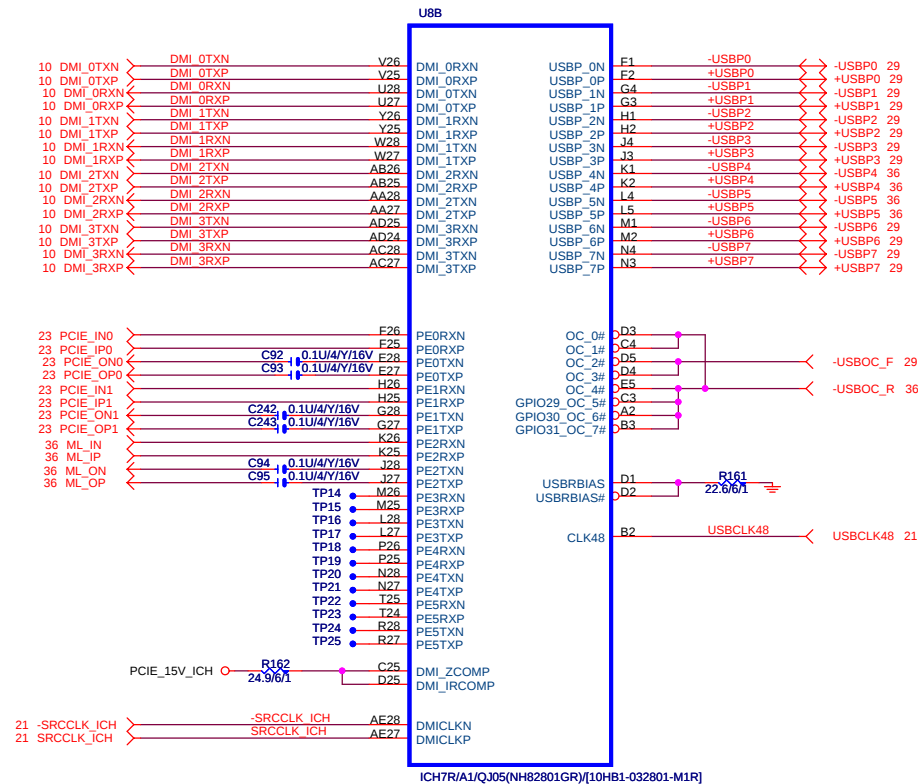
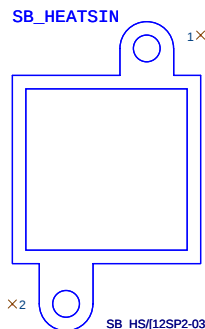
ICH7R/A1/QJ05(NH82801GR)[10HB1-032801-M1R]

01=SPI
10=PCI
11=LPC(DEFAULT)

10HB1-032801-M1 REV:NON

PCI_BT2: OPEN FWH

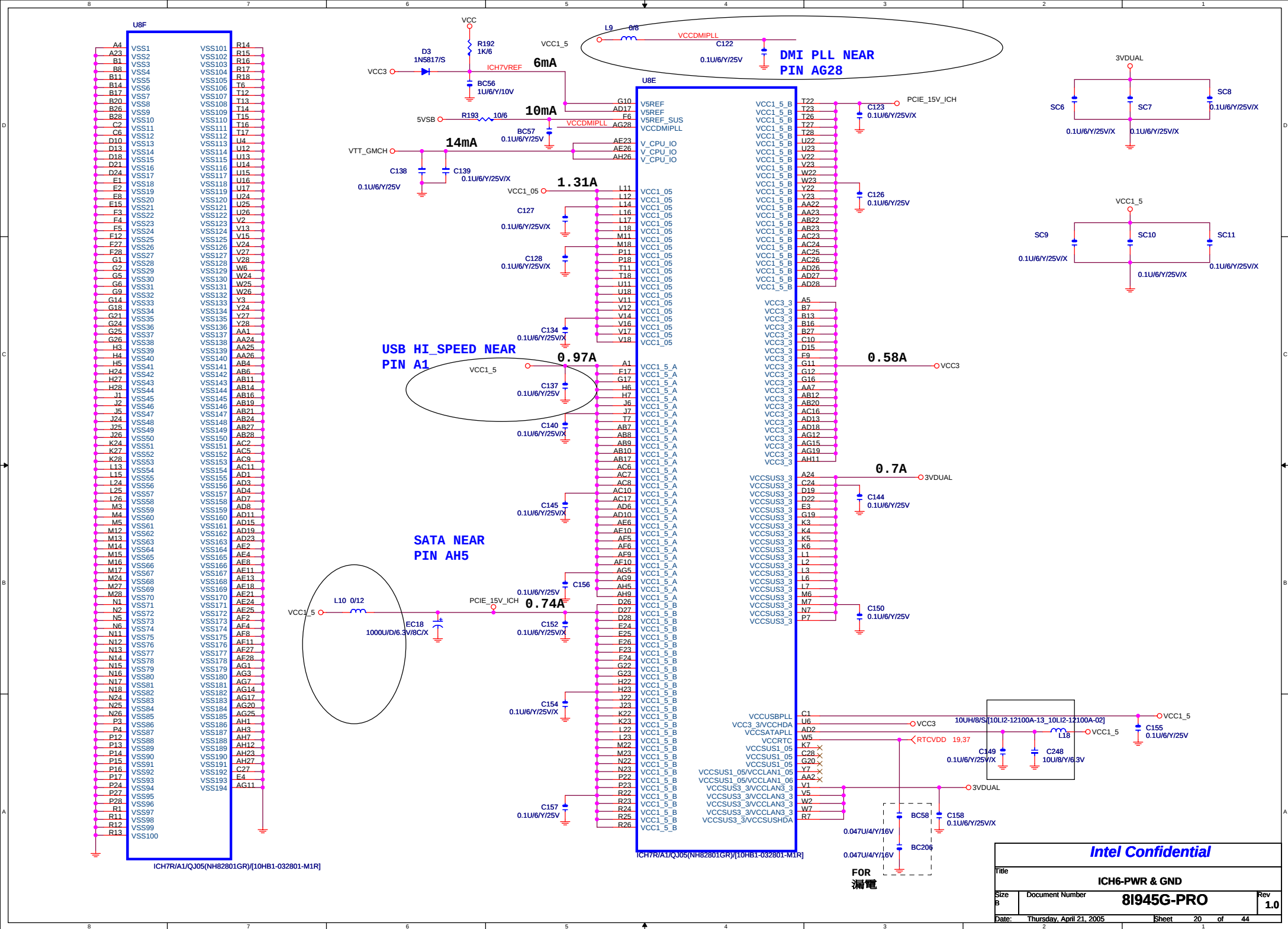
CLOSE SPI

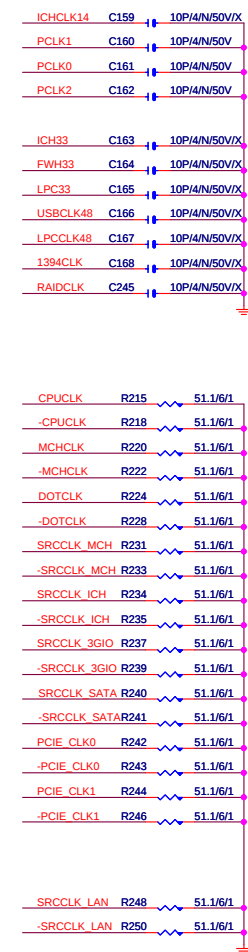
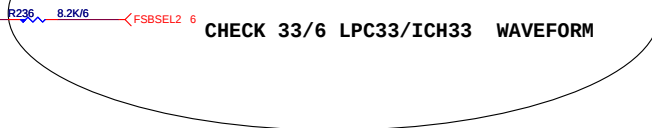
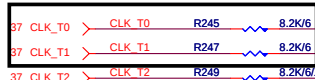


ICH7R/A1/QJ05(NH82801GR)[10HB1-032801-M1R]

Intel Confidential

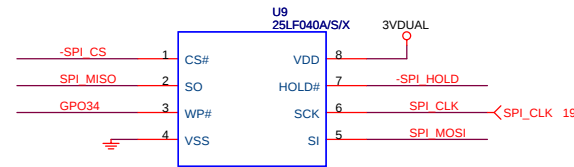
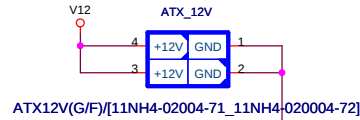
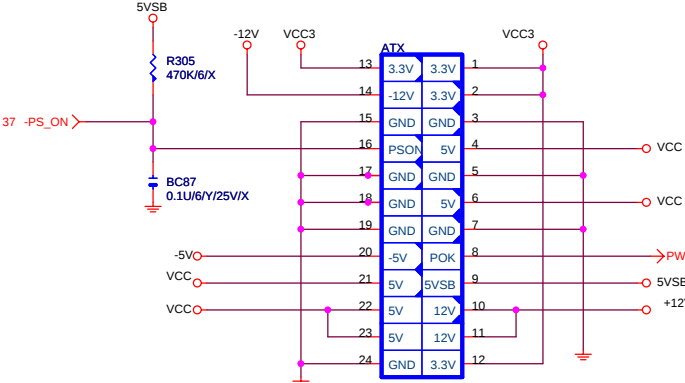
Title			
ICH6-PCI, DMI, LAN, USB			
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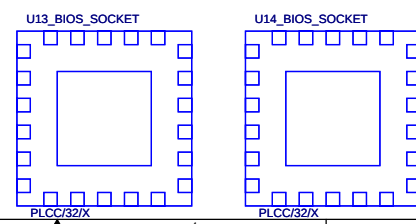
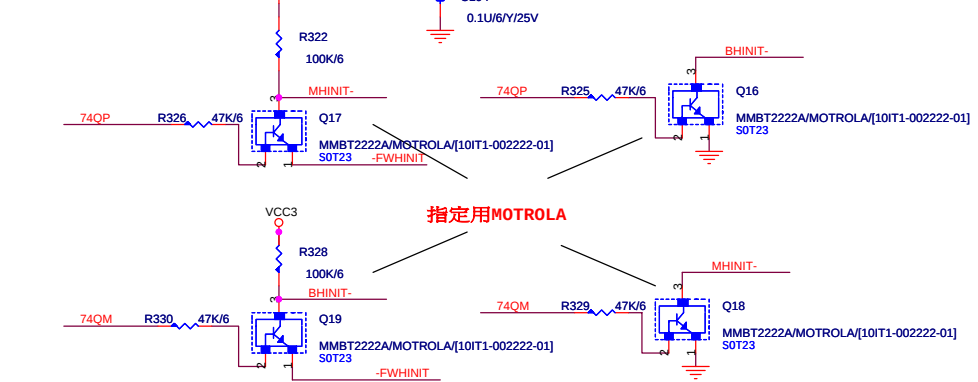
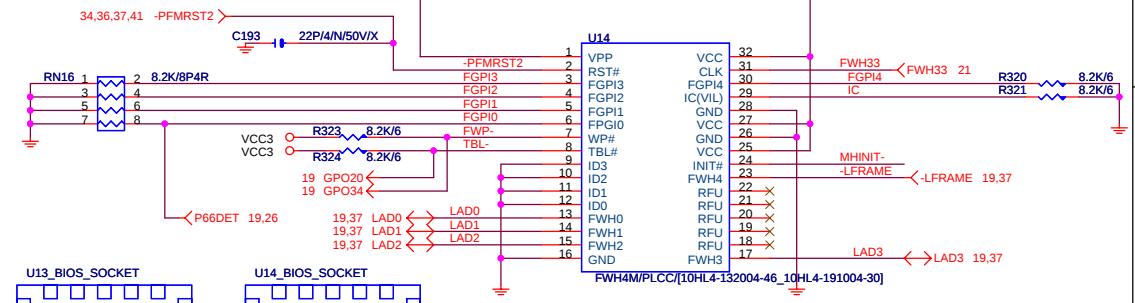
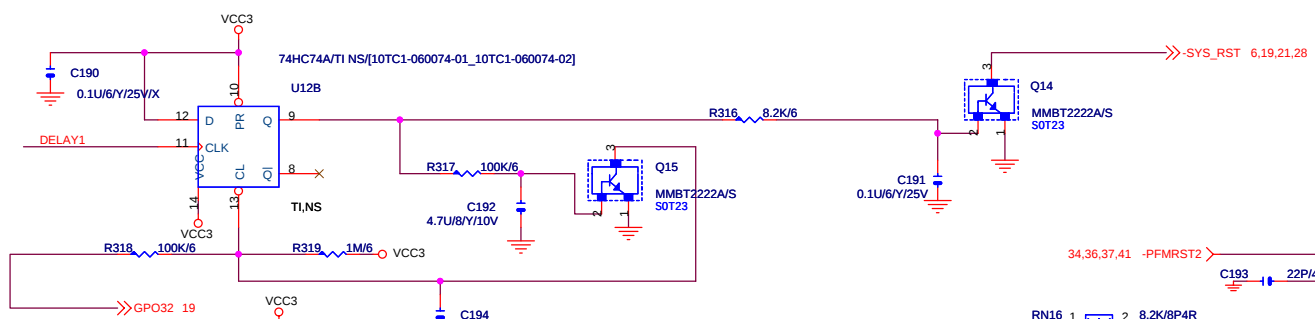
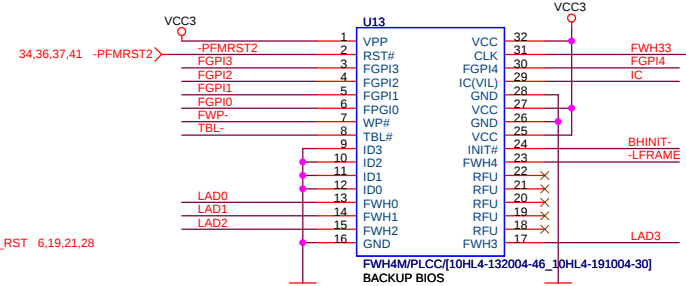
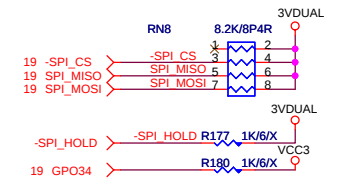
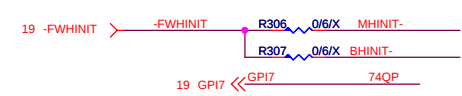
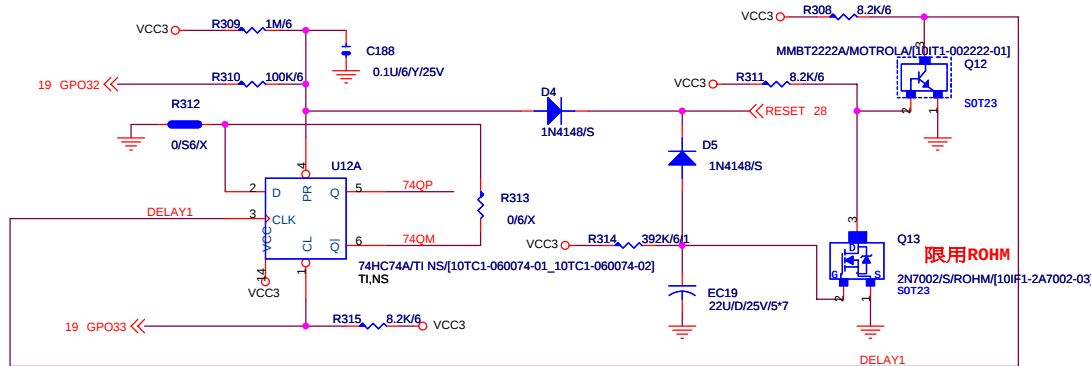
CHECK 33/6 LPC33/ICH33 WAVEFORM

ATX POWER CONNECTOR

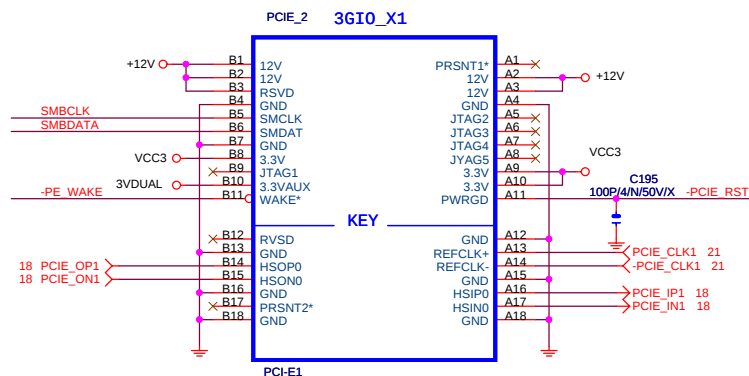
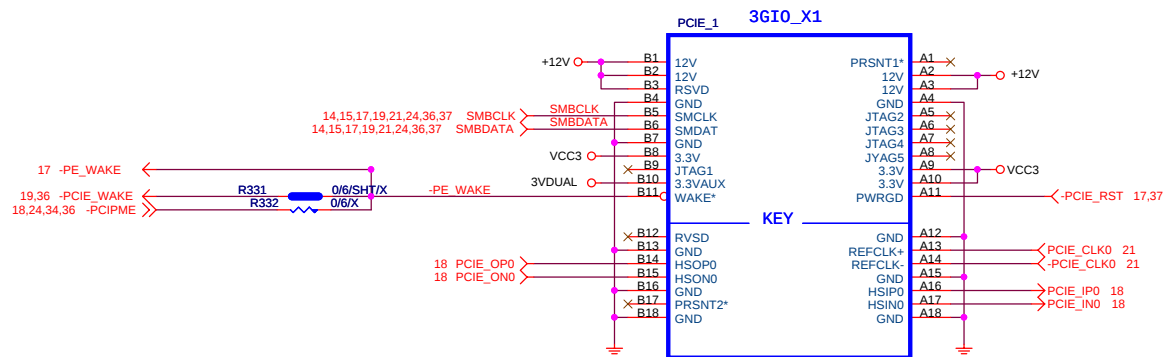


ATX 24PIN(G/F)/[11NH4-020024-31_11NH4-020024-32]

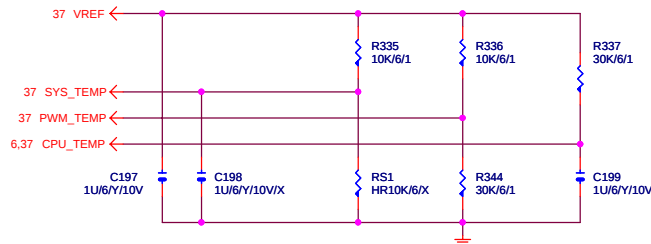
指定用MOTROLA



GIGABYTE			
Title ATX POWER CONNECTOR,DUAL BIOS			
Size B	Document Number	81945G-PRO	Rev 1.0
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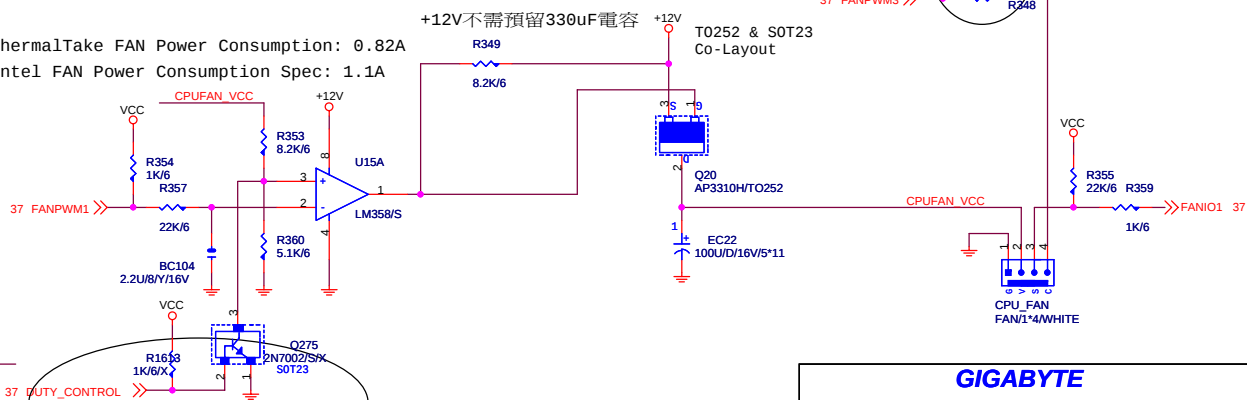
VOLTAGE SENSE



If use PBSS5240 1pcs : (with airflow)

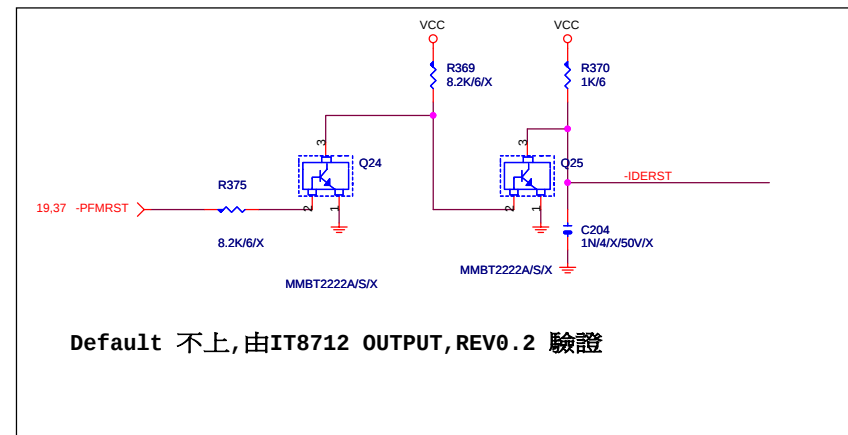
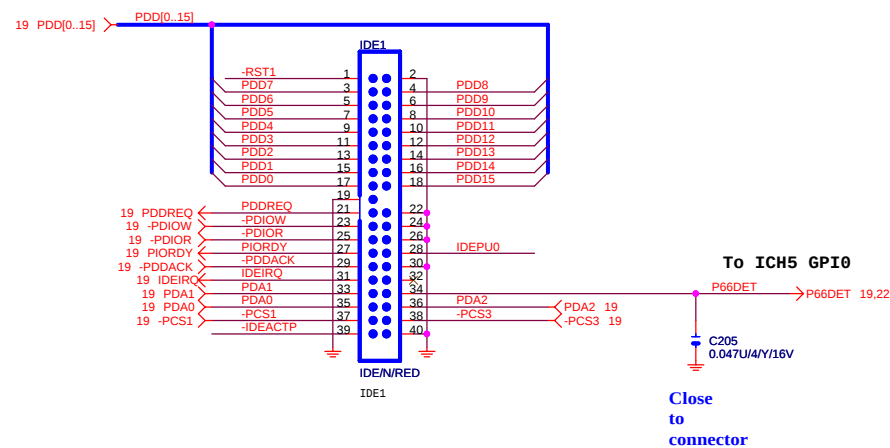
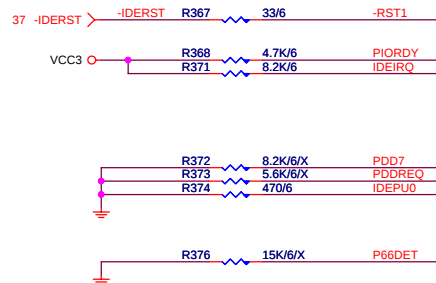
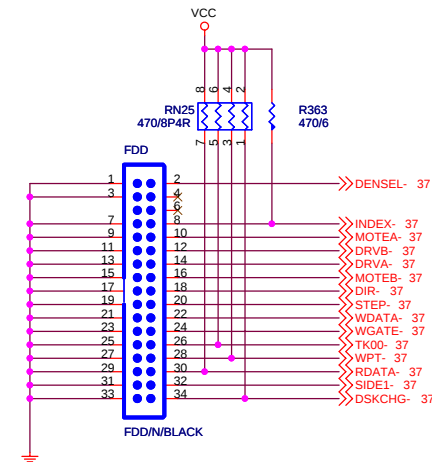
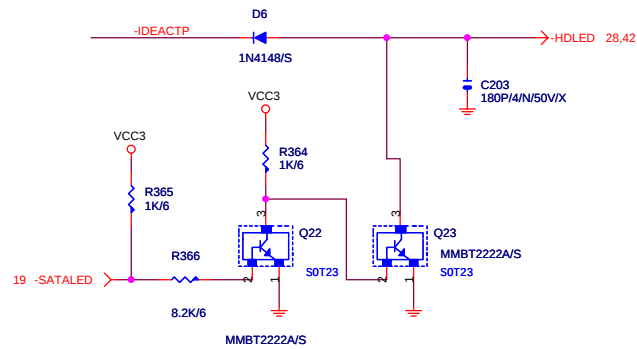
```
CPUFAN_VCC=12V: Temp=33 deg
CPUFAN_VCC=11V: Temp=62 deg
CPUFAN_VCC=10V: Temp=86 deg
CPUFAN_VCC= 9V: Temp=117 deg
CPUFAN_VCC= 8V: Temp>122 deg
```

CRUEAN VCC +12V

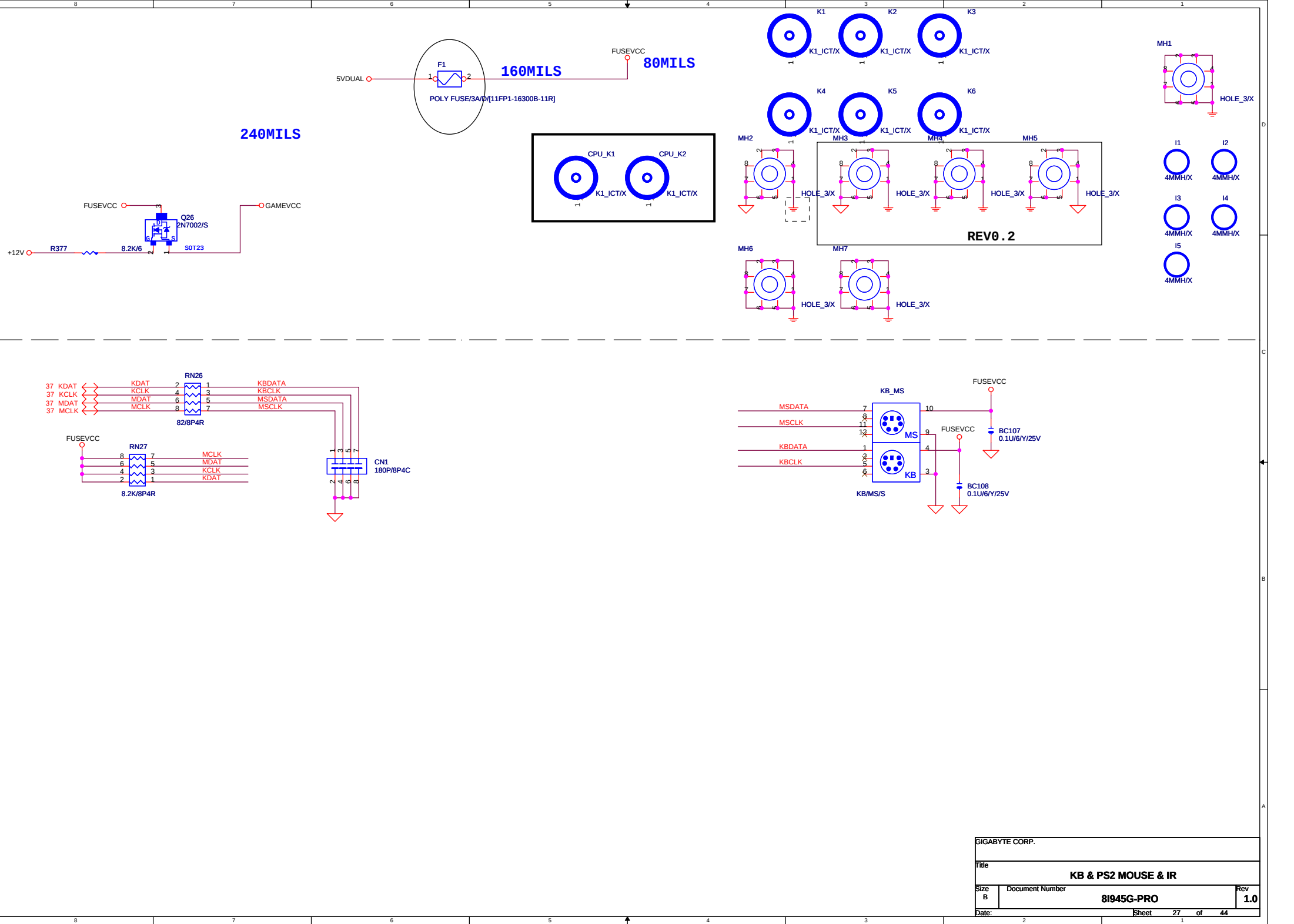


default is high
gpiox pin 可以3.3v or 5v
if 3pin fan 則開機後將program is low

GIGABYTE			
Title HWM/FAN/CI/BIOS			
Size Custom	Document Number	8I945G-PRO	Rev 1.1
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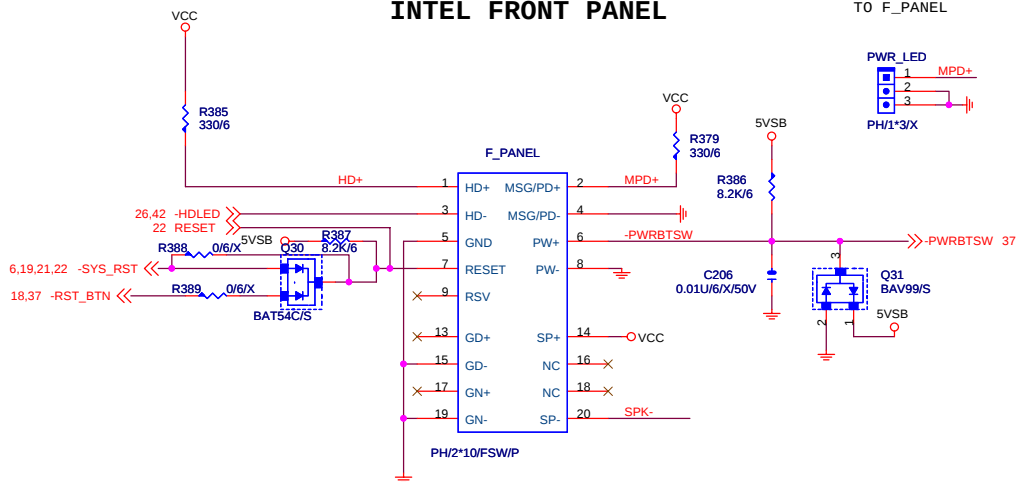


PRIMARY IDE CONNECTOR

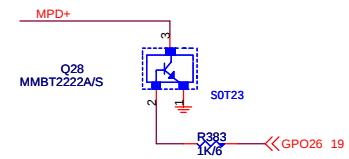
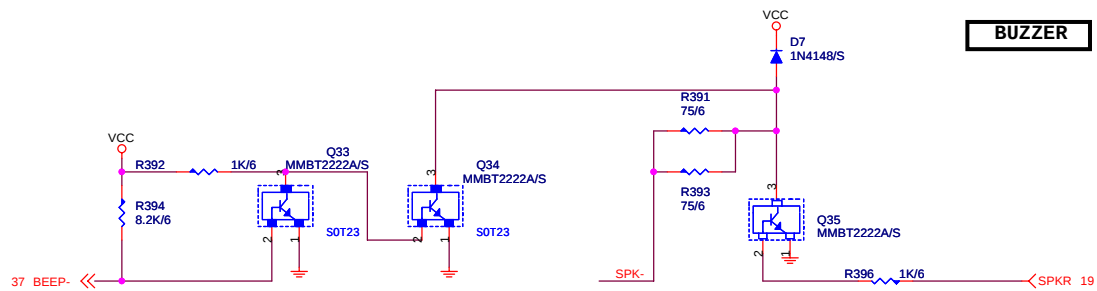


INTEL FRONT PANEL

3 PIN POWER LED
LAYOUT PLACE CLOSE
TO F_PANEL



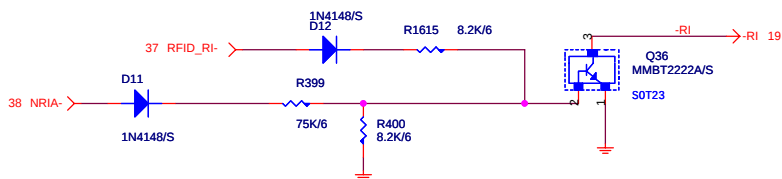
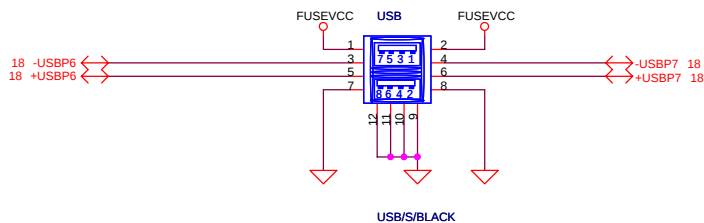
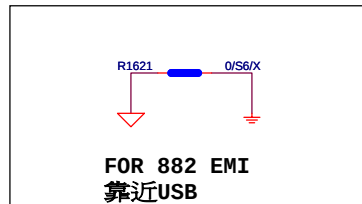
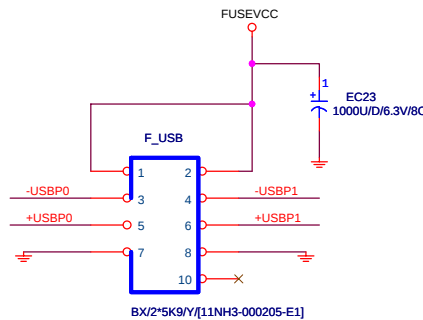
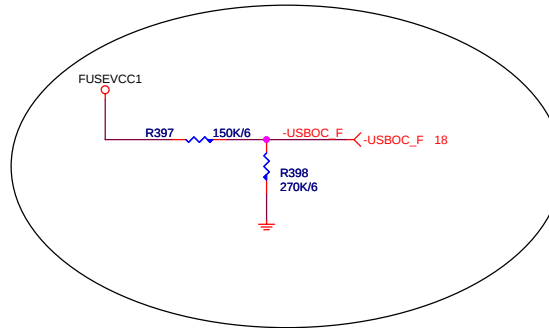
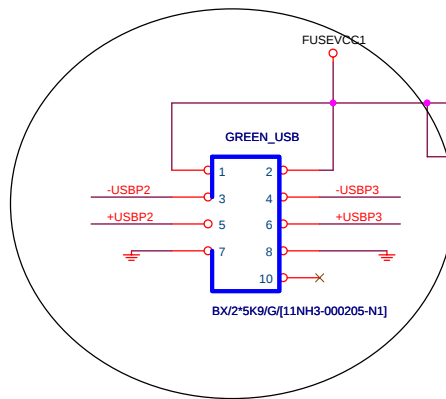
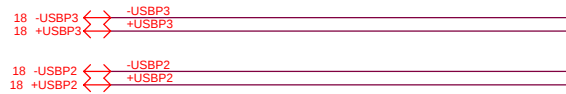
BUZZER



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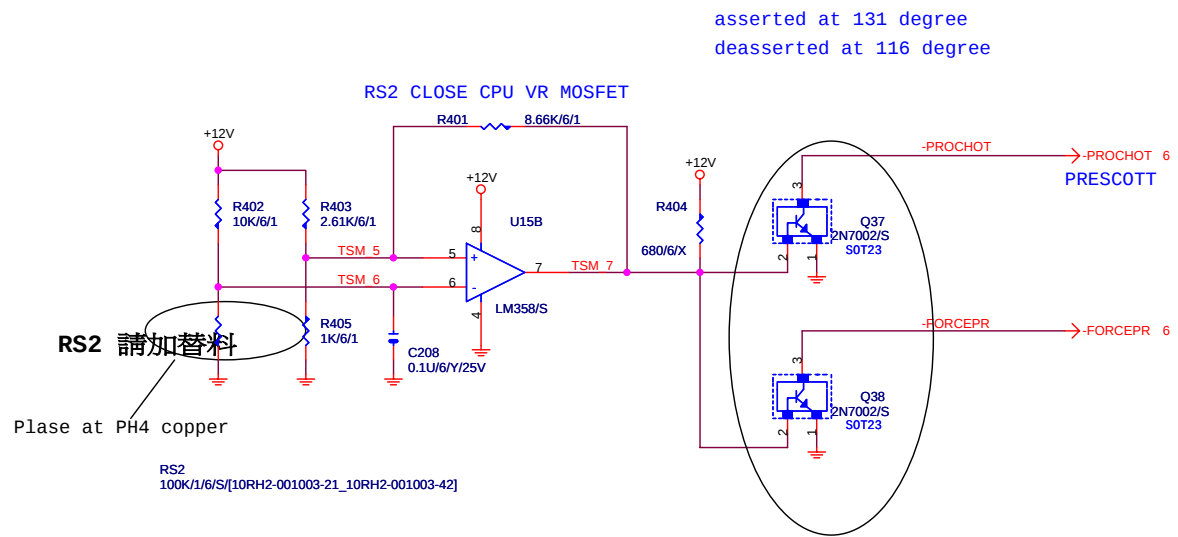
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FRONT PANEL			
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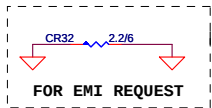
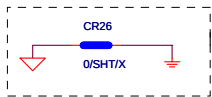
FRONT USB



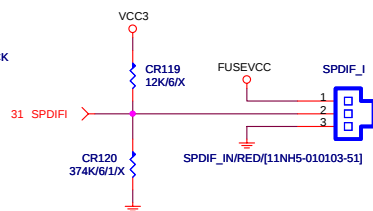
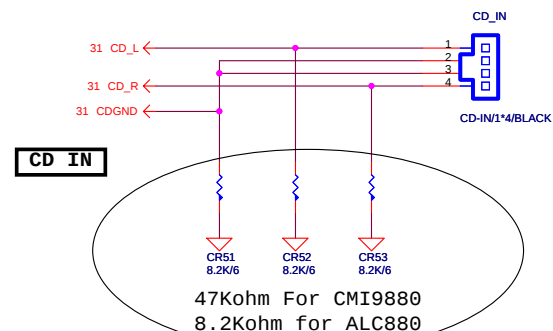
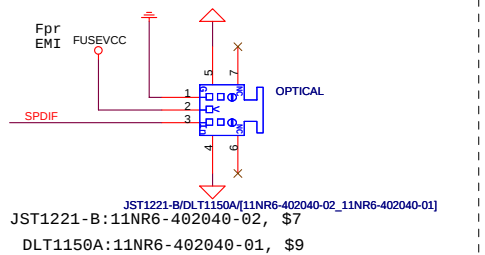
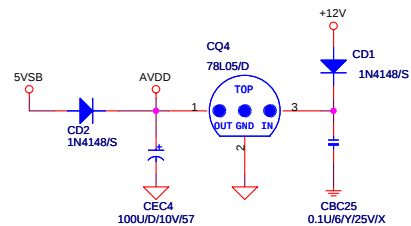
Intel Confidential

Title			
FRONT USB CONNECTOR			
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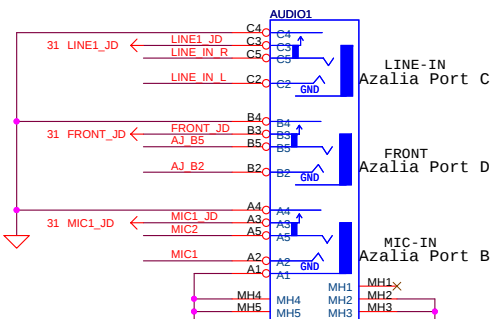


FOR EMI REQUEST

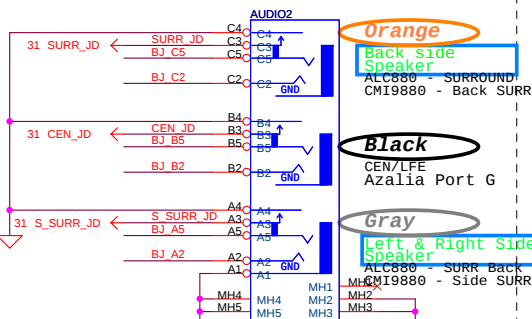


Azalia Jack
Normal --> pin4/pin3 open
Plug jack --> pin4/pin3 close

CMI9880 Port A is Side SURROUND, Port H is Back SURROUND
ALC880 Port A is SURROUND, Port H is SIDE



A3RJ13P/B/[11NR6-403006-01_11NR6-403006-02]

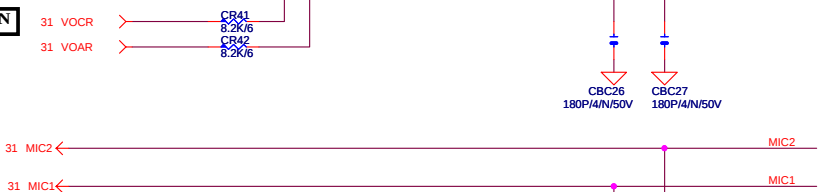


A3RJ13P/OBG/[11NR6-403006-71]

LINE OUT FRONT OUT



LINE-IN



MIC



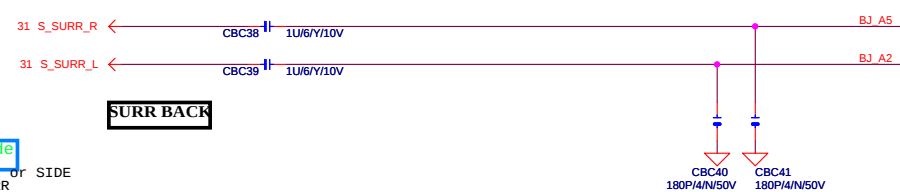
SURROUND



CEN/LFE

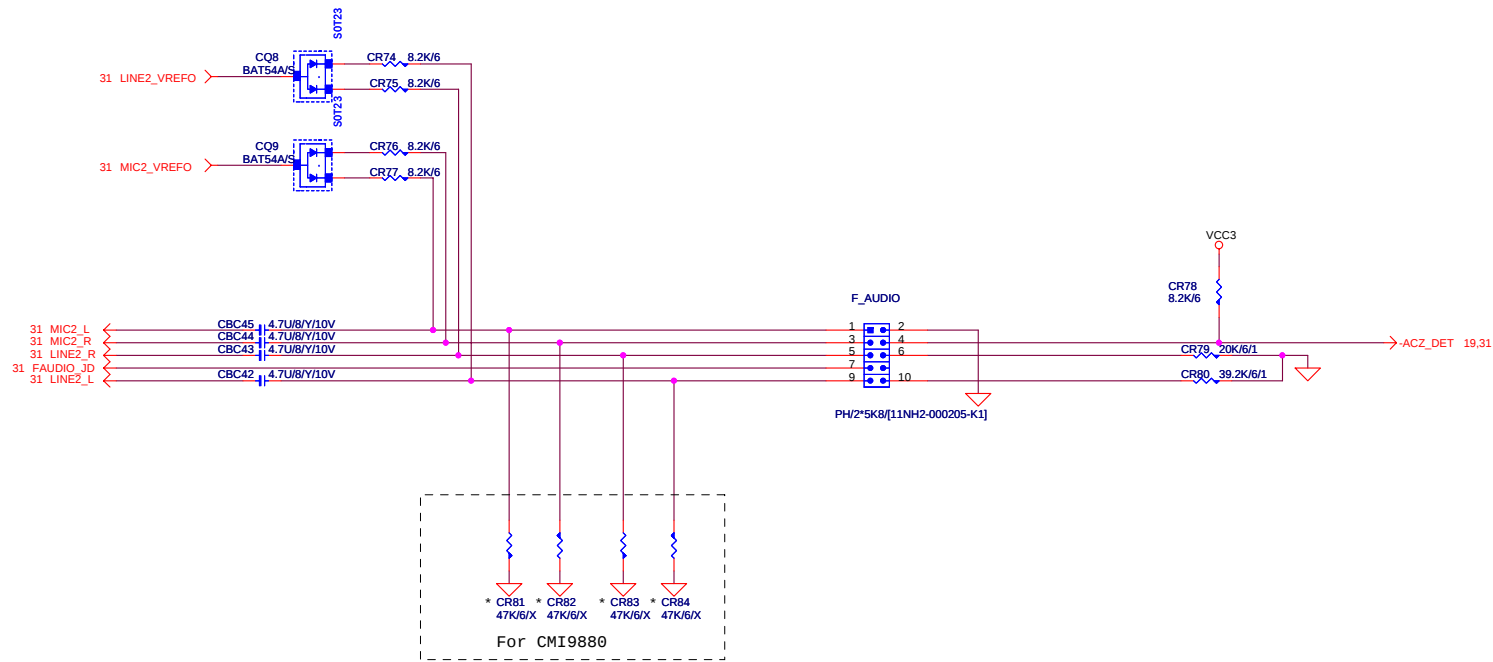


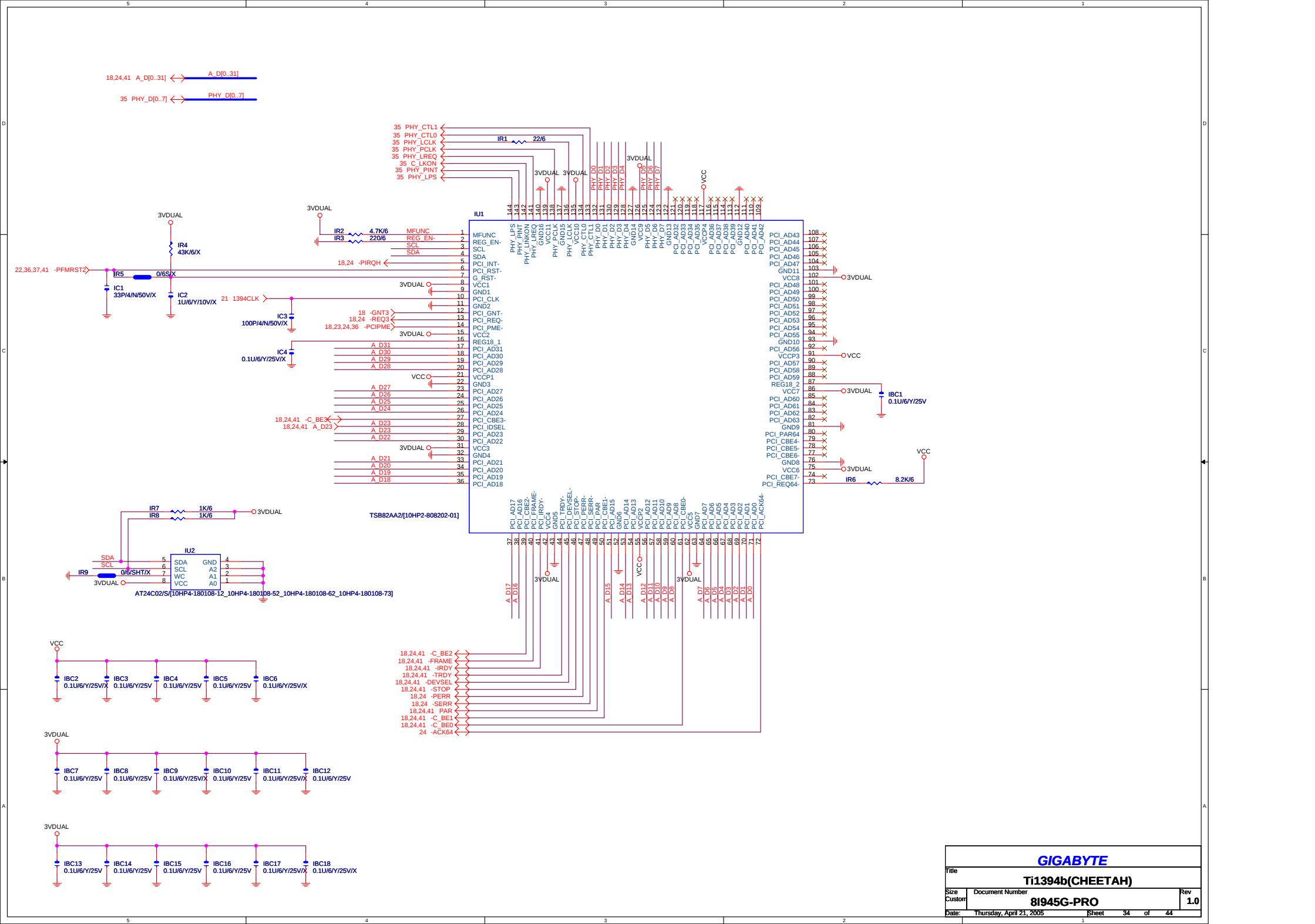
SURR BACK



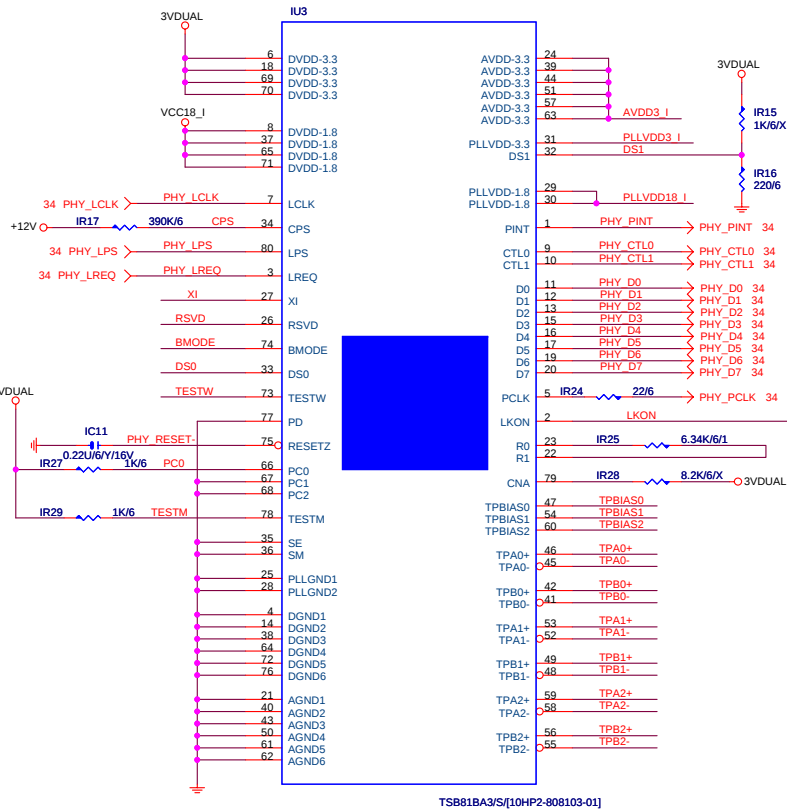
Intel Confidential			
Title			
AUDIO JACK			
Size			
Document Number			
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1.0			
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Azalia Port F
Azalia Port E

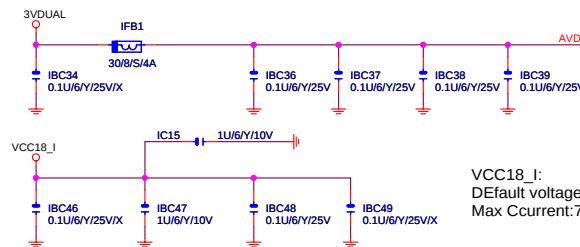
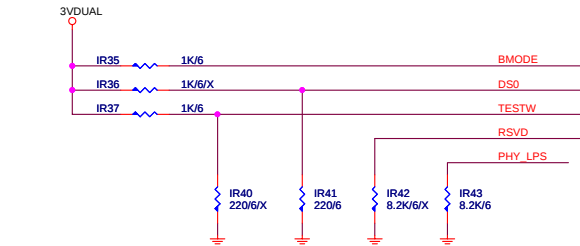




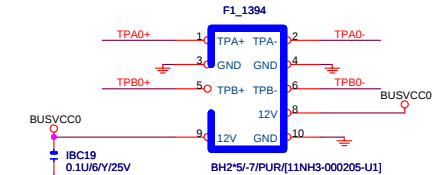
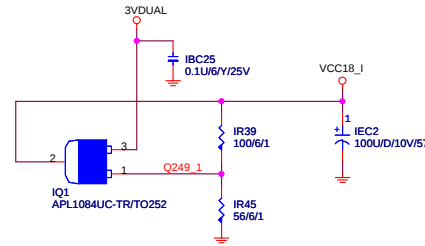
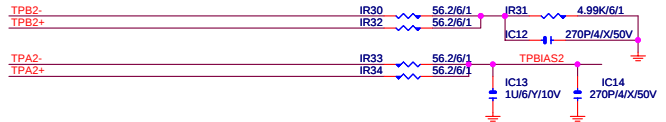
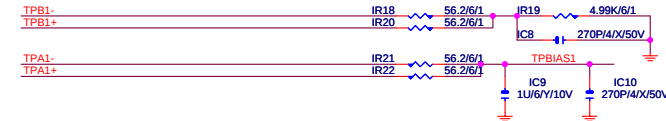
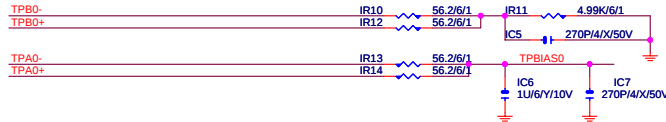
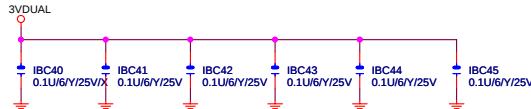
Width & Space --> 20:7.5:7.5:7.5:20



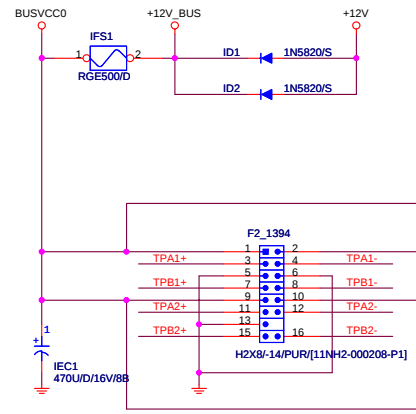
TSB81BA3/S[10HP2-808103-01]



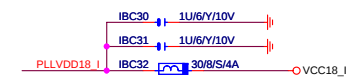
VCC18_I:
Default voltage:1.95V
Max Ccurrent:79mA



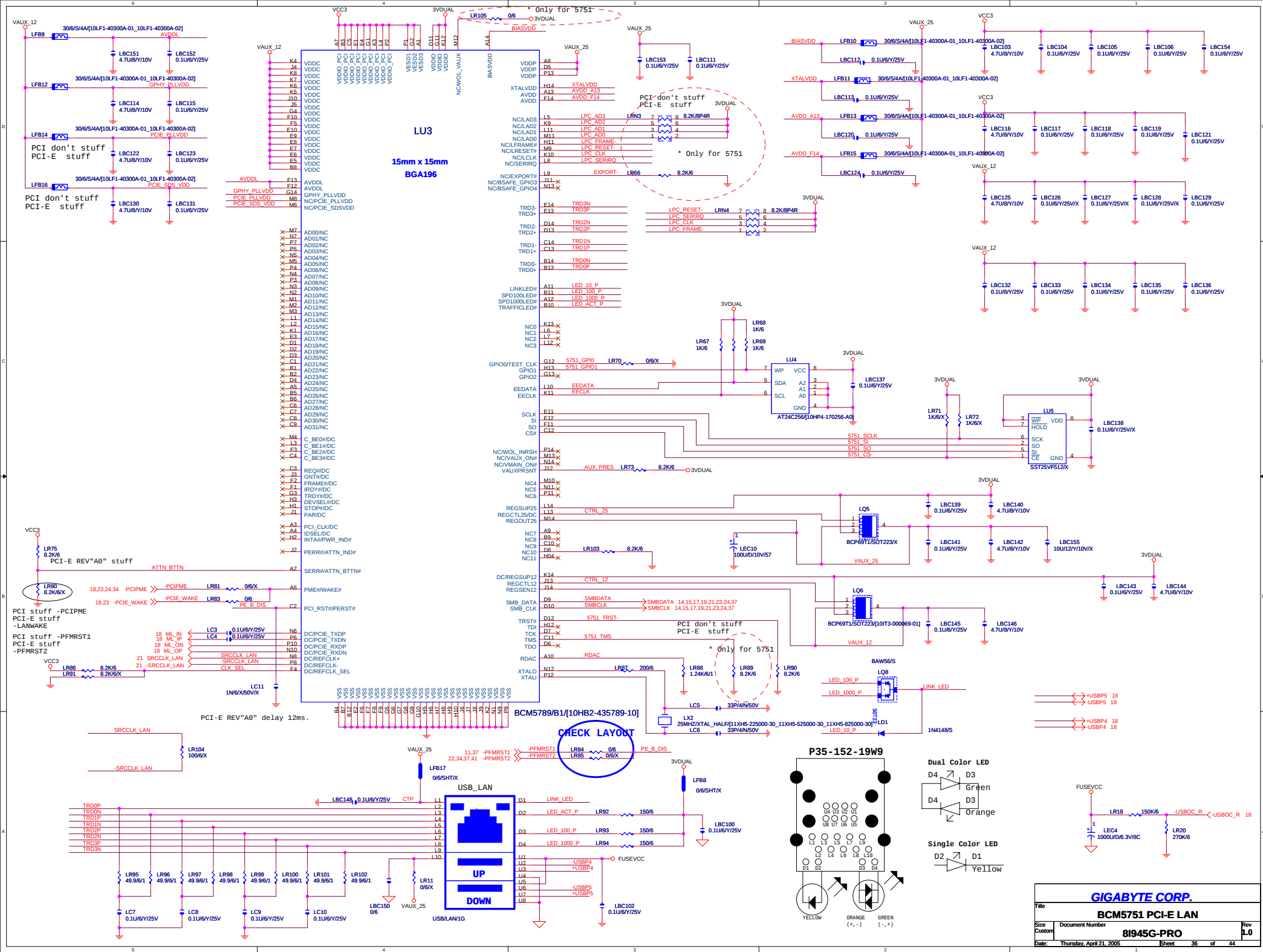
Width & Space --> 20:5:6:5:20

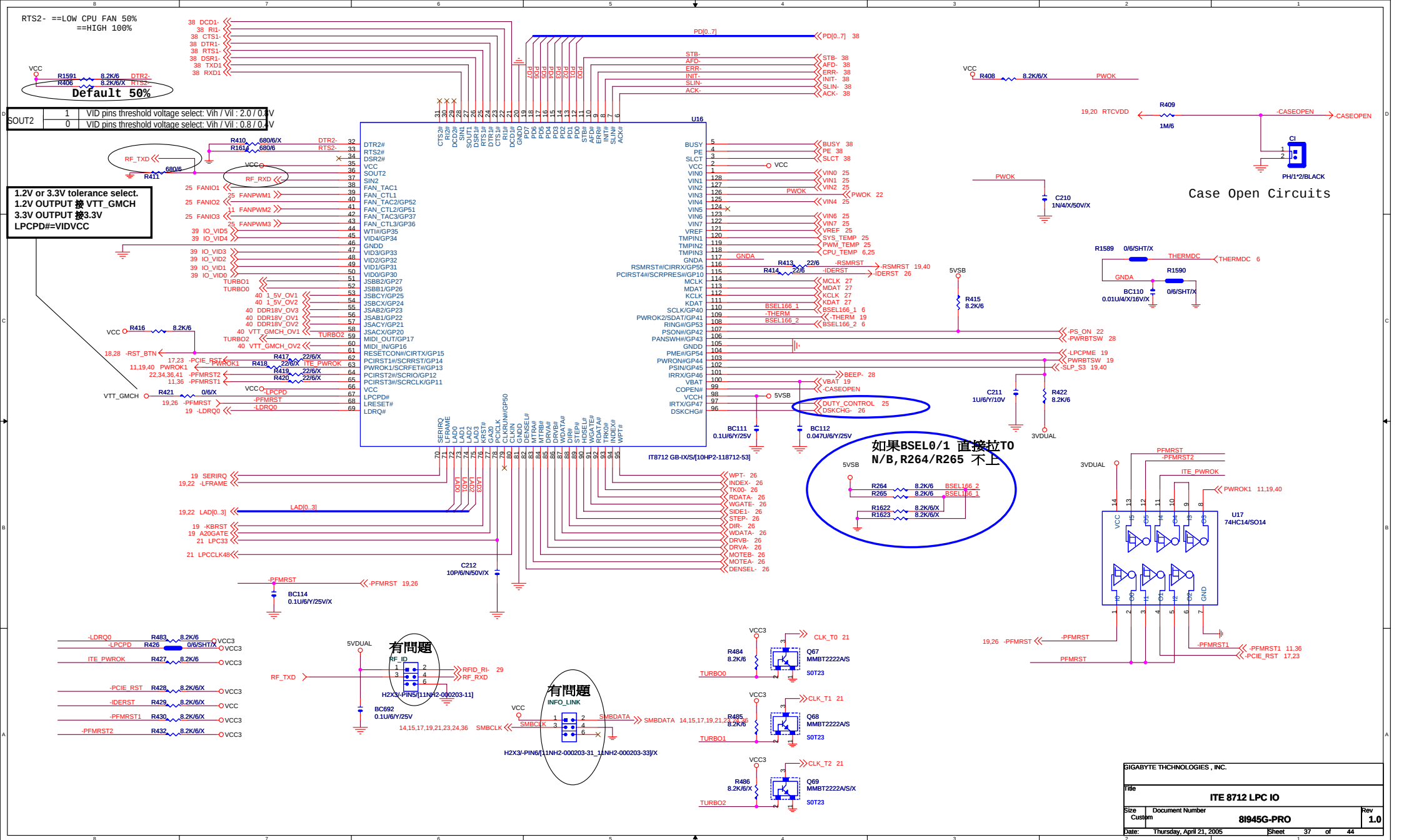


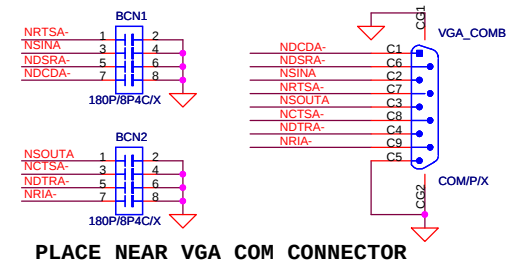
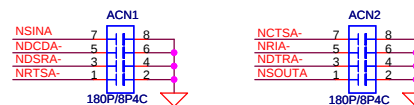
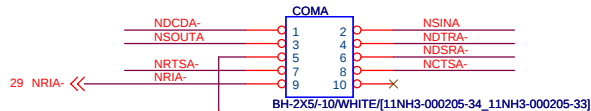
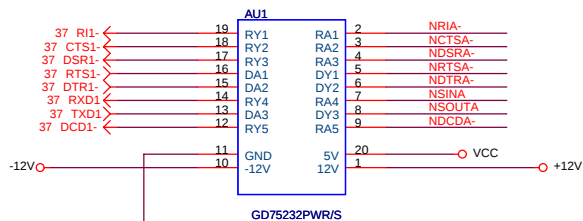
Width & Space --> 20:5:6:5:20



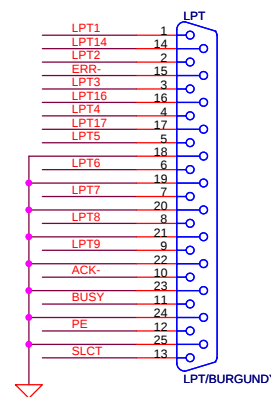
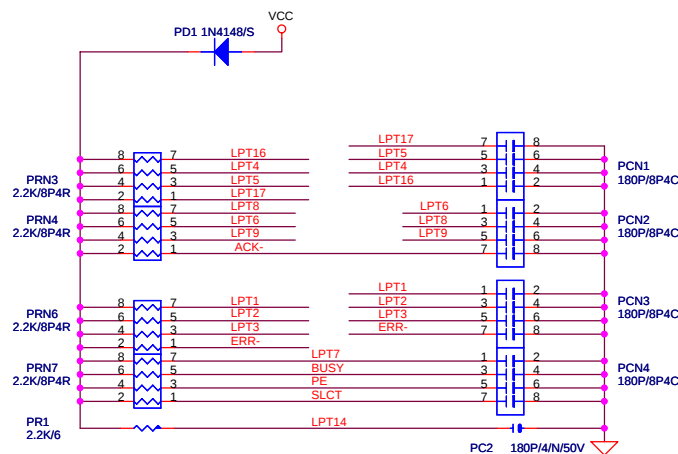
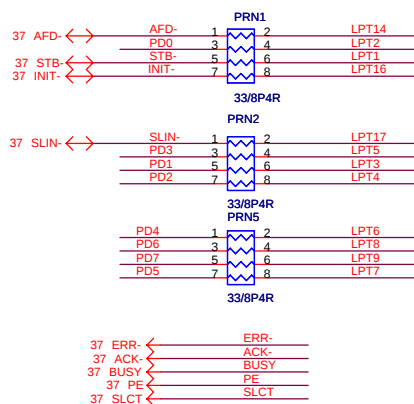
GIGABYTE			
Title			
Ti1394B/TSB81BA3			
Size	Document Number		Rev
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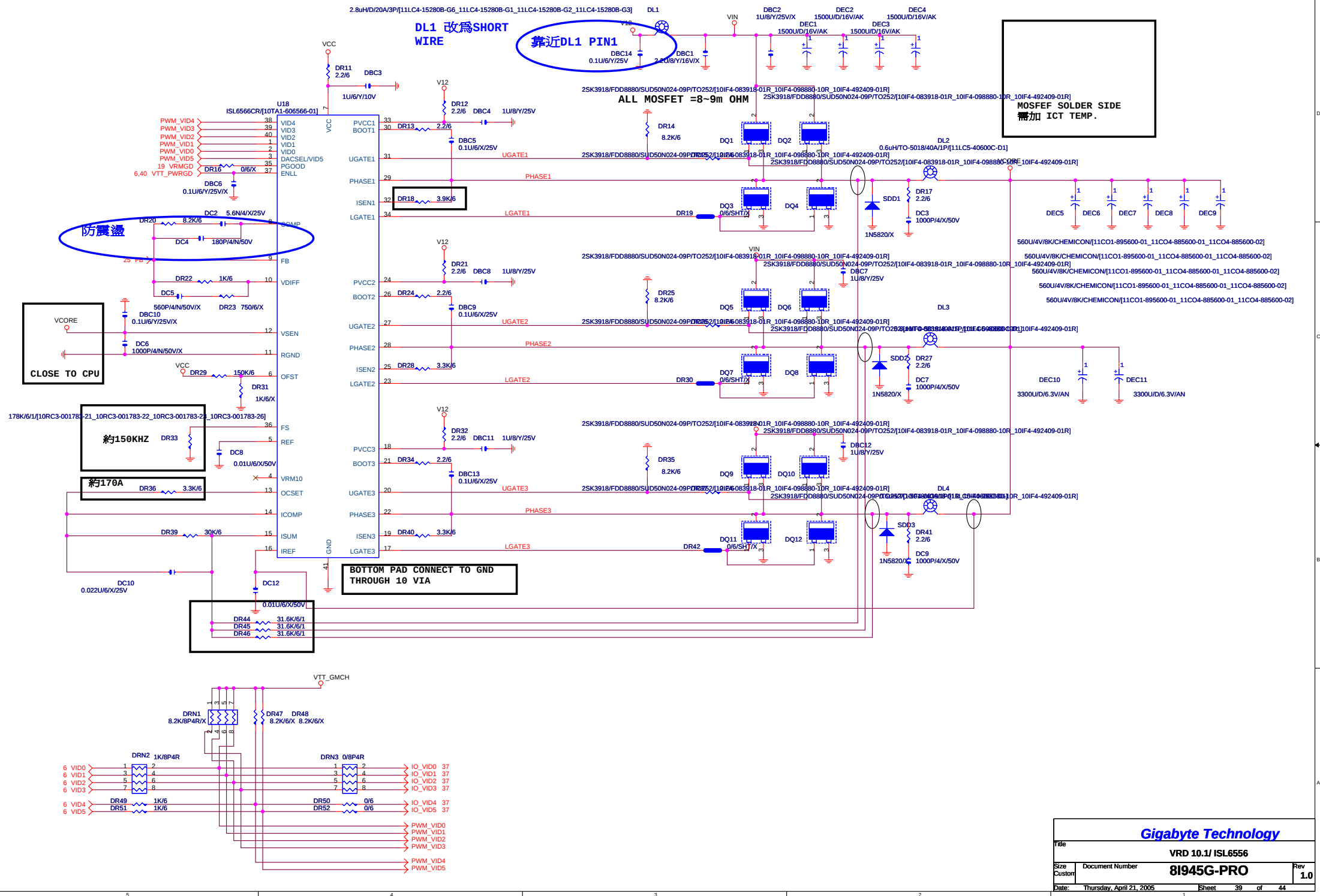






37 PD[0..7] <- PD[0..7]





DL1 改為SHORT WIRE

靠近DL1 PIN1

ALL MOSFET =8~9m OHM

MOSFET SOLDER SIDE 需加 ICT TEMP.

防震邊

BOTTOM PAD CONNECT TO GND THROUGH 10 VIA

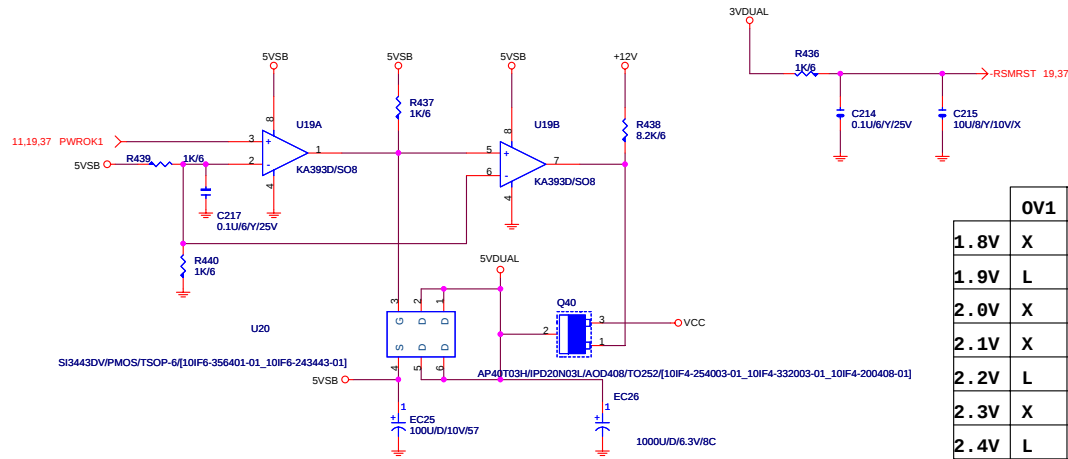
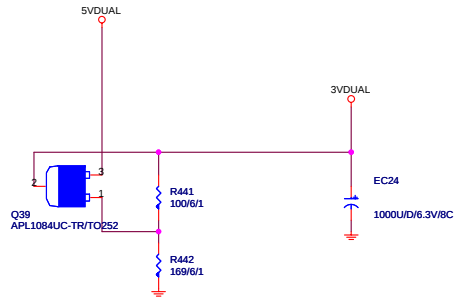
Gigabyte Technology

VRD 10.1/ ISL6556

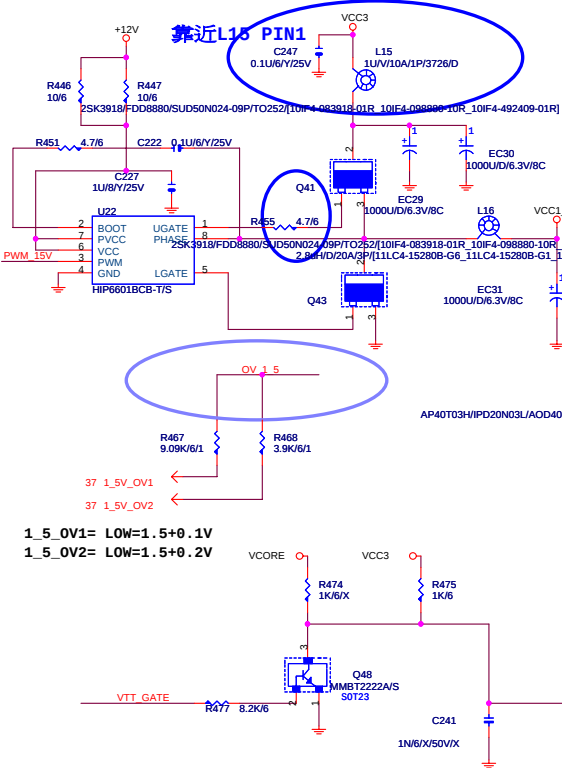
81945G-PRO

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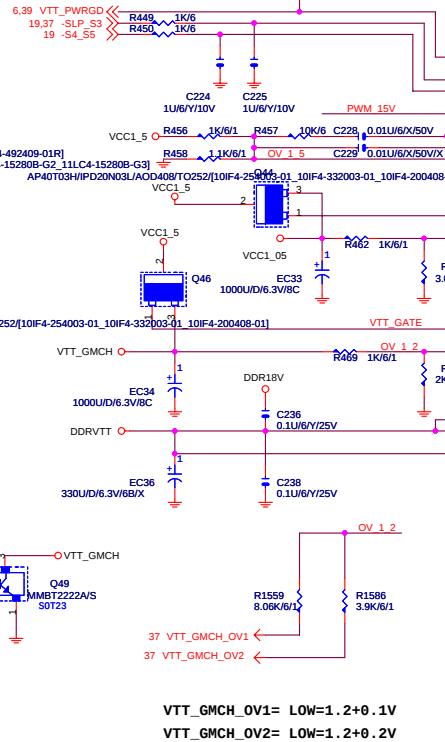


	OV1	OV2	OV3
1.8V	X	X	X
1.9V	L	X	X
2.0V	X	L	X
2.1V	X	X	L
2.2V	L	X	L
2.3V	X	L	L
2.4V	L	L	L

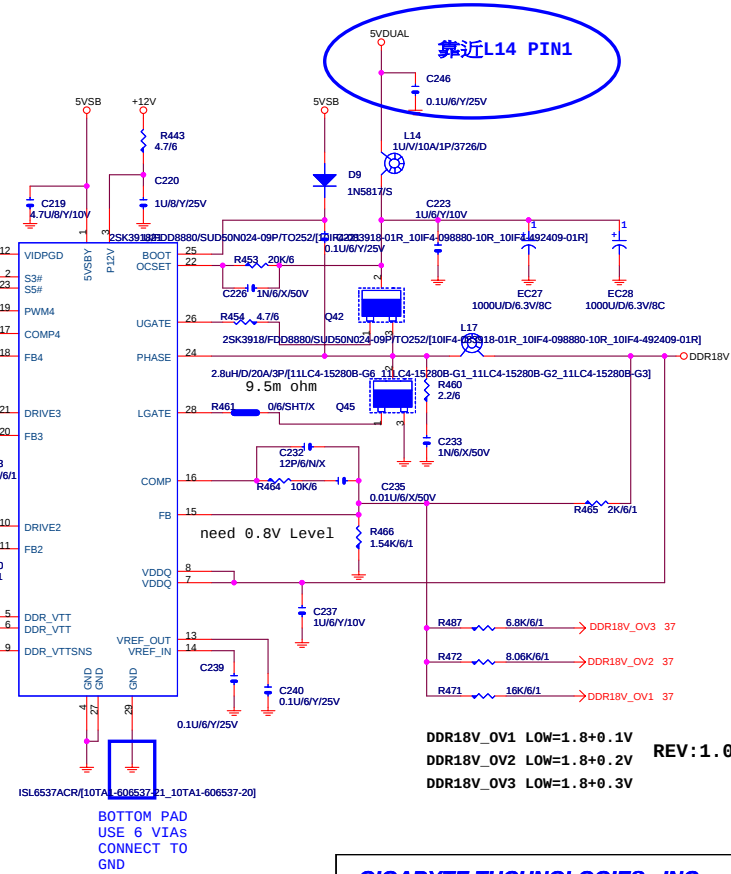


1.5_OV1= LOW=1.5+0.1V
1.5_OV2= LOW=1.5+0.2V

R402 Change to 1uF
If ISL6537 pin12 Fail



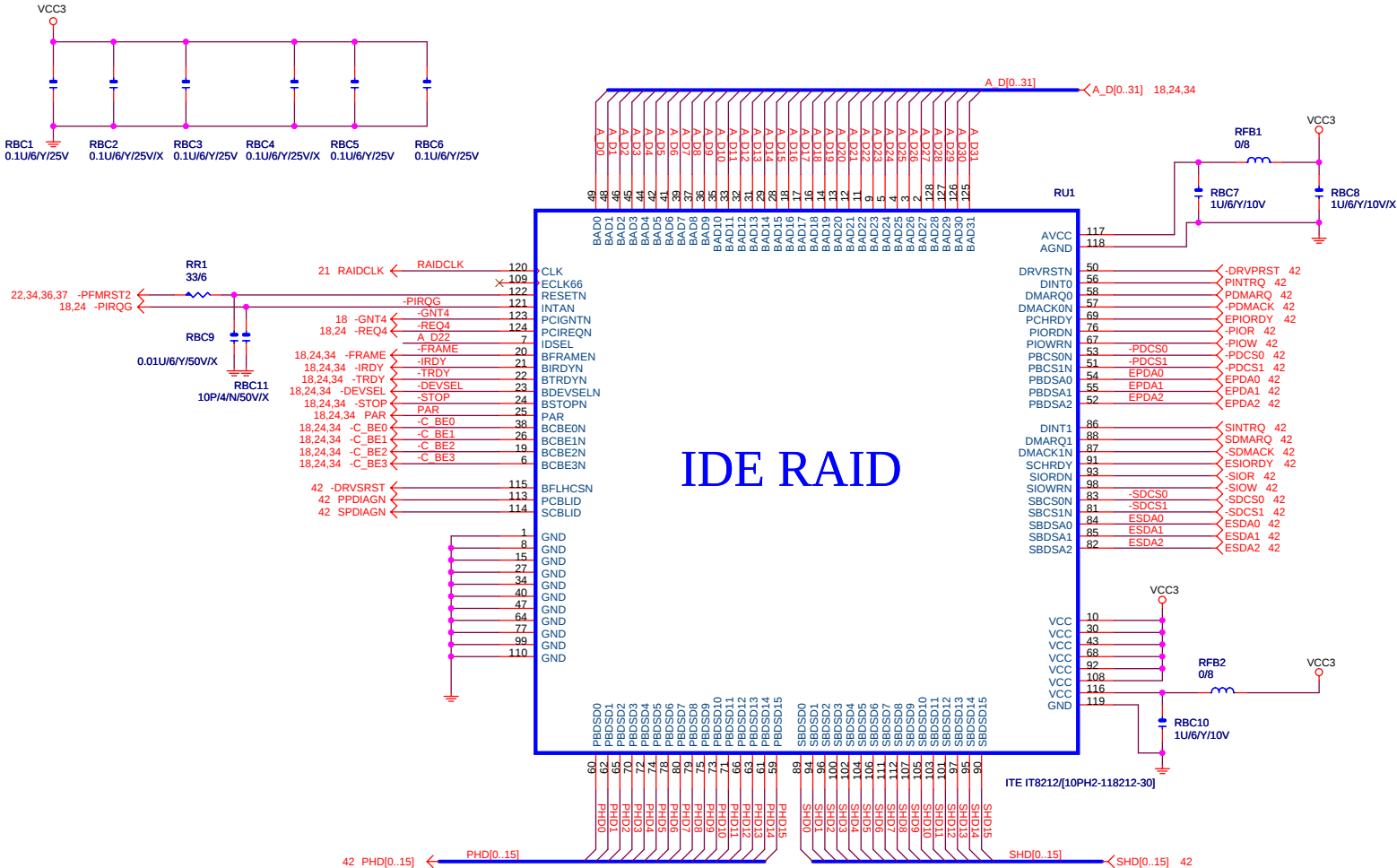
VTT_GMCH_OV1= LOW=1.2+0.1V
VTT_GMCH_OV2= LOW=1.2+0.2V

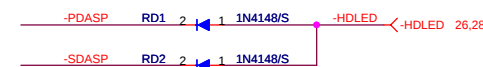
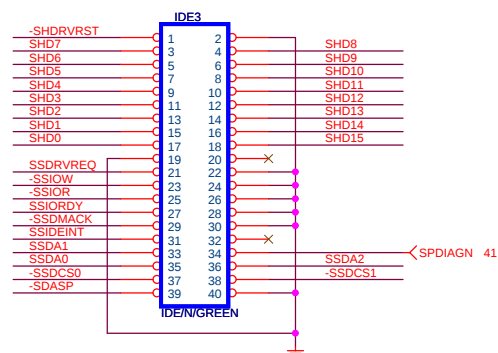
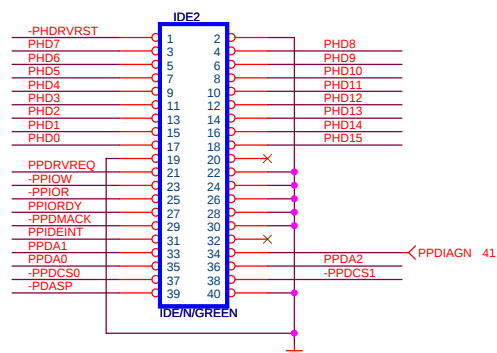
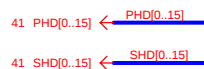
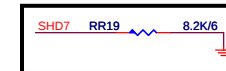
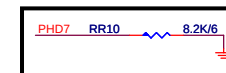
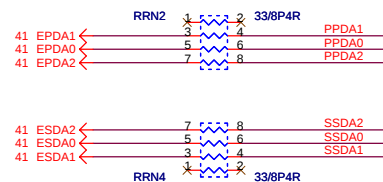
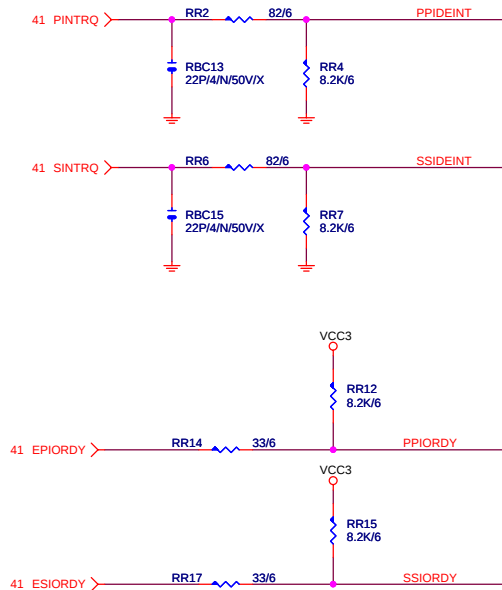
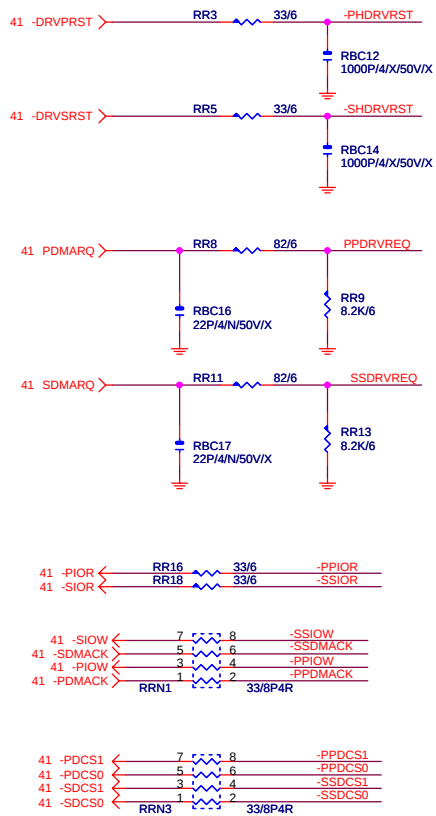


DDR18V_OV1 LOW=1.8+0.1V
DDR18V_OV2 LOW=1.8+0.2V
DDR18V_OV3 LOW=1.8+0.3V

REV:1.0

ALL INPUT PIN MUST HAVE 0.1 CAPACITOR





GPI PIN

Pin Name	Pin Type	Power Well			GPIO Application
GPIO[0]	I/O	VCC3	GPIO/BM_BUSY#	(NA)	(NA)
GPIO[1]	I/O	VCC	-REQ[5]	(P.U VCC)	-REQ[5]
GPIO[5:2]	I/OD	VCC	-PIRQ[H:E]	(P.U VCC)	-PIRQ[H:E]
GPIO[6]	I/O	VCC3	GPI	(NA)	M_ID0 FOR MEDIA
GPIO[7]	I/O	VCC3	GPI	(NA)	DUALBIOS_INPUT
GPIO[8]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-SKTOCC
GPIO[9]	I/O	3VDUAL	GPI	(NA)	P66DET
GPIO[10]	I/O	3VDUAL	GPI	(NA)	M_ID1 FOR MB_ID
GPIO[11]	I/O	3VDUAL	-SMBALERT	(P.U 3VDUAL)	-SMBALRT
GPIO[12]	I/O	3VDUAL	GPI	(NA)	M_ID2 FOR MB_ID
GPIO[13]	I/O	3VDUAL	GPI	(P.U 3VDUAL)	-LPCPME
GPIO[14]	I/O	3VDUAL	GPI	(NA)	M_ID3 FOR MB_ID
GPIO[15]	I/O	3VDUAL	GPI	(NA)	-ACZ_DET
GPIO[16]	I/O	VCC3	GPO	P.D 20K(INT.)	HW RESET
GPIO[17]	I/O	VCC3	GPO/-GNT[5]	(NA)	GPO/-GNT[5]
GPIO[18]	I/O	VCC3	GPO/toggle	(NA)	(NA)
GPIO[19]	I/O	VCC3	SATA1GP	(P.U VCC3)	SATA1GP
GPIO[20]	I/O	VCC3	GPO	(P.U VCC3)	TBL-
GPIO[21]	I/O	VCC3	SATA0GP	(P.U VCC3)	SATA0GP
GPIO[22]	I/O	VCC3	-REQ[4]	(P.U VCC)	-REQ[4]
GPIO[23]	I/O	VCC3	LDRQ1#	(NA)	(NA)
GPIO[24]	I/O	3VDUAL	GPO/reset not cleared	(NA)	(NA)
GPIO[25]	I/O	3VDUAL	GPO	(NA)	PWD_LED
GPIO[26]	I/O	3VDUAL	EL_RSVD	(P.D)	-SPI_WP
GPIO[27]	I/O	3VDUAL	EL_STATE0	(NA)	(NA)
GPIO[28]	I/O	3VDUAL	EL_STATE1	(NA)	(NA)
GPIO[29]	I/O	3VDUAL	OC5#	(P.U VCC 分壓)	OC5#
GPIO[30]	I/O	3VDUAL	OC6#	(P.U VCC 分壓)	OC6#
GPIO[31]	I/O	3VDUAL	OC7#	(P.U VCC 分壓)	OC7#
GPIO[32]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[33]	I/O	VCC3	GPO	(NA)	DUAL_BIOS
GPIO[34]	I/O	VCC3	GPO	(P.U VCC3)	FWP-
GPIO[35]	I/O	VCC3	SATACLKREQ#	(NA)	(NA)
GPIO[36]	I	VCC3	SATA2GP	(P.U VCC3)	SATA2GP
GPIO[37]	I	VCC3	SATA3GP	(P.U VCC3)	SATA3GP

GPO PIN

<i>Pin Name</i>	<i>Pin Number</i>	<i>Power Well</i>	<i>Pin Type</i>		<i>GPIO Application</i>
<i>GPIO[38]</i>	<i>I/O</i>	<i>VCC3</i>	<i>GPI</i>	<i>(NA)</i>	<i>(NA)</i>
<i>GPIO[39]</i>	<i>I/O</i>	<i>VCC3</i>	<i>GPI</i>	<i>(NA)</i>	<i>(NA)</i>
<i>GPIO[40:47]</i>		<i>NOT IMPLEMENTED</i>		<i>NOT IMPLEMENTED</i>	
<i>GPIO[48]</i>	<i>I/O</i>	<i>VCC3</i>	<i>-GNT[4]</i>	<i>(NA)</i>	<i>-GNT[4]</i>
<i>GPIO[49]</i>	<i>I/O</i>	<i>VTT_GMCH</i>	<i>CPUPWRGD</i>	<i>(P.U VTT_OL)</i>	<i>CPUPWROK</i>
<i>PCI1</i>	<i>PCLK0</i>	<i>-PCIRST</i>	<i>-REQ0/-GNT0</i>	<i>-PIRQE</i>	<i>A_D16</i>
<i>PCI2</i>	<i>PCLK1</i>	<i>-PCIRST</i>	<i>-REQ1/-GNT1</i>	<i>-PIRQD</i>	<i>A_D17</i>
<i>PCI3</i>	<i>PCLK2</i>	<i>-PCIRST</i>	<i>-REQ2/-GNT2</i>	<i>-PIRQC</i>	<i>A_D18</i>
<i>1394b</i>	<i>1394CLK</i>	<i>-PFMRST2</i>	<i>-REQ3/-GNT3</i>	<i>-PIRQH</i>	<i>A_D23</i>
<i>IT8212</i>	<i>RAIDCLK</i>	<i>-PFMRST2</i>	<i>-REQ4/-GNT4</i>	<i>-PIRQG</i>	<i>A_D22</i>

ICH6 GPIO Table:

NAME	PWR LANE	USAGE	NAME	PWR LANE	USAGE
GPI0	V5REF	M/B ID (-REQ6)	GPI41	VCC3	M/B ID
GPI1	V5REF	-REQ5	GP048	VCC3	-GNT4
GPI2	V5REF	-PIRQE	GP049	V-CPUIO	CPUPWOK
GPI3	V5REF	-PIRQF			
GPI4	V5REF	-PIRQG			
GPI5	V5REF	-PIRQH			
GPI6	VCC3	-SLP BTN			
GPI7	VCC3	DUAL BIOS			
GPI8	3VDAUL	-LANWAKE			
GPI9	3VDAUL	-USBOC4			
GPI10	3VDAUL	-USBOC5			
GPI11	3VDAUL	-SMBALT			
GPI12	VCC3	ATX_DET			
GPI13	3VDAUL	-LPCPME			
GPI14	3VDAUL	-USBOC6			
GPI15	3VDAUL	-USBOC7			
GP016	VCC3	CPU OV1 (-GNT6)			
GP017	VCC3	-GNT5			
GP018	VCC3	CPU OV2			
GP019	VCC3	DUAL BIOS			
GP020	VCC3	BIOS T-BLOCK			
GP021	VCC3	DUAL BIOS			
GP023	VCC3	DDR OV0			
GPI024	3VDAUL	GREEN LED			
GPI025	3VDAUL	DDR OV1			
GPI26	VCC3	SATA GP0			
GPI027	3VDAUL	+PWRLED			
GPI028	3VDAUL	-PWRLED			
GPI29	VCC3	SATA GP1			
GPI30	VCC3	SATA GP2			
GPI31	VCC3	SATA GP3			
GPI032	VCC3	BIOS WP			
GPI033	VCC3	AZALIA DET			
GPI034	VCC3	PWRLED			
GPI40	V5REF	-REQ4			

PWR0K/RESET Table:

ITE8712BHX PIN	NET NAME	TARGET
PIN62/-PCIRST1	-PCIE_RST	1. PCI-E * 1 Slot1 2. PCI-E * 1 Slot2 3. PCI-E * 1 Slot3 4. PCI-E * 16 Slot
PIN64/-PCIRST2	-PFMRST2	1. Onboard PCI Lan 2. Onboard 1394 Chip 3. OnBoard FWH
PIN65/-PCIRST3	-PFMRST1	1. Onboard PCI-E Lan 2. Onboard SATA Chip 3. GMCH
PIN115/-PCIRST4	-PFMRST_ -IDERST	Reserved For IDE
PIN63/PWROK1	PWROK1	1. GMCH 2. ICH6 3. 5VDUAL SWITCH 4. DPS CONTROL
PIN109/PWROK2	-THERM	1. ICH6

GIGABYTE THCNOLOGIES , INC.

Title			GPIO/RESET TABLE	
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