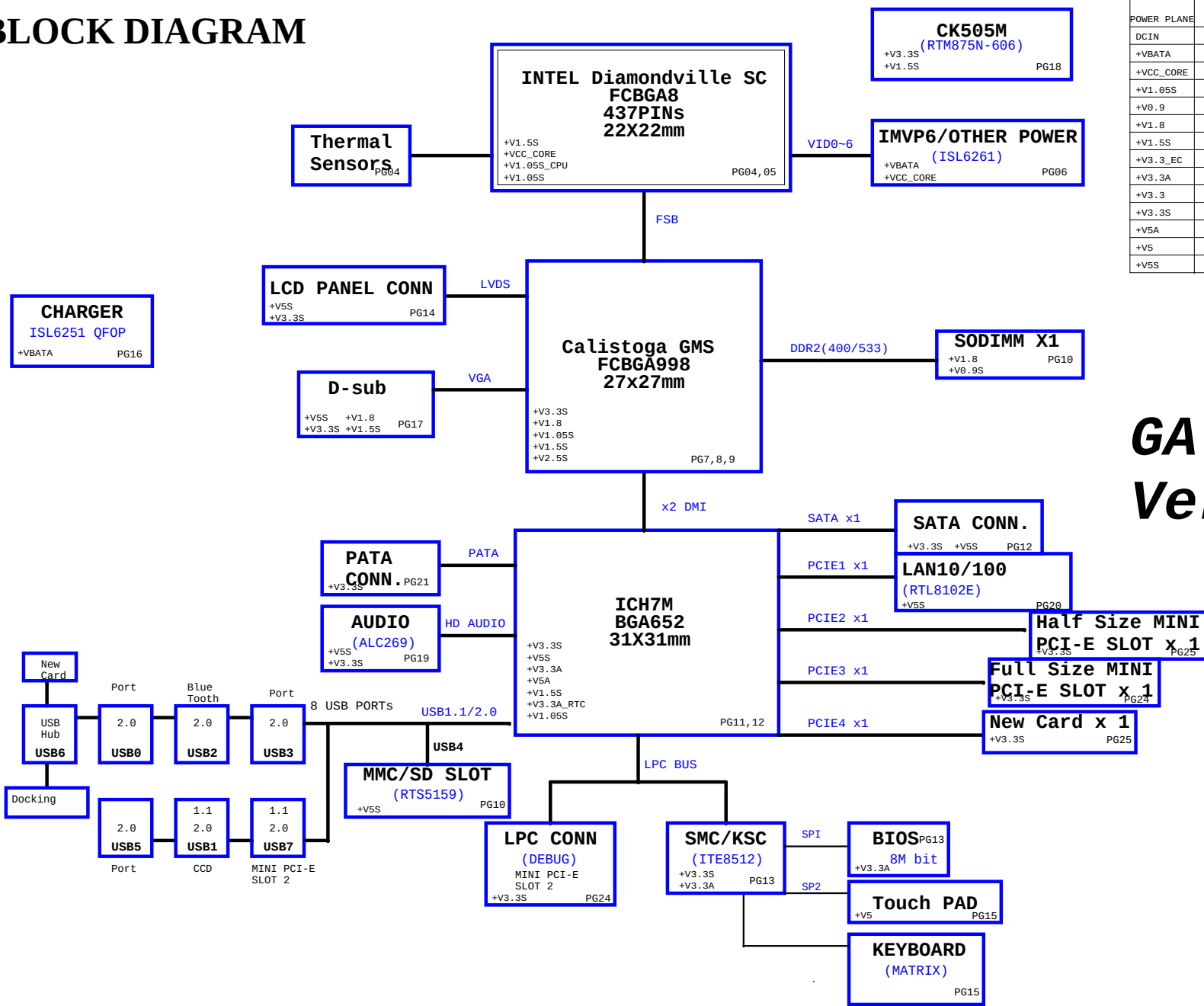


BLOCK DIAGRAM



POWER PLANE	Description	Voltage	S0	S3	S4	S5
DCIN	Adapter power supply	12V	✓	✓	✓	✓
+VBATA	Battery power	6-8.4V	✓	✓	✓	✓
+VCC_CORE	Core voltage for CPU	0.5-1.2V	✓	✗	✗	✗
+V1.05S	Chipset Power Rail	1.05V	✓	✗	✗	✗
+V0.9	0.9V for DDR2 SSTL Term and COMP	0.9V	✓	✗	✗	✗
+V1.8	DDR2 SM	1.8V	✓	✗	✗	✗
+V1.5S	Chipset Power Rail	1.5V	✓	✗	✗	✗
+V3.3_EC	FOR EC	3.3V	✓	✓	✓	✓
+V3.3A	Peripheral Circuit	3.3V	✓	✓	✗	✗
+V3.3	Peripheral Circuit	3.3V	✓	✗	✗	✗
+V3.3S	Peripheral Circuit	3.3V	✓	✗	✗	✗
+V5A	Peripheral Circuit	5V	✓	✗	✗	✗
+V5	Peripheral Circuit	5V	✓	✗	✗	✗
+V5S	Peripheral Circuit	5V	✓	✗	✗	✗

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6	5	4	3	2	1
D					
C					
B					
A					

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Power Sequencing Timing			
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D

E

C

c

B

E

A

A



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Title

BOM & PCB MODIFY HISTORY

Size

Document Number

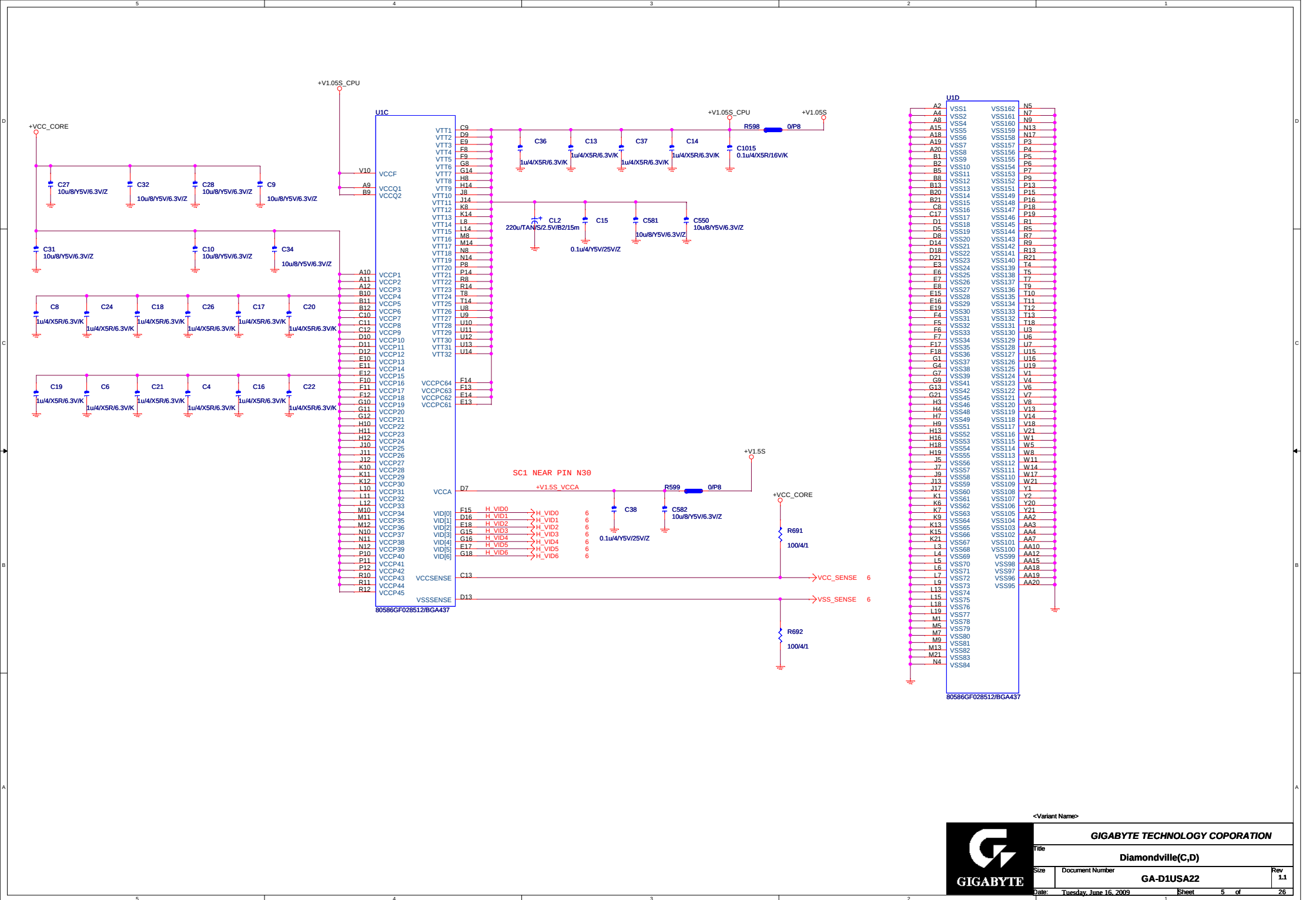
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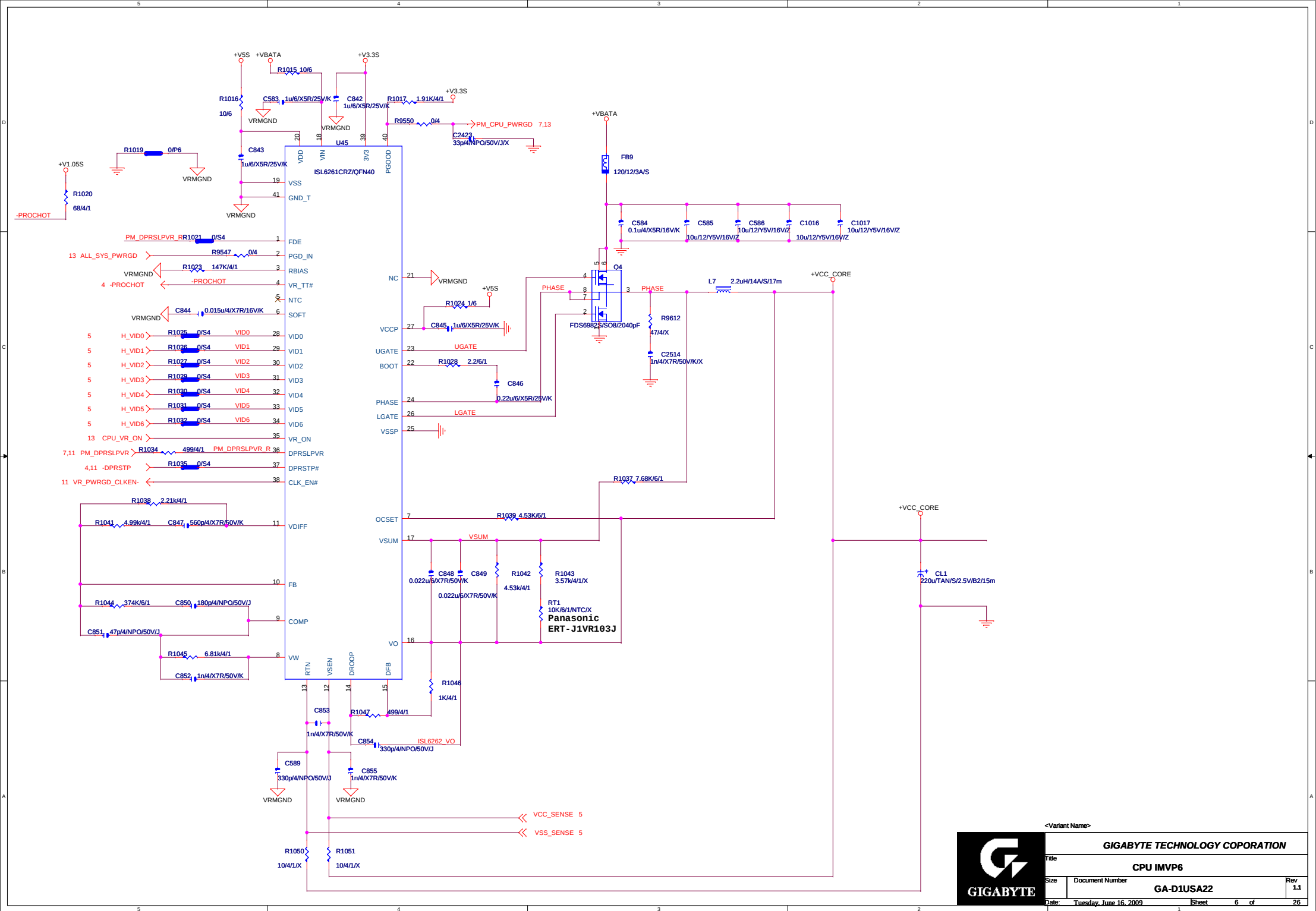
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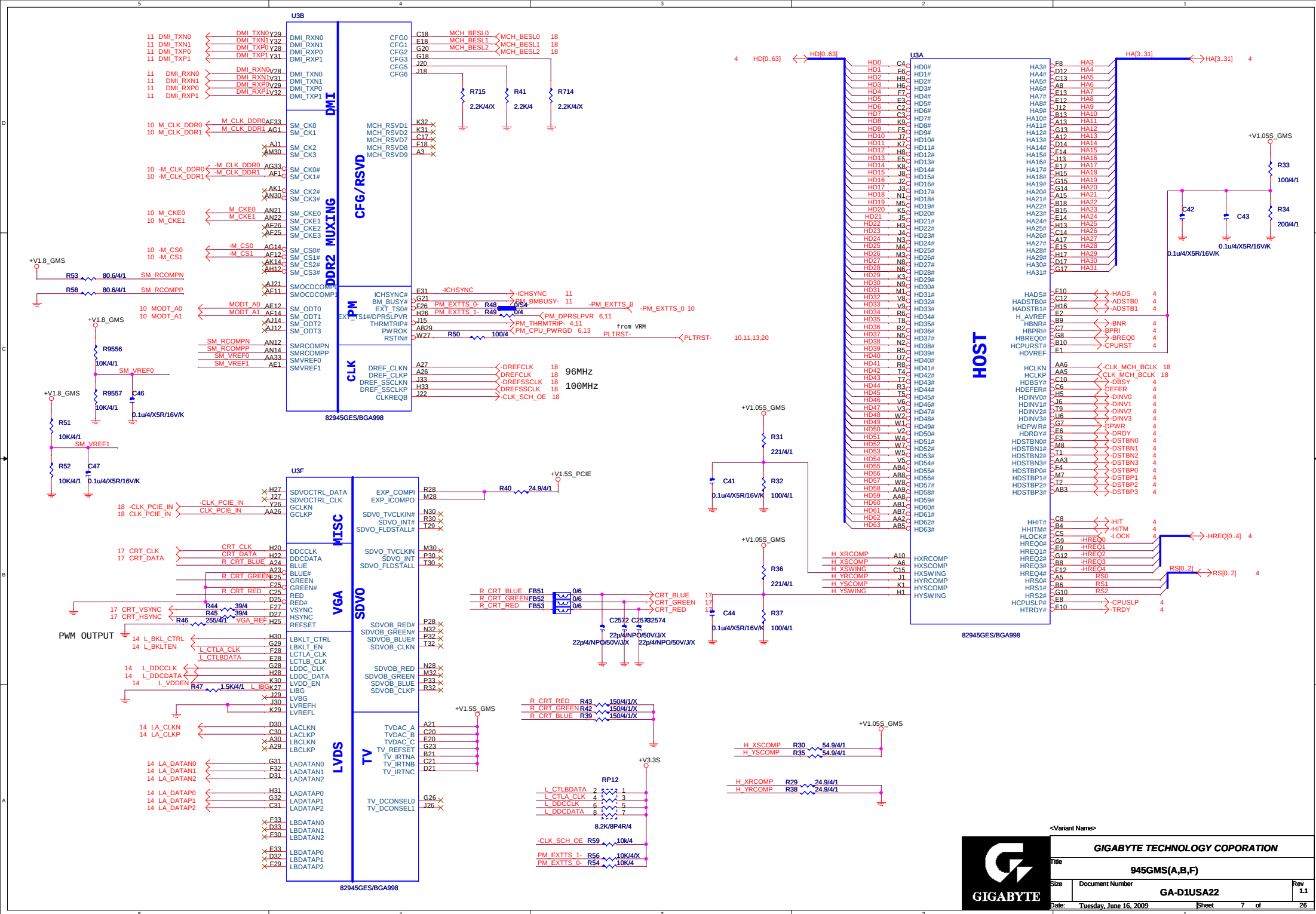
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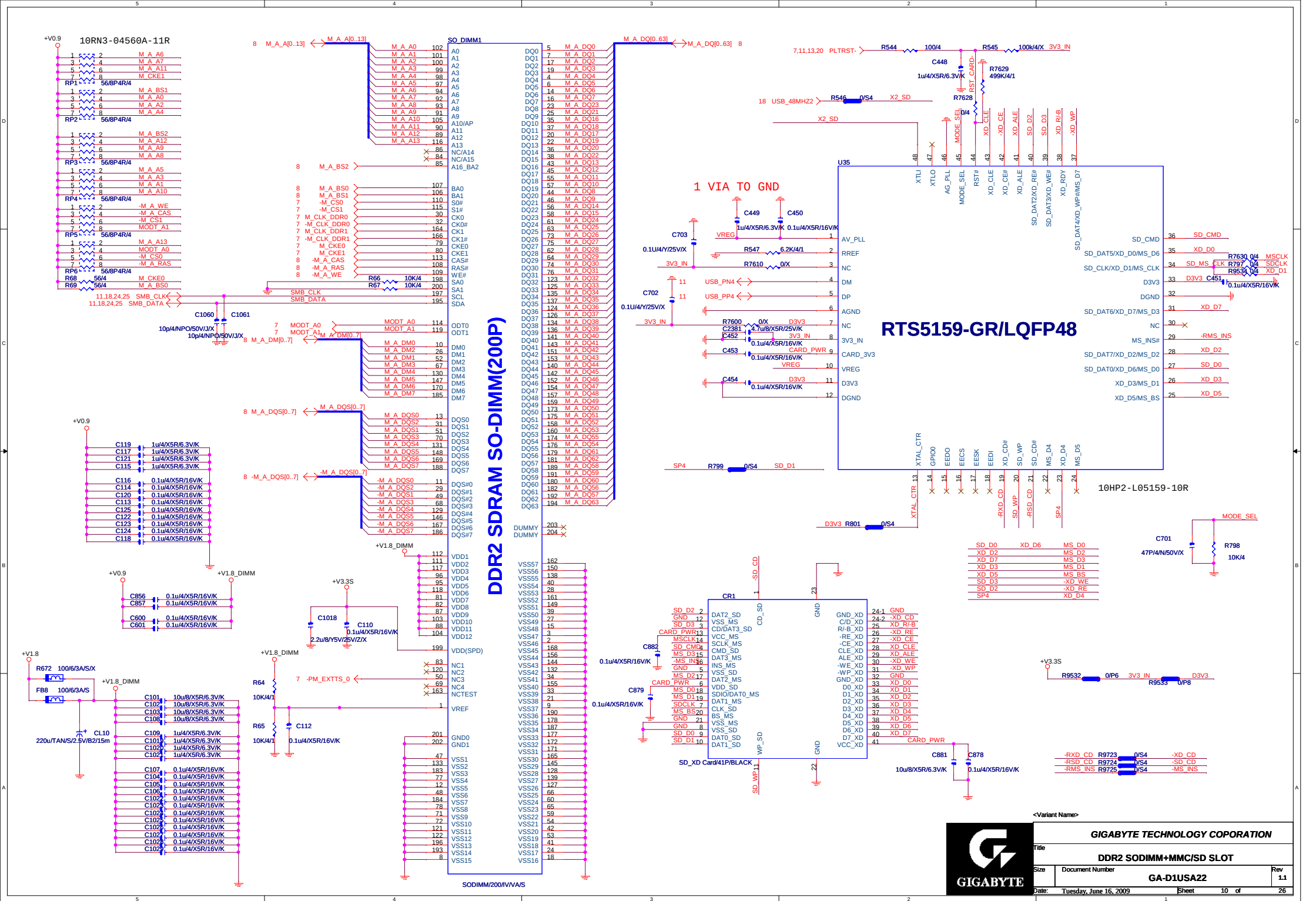
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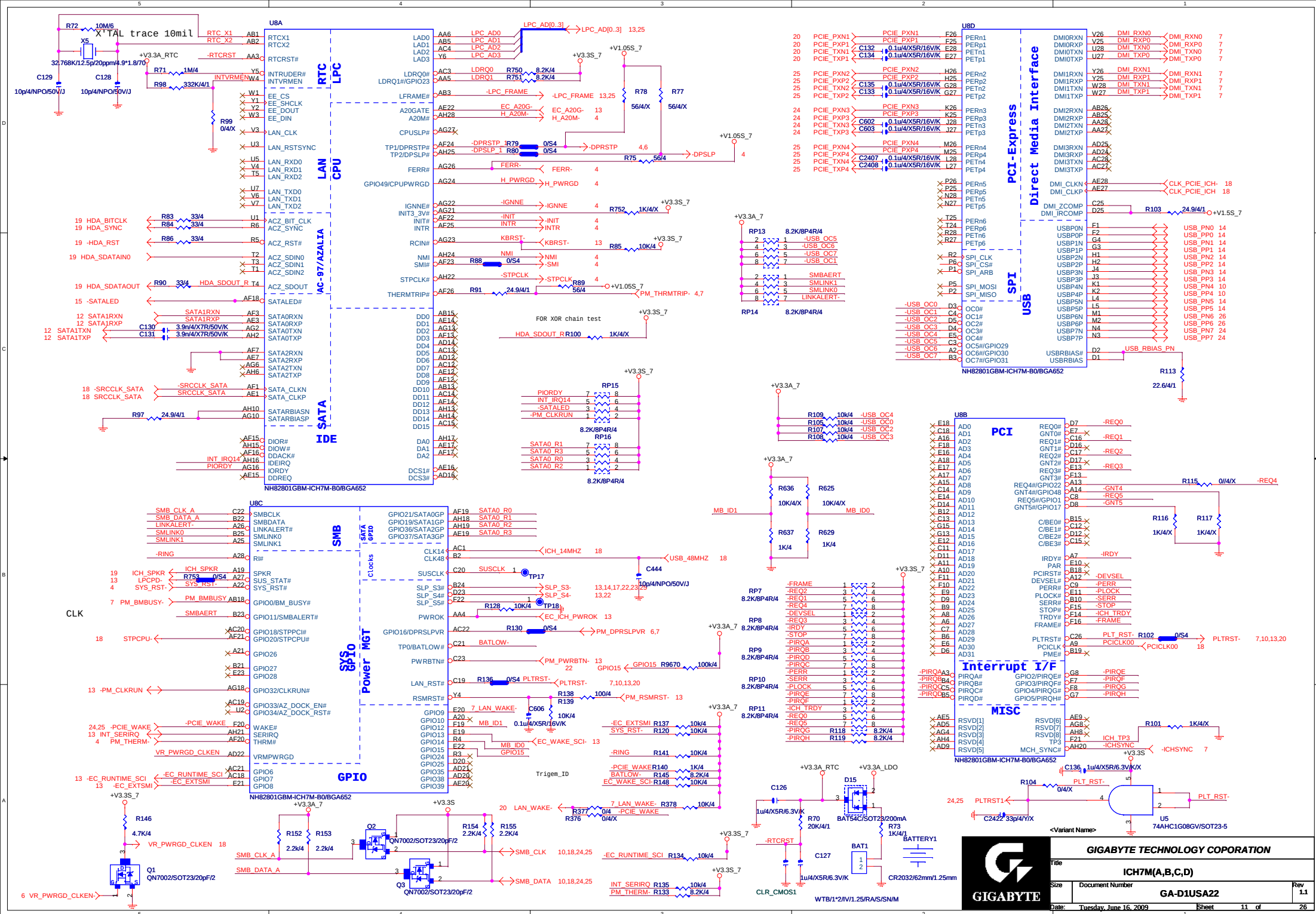
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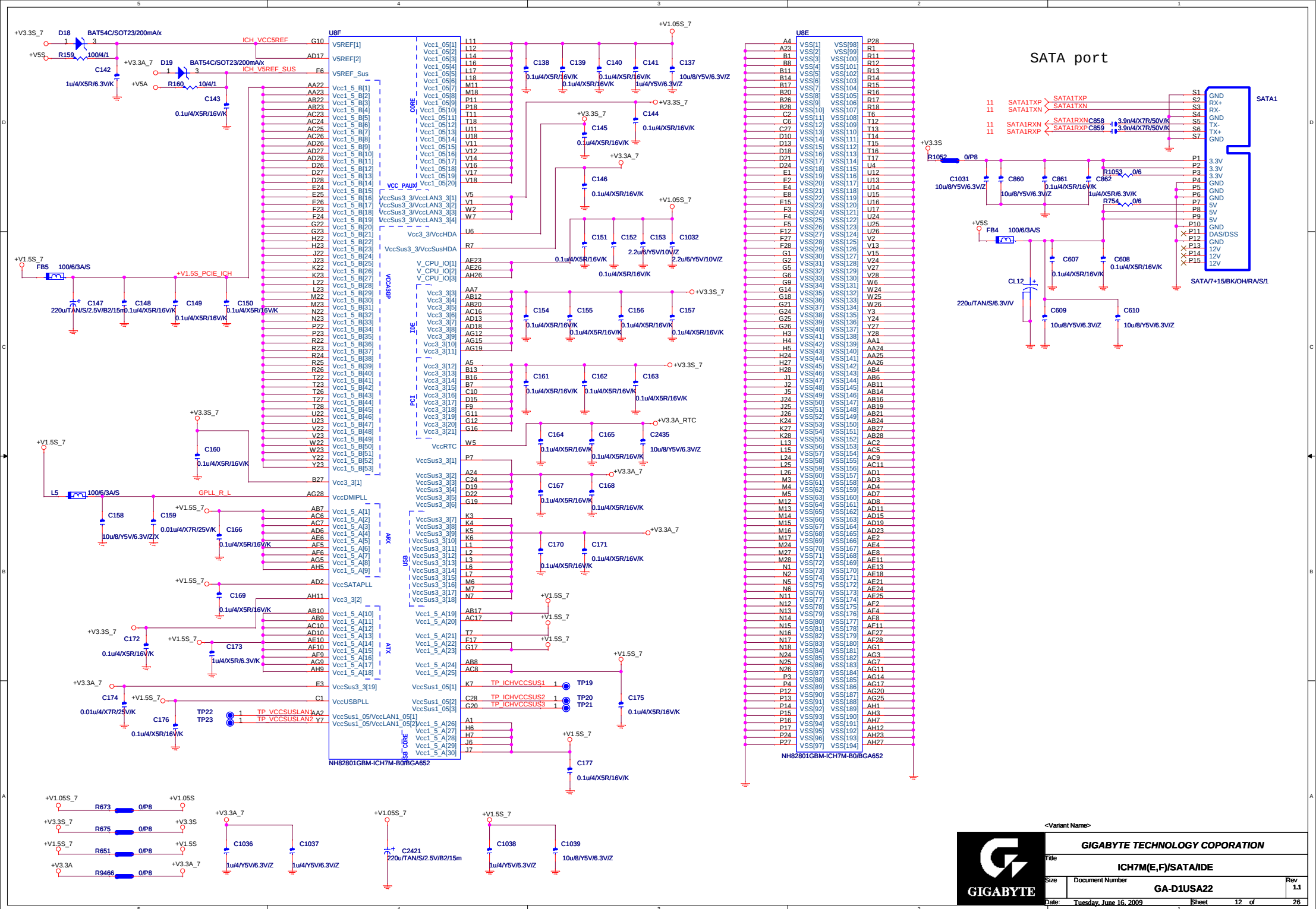


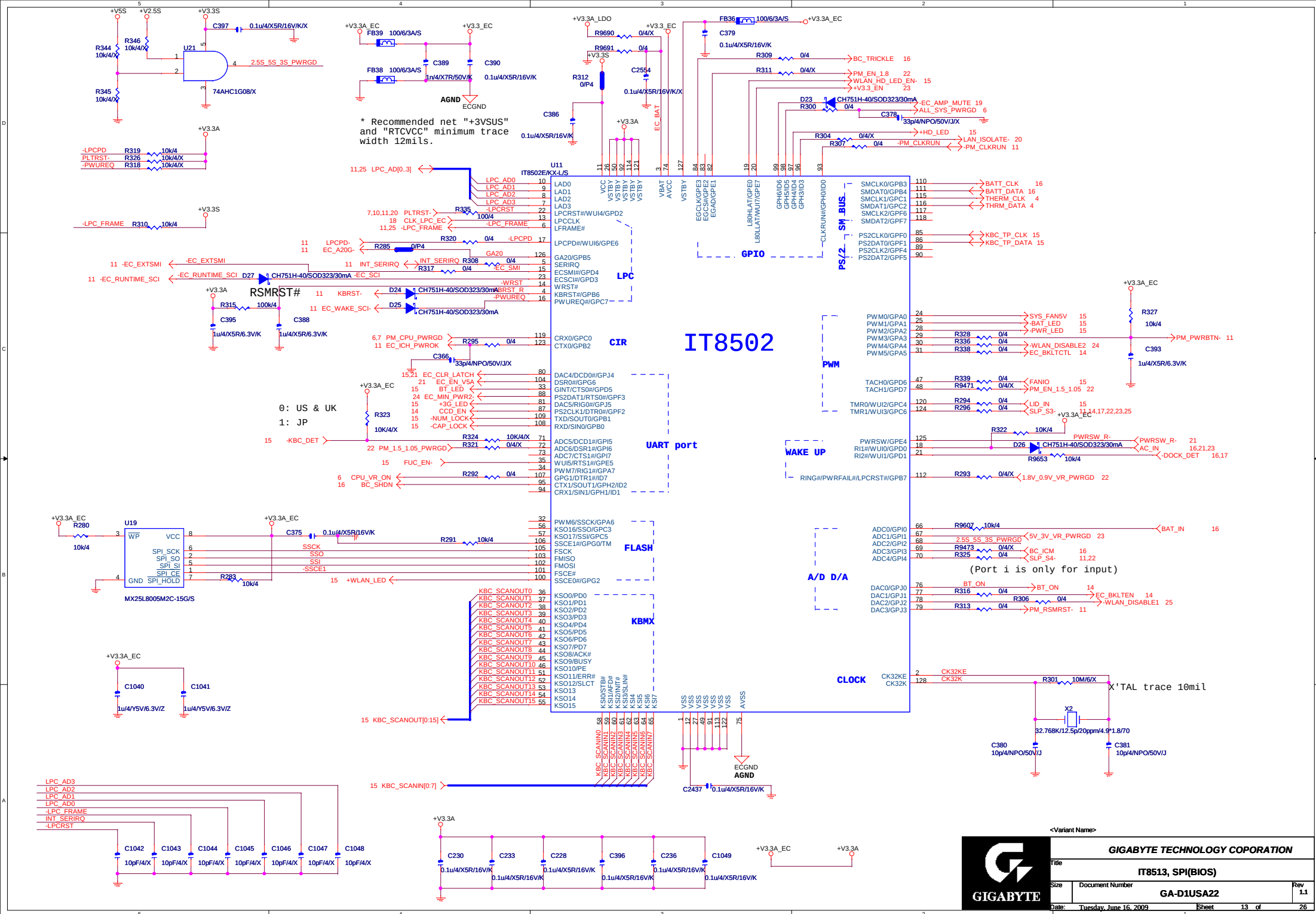


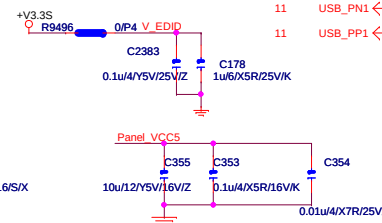
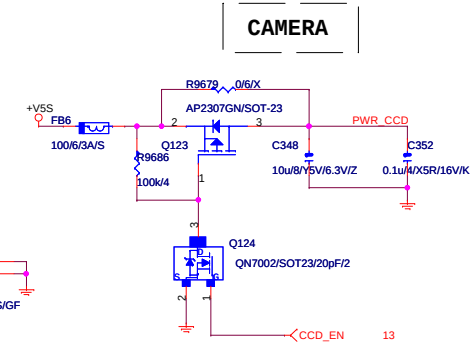
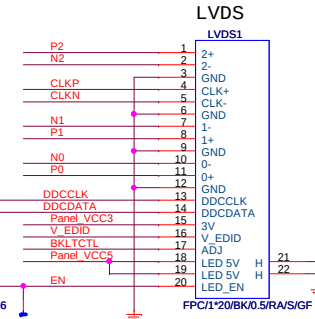
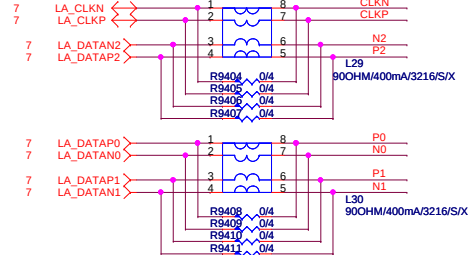
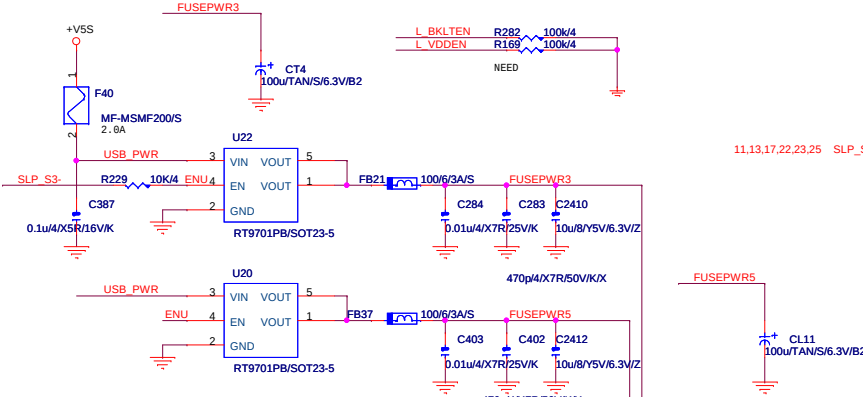
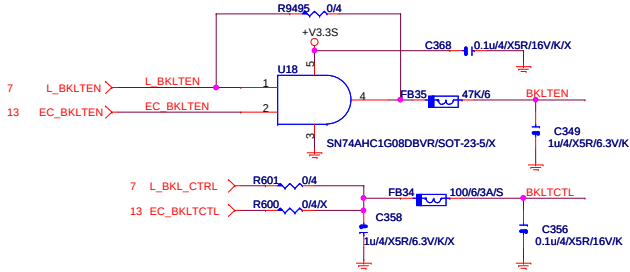
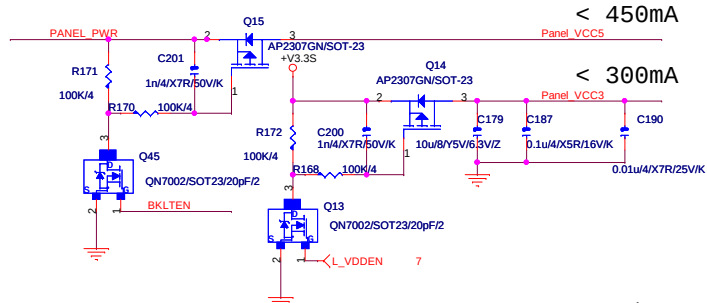
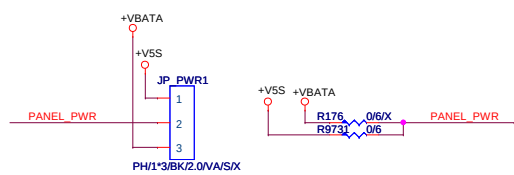




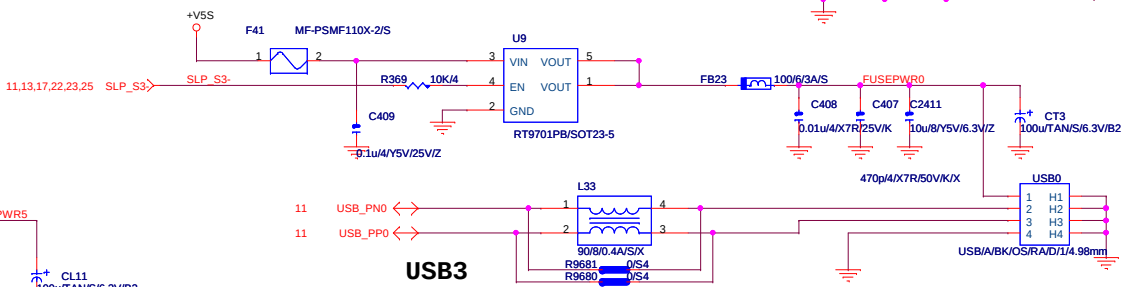
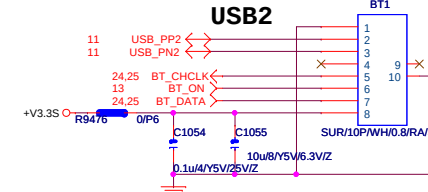




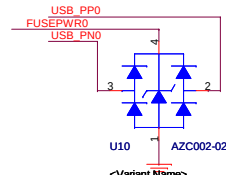
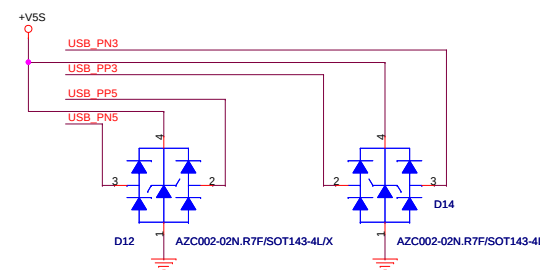




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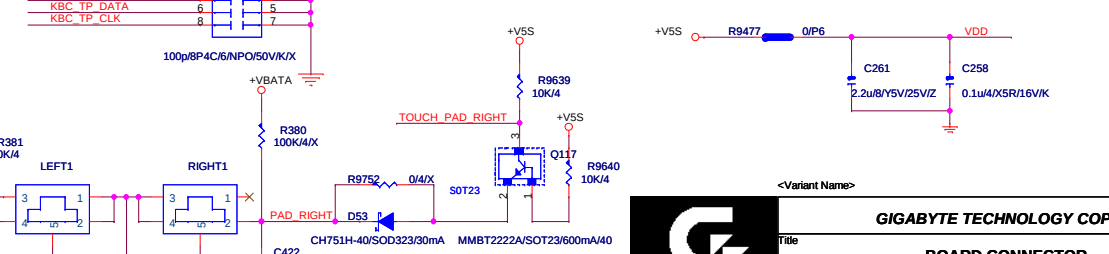
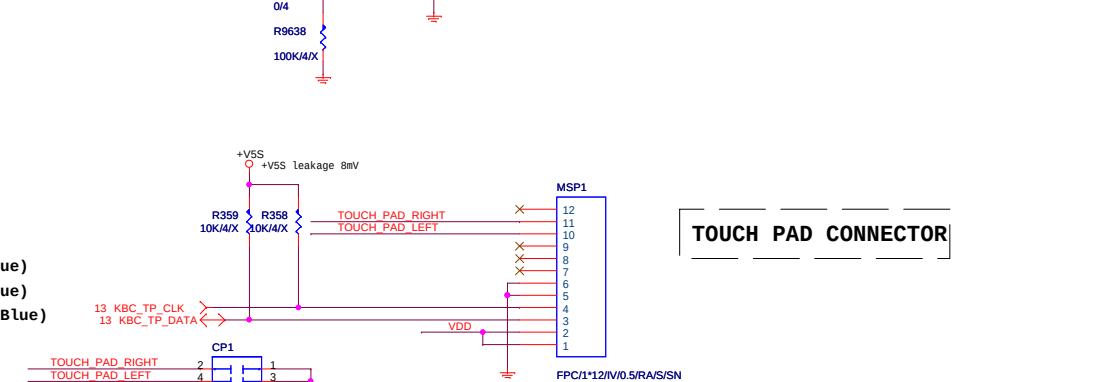
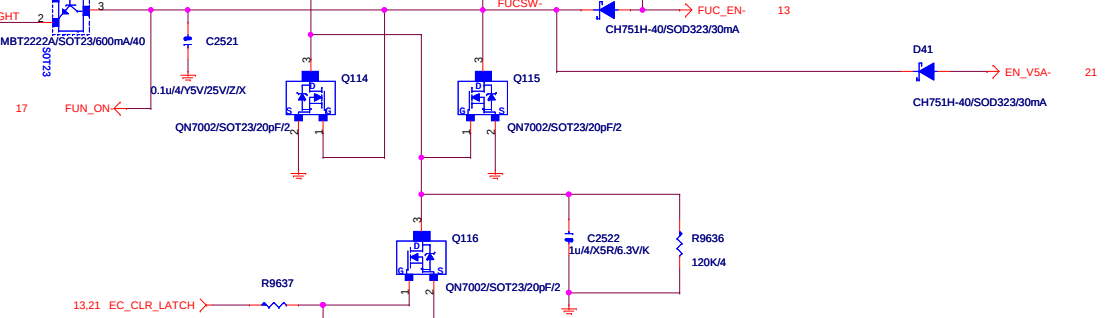
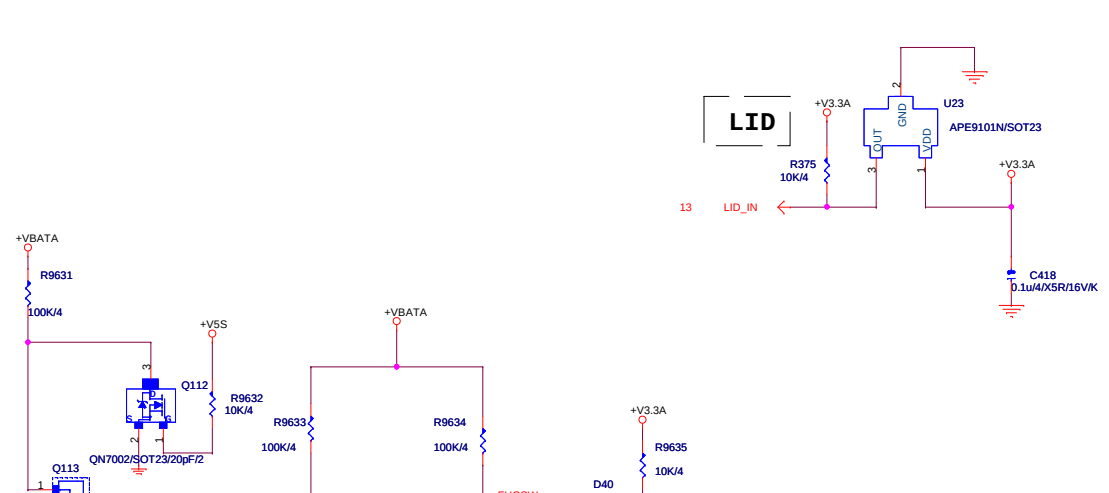
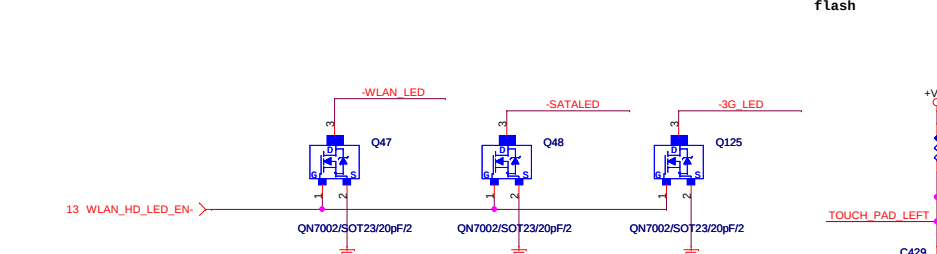
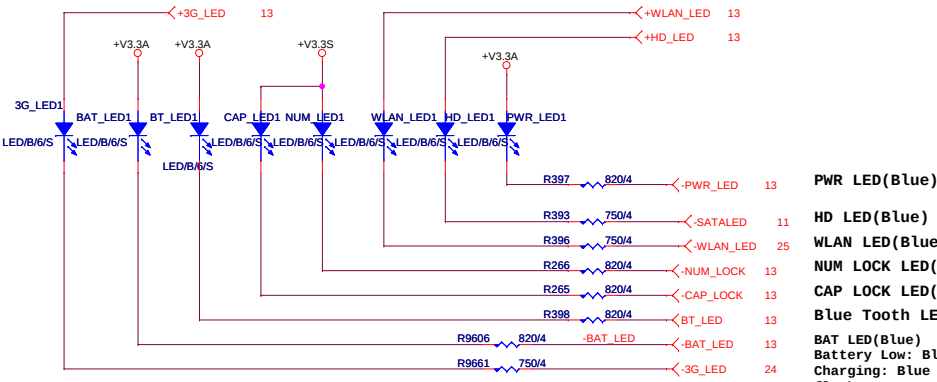
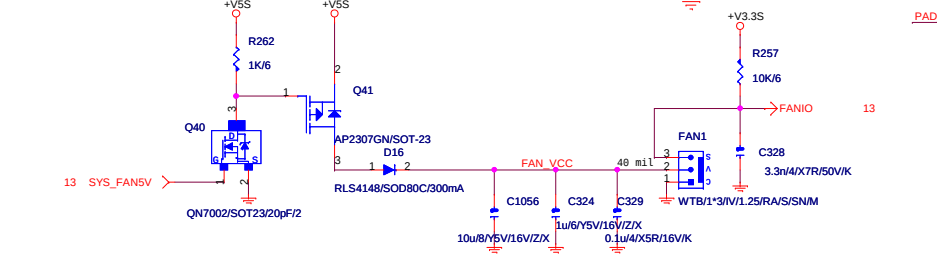
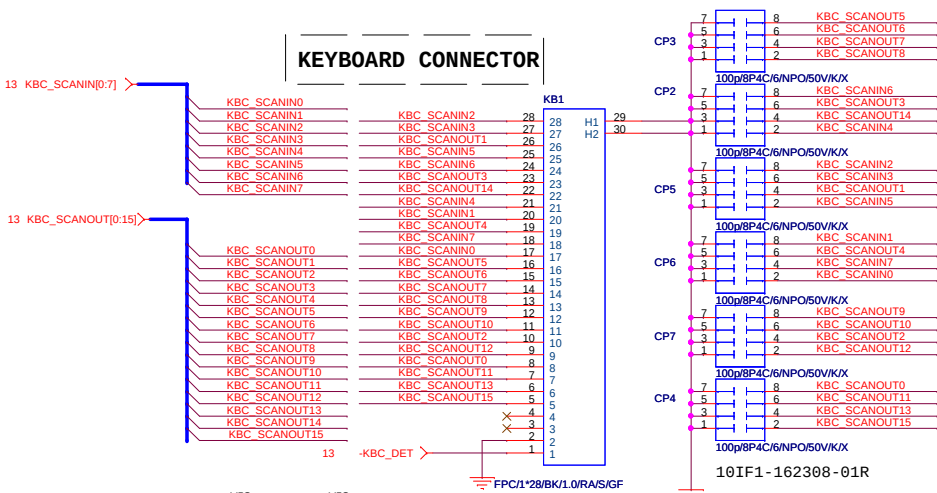


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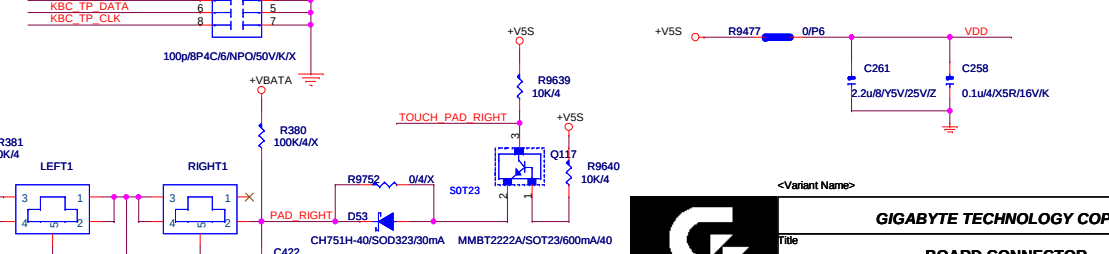
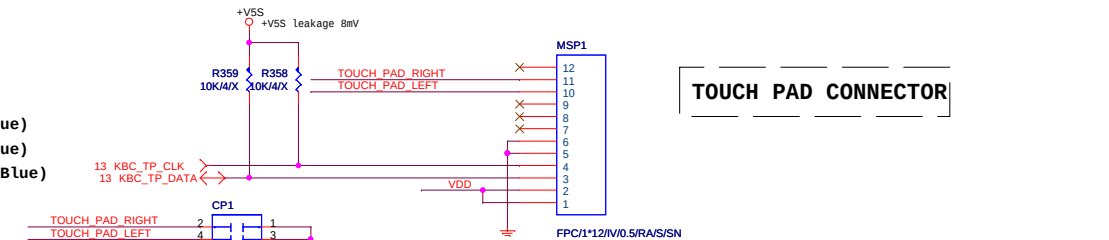


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LCD Panel, Touch screen, USB CONN			
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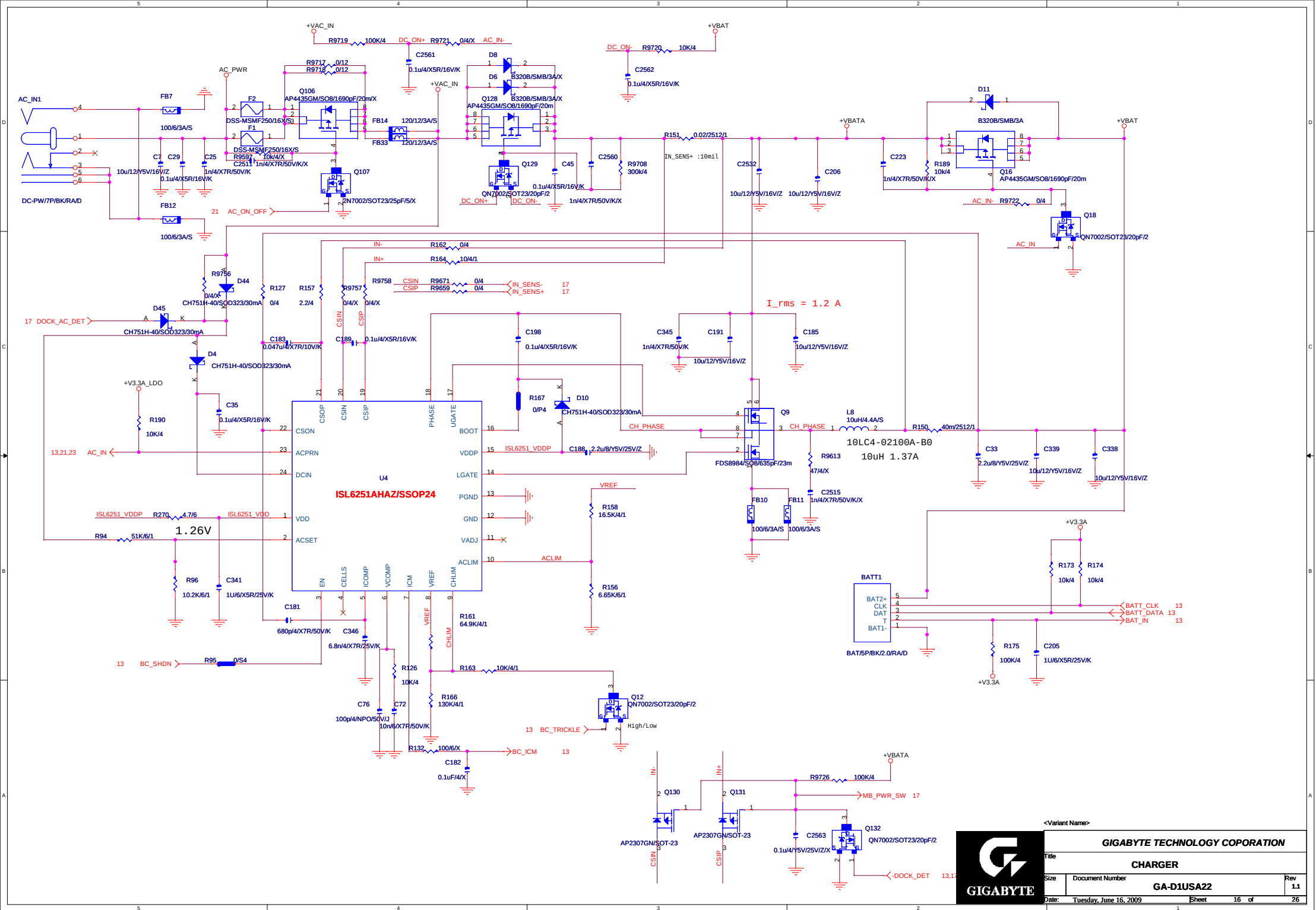
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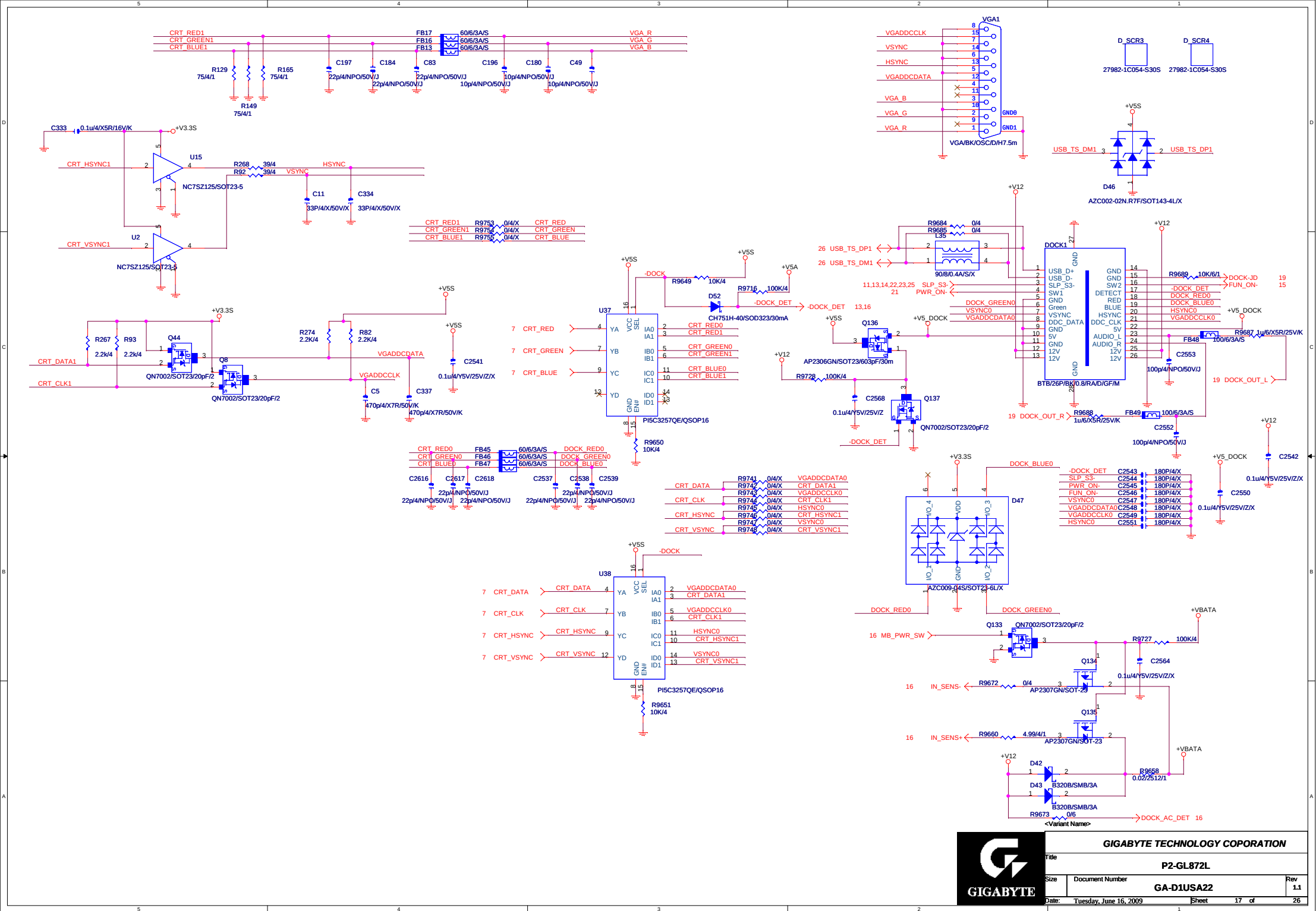


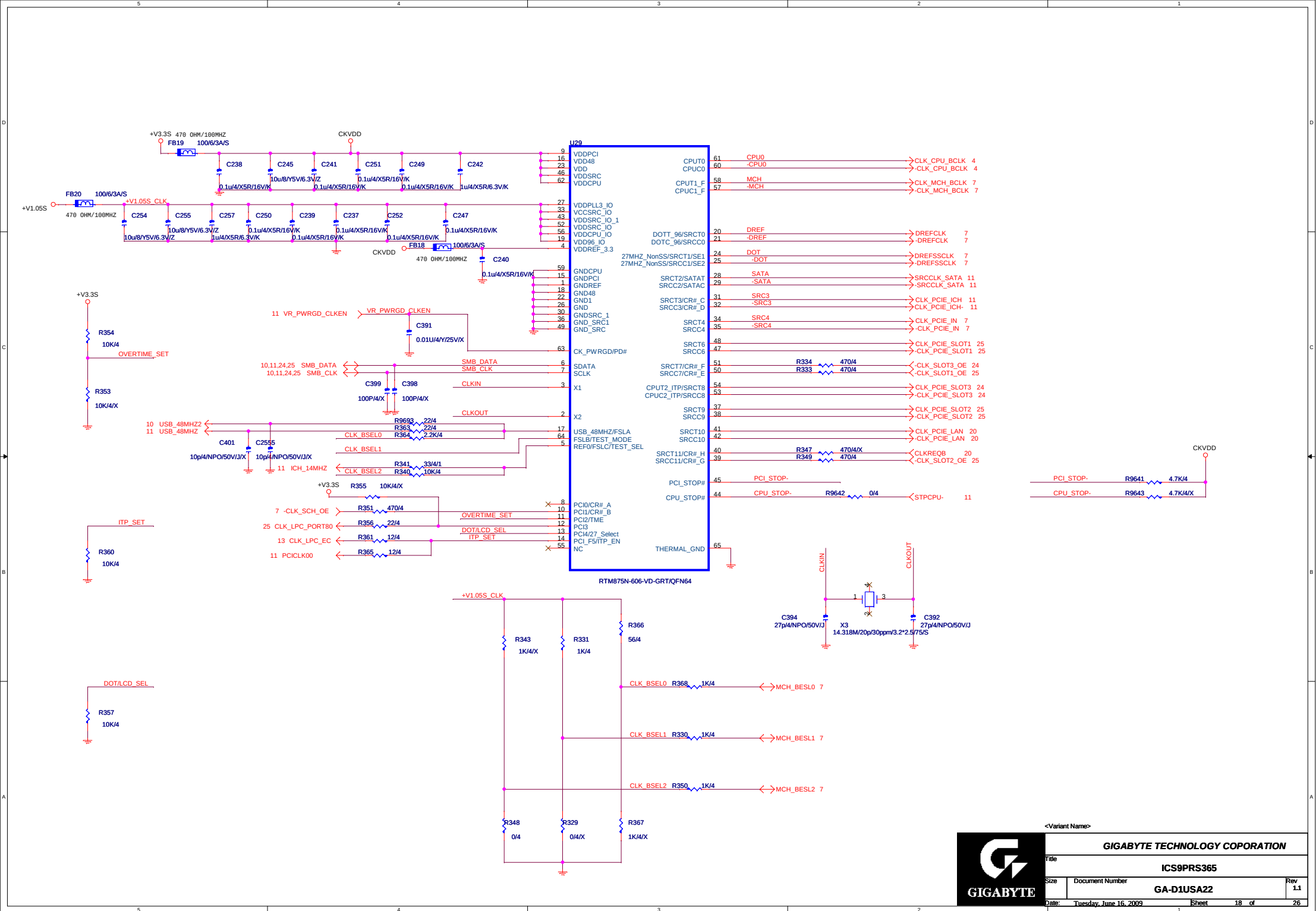
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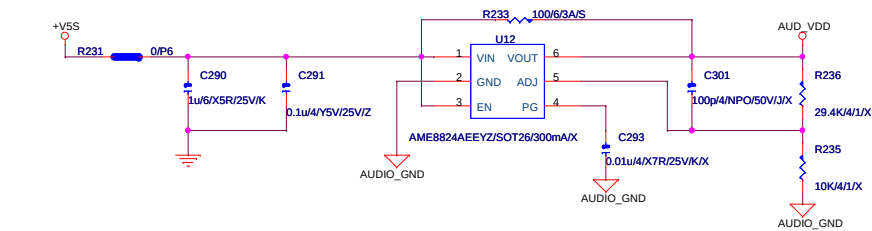


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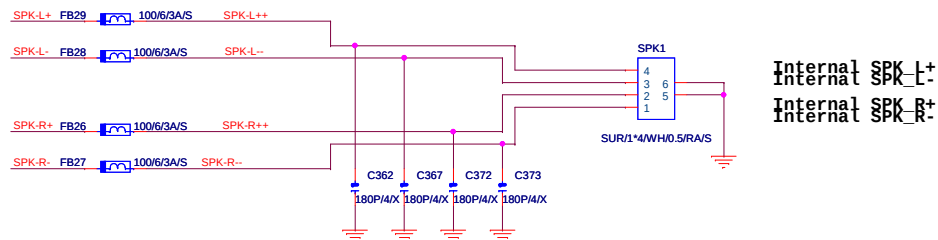




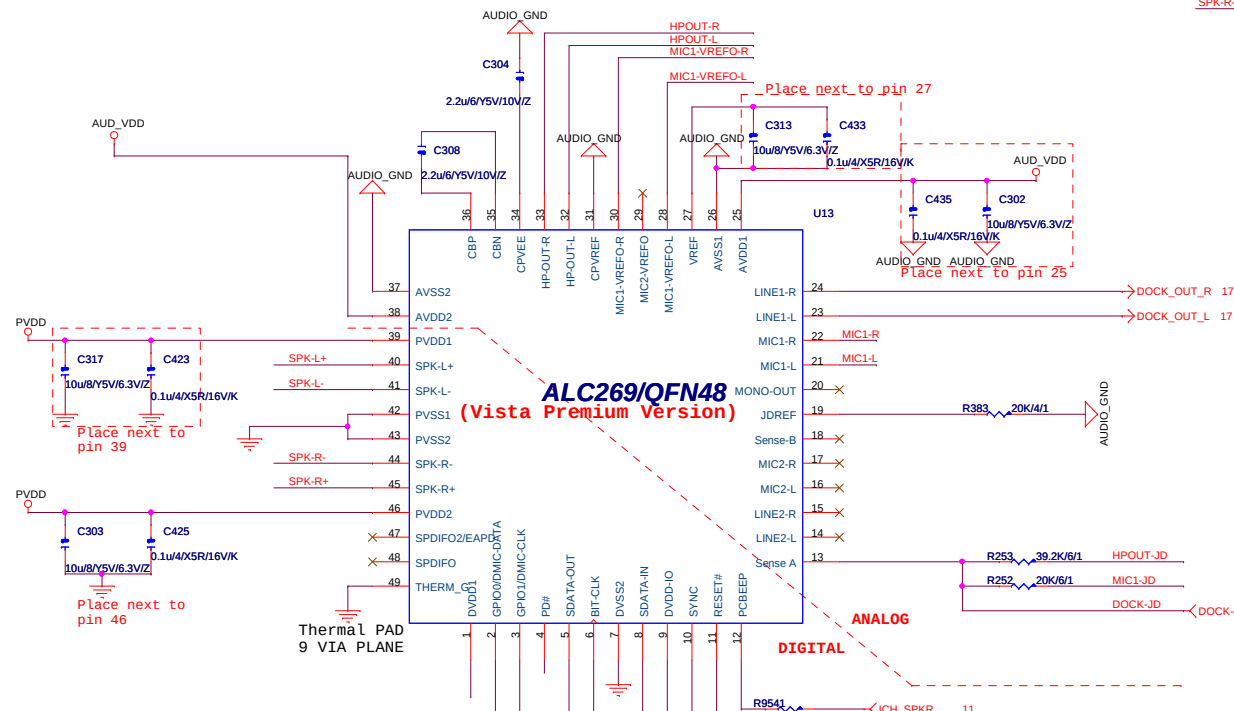




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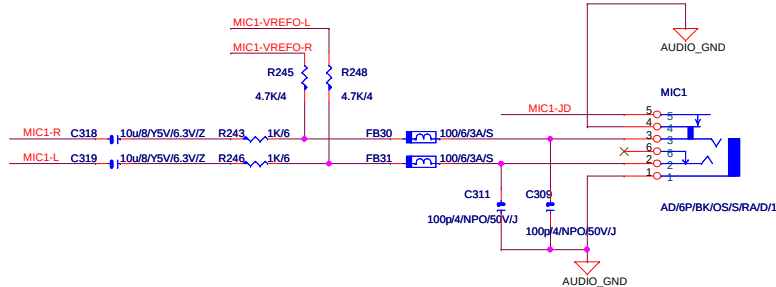
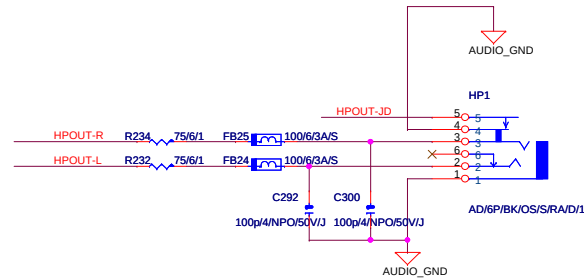
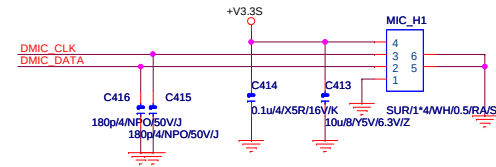


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Internal SPK-L-
Internal SPK-R+
Internal SPK-R-



ALC269/QFN48
(Vista Premium Version)

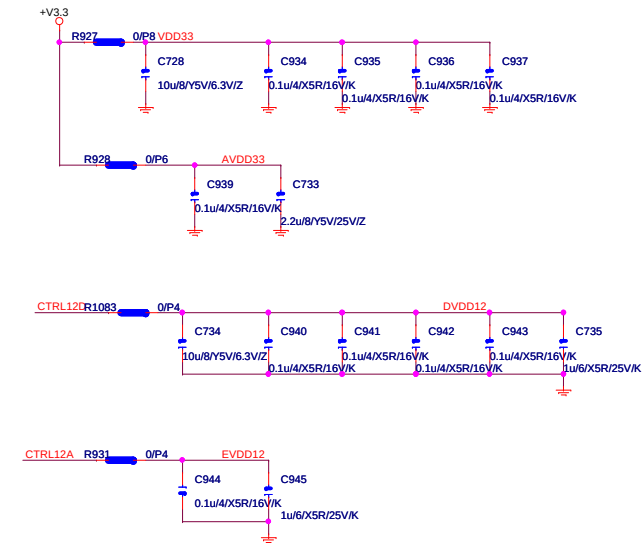
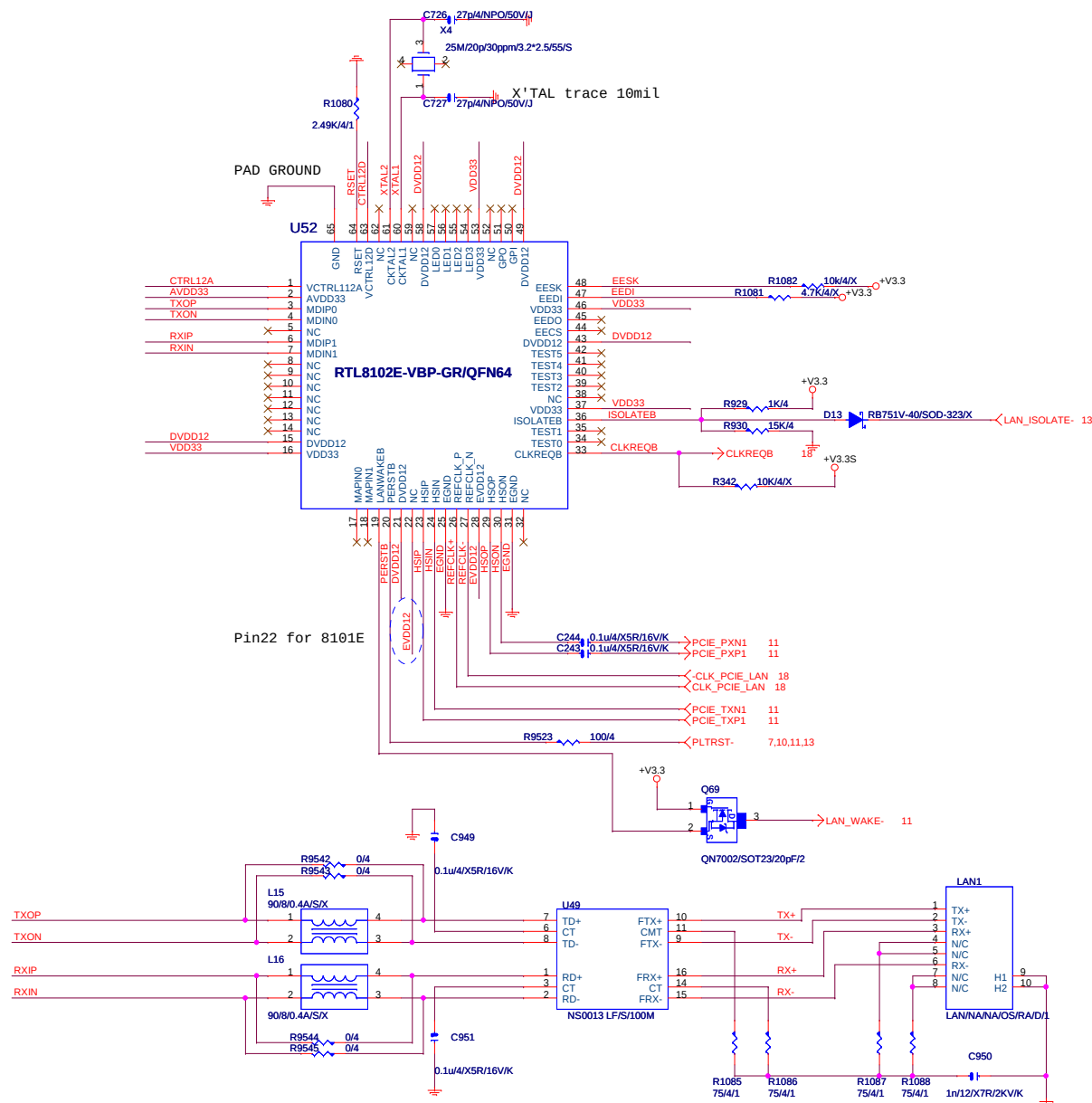
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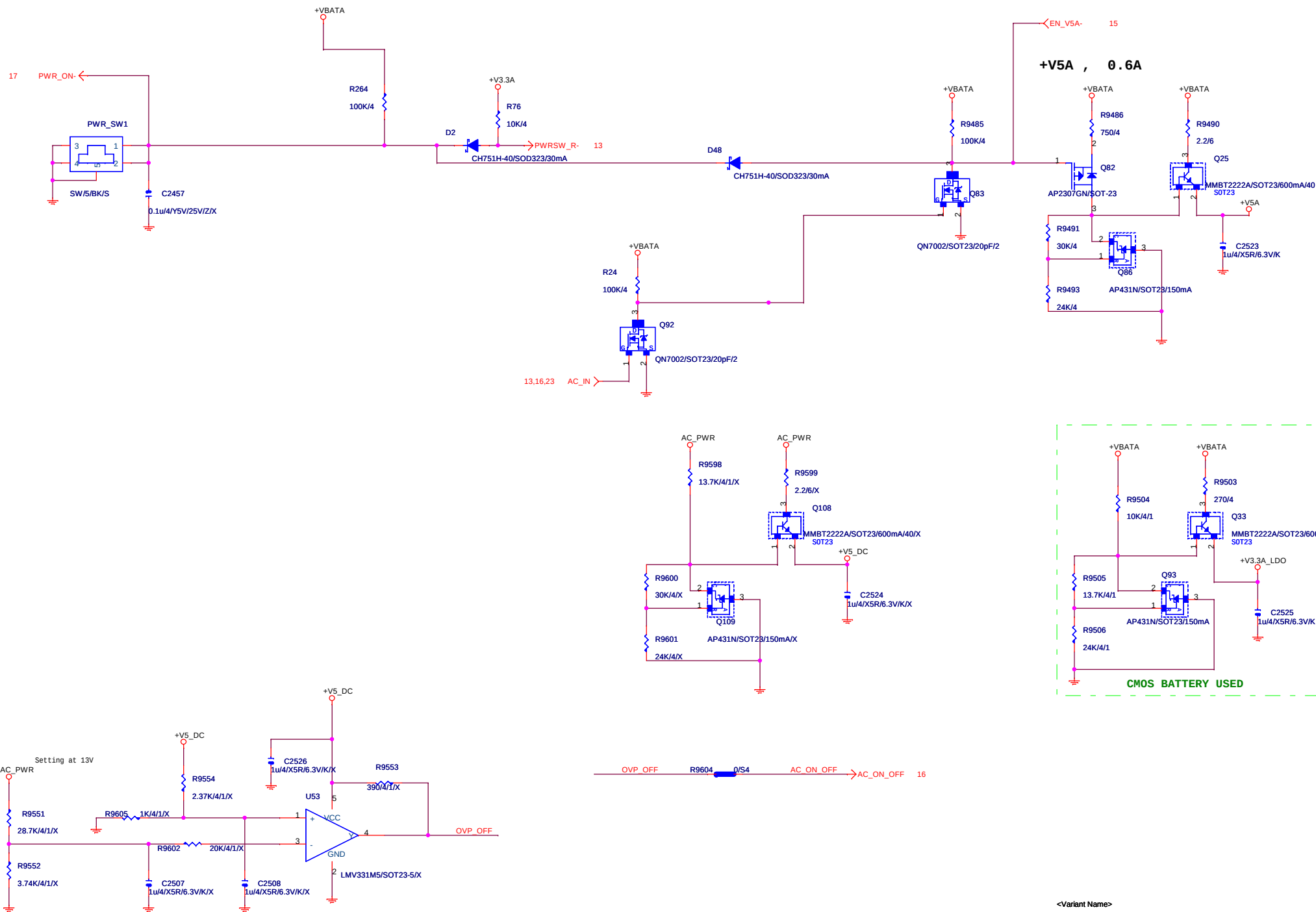


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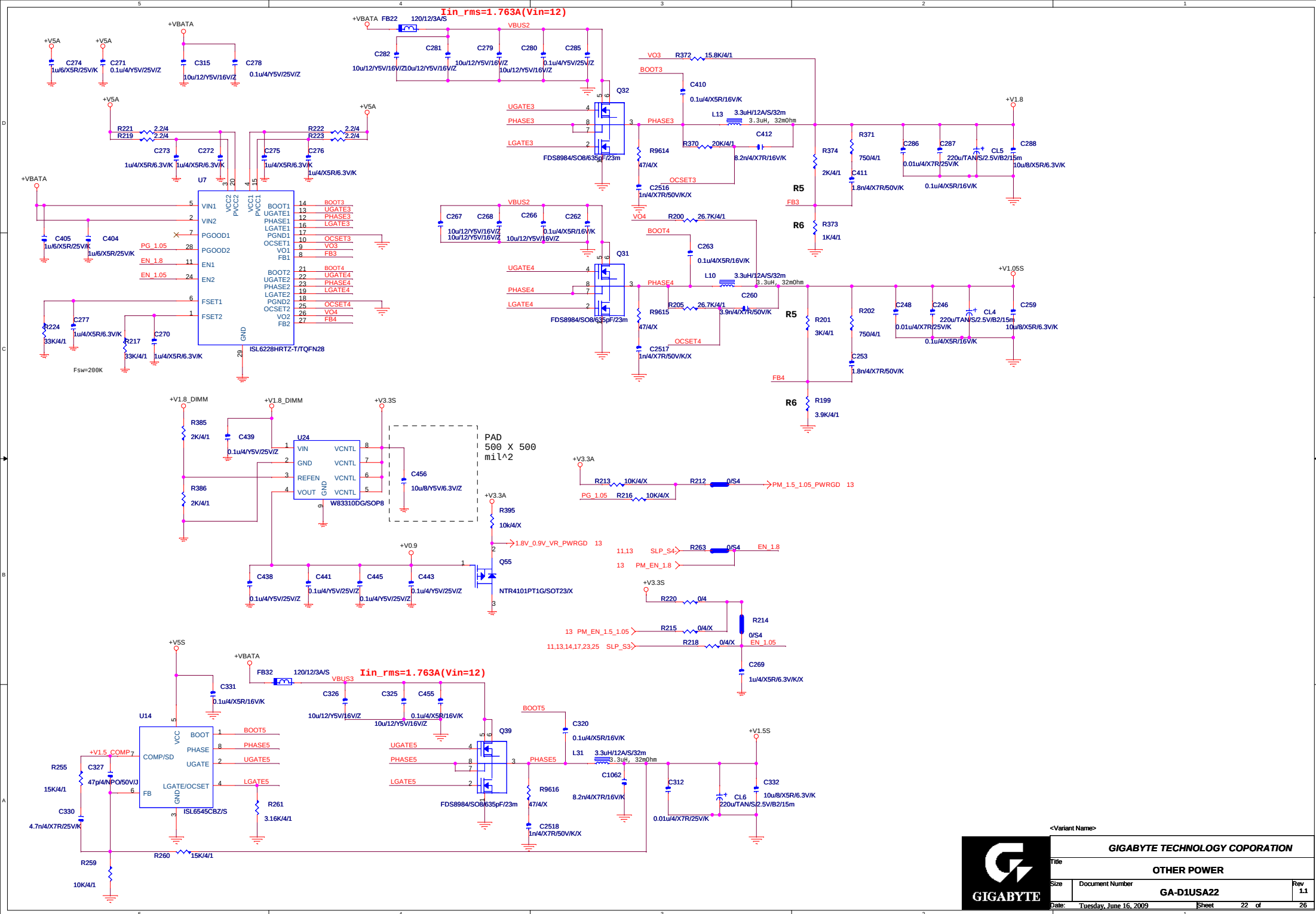
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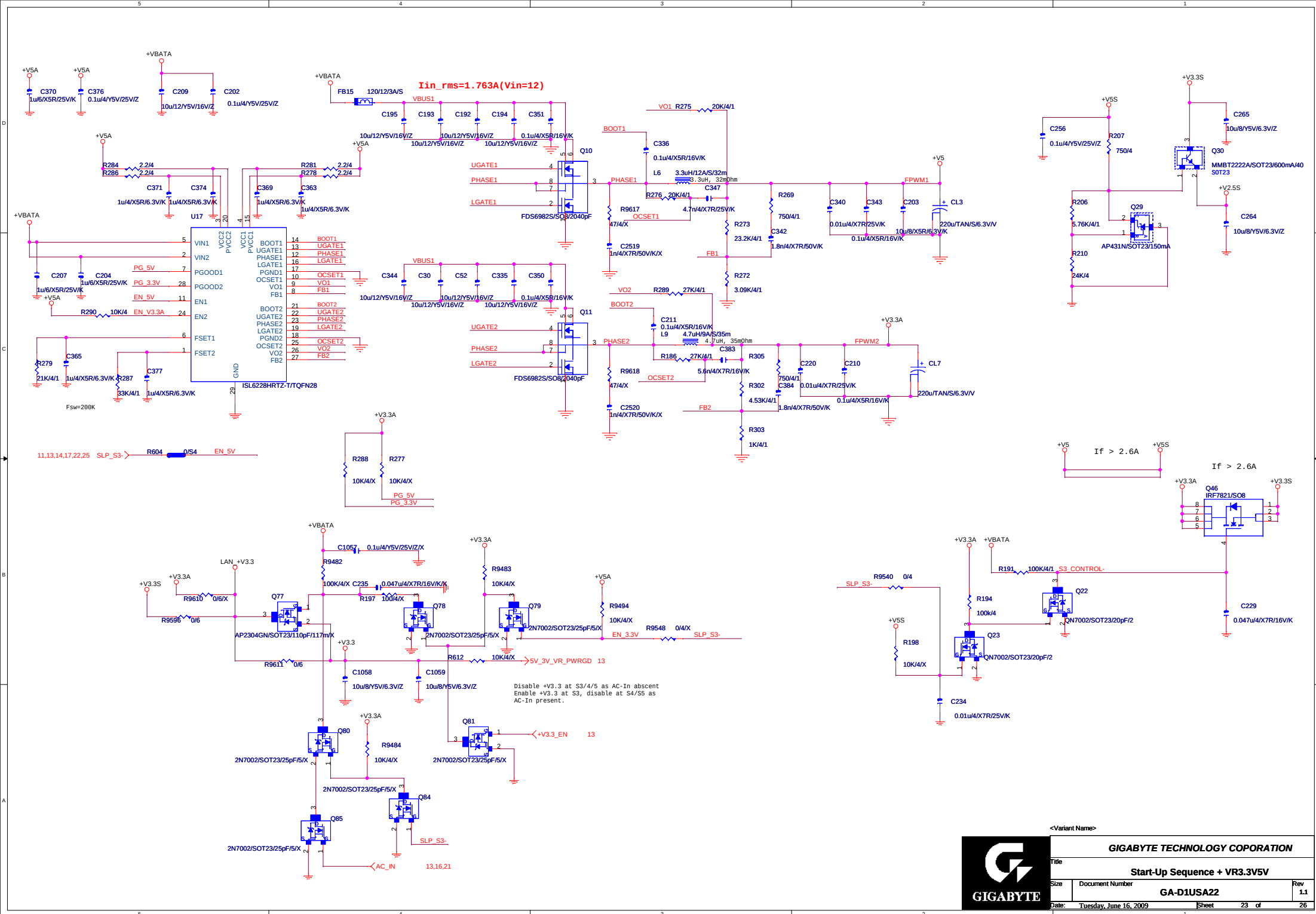


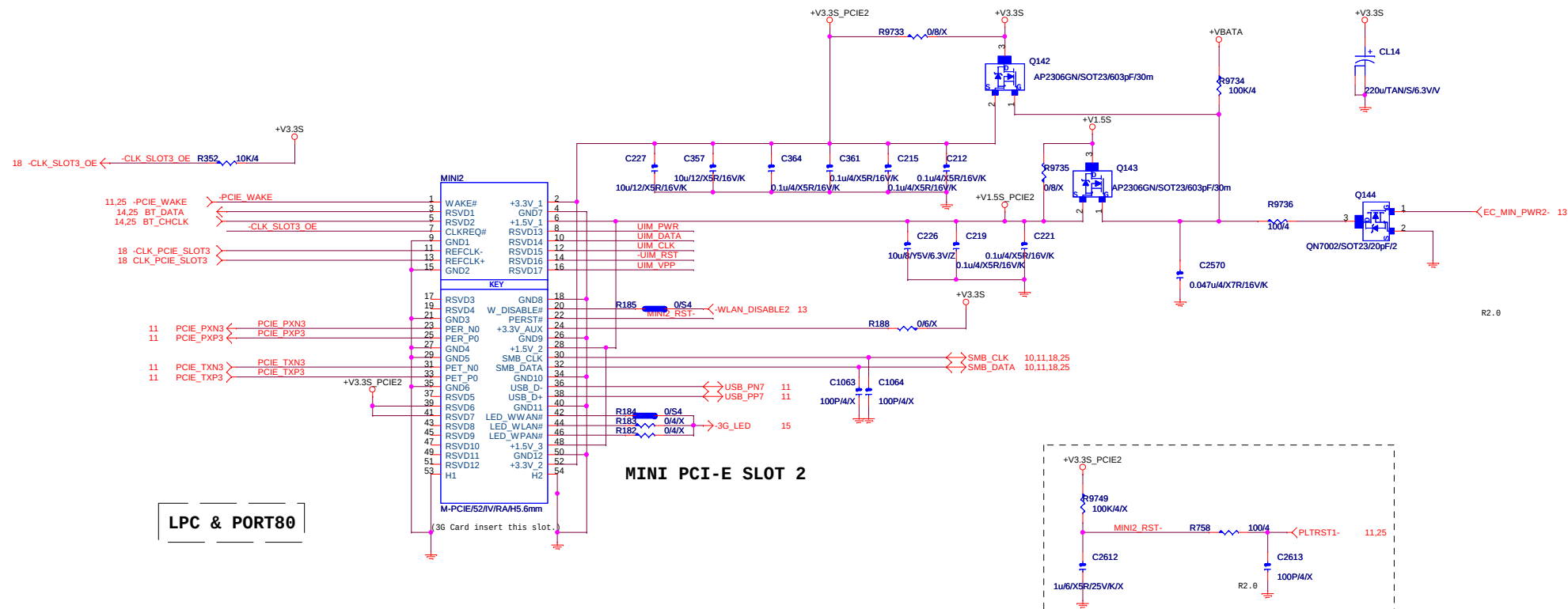
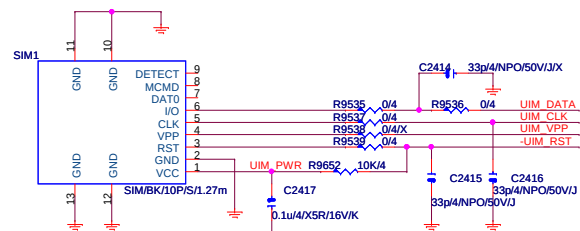


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NEW CARD

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