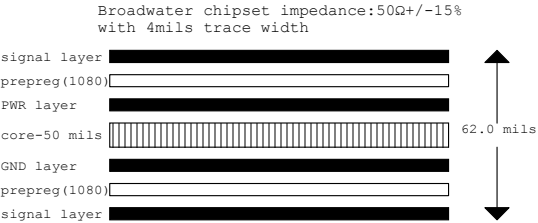


01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	TABLE LIST
05	LGA775_A
06	LGA775_B/D
07	LGA775_C
08	LGA775_E/F/G/H
09	GMCH-BROADWATER_HOST
10	GMCH-BROADWATER_DDRII
11	GMCH-BROADWATER_PCI E / DMI
12	GMCH-BROADWATER_VGA
13	GMCH-BROADWATER_GND
14	GMCH-BROADWATER_PWR
15	DDRII-CHANNEL A 1,2
16	DDRII-CHANNEL B 1,2
17	DDRII-TERMINATION
18	PCI EXPRESS*16 SLOT
19	ICH8-PCI / DMI / LAN / USB
20	ICH8-GPIO / CTRL
21	ICH8-SATA / FAN PWM
22	ICH8-PWR / GND
23	CLOCK GEN
24	PCI EXPRESS*1 / PCI SLOT 1,2
25	ITE8718F
26	COM / LPT / SYS_FAN / CPU_FAN
27	BIOS / CI / HW MONITOR / KB / MS

28	CODEC ALC888
29	AUDIO JACK
30	PWM FAN5032MPX
31	DISCRETE POWER
32	ATX POWER CONNECTOR
33	JMICRON JMB368
34	VT6308P 1394
35	RTL8101E_8111B , REAR USB2
36	FP / F_USB / USB PWR / FDD / BZ



Version: 2.1

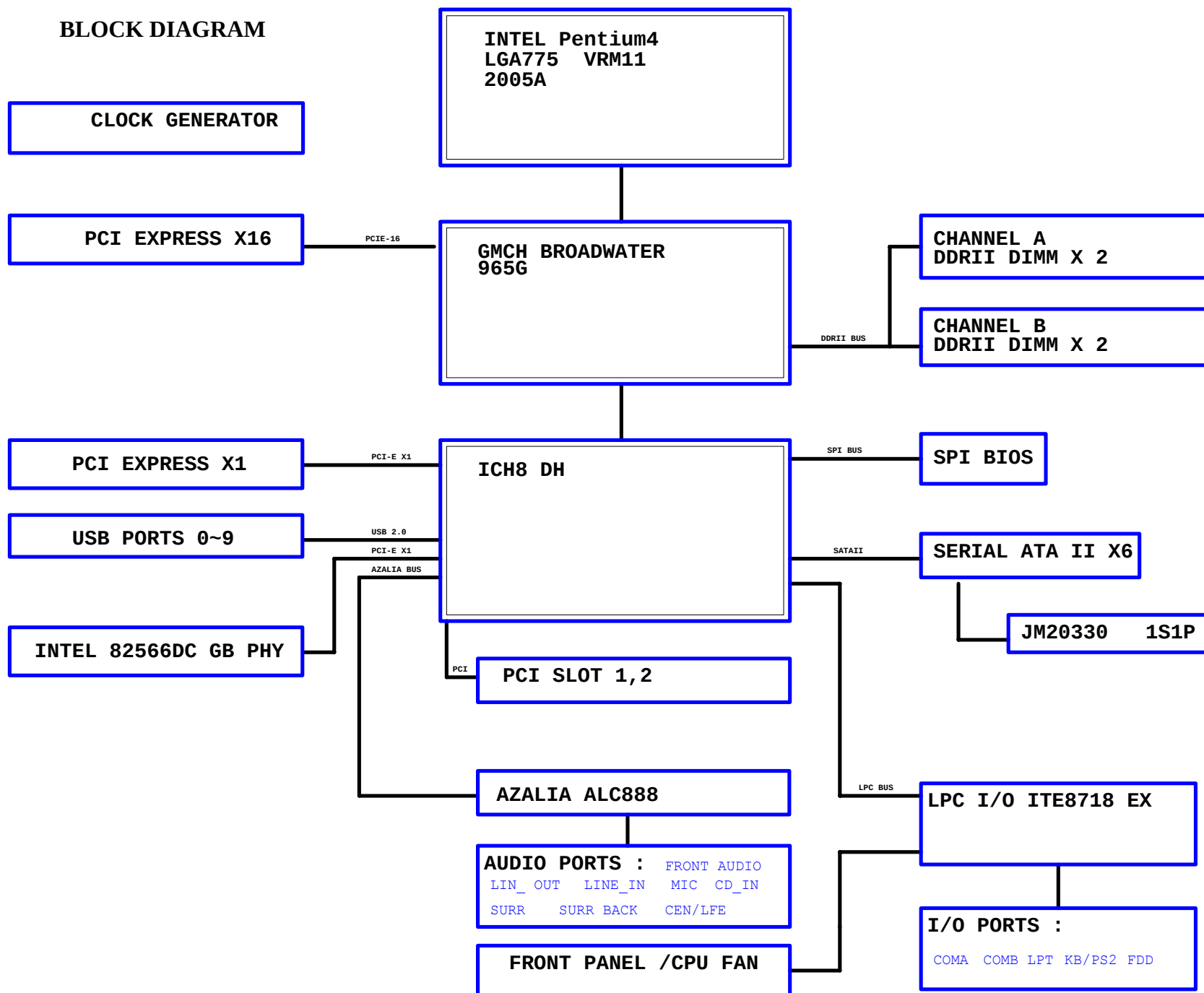
2006/03/14

[illegible]

Circuit or PCB layout change for next version

[illegible]

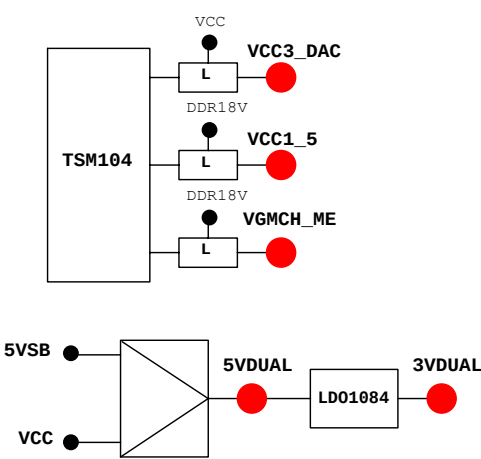
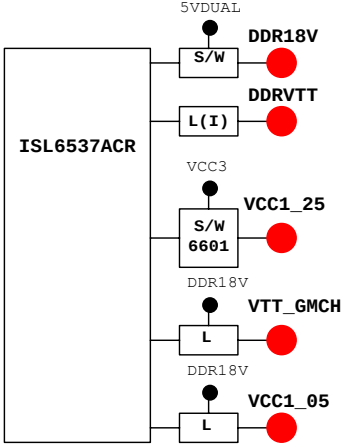
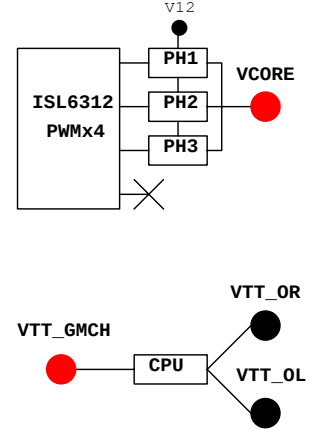
BLOCK DIAGRAM



ICH8 GPIO LIST TABLE

PIN NAME	PWR WELL	AFTER/ ELTRST	USAGE	NOTE
GP0	MAIN	IN	-ACZ_DET	P/U 8.2K VCC3
GP1/TACH1	MAIN	IN	N/A	P/U 8.2K VCC3
GP2/PIRQE#	MAIN	IN	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN	IN	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN	IN	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN	IN	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN	IN	N/A	P/U 8.2K VCC3
GP7/TACH3	MAIN	IN	N/A	P/U 8.2K VCC3
GP8	STBY	IN	GPIO8	P/U 8.2K 3VDUAL
GP9	STBY	OUT	WOL_ONLY	P/D 100K GND
GP10	STBY	OUT	GPIO10	P/U 8.2K 3VDUAL
GP11/SMBALERT#	STBY	OUT	-SMBALRT	P/U 8.2K 3VDUAL
GP12	STBY	IN	MB_ID3	P/U 8.2K 3VDUAL
GP13	STBY	IN	-LPCPME	P/U 8.2K 3VDUAL
GP14	STBY	IN	GPIO14	P/U 8.2K 3VDUAL
GP15	STBY	OUT	GPIO15(TP)	N/A
GP16	MAIN	IN	MB_ID1	P/U 8.2K VCC3
GP17/TACH0	MAIN	IN	N/A(OPT-ITE)	P/U 8.2K VCC3
GP18	MAIN	IN	MB_ID2	P/U 8.2K VCC3
GP19	MAIN	IN	GPIO19	P/U 8.2K VCC3
GP20	MAIN	OUT	GPIO20	N/A
GP21	MAIN	IN	GPIO21	P/U 8.2K VCC3
GP22	MAIN	IN	GPIO22	P/U 8.2K VCC3
GP23	MAIN	OUT	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	OUT	GPIO24(TP)	N/A
GP25	STBY	IN	MB_ID4	P/U 8.2K 3VDUAL
GP26/S4_STATE#	STBY	OUT	GPIO26	P/U 8.2K 3VDUAL
GP27	STBY	LOW	GPIO27	ENERGY-LAKE LED:GREEN
GP28	STBY	LOW	GPIO28	ENERGY-LAKE LED:YEL
GP29/OC5#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP30/OC6#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP31/OC7#	STBY	IN	-USB0C_F	P/U FUSEVCC1
GP32	MAIN	IN	MB_ID0	P/U 8.2K VCC3
GP33	MAIN	IN	GPIO33	P/U 8.2K VCC3
GP34	MAIN	IN	GPIO34	P/U 8.2K VCC3
GP35	MAIN	IN	N/A(TP)	N/A
GP36	MAIN	IN	GPIO36	P/U 8.2K VCC3
GP37	MAIN	IN	GPIO37	P/U 8.2K VCC3
GP38	MAIN	IN	GPIO38	P/U 8.2K VCC3
GP39	MAIN	IN	GPIO39	P/D 8.2K GND
GP48	MAIN	IN	GPIO48	P/U 8.2K VCC3
GP49	MAIN	IN	CPUPWROK	P/U 100 VTT_OL

VCORE:3 PHASE PWM--ISL6312



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Title		TABLE LIST	
Size B	Document Number	Rev	2.1
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Gigabyte Technology

Title

TABLE LIST

Size B

Document Number

Rev

GA-TG965MP-RH-GW

2.1

Date:

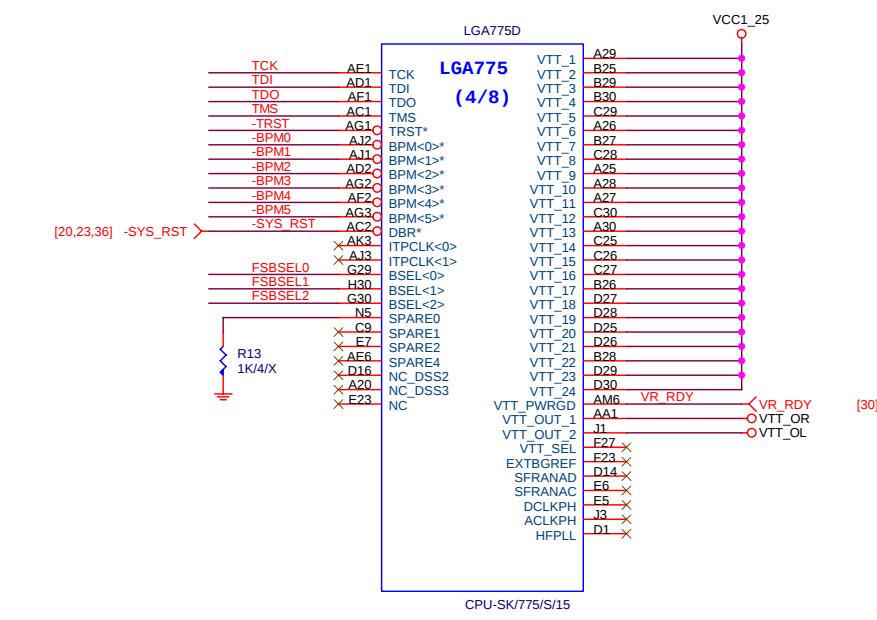
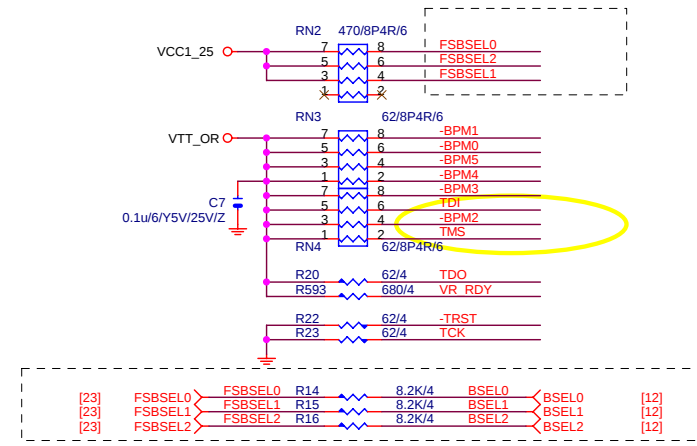
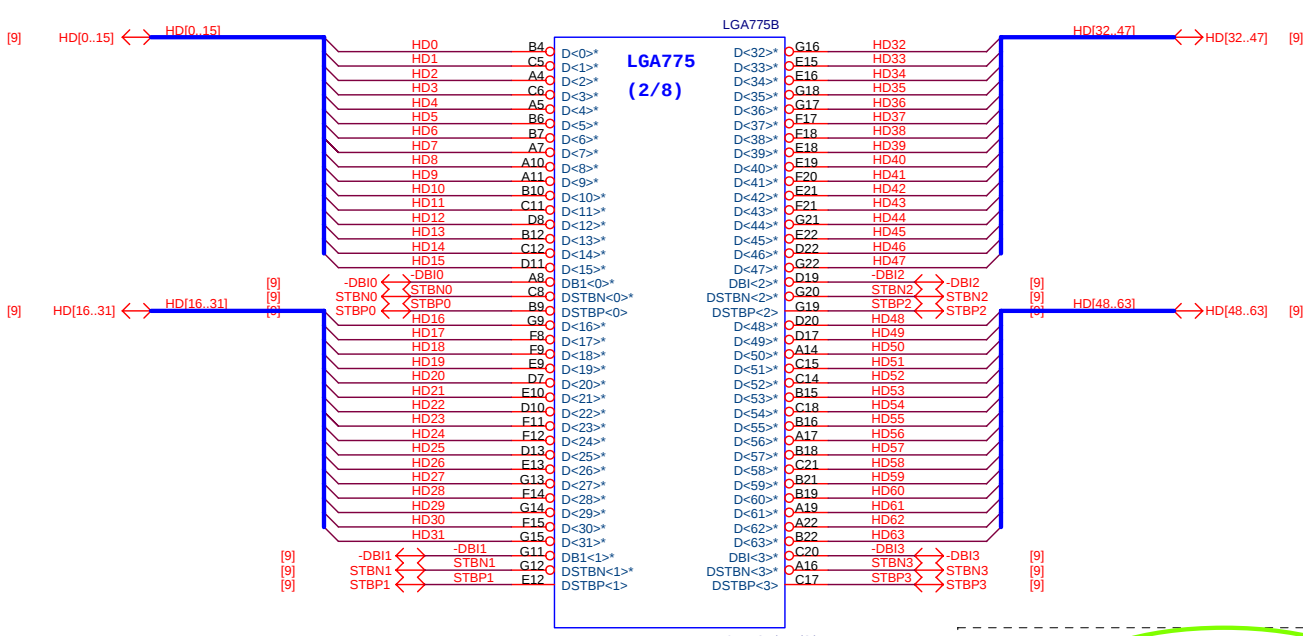
Monday, May 14, 2007

Sheet

4

of

36



FOR ALL DDR CLK RATIO

CHECK BSEL0/1 ITE8712 POWER ON 時電壓是否會高於1.2V

CPU

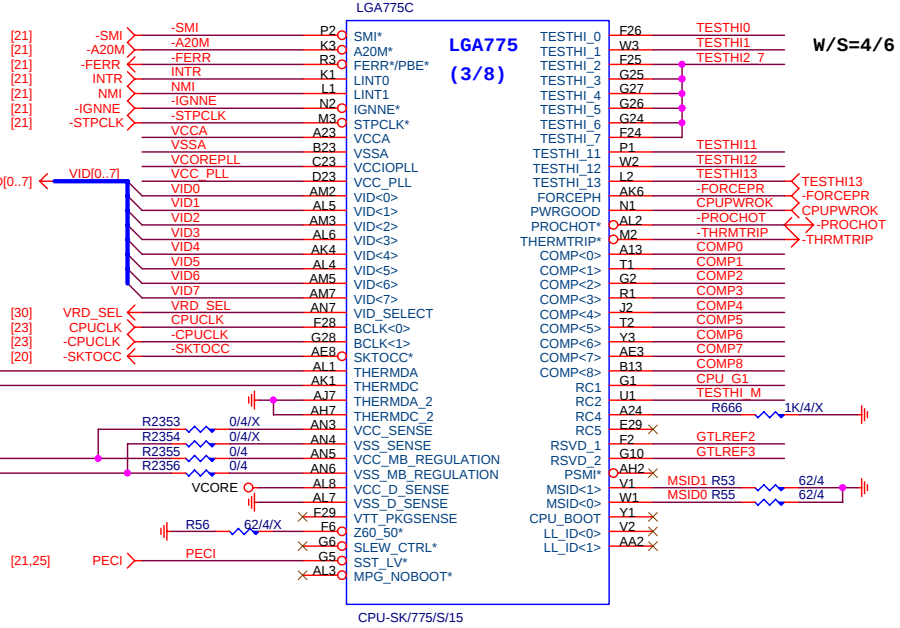
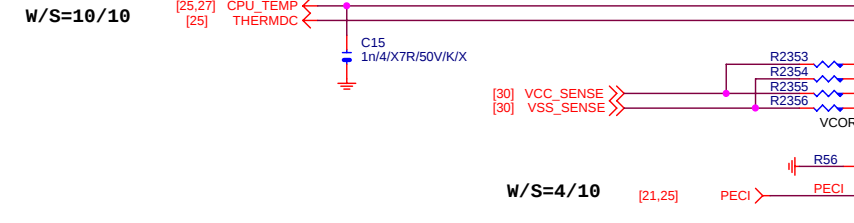
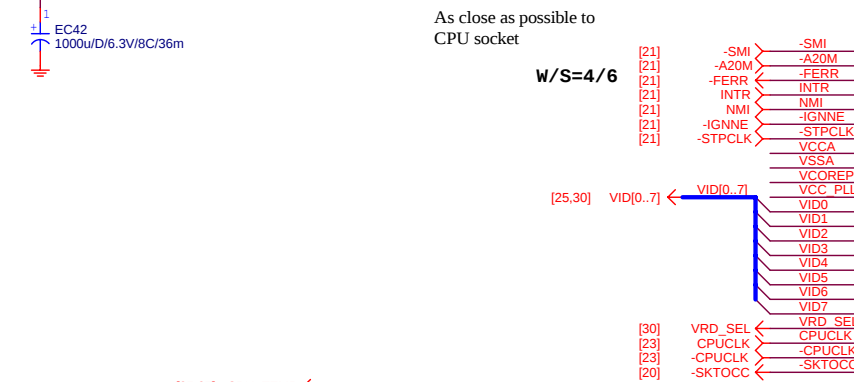
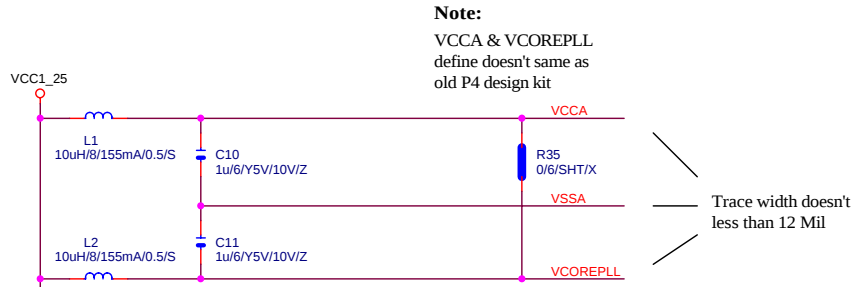
FSA	FSB	NA	Clock
FSBSEL0	FSBSEL1	FSBSEL2	
1	0	1	100MHz
1	0	0	133MHz
1	1	0	166MHz
0	1	0	200MHz
0	0	0	266MHz

POWER ON

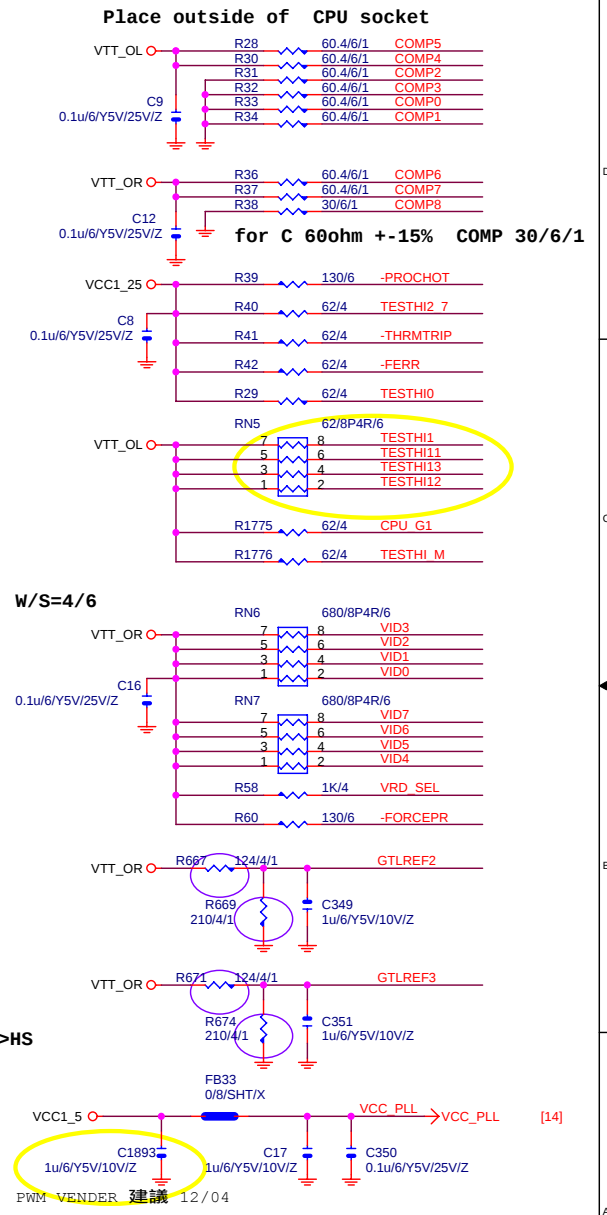
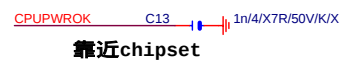
400/533

400/533/667/800

533/667/800

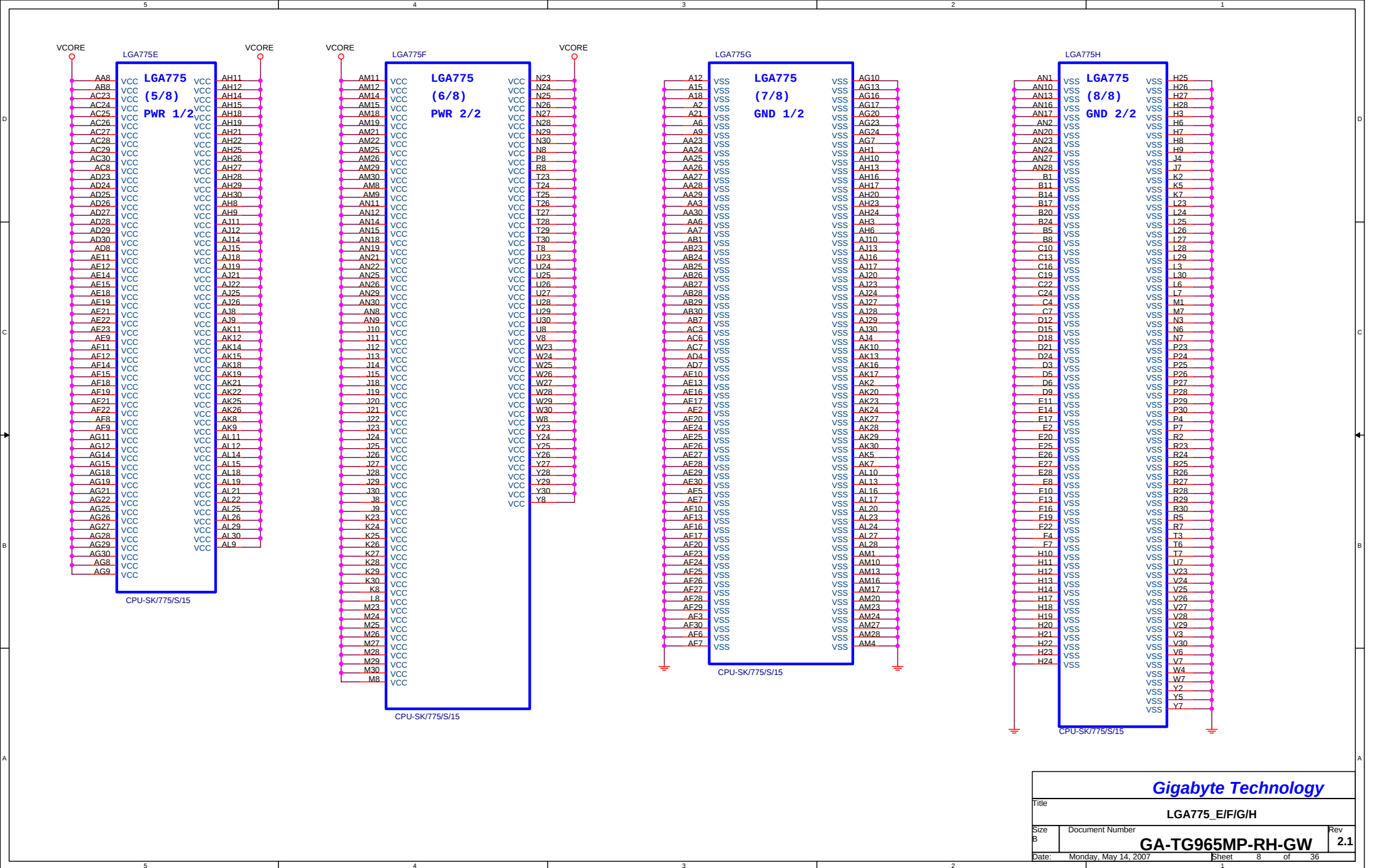


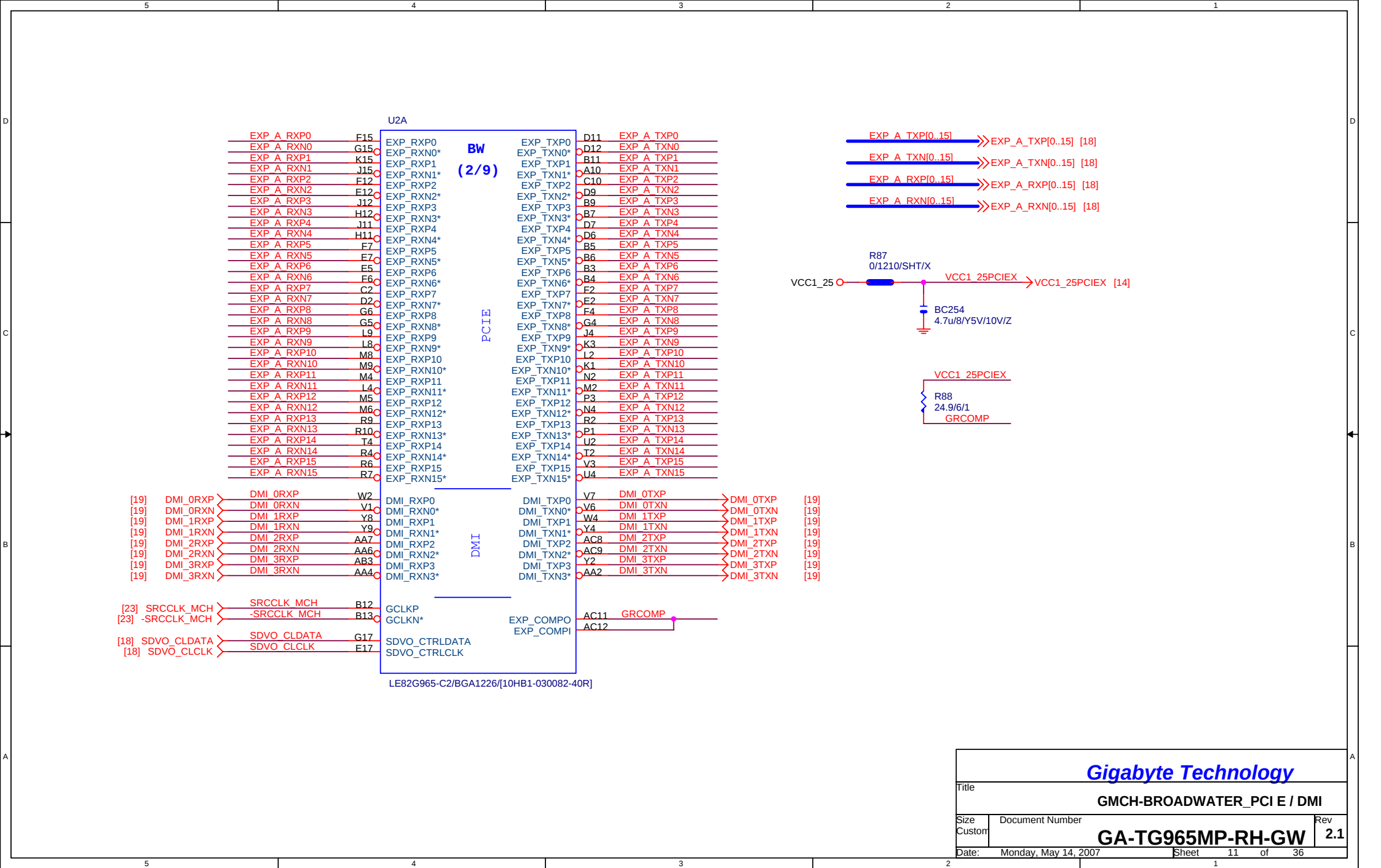
COMP[8]:W/S=15/7-->LS;W/S=15/14-->HS
COMP[7..0]:W/S=10/7-->LS;W/S=10/14-->HS



PECI:Platform Environment Control Interface

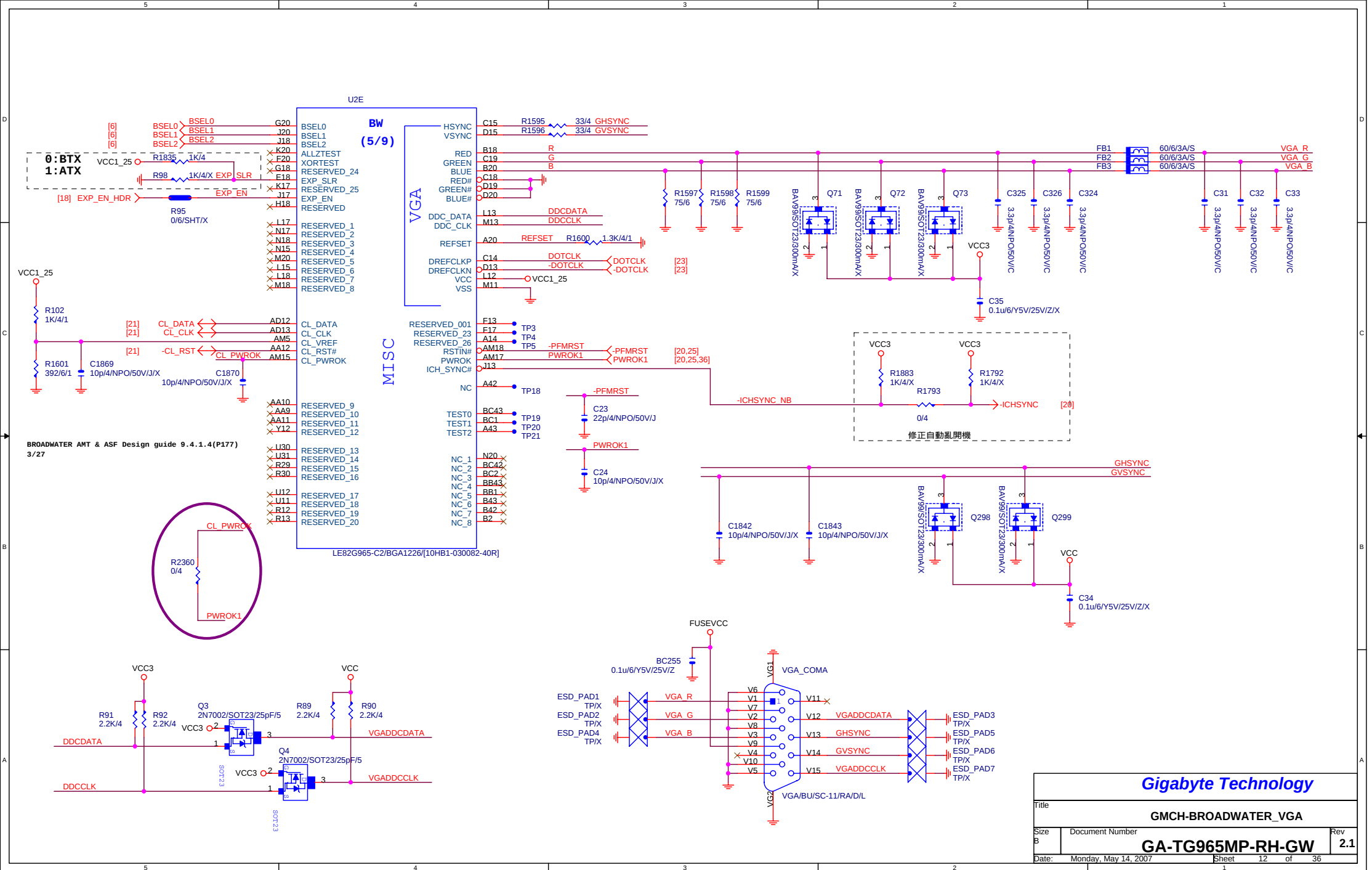
Gigabyte Technology		
LGA775-C		
Title	Document Number	Rev
	GA-TG965MP-RH-GW	2.1
Date:	Monday, May 14, 2007	Sheet 7 of 36

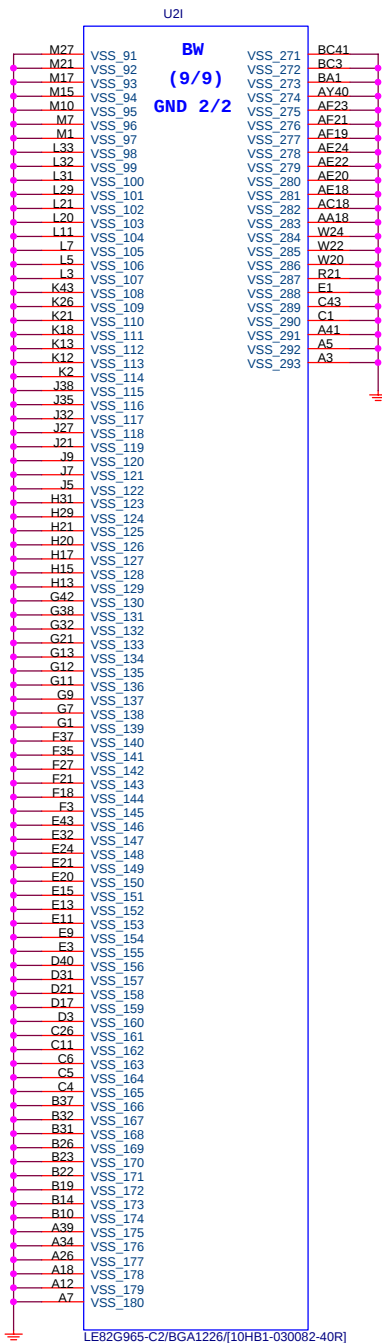
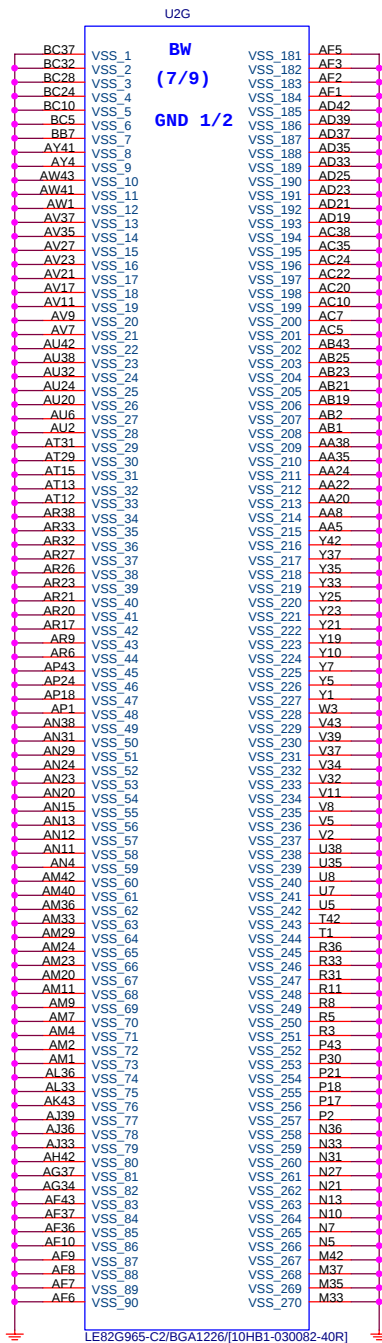


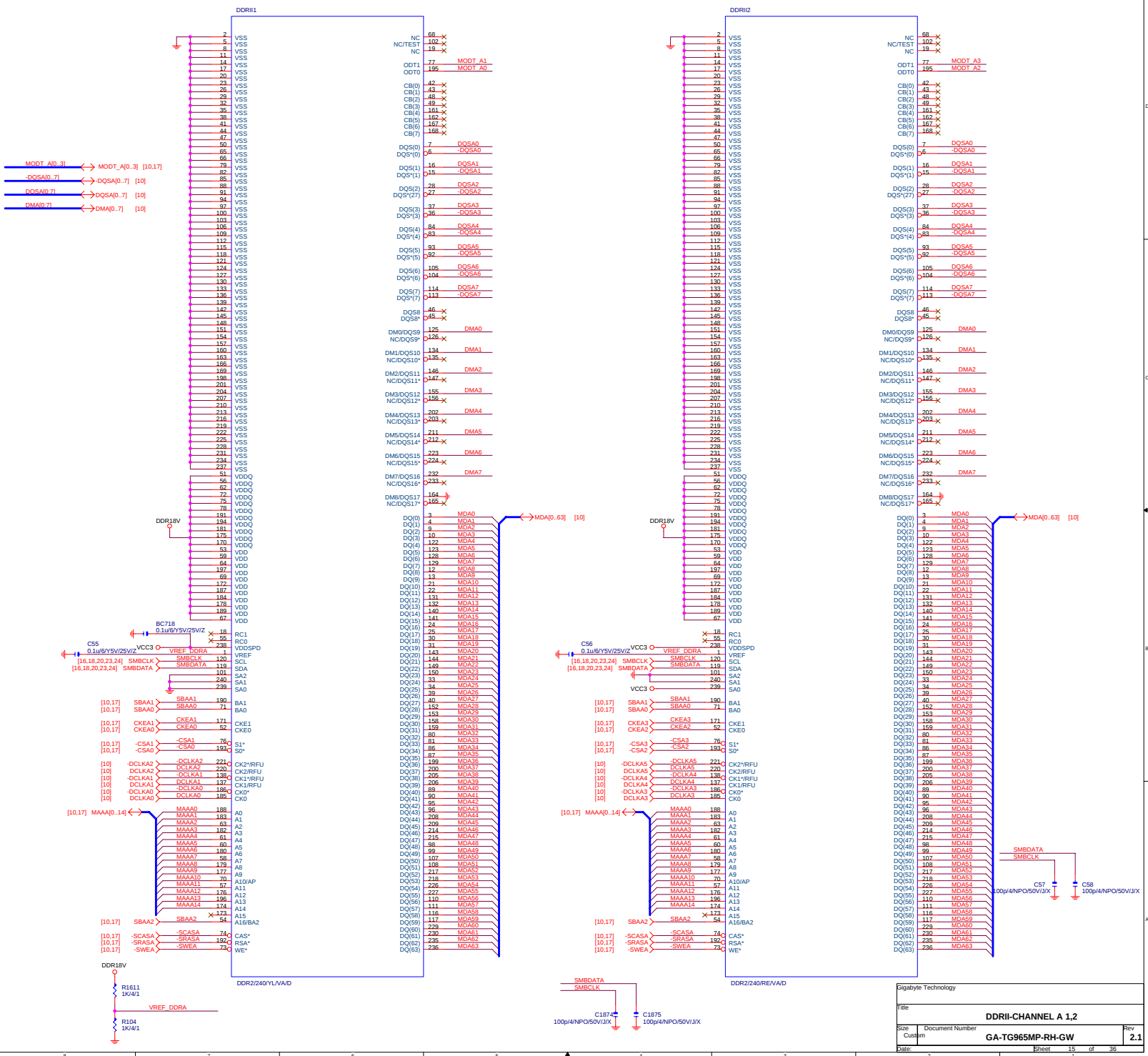


Gigabyte Technology

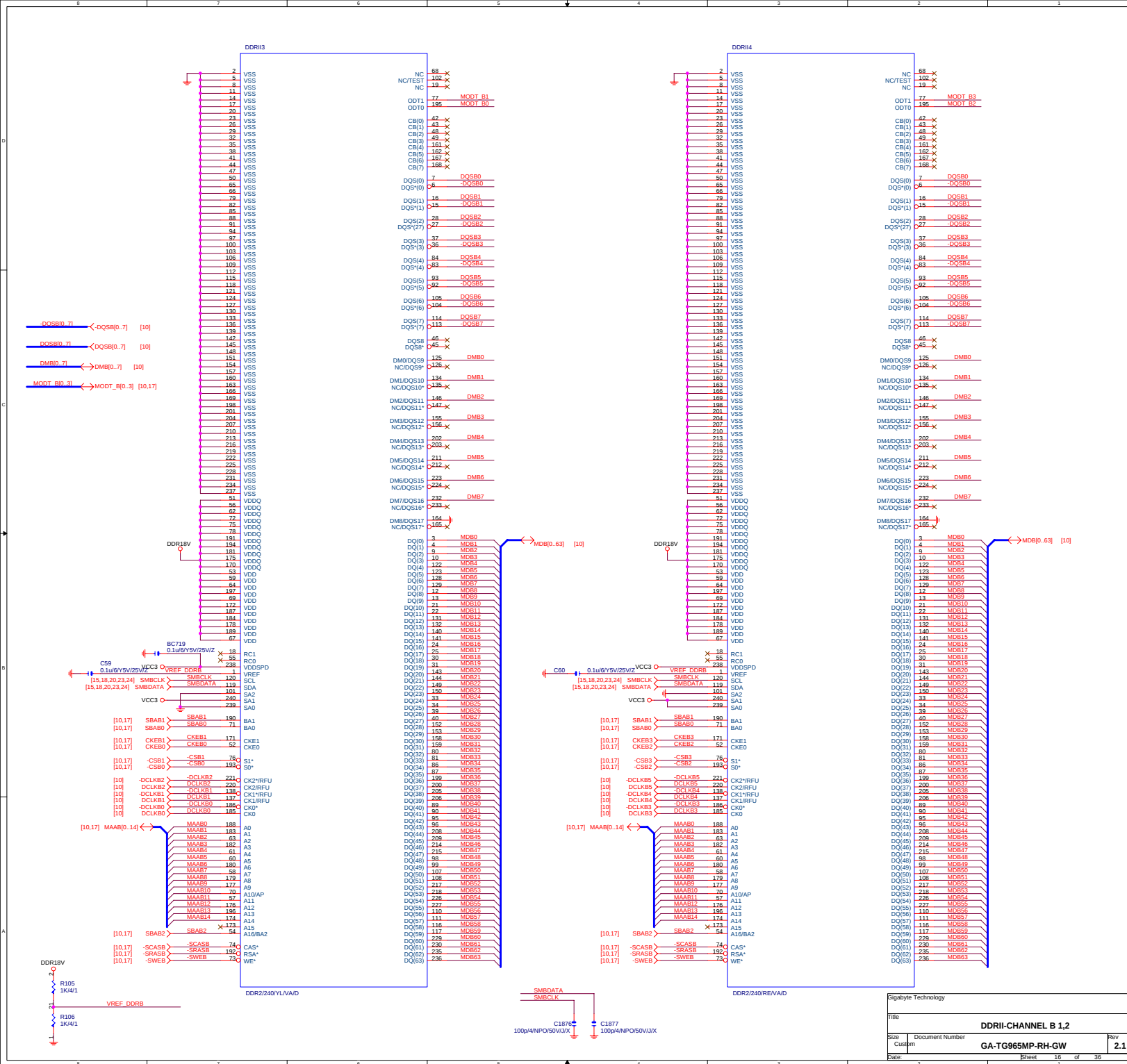
Title			GMCH-BROADWATER_PCI E / DMI	
Size	Document Number	GA-TG965MP-RH-GW		Rev
Custon				2.1
Date:	Monday, May 14, 2007	Sheet	11	of 36





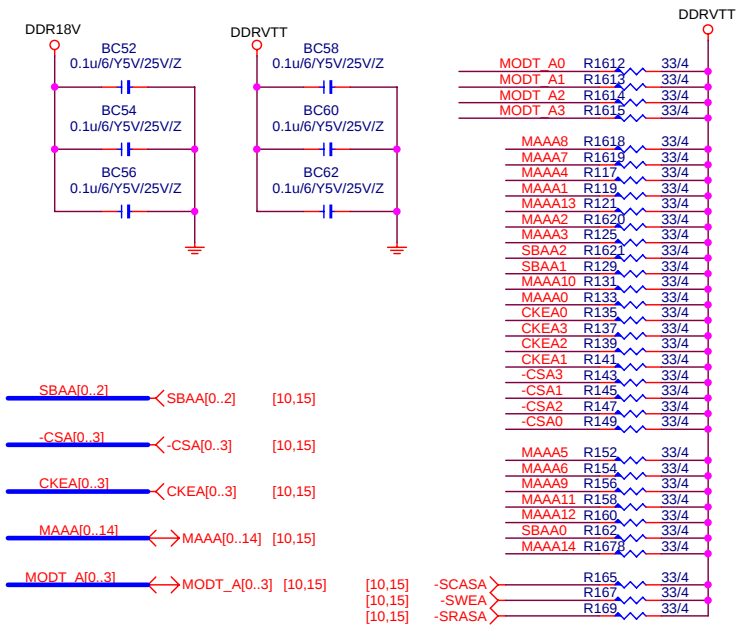


Sigabite Technology		
Title		
DDRII-CHANNEL A 1,2		
Size Document Number		
GA-TG965MP-RH-GW		
Rev		
2.1		
Date		
Sheet 15 of 36		

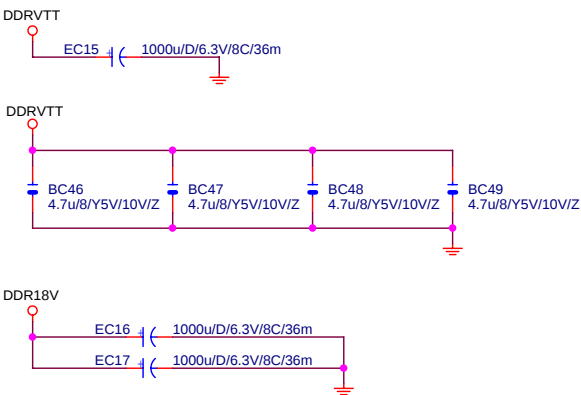


DDR TERMINATION
CHANNEL A

DDR18V Decouple DDRVTT Decouple

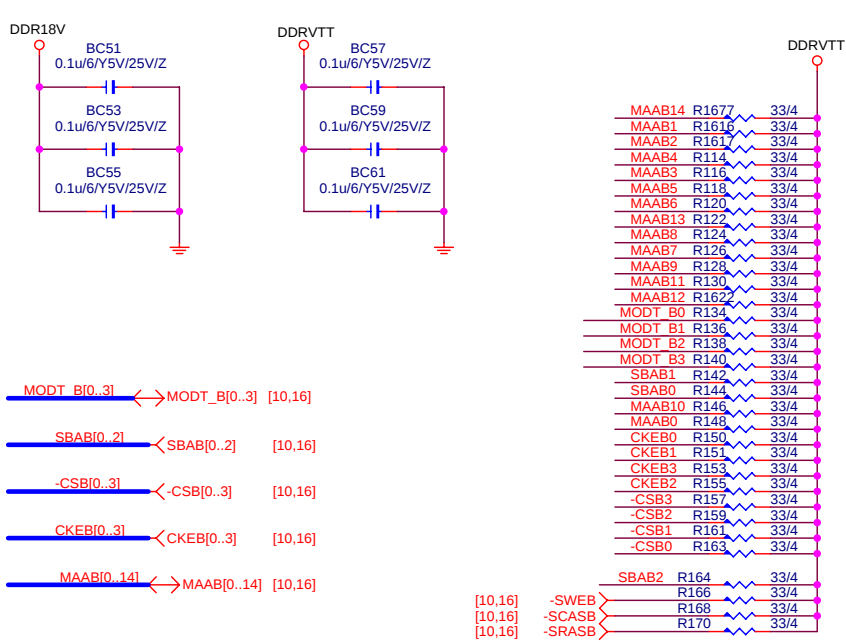


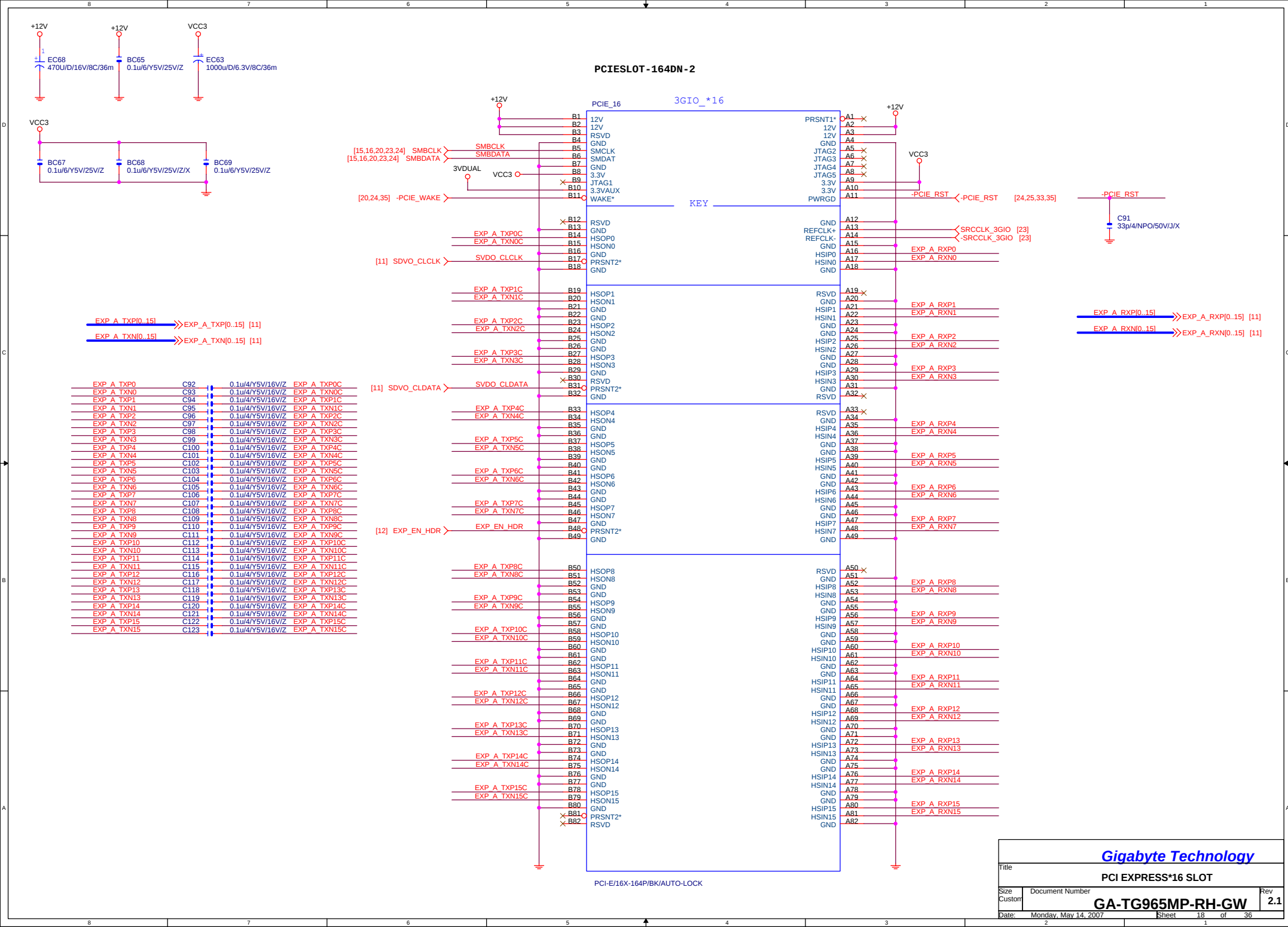
DDRVTT Decouple

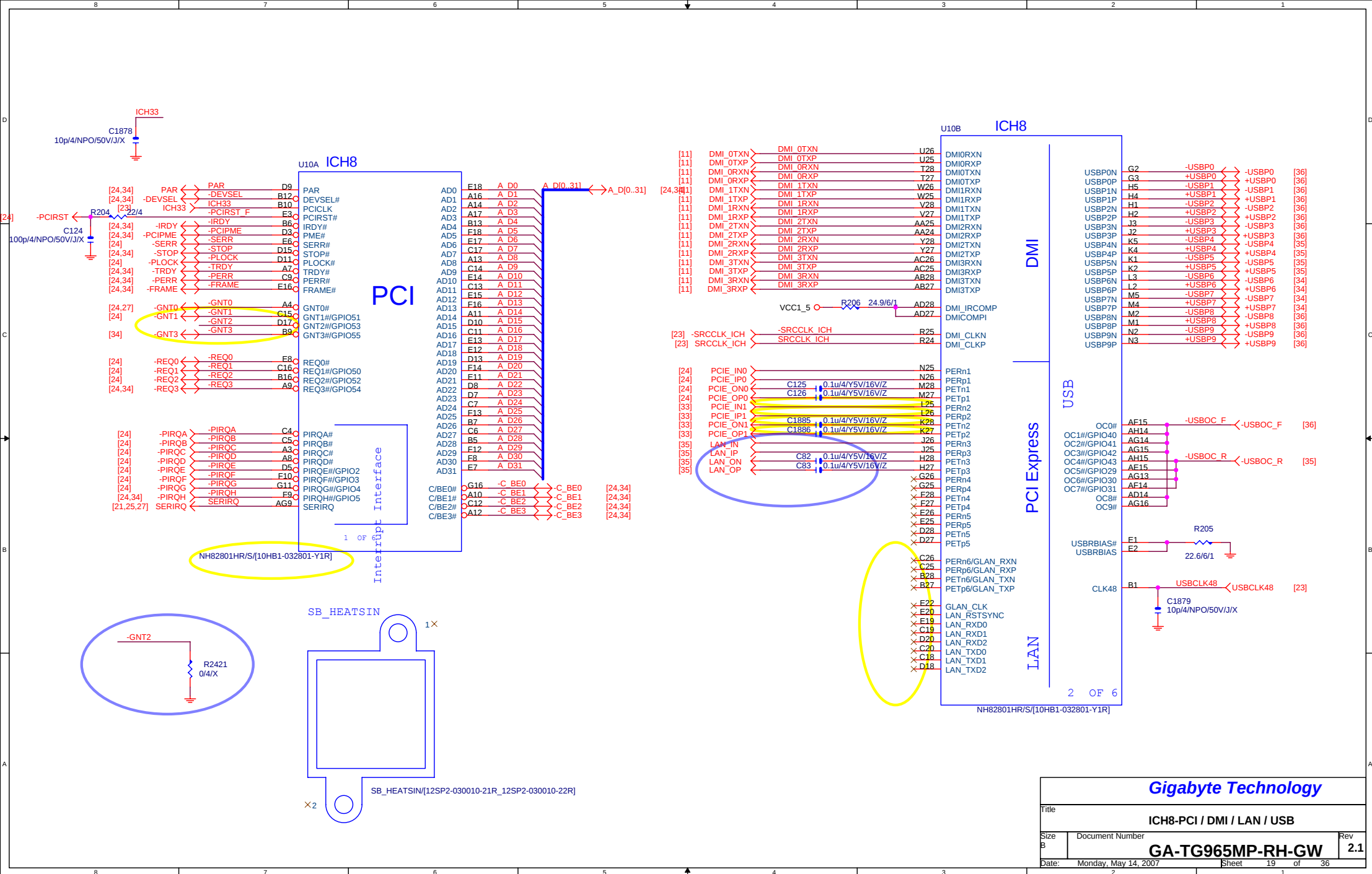


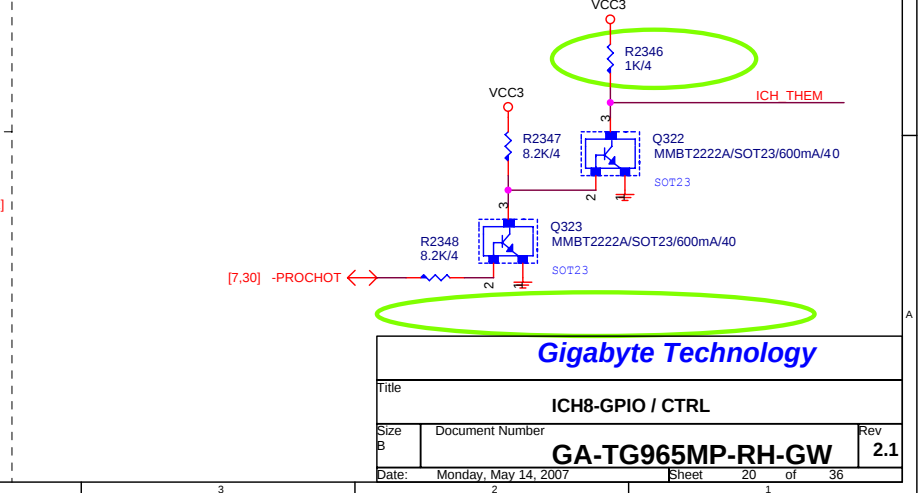
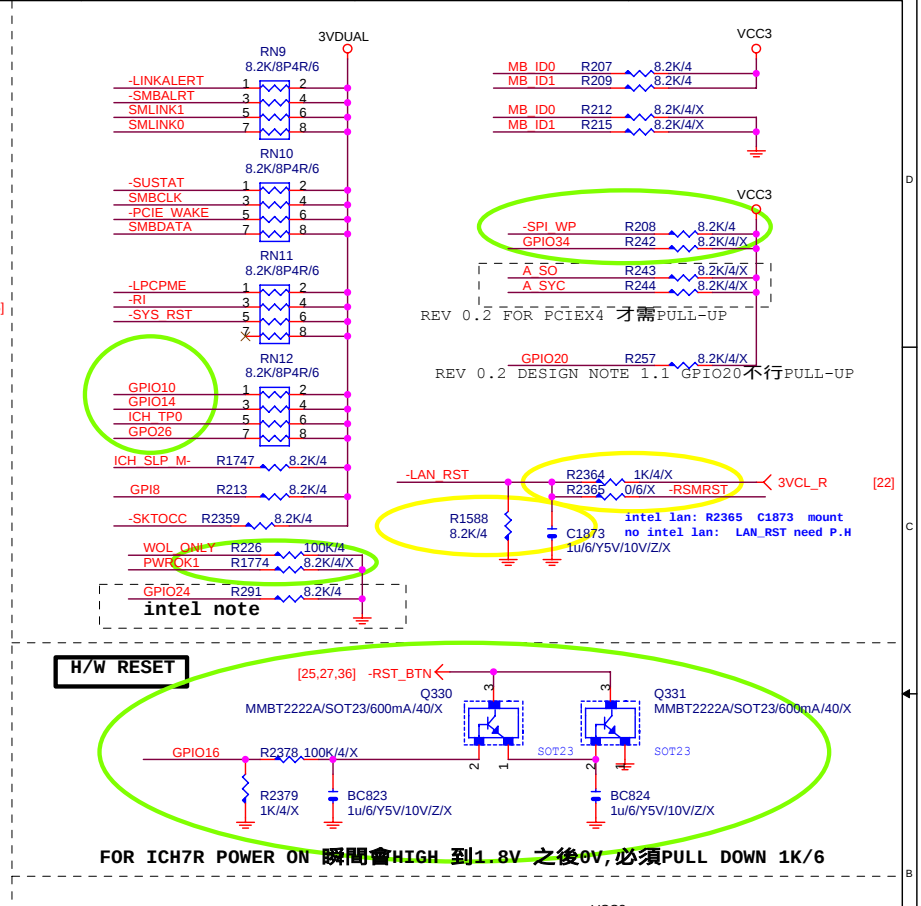
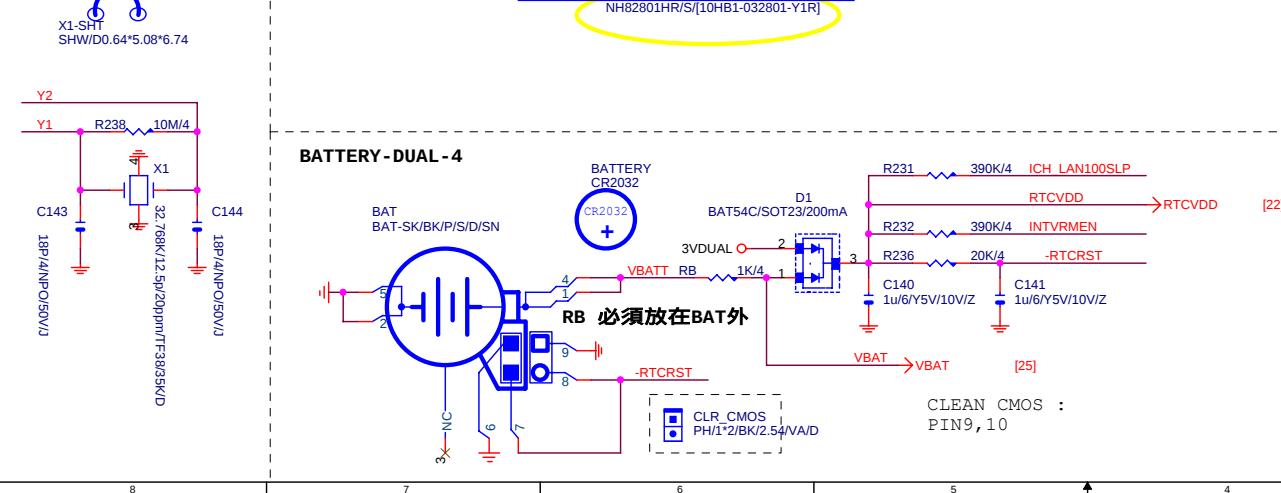
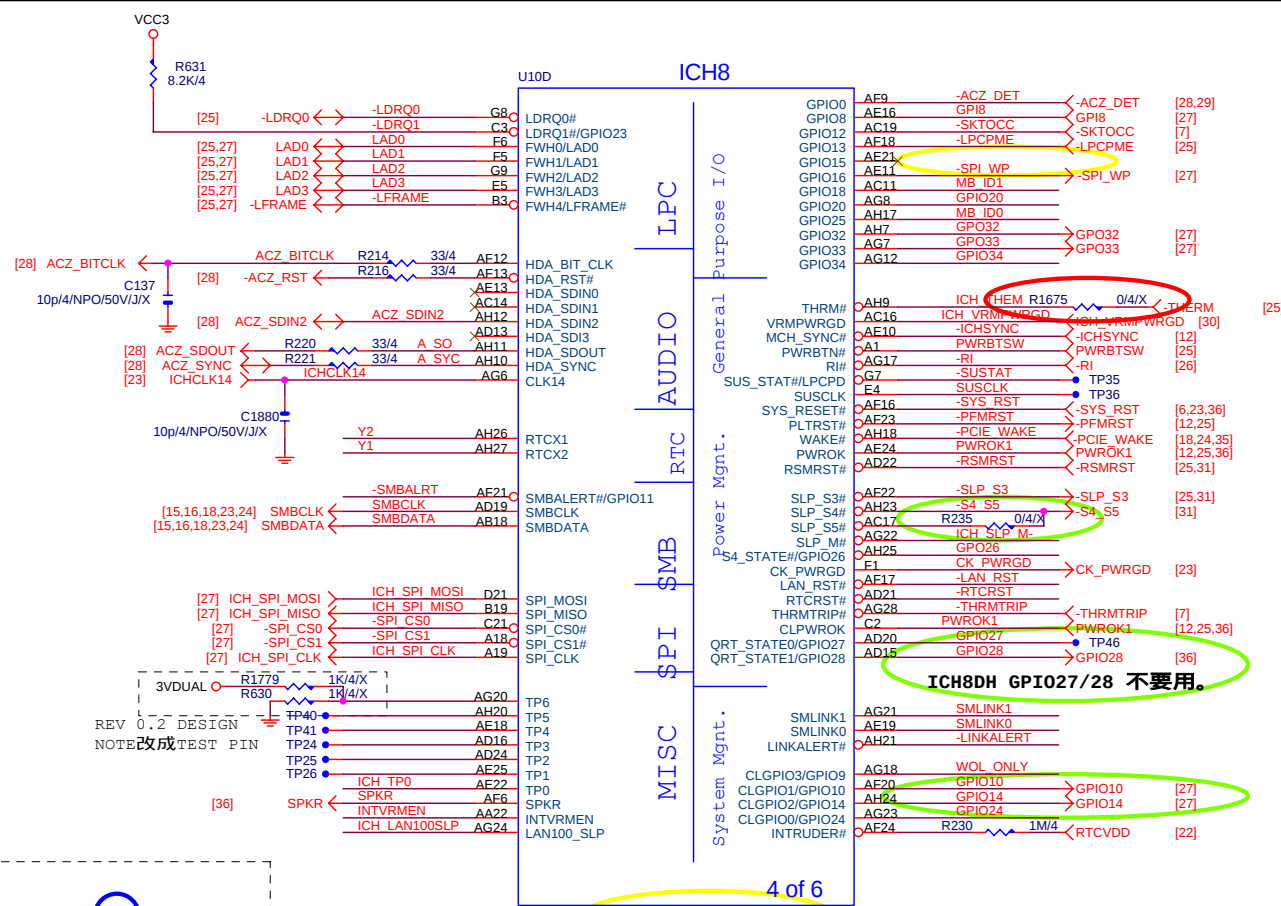
DDR TERMINATION
CHANNEL B

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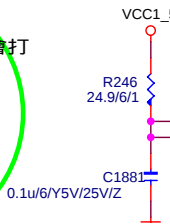
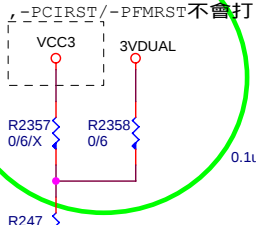




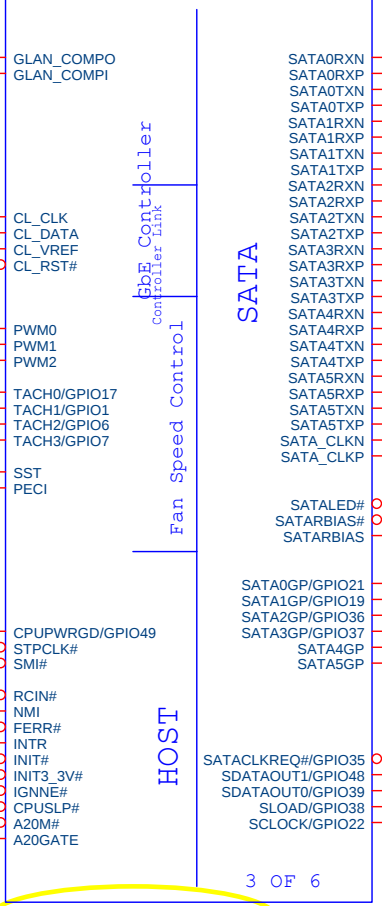




REV 0.2 修改成 VCC3,
否則造成不開機



ICH8



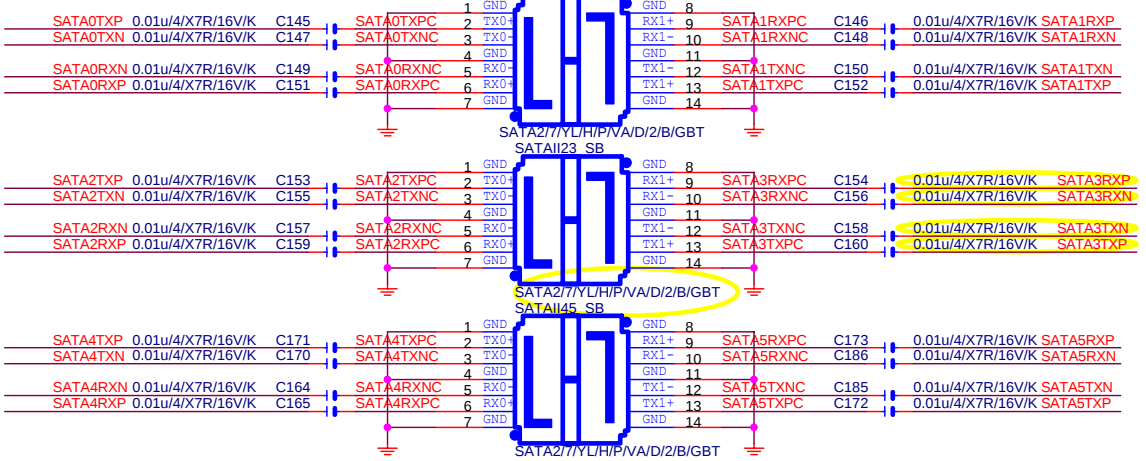
SATA

HOST

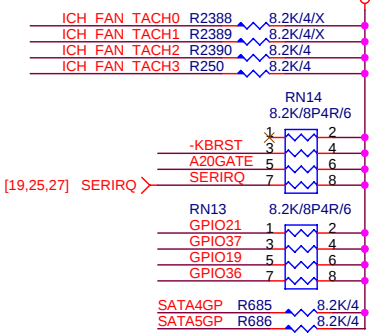
3 OF 6

NH82801HR/S/[10HB1-032801-Y1R]

SATAI01_SB



ICH8 FAN CTRL 沒用到的要 P.H



The ICH8 integrated GbE LAN test mode is activated any time the ICH8 GPIO39 signal is not at a low logic level.

Workaround

Under investigation. Possible workaround is to use a weak pulldown resistor on GPIO39 to ensure signal is always low

Gigabyte Technology

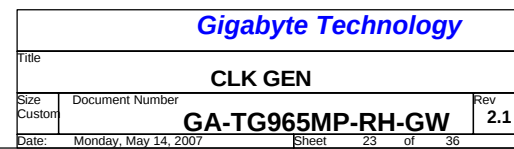
ICH8-SATA / FAN PWM

GA-TG965MP-RH-GW

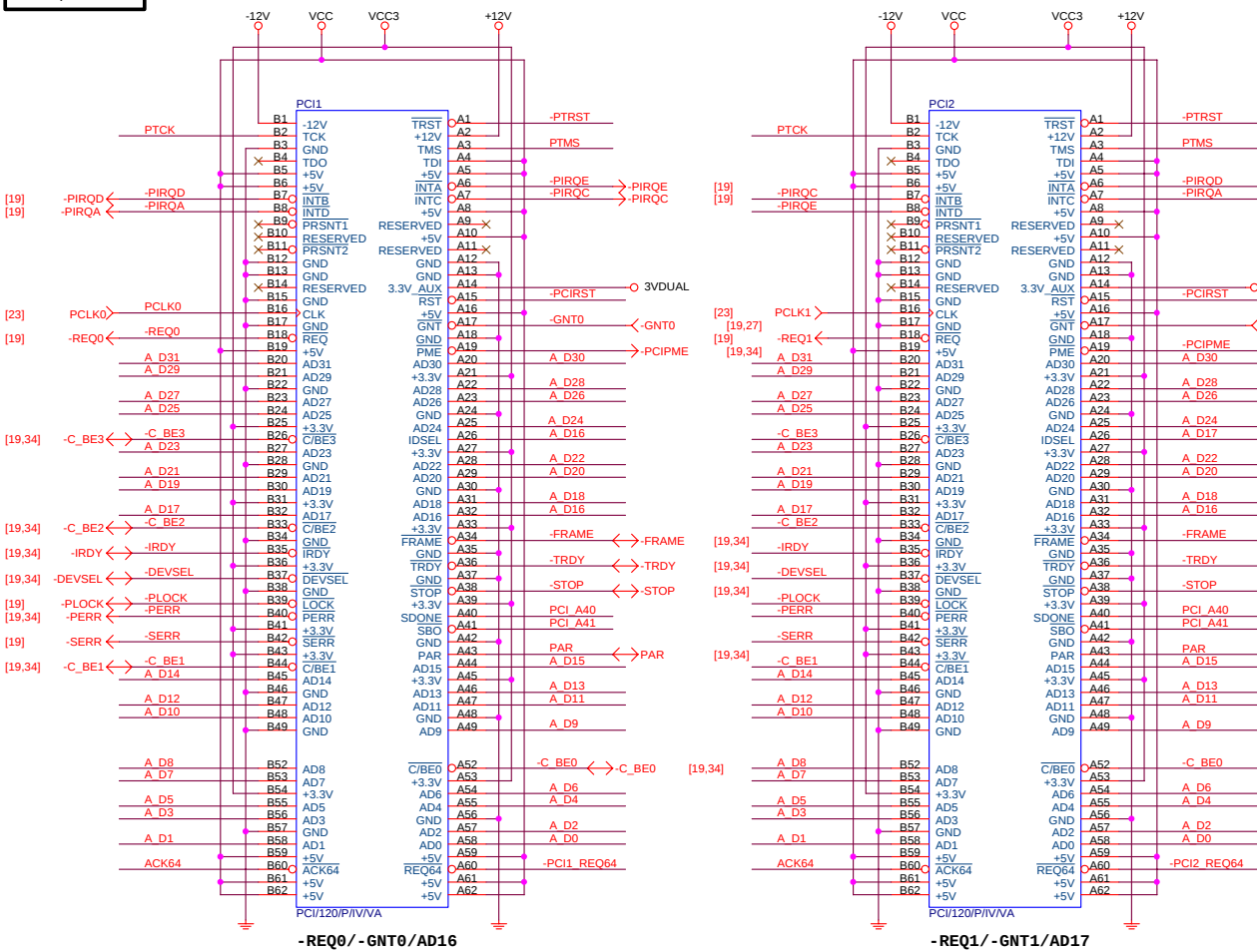
Rev 2.1

U10F CH8

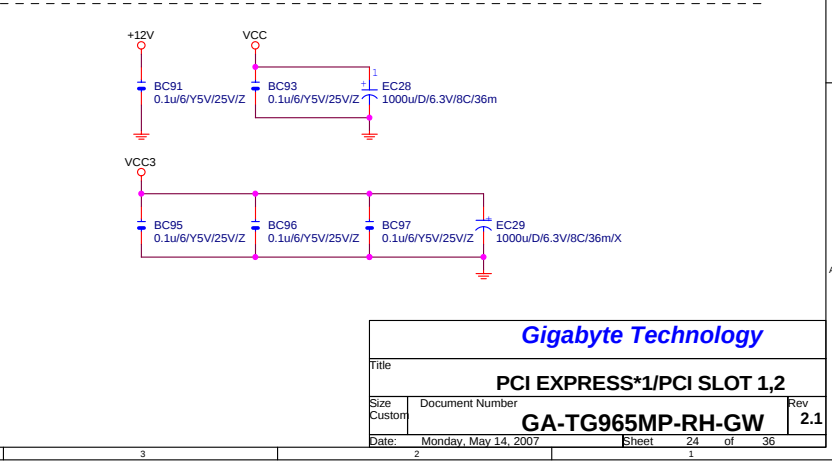
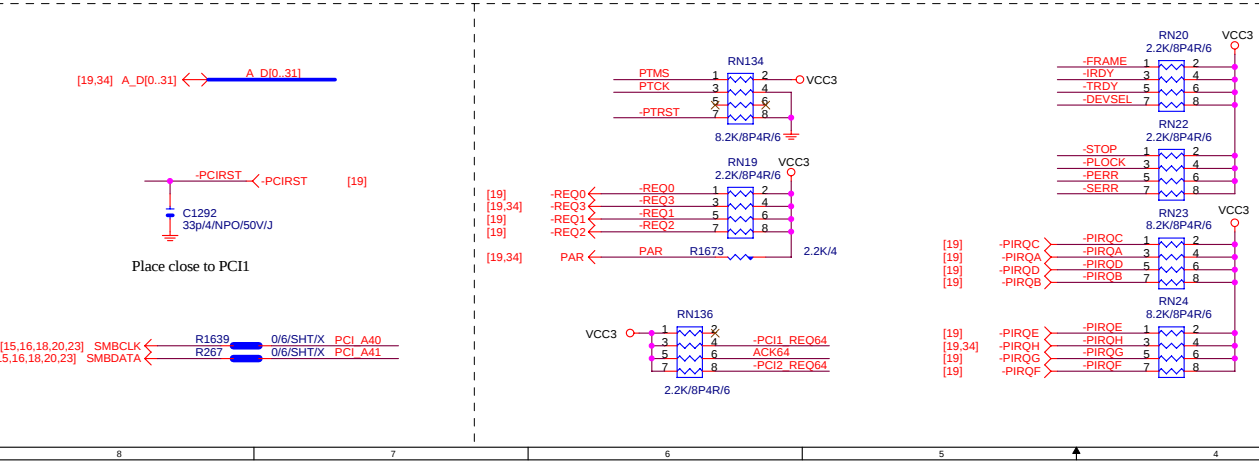
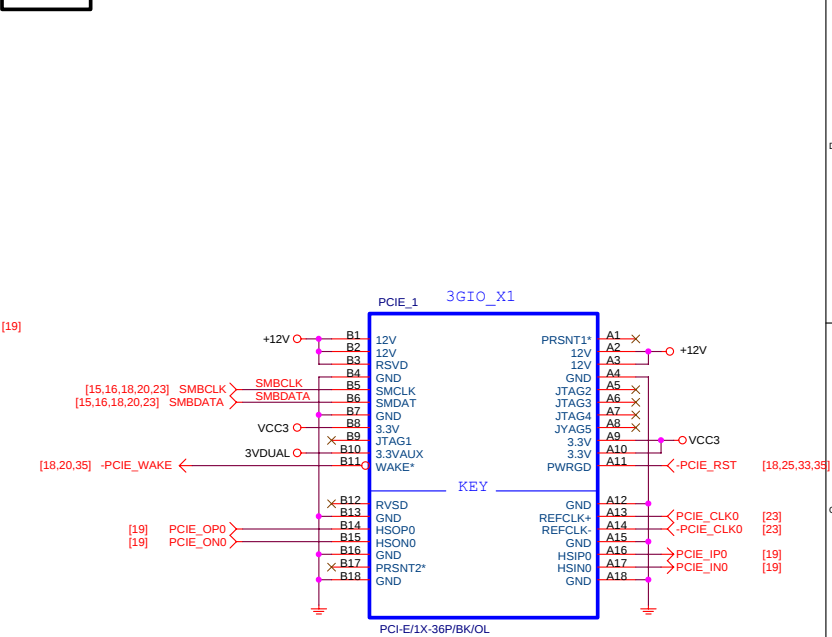
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R12	VSS_100	P22	VSS_099
R13	VSS_101	P22	VSS_099
R14	VSS_102	P17	VSS_099
R15	VSS_103	P16	VSS_099
R16	VSS_104	P15	VSS_099
R17	VSS_105	P14	VSS_099
R18	VSS_106	P13	VSS_099
T3	VSS_107	P12	VSS_099
T4	VSS_108	N28	VSS_099
T5	VSS_109	N27	VSS_099
T6	VSS_110	N26	VSS_099
T12	VSS_111	N25	VSS_099
T13	VSS_112	N24	VSS_099
T14	VSS_113	N23	VSS_099
T15	VSS_114	N22	VSS_099
T16	VSS_115	N21	VSS_099
T17	VSS_116	N20	VSS_099
T22	VSS_117	N19	VSS_099
T25	VSS_118	N18	VSS_099
T26	VSS_119	N17	VSS_099
U1	VSS_120	N16	VSS_099
U2	VSS_121	N15	VSS_099
U12	VSS_122	N14	VSS_099
U13	VSS_123	N13	VSS_099
U14	VSS_124	N12	VSS_099
U15	VSS_125	N11	VSS_099
U16	VSS_126	N10	VSS_099
U17	VSS_127	N9	VSS_099
U27	VSS_128	N8	VSS_099
U28	VSS_129	N7	VSS_099
V7	VSS_130	N6	VSS_099
V13	VSS_131	N5	VSS_099
V15	VSS_132	N4	VSS_099
V22	VSS_133	N3	VSS_099
V25	VSS_134	N2	VSS_099
V26	VSS_135	N1	VSS_099
W27	VSS_136	M26	VSS_099
W28	VSS_137	M25	VSS_099
Y3	VSS_138	M24	VSS_099
Y4	VSS_139	M23	VSS_099
Y5	VSS_140	M22	VSS_099
AA1	VSS_141	M21	VSS_099
AA2	VSS_142	M20	VSS_099
AA23	VSS_143	M19	VSS_099
AB24	VSS_144	M18	VSS_099
AB25	VSS_145	M17	VSS_099
AC6	VSS_146	M16	VSS_099
AC3	VSS_147	M15	VSS_099
AC15	VSS_148	M14	VSS_099
AC18	VSS_149	M13	VSS_099
AC21	VSS_150	M12	VSS_099
AC27	VSS_151	M11	VSS_099
AC28	VSS_152	M10	VSS_099
AD3	VSS_153	M9	VSS_099
AD4	VSS_154	M8	VSS_099
AD5	VSS_155	M7	VSS_099
AD11	VSS_156	M6	VSS_099
AD26	VSS_157	M5	VSS_099
AE1	VSS_158	M4	VSS_099
AE2	VSS_159	M3	VSS_099
AE5	VSS_160	M2	VSS_099
AE8	VSS_161	M1	VSS_099
AE14	VSS_162	L28	VSS_099
AE17	VSS_163	L27	VSS_099
AE20	VSS_164	L26	VSS_099
AE23	VSS_165	L25	VSS_099
AE3	VSS_166	L24	VSS_099
AE4	VSS_167	L23	VSS_099
AG1	VSS_168	L22	VSS_099
AG2	VSS_169	L21	VSS_099
AG5	VSS_170	L20	VSS_099
AG11	VSS_171	L19	VSS_099
AG25	VSS_172	L18	VSS_099
AG27	VSS_173	L17	VSS_099
AH4	VSS_174	L16	VSS_099
AH13	VSS_175	L15	VSS_099
AH16	VSS_176	L14	VSS_099
AH19	VSS_177	L13	VSS_099
AH22	VSS_178	L12	VSS_099
	VSS_179	L11	VSS_099
	VSS_180	L10	VSS_099
	VSS_181	L9	VSS_099
	VSS_182	L8	VSS_099
	VSS_183	L7	VSS_099
	VSS_184	L6	VSS_099
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	VSS_214	K2	VSS_099
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	VSS_261	G11	VSS_099
	VSS_262	G10	VSS_099
	VSS_263	G9	VSS_099
	VSS_264	G8	VSS_099
	VSS_265	G7	VSS_099
	VSS_266	G6	VSS_099
	VSS_267	G5	VSS_099
	VSS_268	G4	VSS_099
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	VSS_270	G2	VSS_099
	VSS_271	G1	VSS_099
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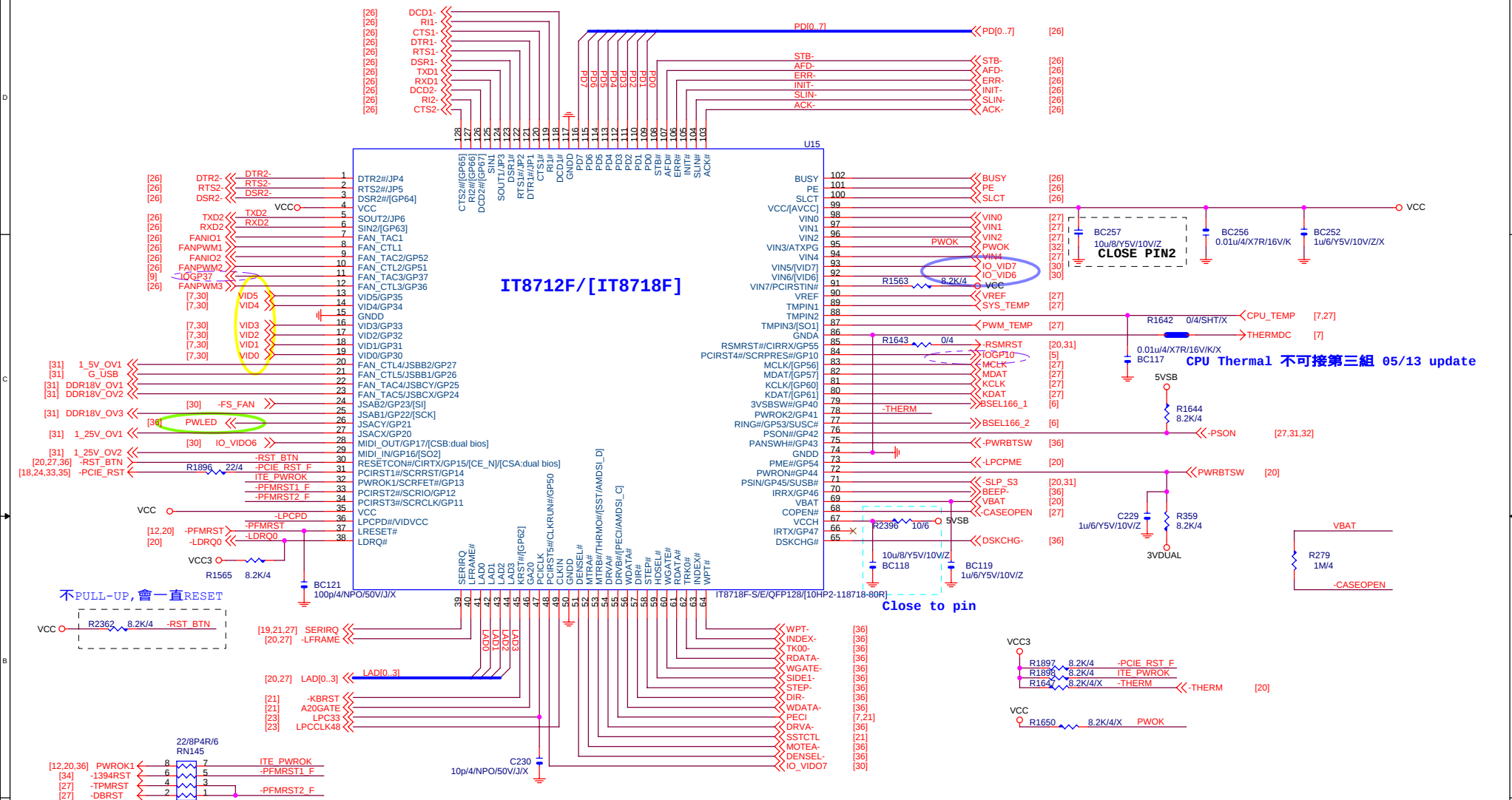
PCI1, 2 SLOT



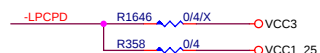
PCI-E*1



IT8718F LPC I/O



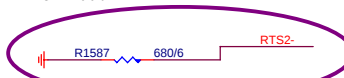
```
1.2V or 3.3V tolerance select.  
1.2V OUTPUT 接 VTT_GMCH  
3.3V OUTPUT 接3.3V  
LPCPD# = VIDVCC
```



SOUT2	1	VID pins threshold voltage select: $V_{ih} / V_{il} : 2.0 / 0.8V$
	0	VID pins threshold voltage select: $V_{ih} / V_{il} : 0.8 / 0.4V$



```
RTS2- ==LOW CPU FAN 50% (DEFAULT 50%)
      ==HIGH 100%
```



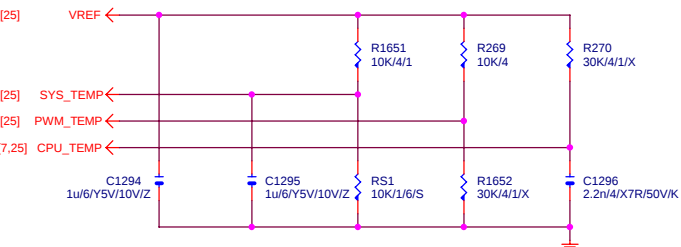
Gigabyte Technology

Title	ITE8718F
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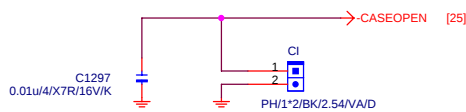
Size	Document Number
Custom	GA-TG965MP-RH-GW

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TEMP H/W MONITOR

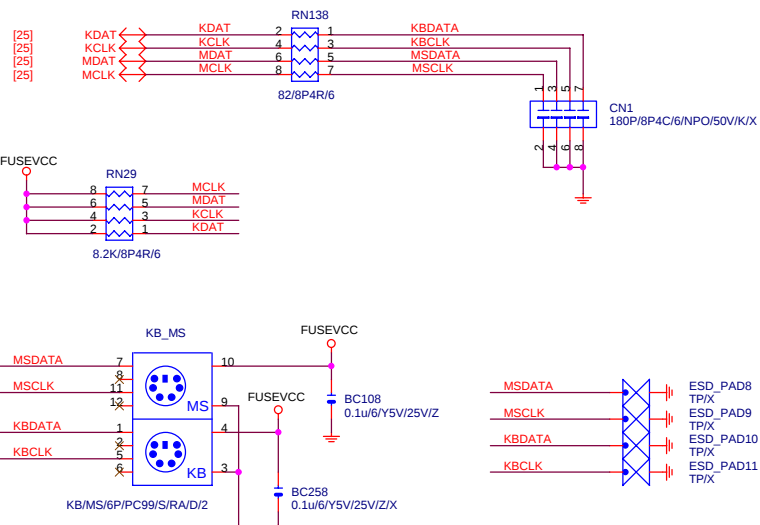


CASE OPEN

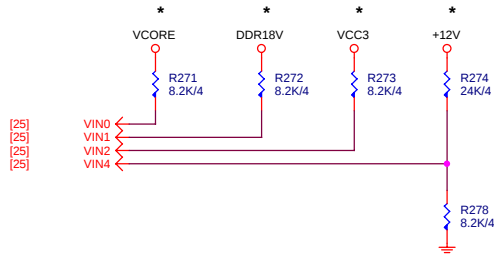


Case Open Circuits

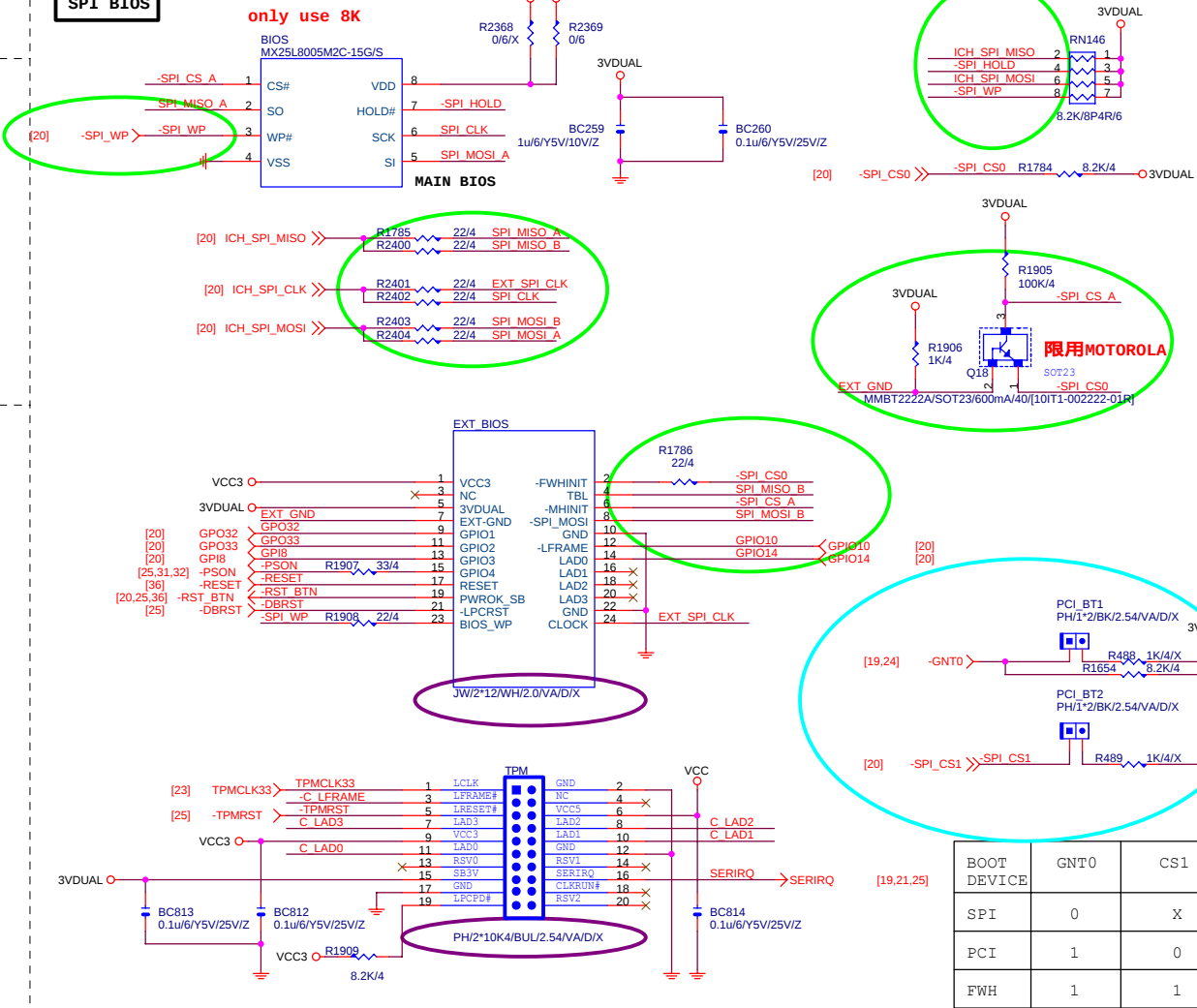
KB/MS



VOLTAGE -- H/W MONITOR

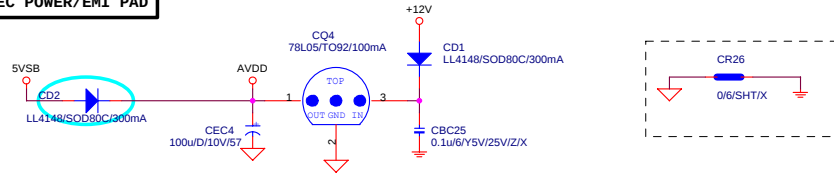


SPI BIOS

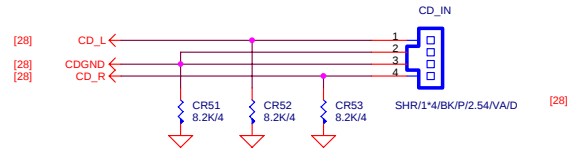


BOOT DEVICE	GNT0	CS1
SPI	0	X
PCI	1	0
FWH	1	1

CODEC POWER/EMI PAD

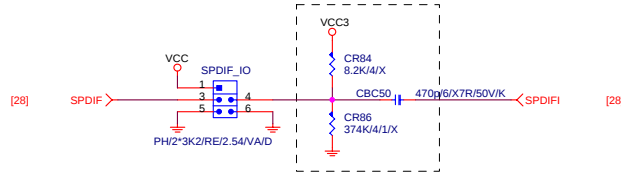


CD IN

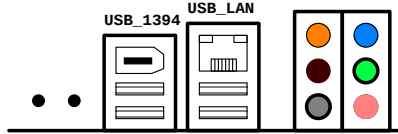


SPDIF

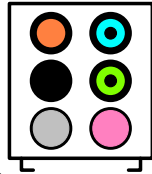
realtek: for 192K S/PDIF IN compatible



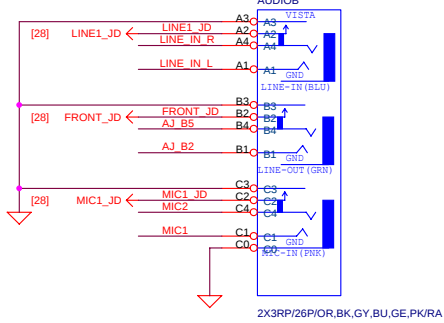
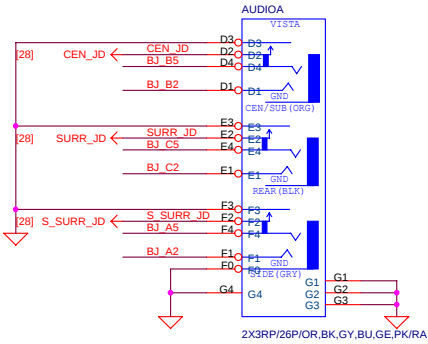
AZALIA JACK



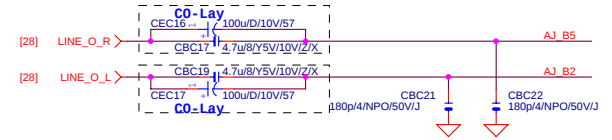
BTX AZALIA CONNECTOR



3RP/26P/OR, BK, GY, BU, GE, PK/RA/D/1/B
VISTA規範: REAR-->BLK, CEN/SUB-->ORG



LINE-OUT



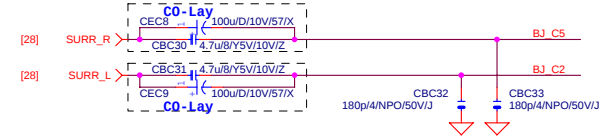
LINE-IN



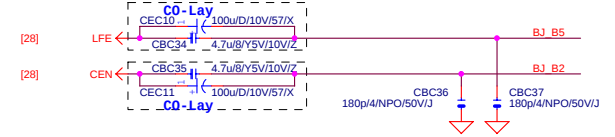
MIC-IN



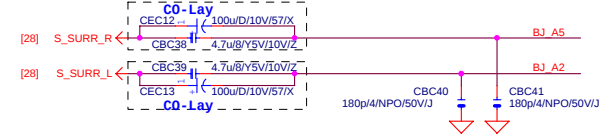
SURROUND



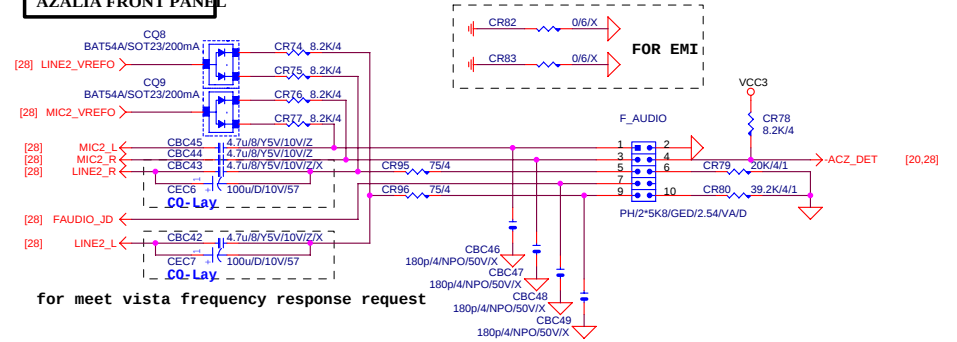
CEN/LFB



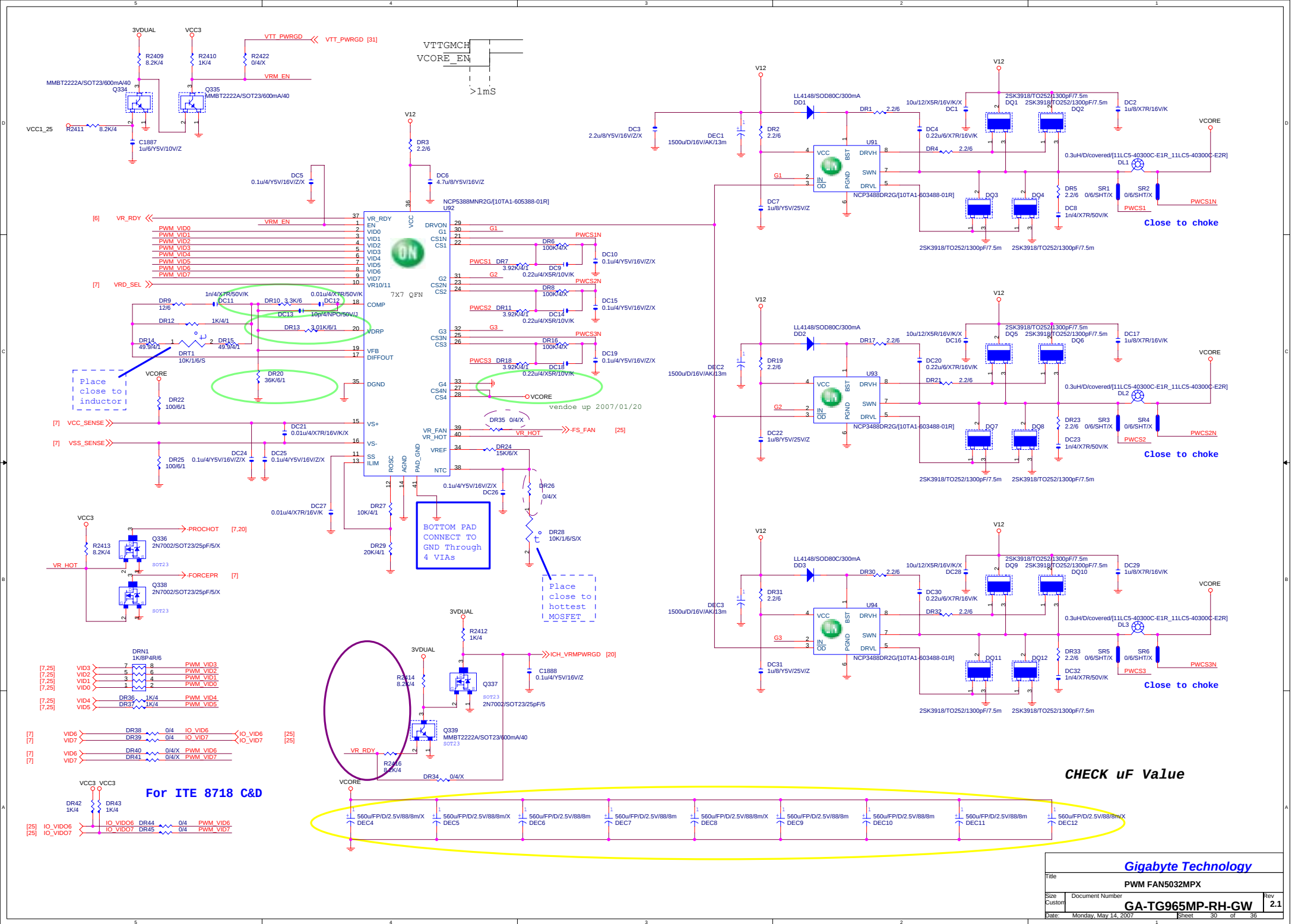
SURR BACK



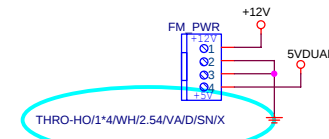
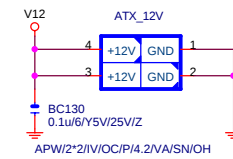
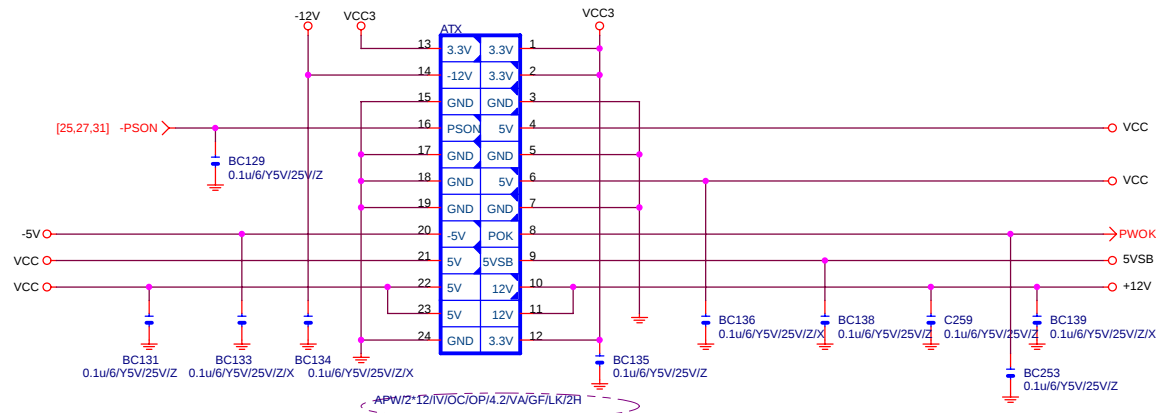
AZALIA FRONT PANEL



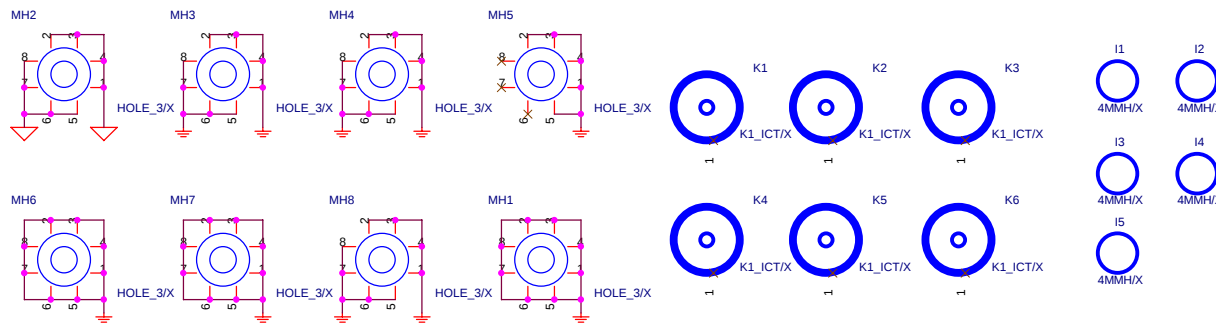
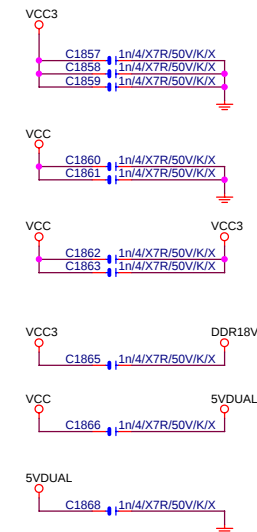
for meet vista frequency response request



ATX POWER CONNECTOR

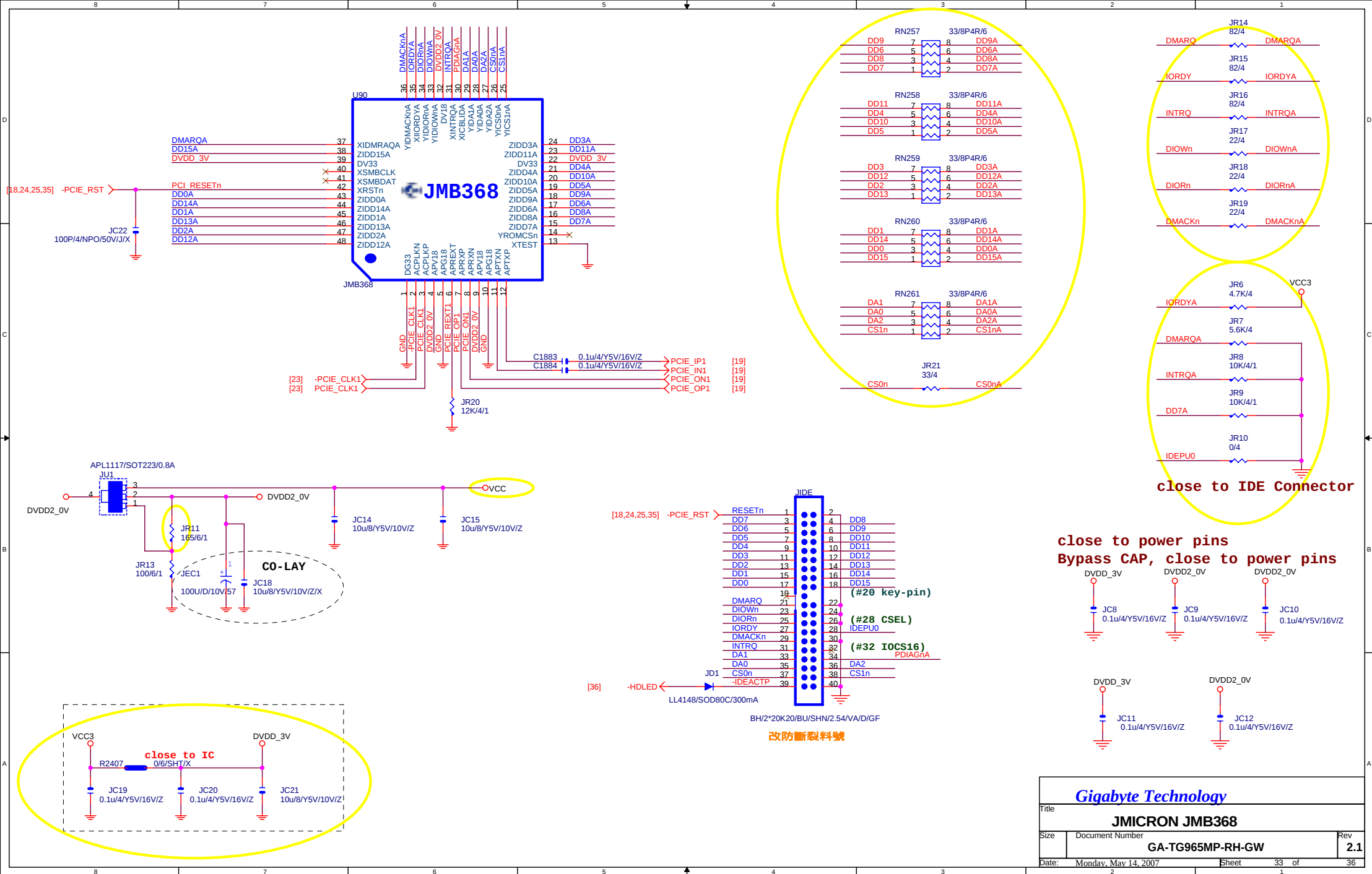


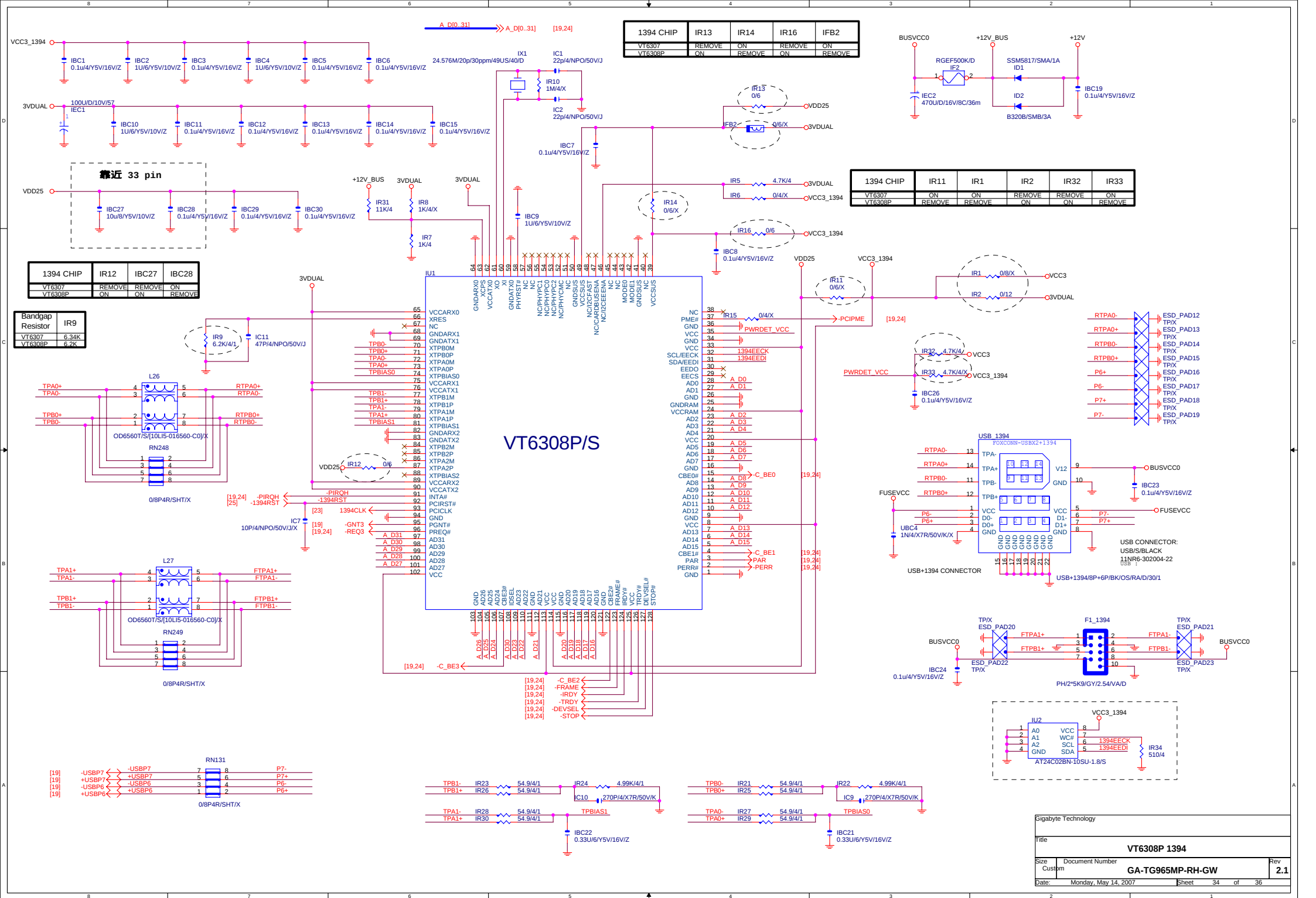
EMI



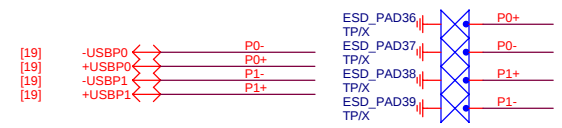
Gigabyte Technology

Title			
ATX POWER CONNECTOR			
Size	Document Number	Rev	
B		2.1	
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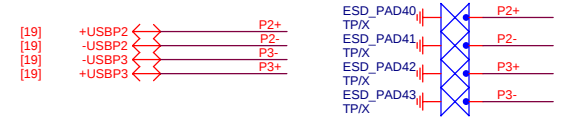




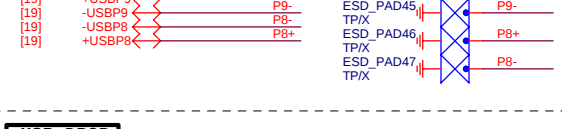
FRONT USB1



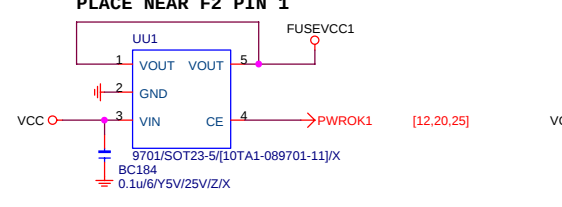
FRONT USB2



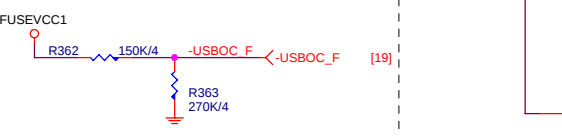
FRONT USB3



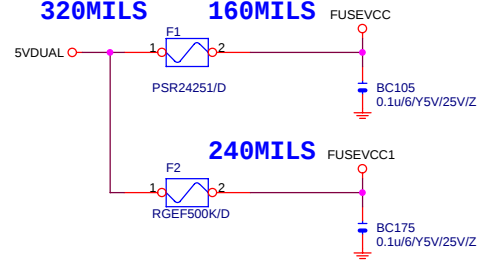
USB DROP



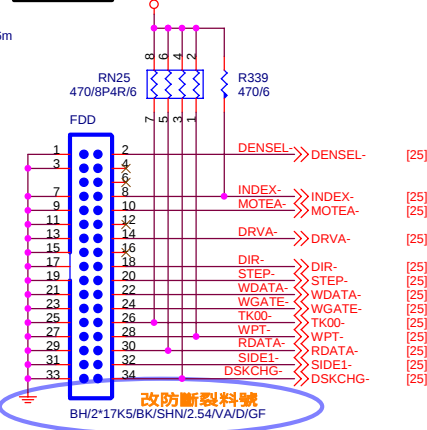
FRONT USB OC1



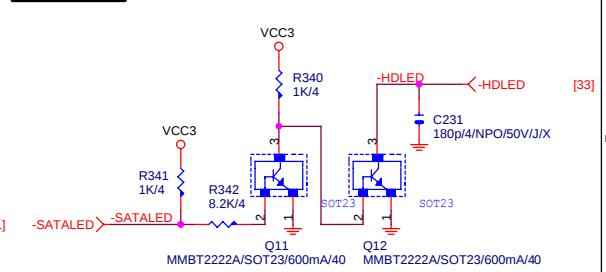
USB POWER



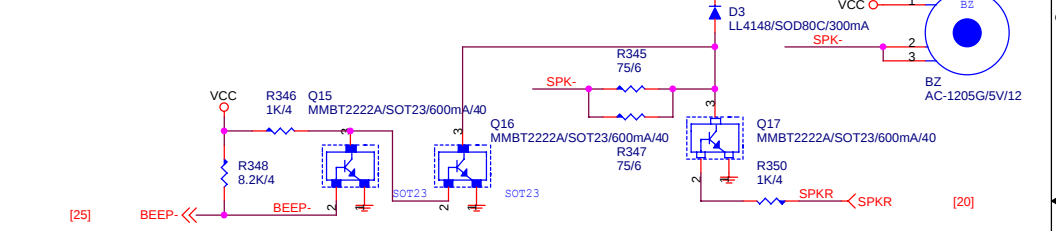
FLOPPY



SATA LED



BUZZER



INTEL FRONT PANEL

