

	1	2	3	4	5	6	7	8																																																																																
A									A																																																																															
B									B																																																																															
C									C																																																																															
D									D																																																																															
E									E																																																																															
F									F																																																																															
ASPEN UMA MV - BUILD FINAL BUILD 2004.12.13																																																																																								
<table><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td>DATE</td><td>CHANGE NO.</td><td>REV</td></tr></table>																											DATE	CHANGE NO.	REV	<table><tr><td>DRAWER</td><td>EE</td><td>DATE</td><td>POWER</td><td>DATE</td><td rowspan="4">INVENTEC</td></tr><tr><td>DESIGN</td><td></td><td></td><td></td><td></td></tr><tr><td>CHECK</td><td></td><td></td><td></td><td></td></tr><tr><td>RESPONSIBLE</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="5">SIZE = 3</td><td>VER :</td><td rowspan="2">TITLE ASPEN-UMA</td></tr><tr><td colspan="5">FILE NAME : XXXX-XXXXXX-XX</td><td></td></tr><tr><td>P/N</td><td colspan="4">XXXXXXXXXXXX</td><td>SIZE A3</td><td>CODE CS</td><td>DOC. NUMBER</td><td>REV</td></tr><tr><td colspan="5"></td><td colspan="2">SHEET</td><td colspan="2">OF</td></tr></table>							DRAWER	EE	DATE	POWER	DATE	INVENTEC	DESIGN					CHECK					RESPONSIBLE					SIZE = 3					VER :	TITLE ASPEN-UMA	FILE NAME : XXXX-XXXXXX-XX						P/N	XXXXXXXXXXXX				SIZE A3	CODE CS	DOC. NUMBER	REV						SHEET		OF	
DATE	CHANGE NO.	REV																																																																																						
DRAWER	EE	DATE	POWER	DATE	INVENTEC																																																																																			
DESIGN																																																																																								
CHECK																																																																																								
RESPONSIBLE																																																																																								
SIZE = 3					VER :	TITLE ASPEN-UMA																																																																																		
FILE NAME : XXXX-XXXXXX-XX																																																																																								
P/N	XXXXXXXXXXXX				SIZE A3	CODE CS	DOC. NUMBER	REV																																																																																
					SHEET		OF																																																																																	
	1	2	3	4	5	6	7	8																																																																																

TABLE OF CONTENTS

PAGE
5- DC& BATTERY CHANGER
6- SELECT & BATTERY CONN
7- SYSTEM POWER(3V/5V/12V)
8- SYSTEM POWER(V1.2L/V2.5S)
9- SYSTEM POWER(VGAVCC/V1.5A)
10- D/B SYSTEM POWER(V1.8/VCCP)
11- CPU POWER(VCC_CORE)
12- SYSTEM POWER (V0.9)
13- POWER(SLEEP)
14- POWER(SEQUENCE)
15- CLOCK_GENERATOR
16- DOTHAN-1
17- DOTHAN-2
18- DOTHAN-3
19- DOTHAN-4
20- THERMAL&FAN CONTROLLER
21- ALVISO-1
22- ALVISO-2
23- ALVISO-3
24- ALVISO-4
25- ALVISO-5
26- DDR2-DIMM0
27- DDR2-DIMM1
28- DDR2-DAMPING

PAGE
29- DVI-CONTROLLER
30- BLANK
31- BLANK
32- BLANK
33- BLANK
34- BLANK
35- CRT& SVEDIO CONN
36- LCD CONN
37- ICH6-1
38- ICH6-2
39- ICH6-3
40- ICH6-4
41- ICH6-5
42- KBC
43- FWH
44- INT.KBC/POINT DEVICES
45- SUPER I/O
46- SERIAL PORT (SERIAL DAUGHTER BOARD)
47- BLANK
48- AC97 CODEC
49- EQ&MIC JACK (USB DAUGHTER BOARD)
50- AUDIO AMP&HP JACK

PAGE
51- HDD CONN
52- MULTIBAY CONN
53- USB&IR CONN (USB DAUGHTER BOARD)
54- CARDBUS CONTROLL
55- PC CARD SLOT
56- SD CARD CONN
57- BLANK
58- MINIPCI CONN
59- LAN INTERFACE-1
60- LAN RJ45 CONN
61- DOCKING CONN
62- SCREW
63- MDC CONN
64- TCPA CONN
65- BLUETOOTH
66- LED / LID_SWITCH
67- LED BOARD / CNN (LED DAUGHTER BOARD)

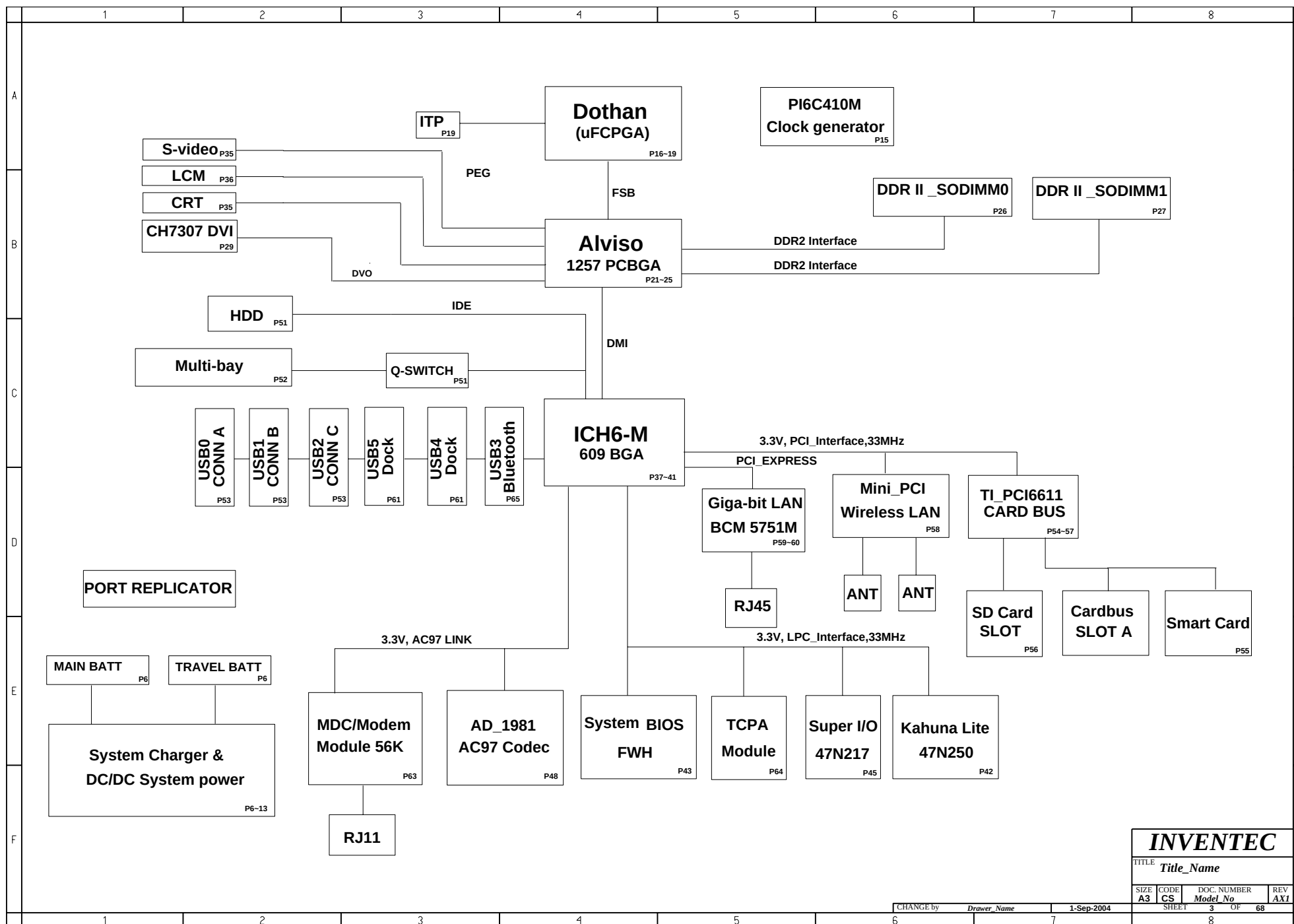
INVENTEC

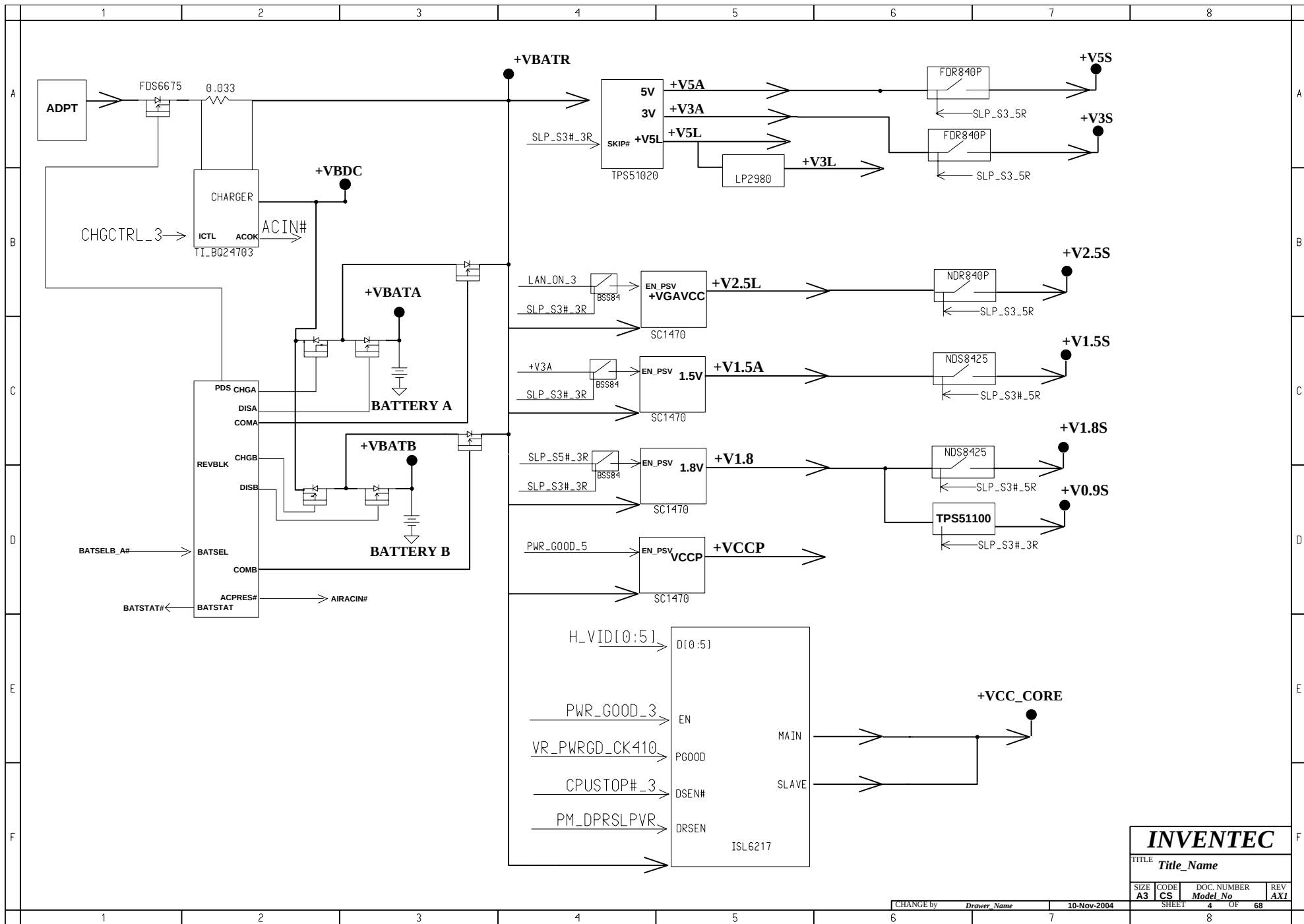
TITLE *Title_Name*

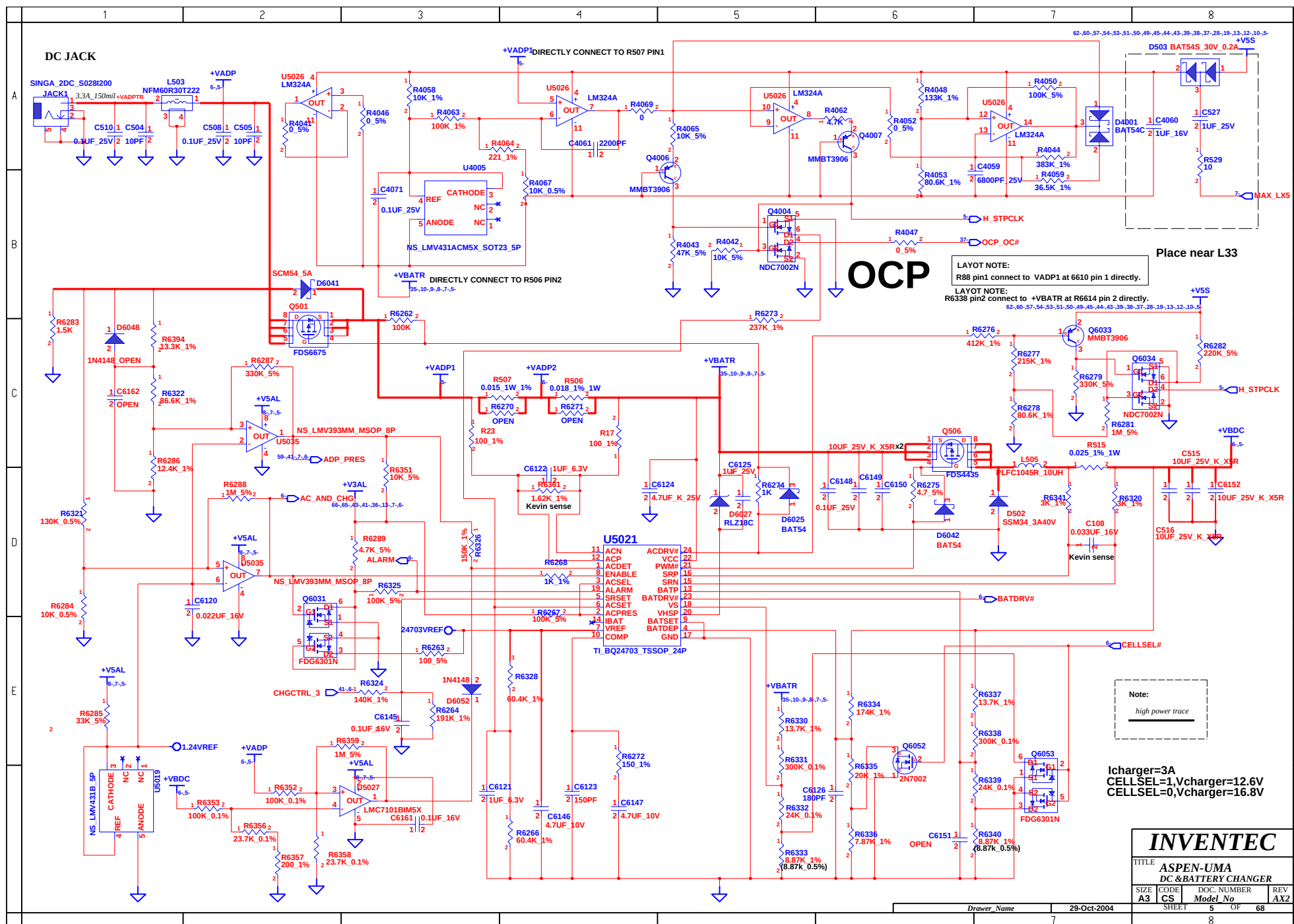
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
-------------------	-------------------	--------------------------------	-------------------

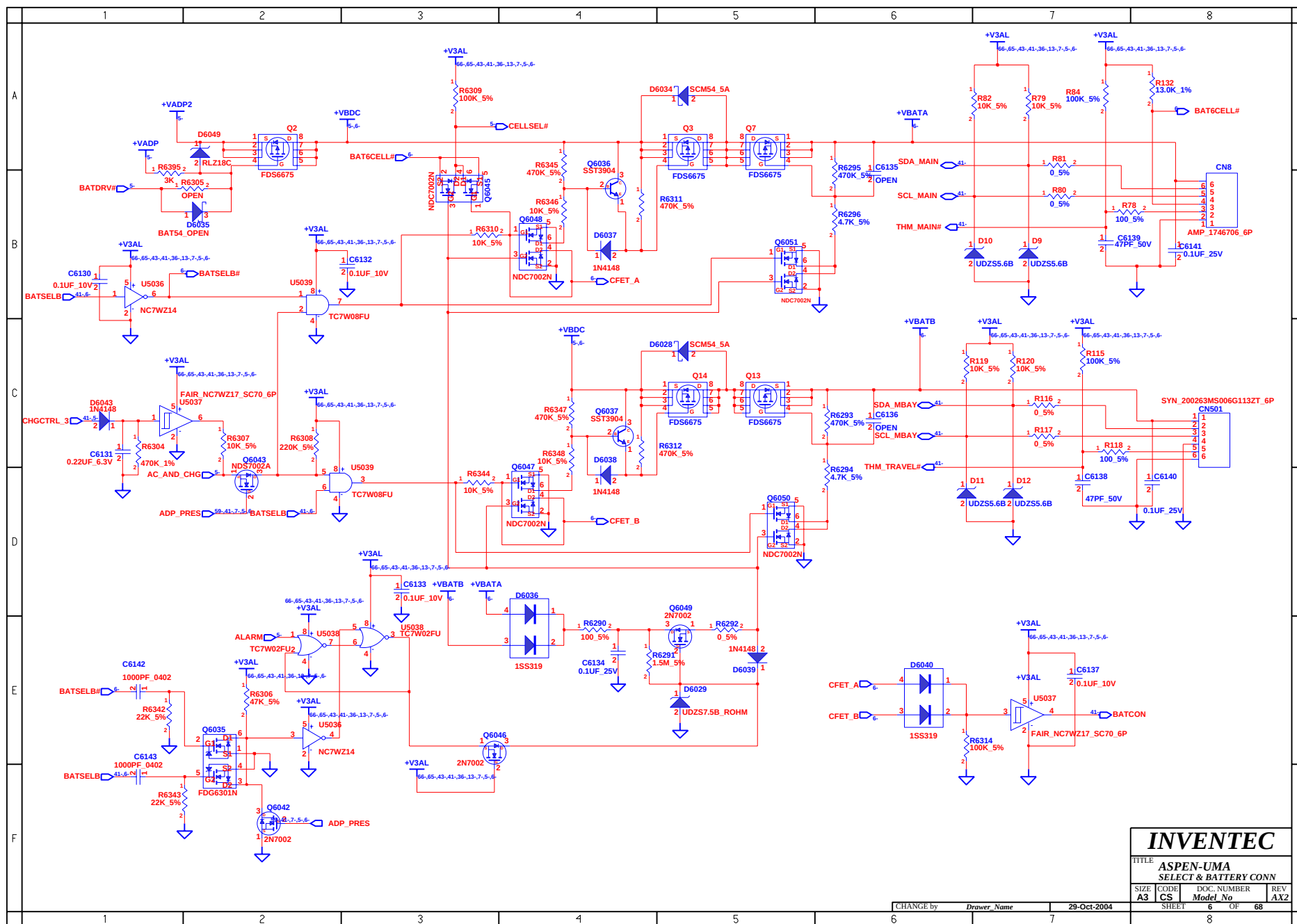
CHANGE by *Drawn Name* 1-Sep-2004

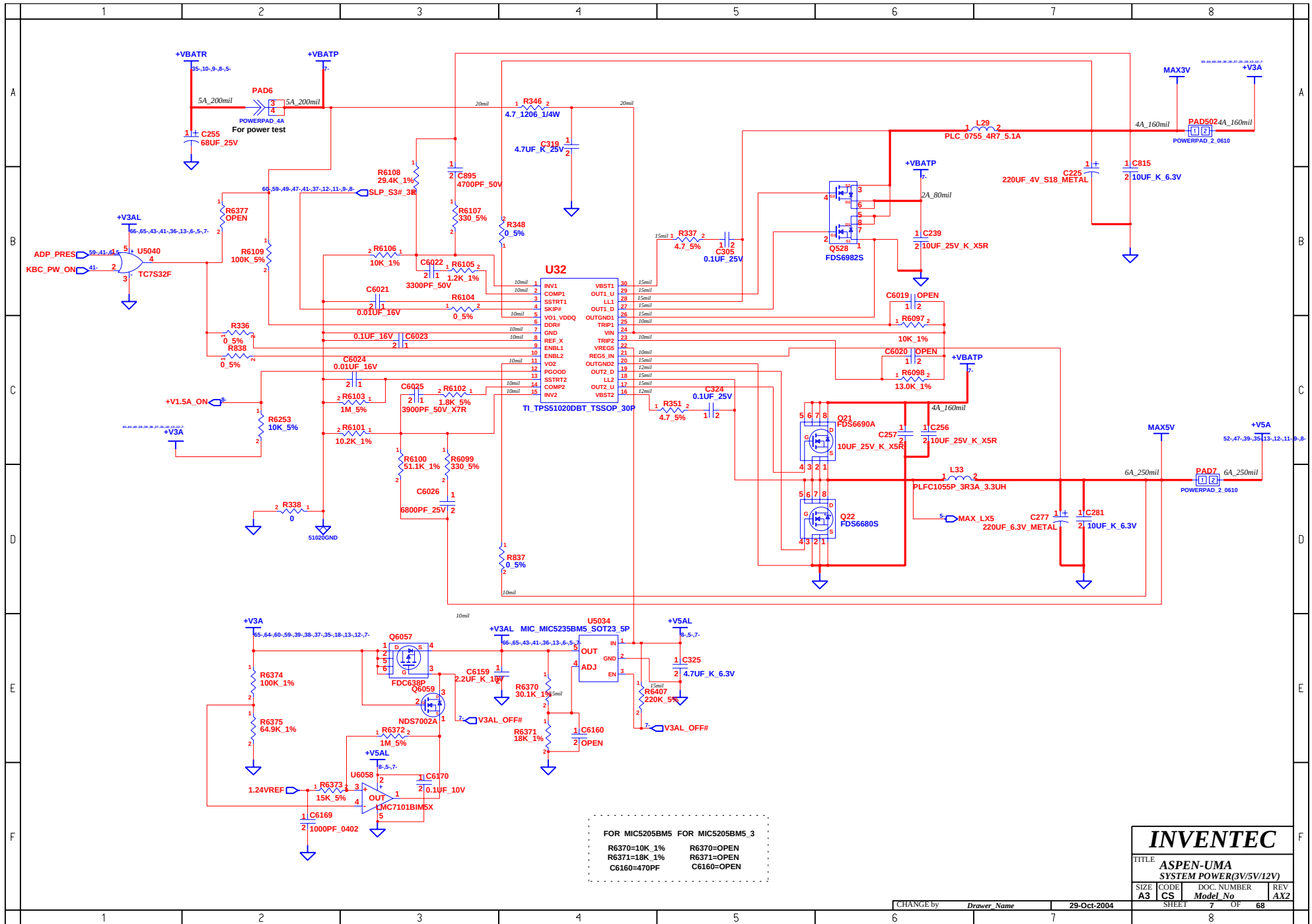
SHEET 2 OF 68

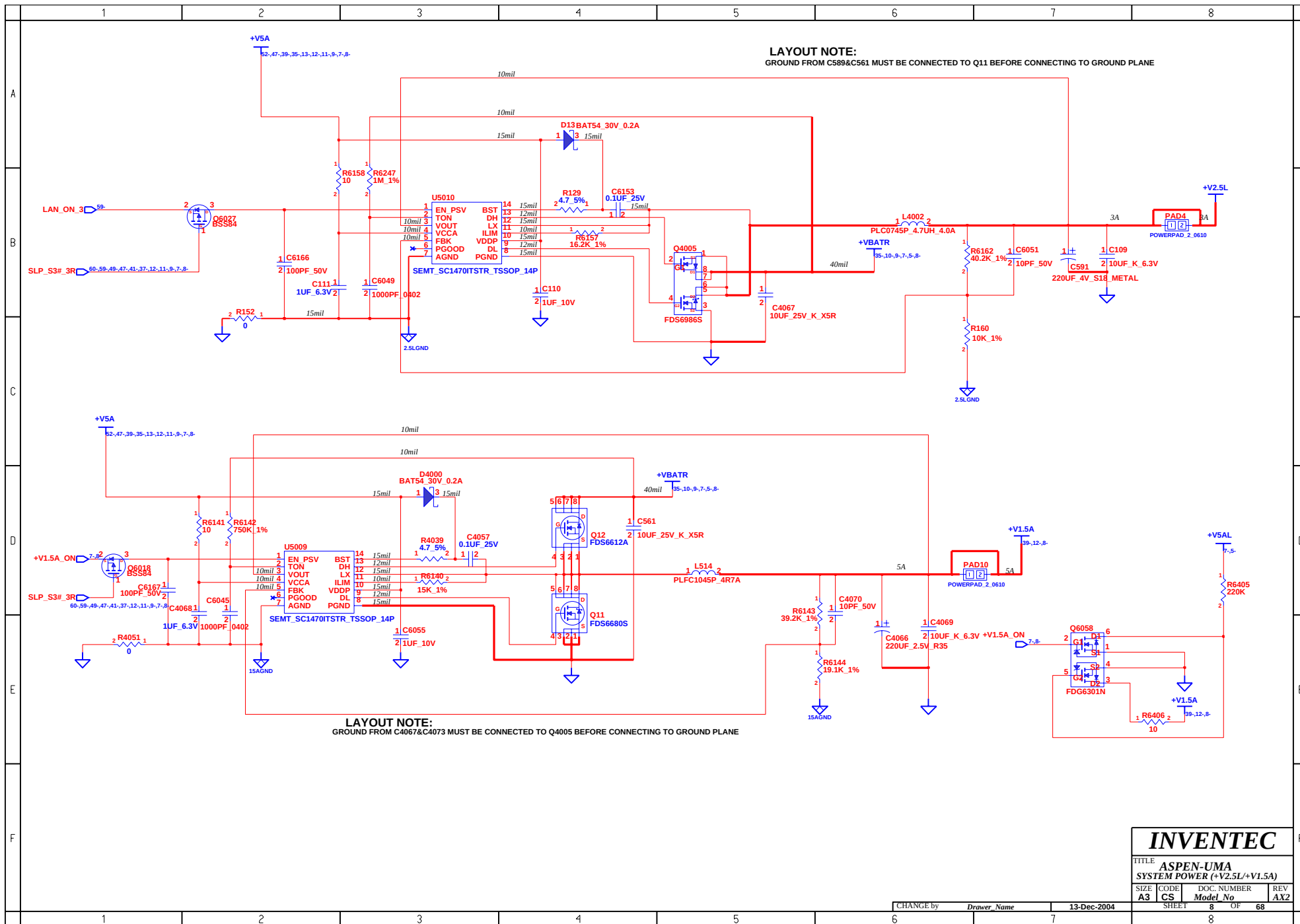


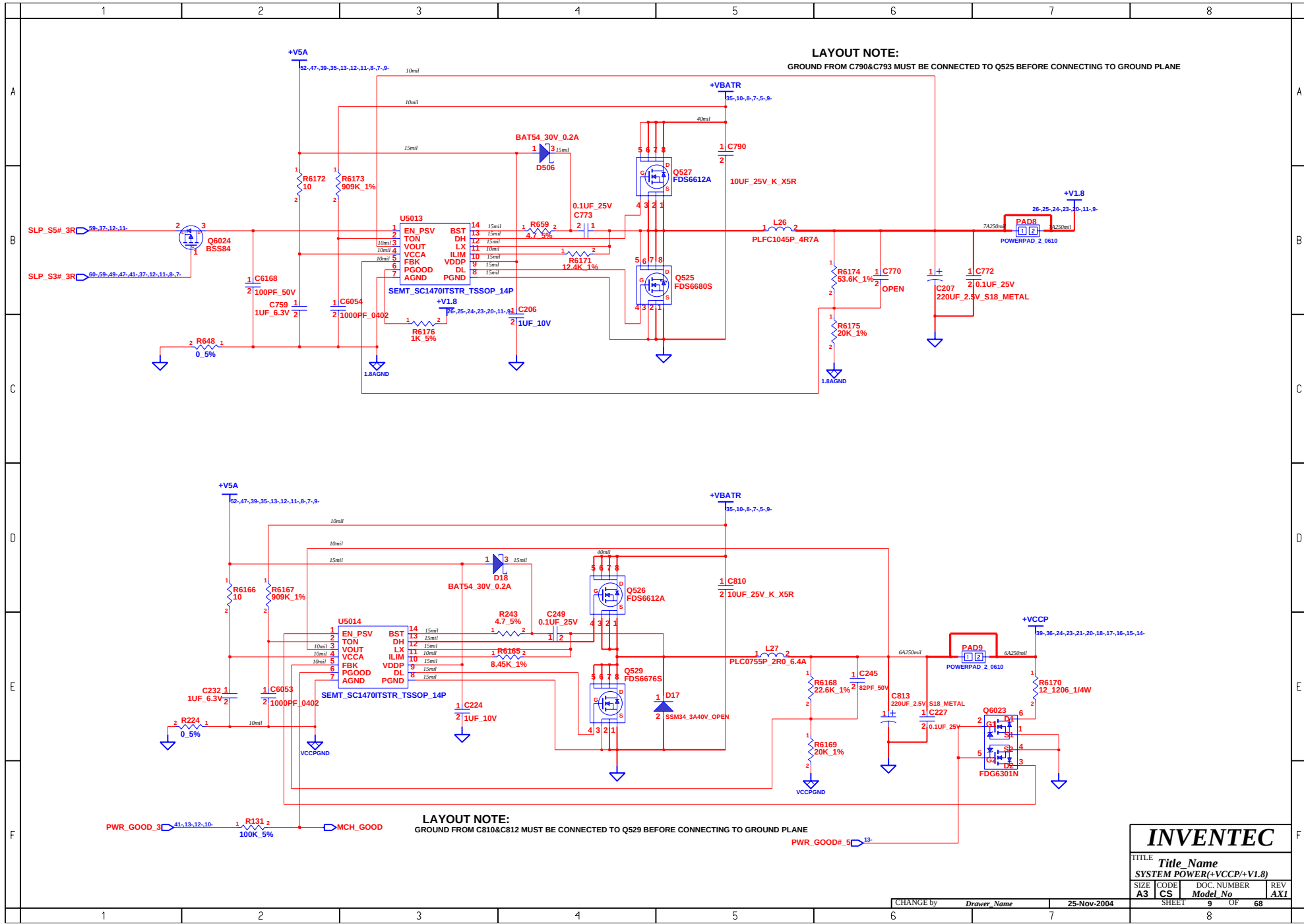












LAYOUT NOTE:
GROUND FROM C790&C793 MUST BE CONNECTED TO Q525 BEFORE CONNECTING TO GROUND PLANE

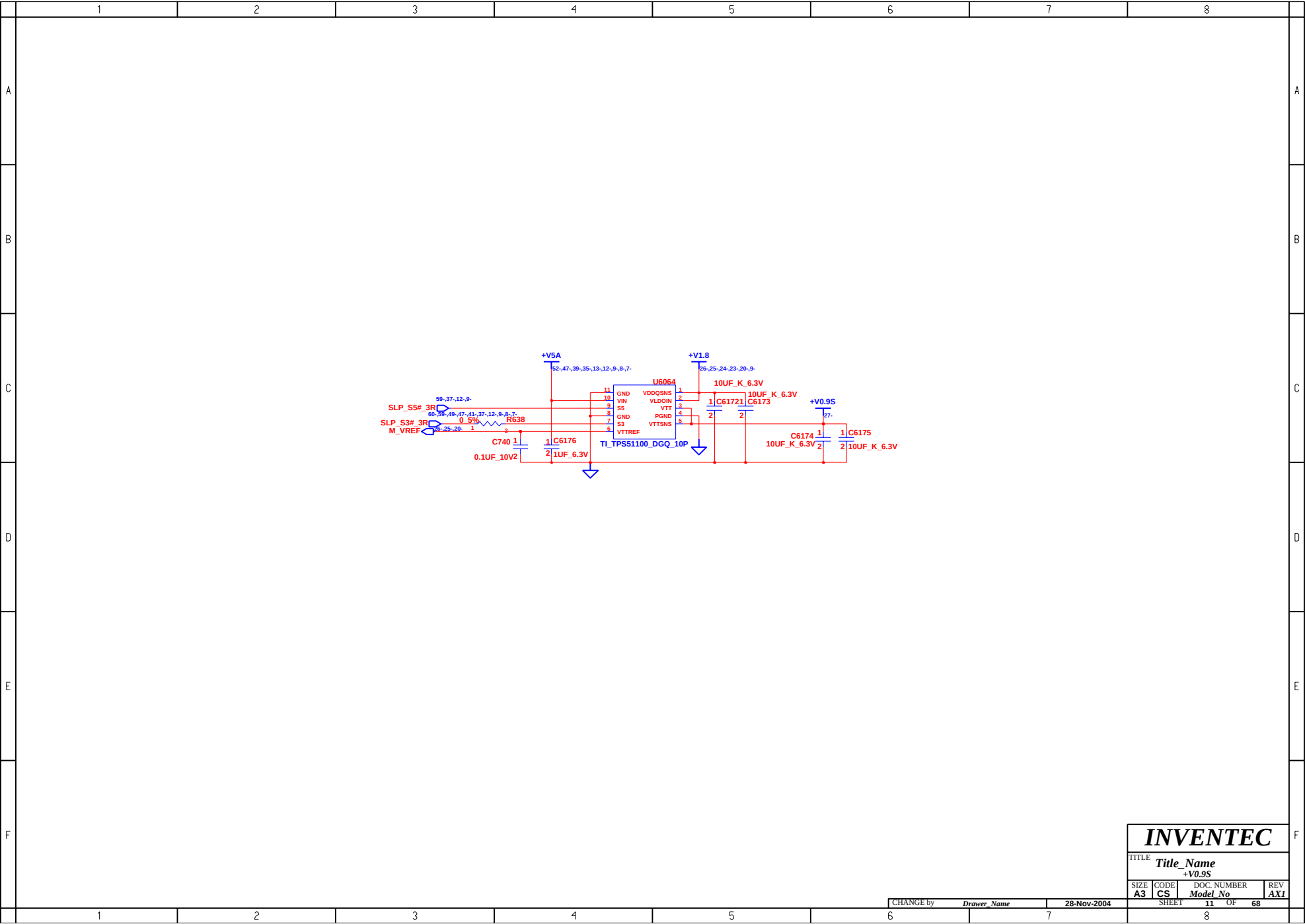
LAYOUT NOTE:
GROUND FROM C810&C812 MUST BE CONNECTED TO Q529 BEFORE CONNECTING TO GROUND PLANE

INVENTEC

TITLE			
Title Name			
SYSTEM POWER(+VCCP/+V1.8)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by *Drawer Name* 25-Nov-2004

SHEET 9 OF 68

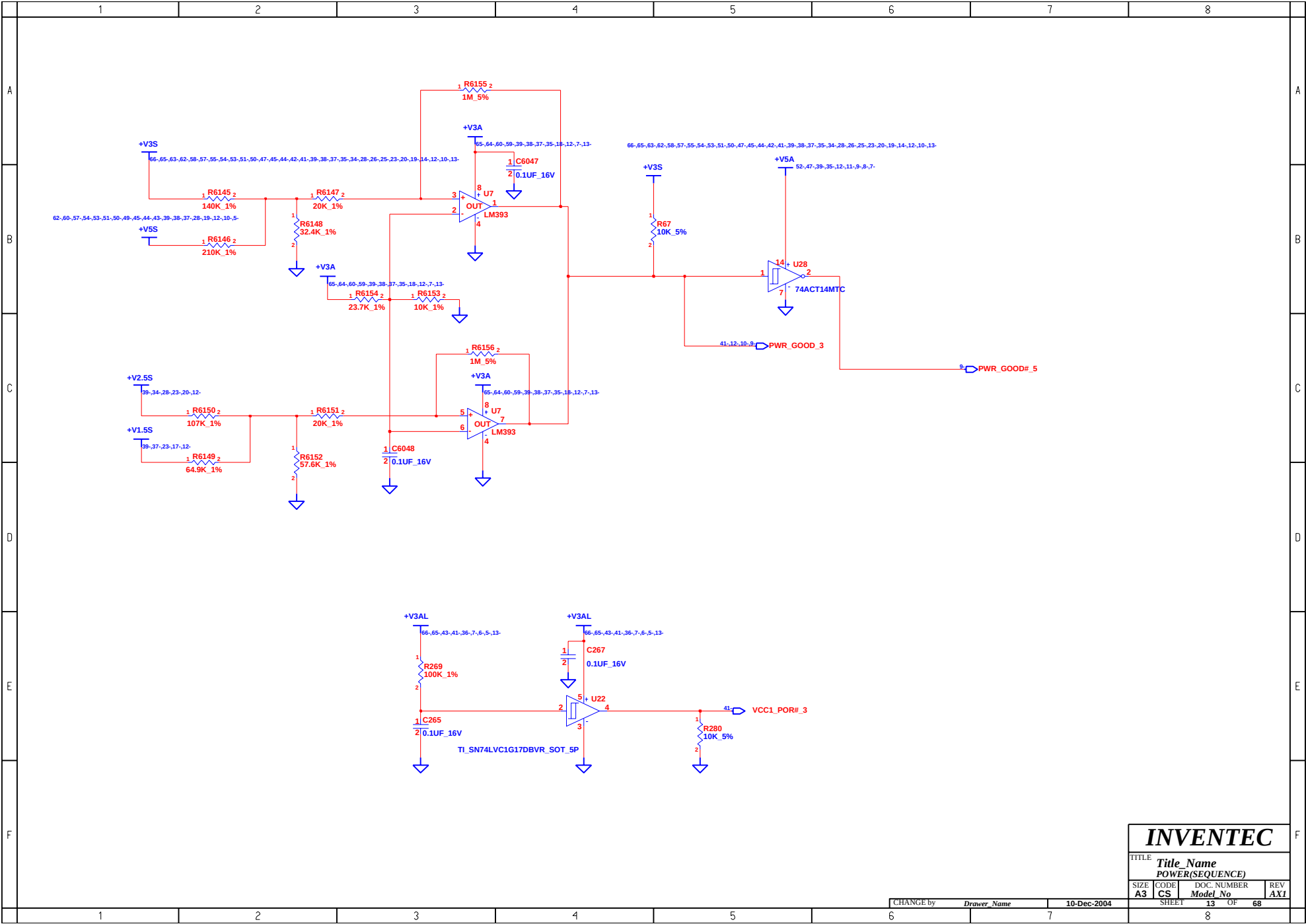


INVENTEC

TITLE			
Title_Name			
+V0.9S			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by Drawer Name 28-Nov-2004

SHEET 11 OF 68

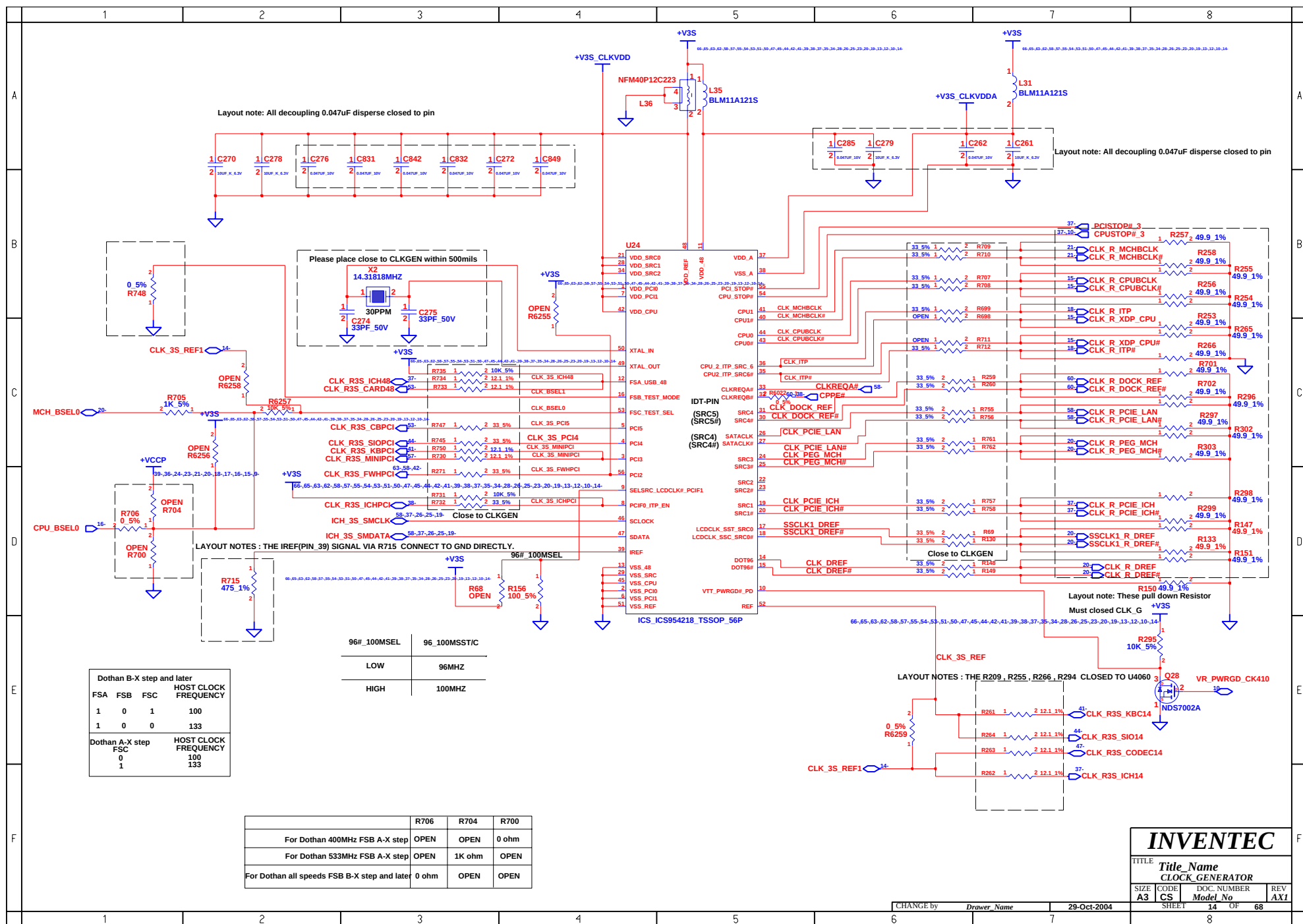


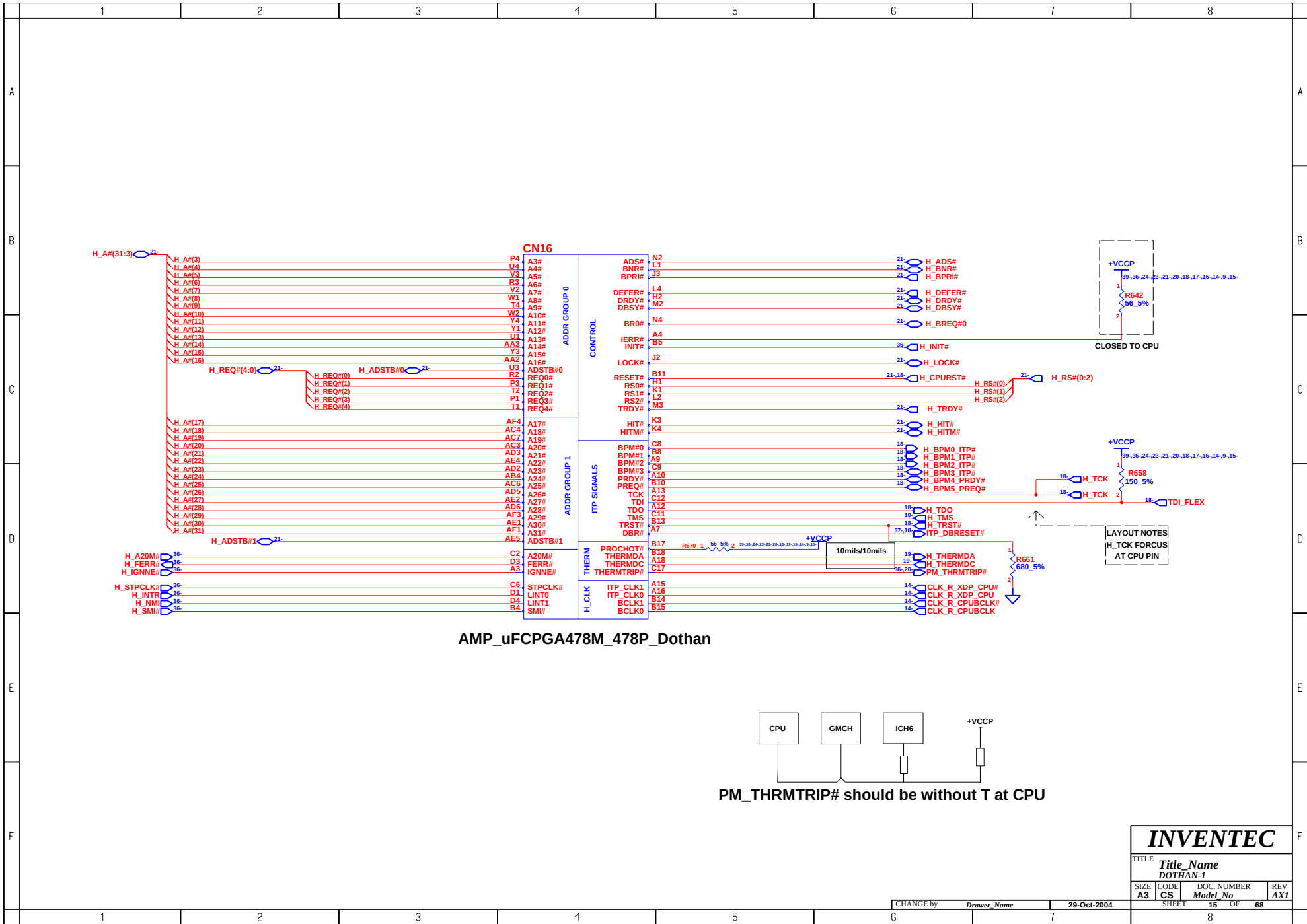
INVENTEC

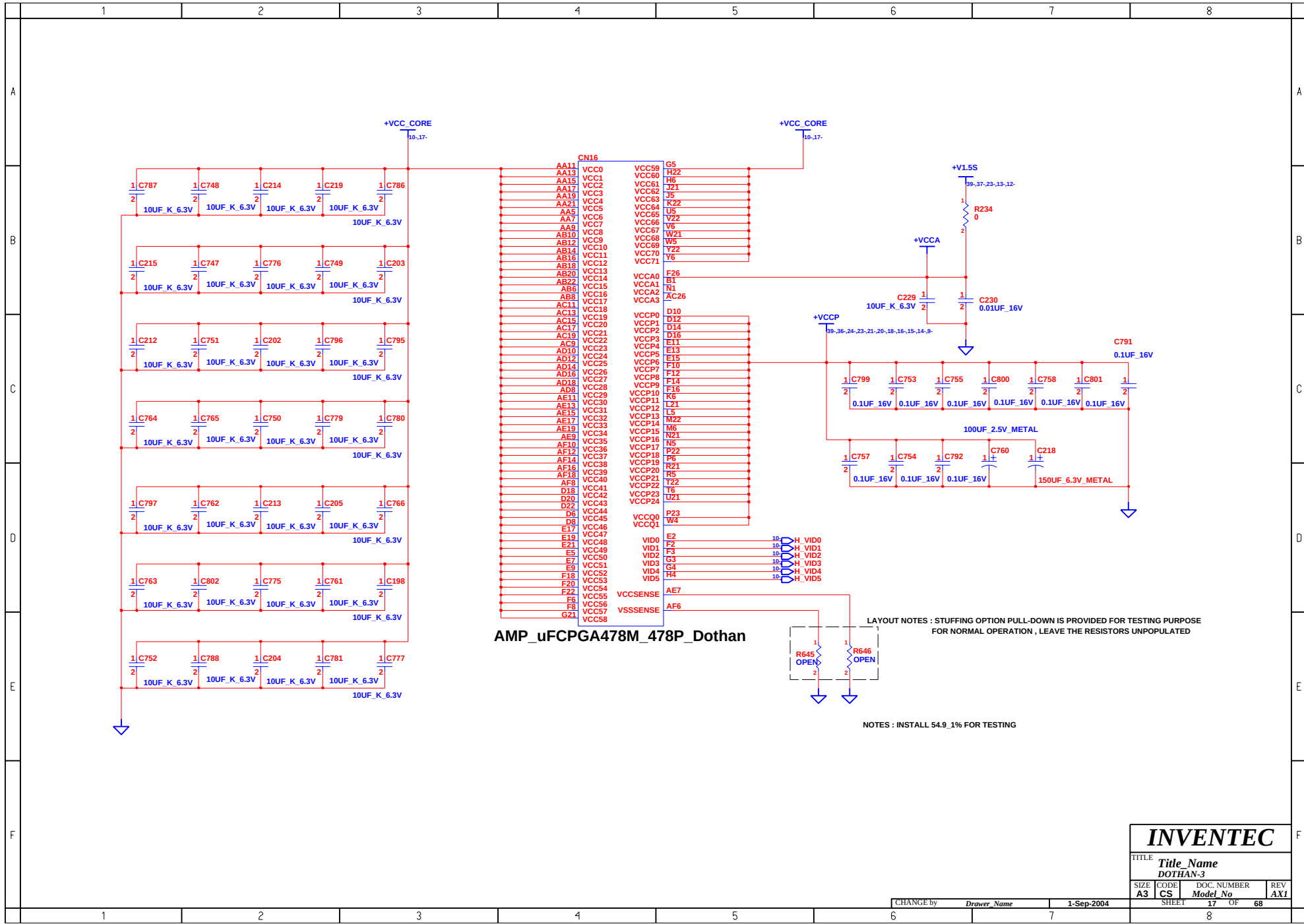
TITLE			
Title_Name			
POWER(SEQUENCE)			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by: *Drawer Name* 10-Dec-2004

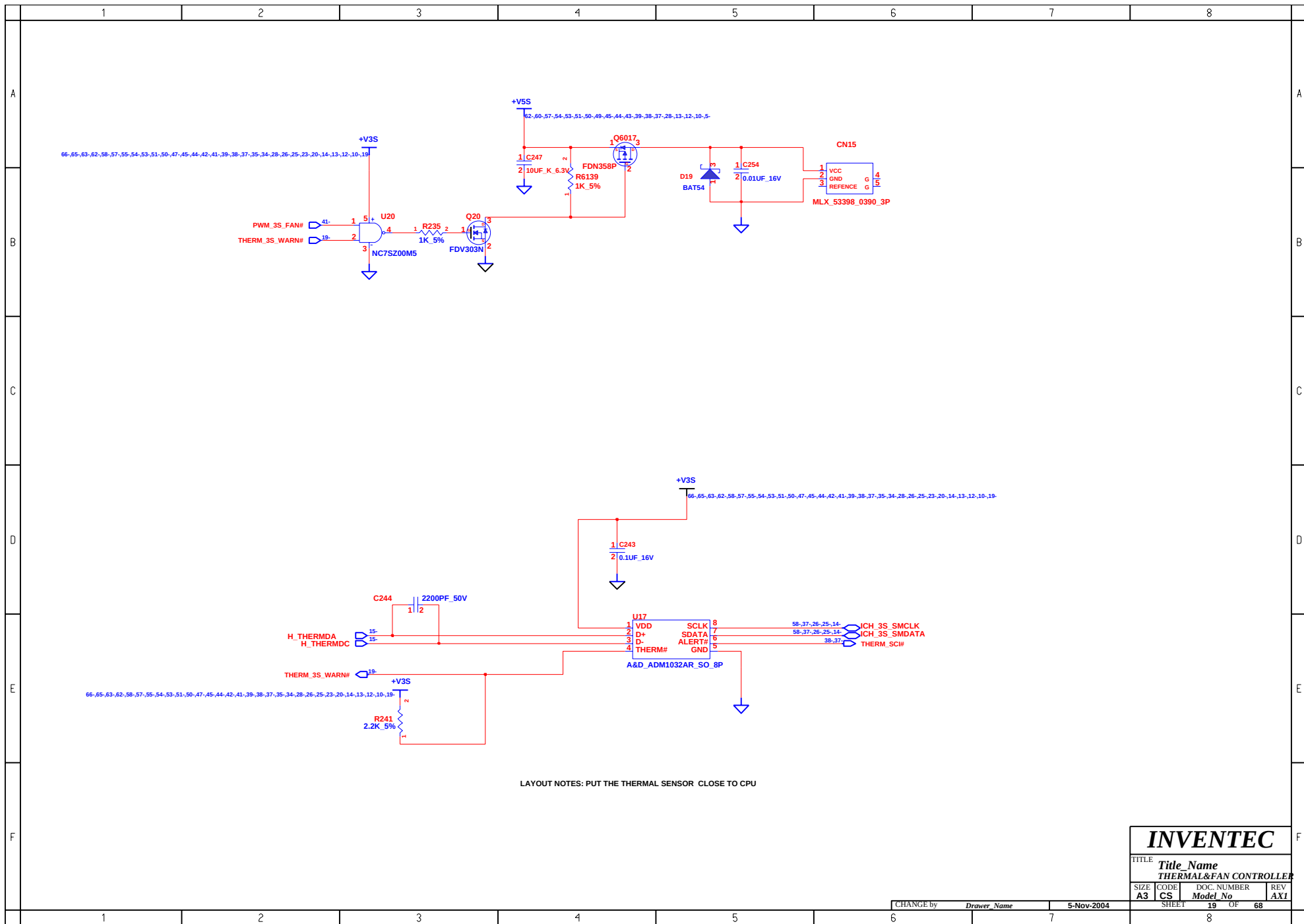
SHEET 13 OF 68



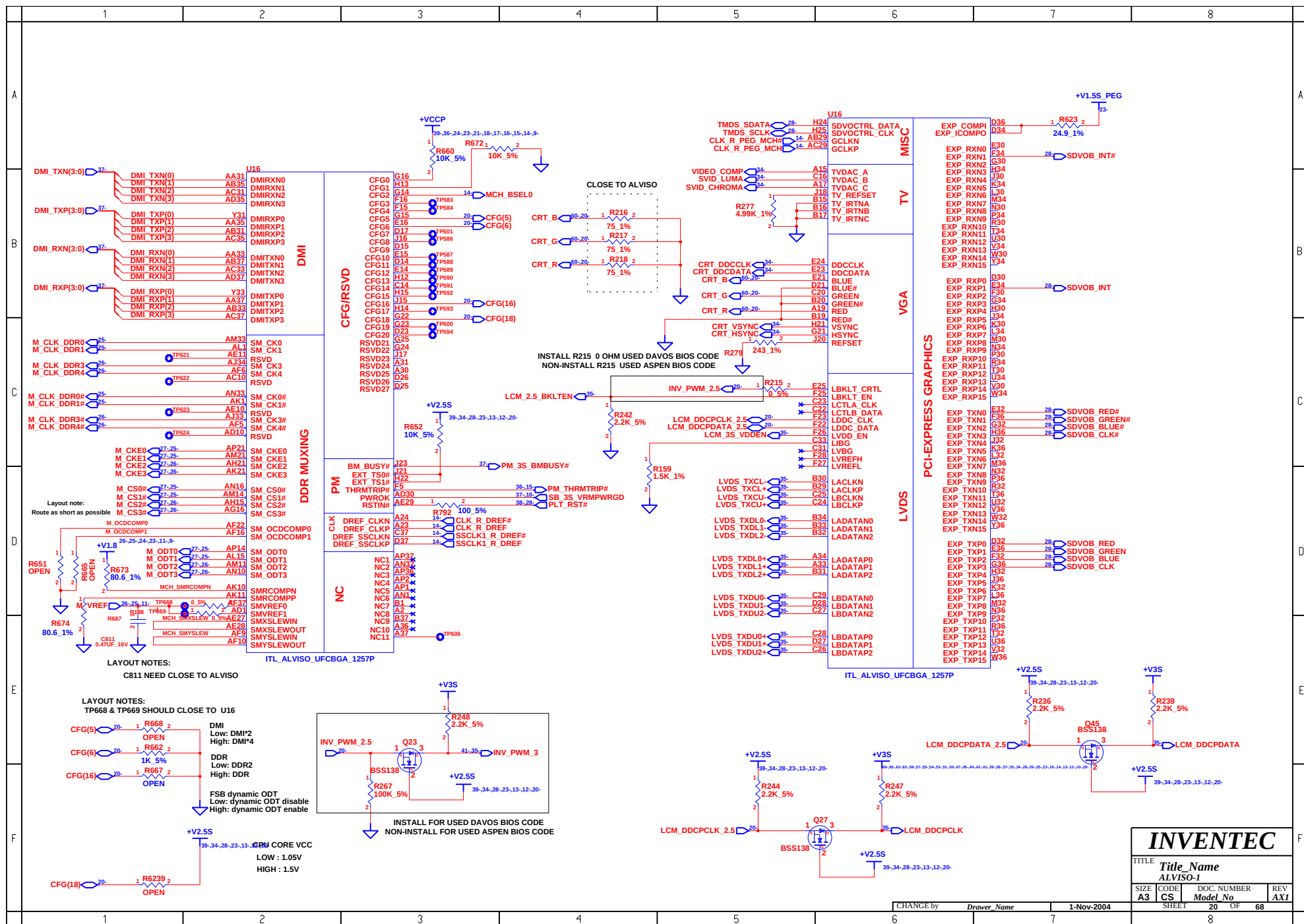


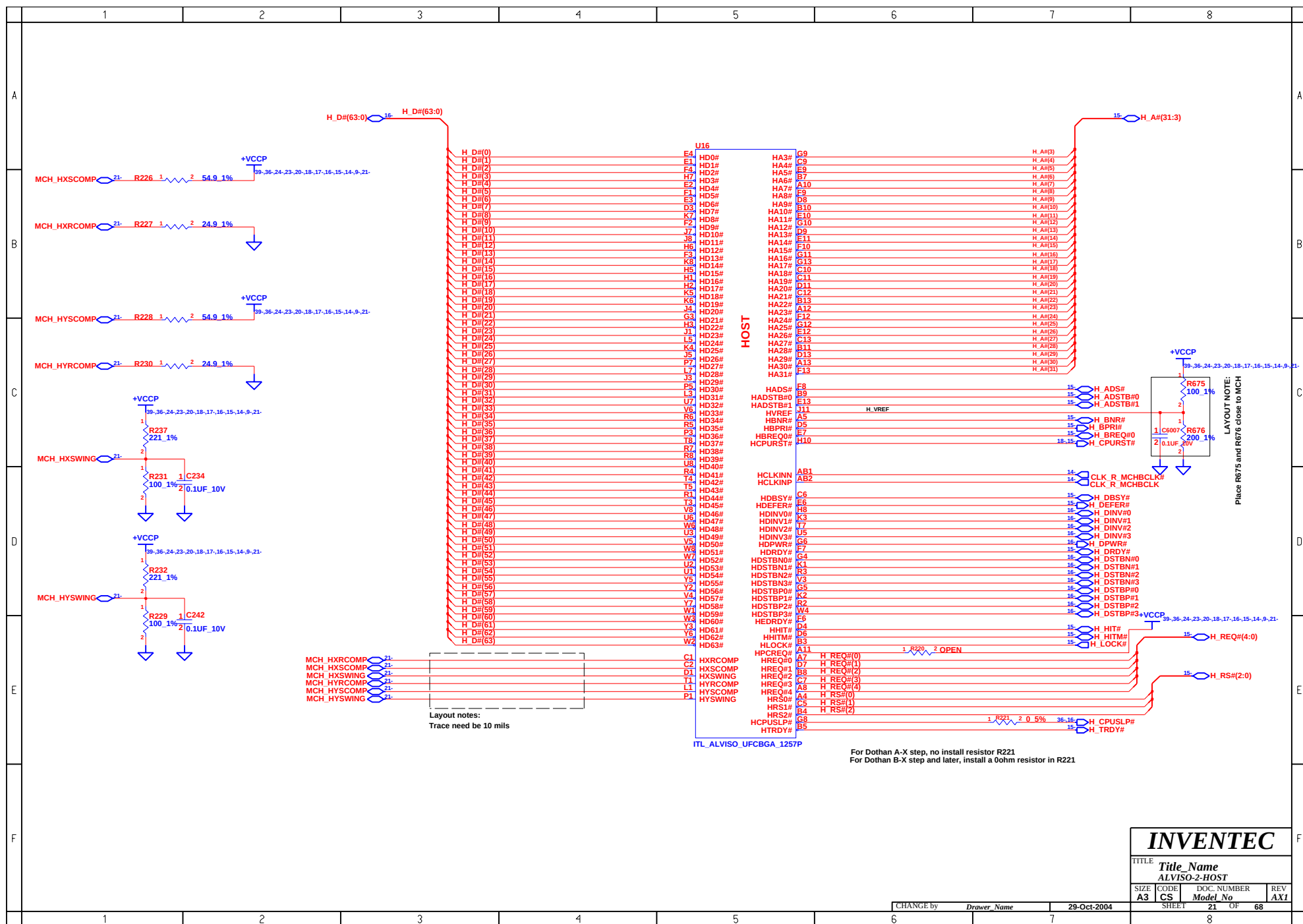


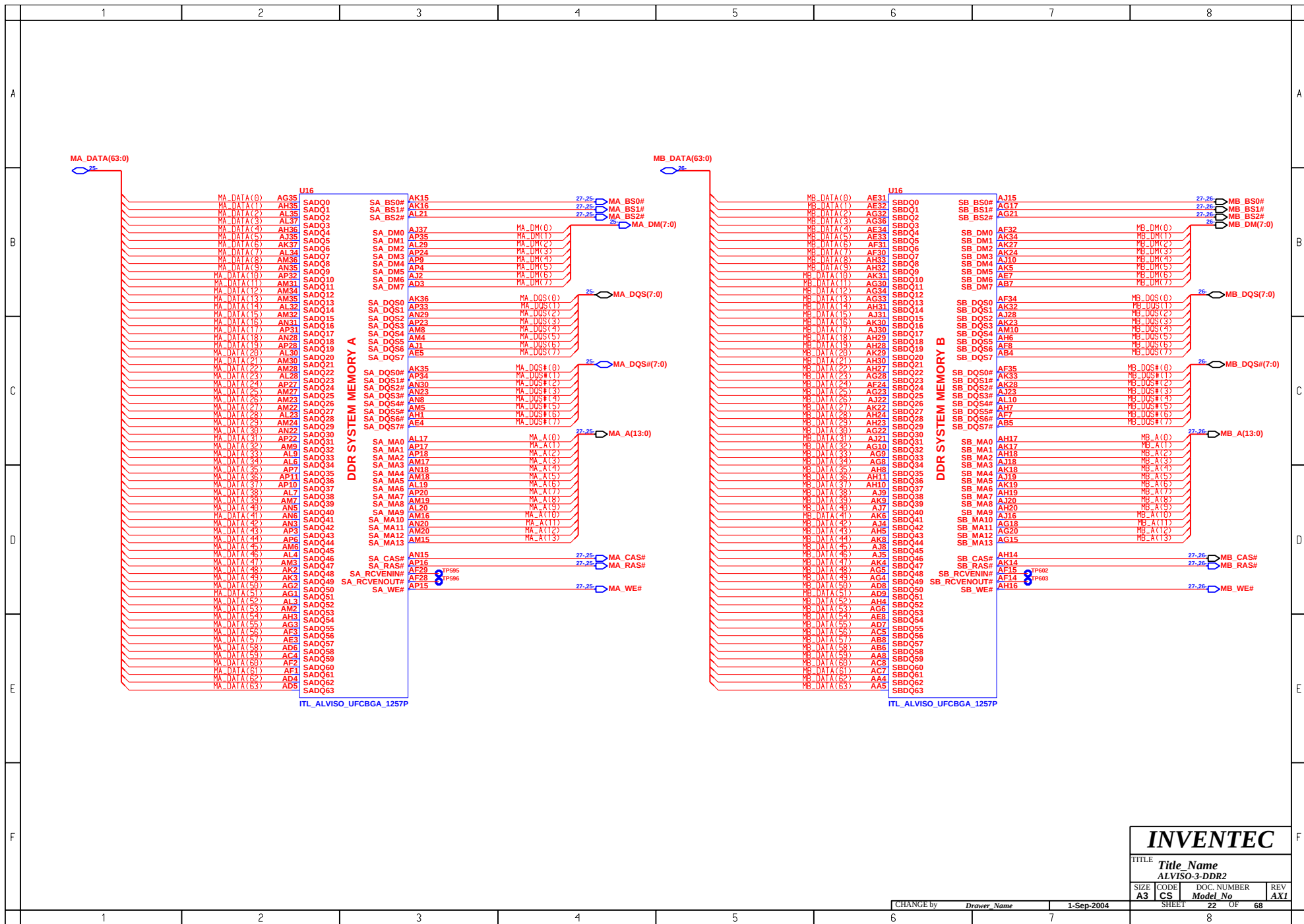
INVENTEC				
TITLE <i>Title_Name</i> DOTHAN-3				
SIZE A3	CODE CS	DOC. NUMBER <i>Model_No</i>		REV AX1
SHEET		17	OF	68
8				



INVENTEC			
TITLE			
Title_Name			
THERMAL&FAN CONTROLLER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1
CHANGE by		Drawer Name	5-Nov-2004
SHEET		19	OF 68





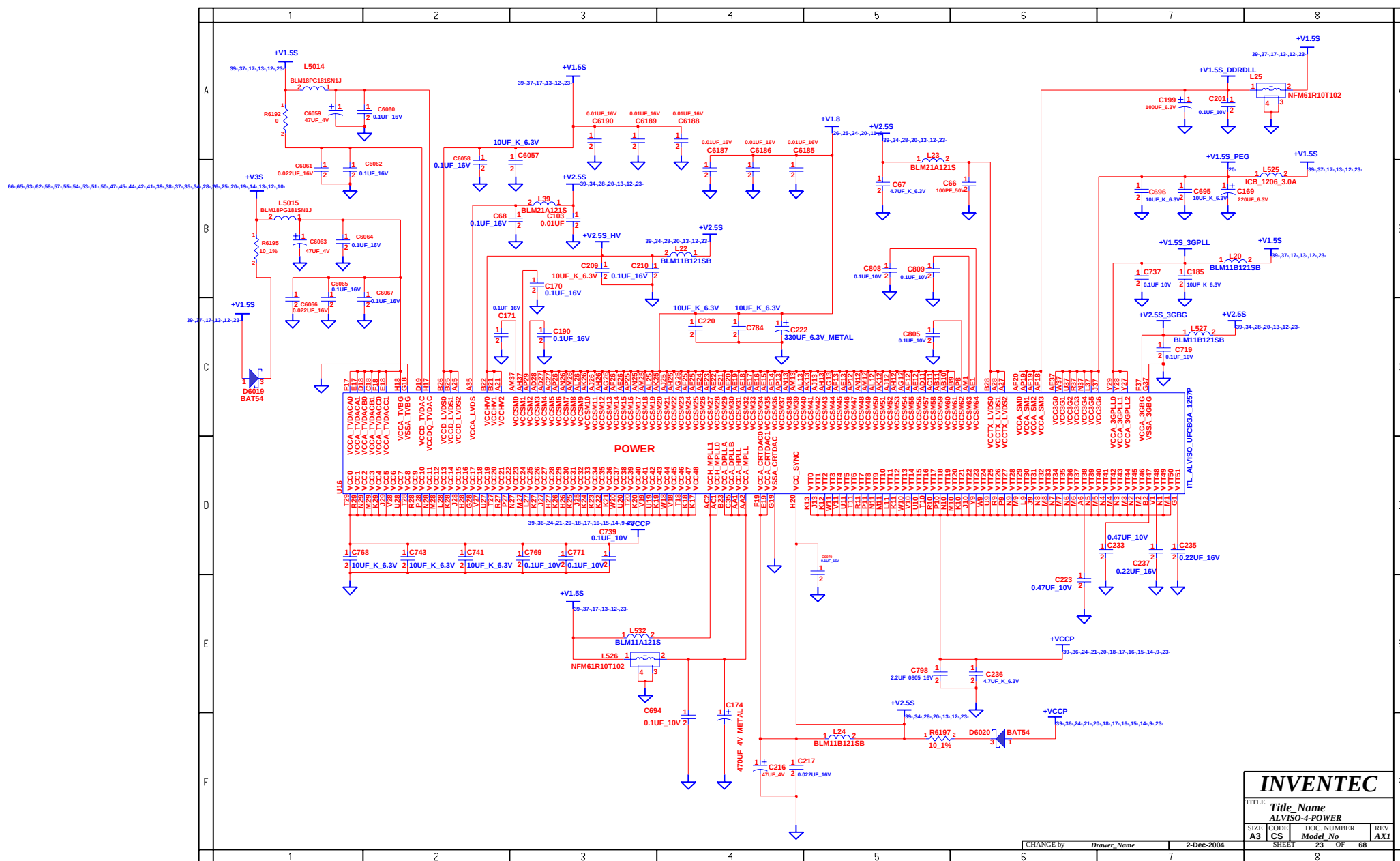


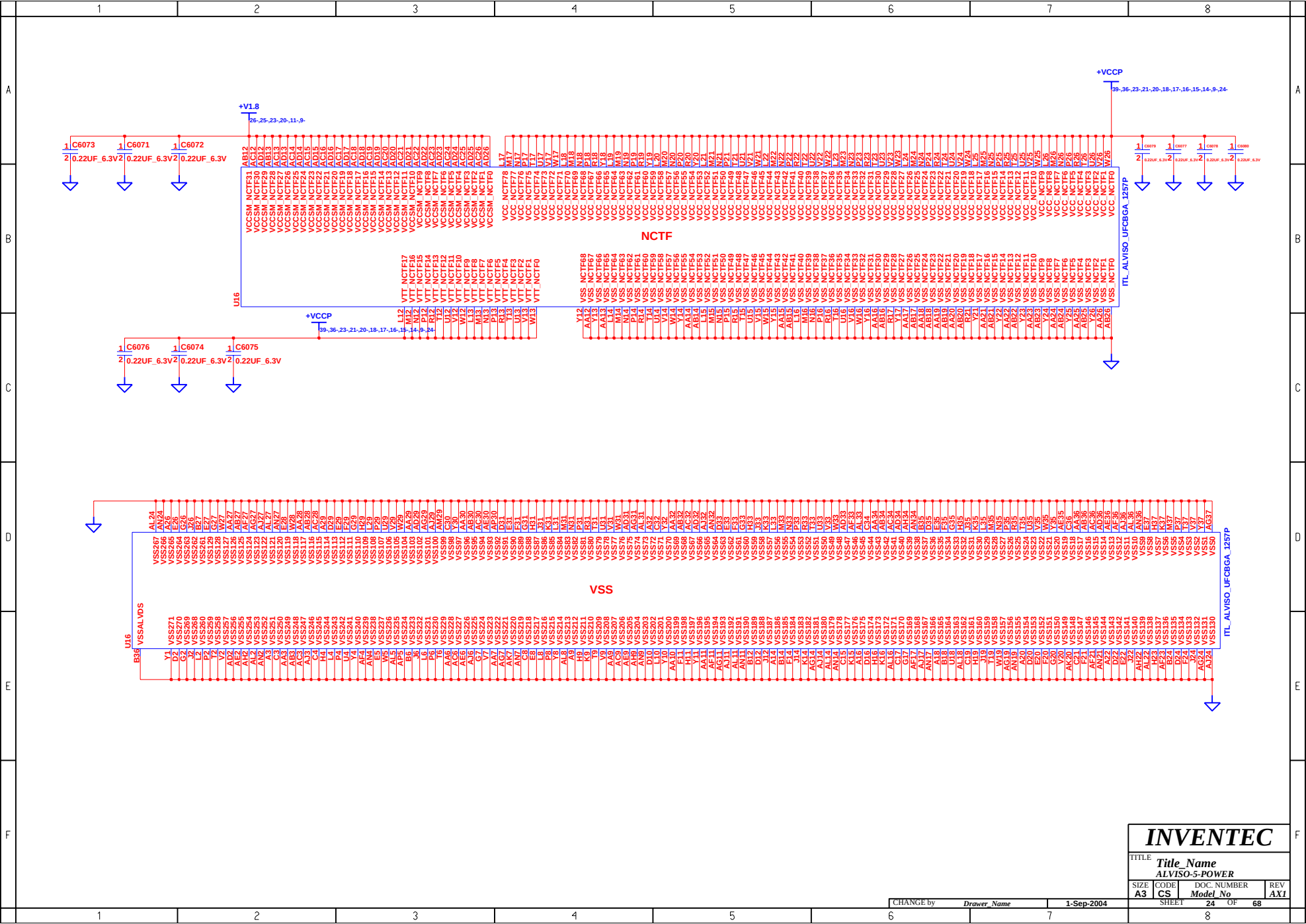
INVENTEC

TITLE			
Title Name			
ALVISO-3-DDR2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX1

CHANGE by Draver Name 1-Sep-2004

SHEET 22 OF 68



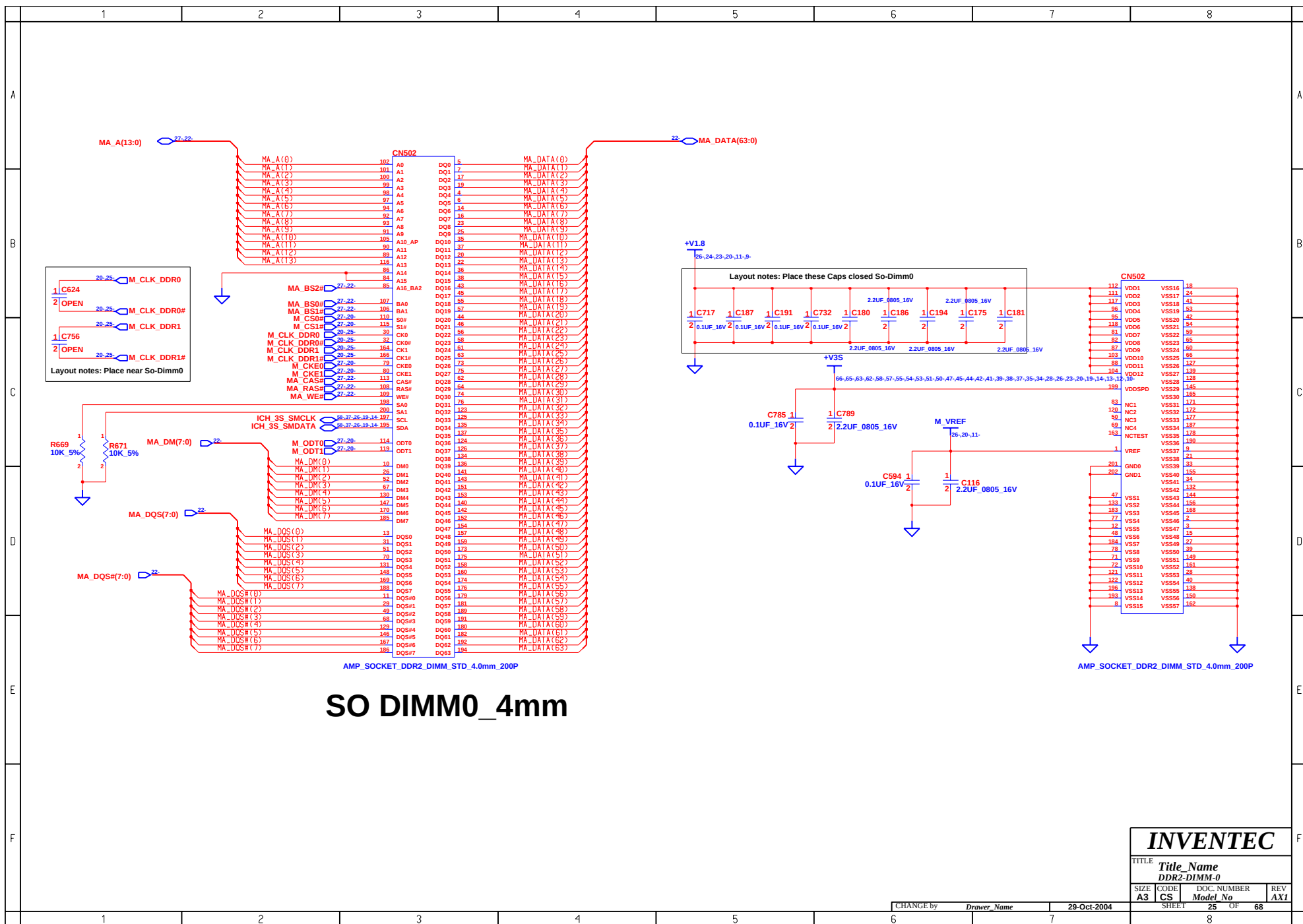


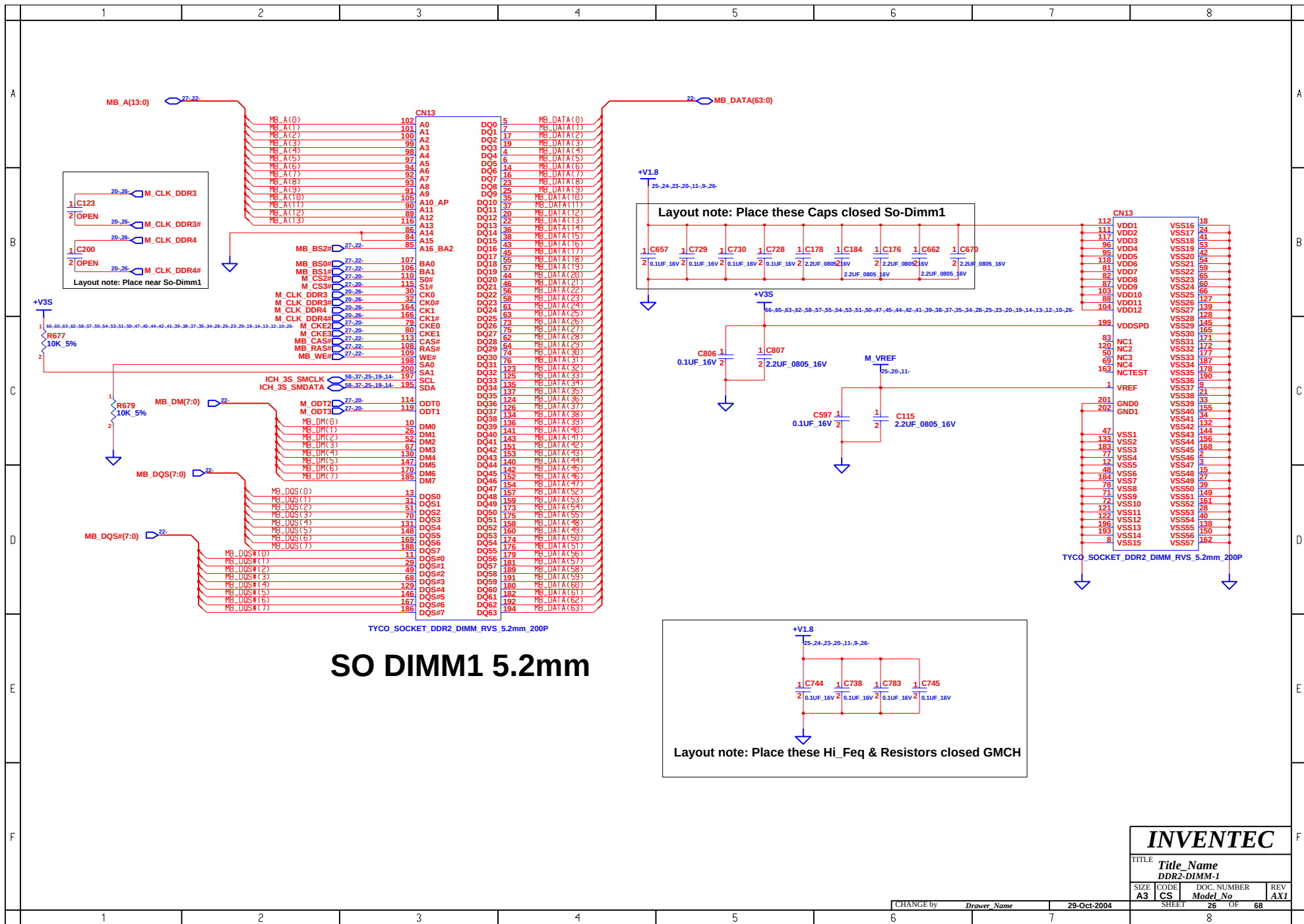
INVENTEC

TITLE			
Title Name			
ALVISO-5-POWER			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No.	AX1

CHANGE by *Drawn Name* 1-Sep-2004

SHEET 24 OF 68

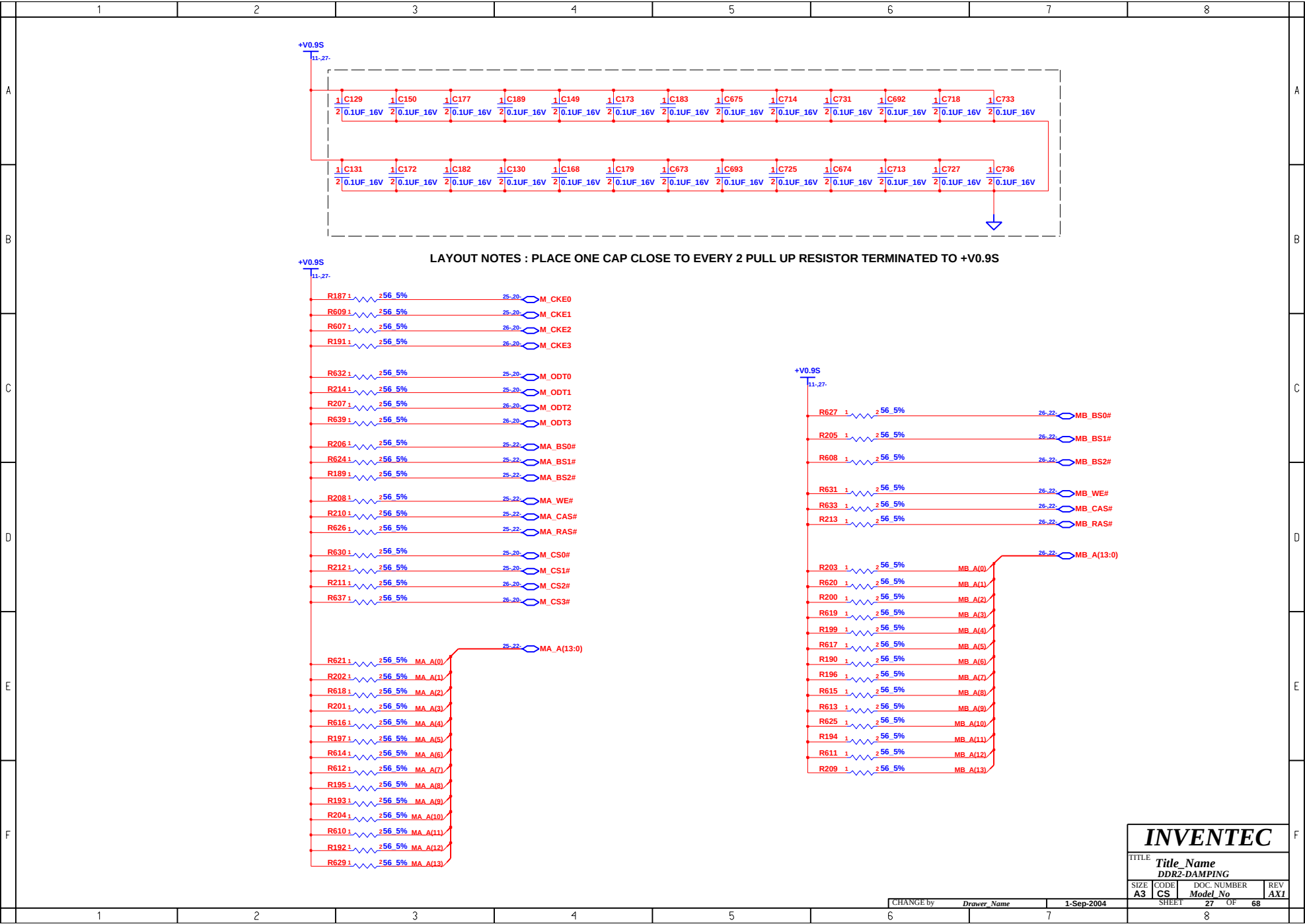


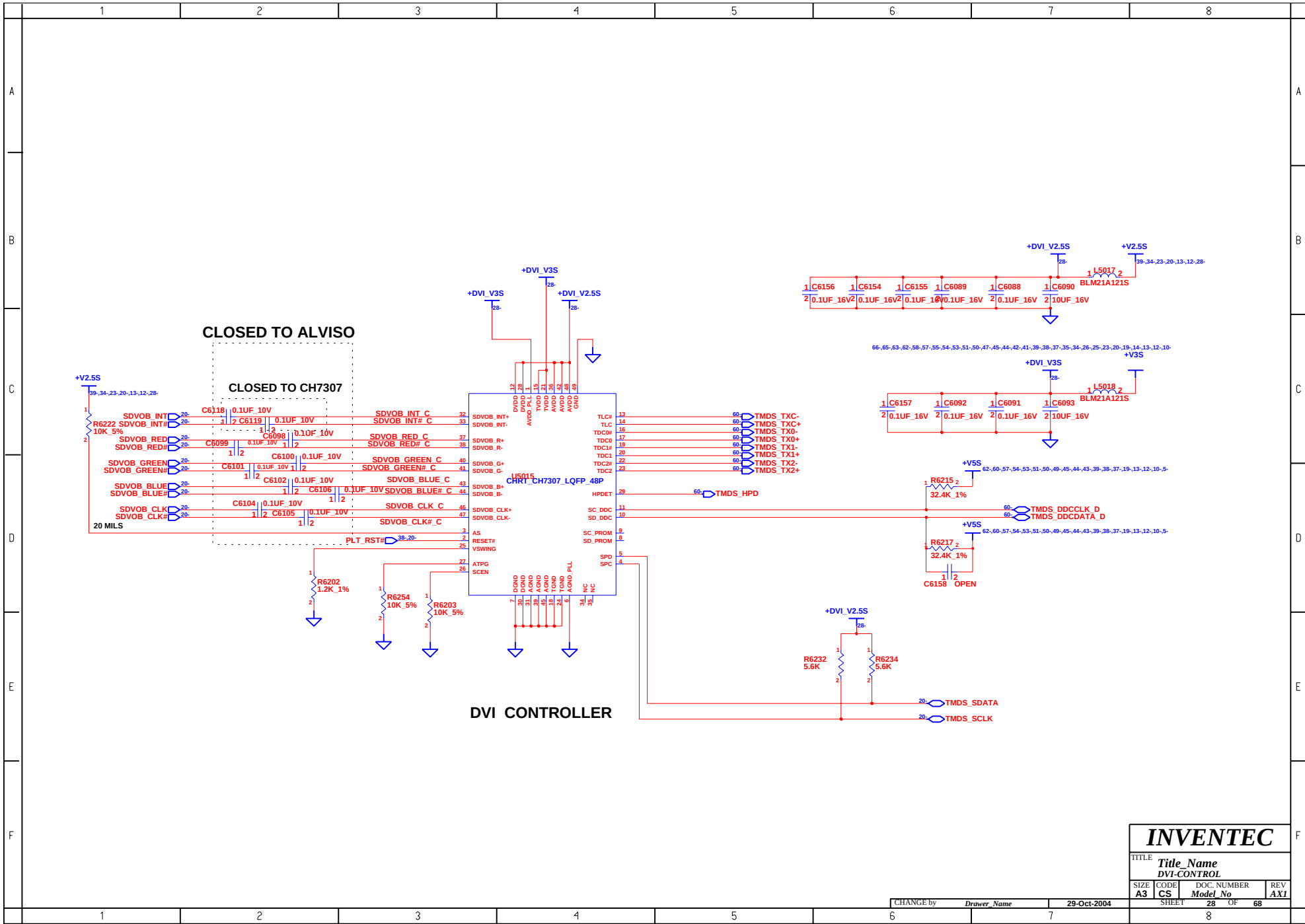


SO DIMM1 5.2mm

INVENTEC

TITLE Title Name DDR2-DIMM-1			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1





	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>Title_Name</i> <i>ATI-M24-P-3</i>			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1
CHANGE by <i>Drawn Name</i>		1-Sep-2004	
SHEET 29		OF 68	

	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>Title_Name</i> ATI-M24-P-4			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
SHEET 30		OF 68	

CHANGE by *Drawn Name* 1-Sep-2004

	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC

TITLE Title Name			
VGA DAMPING RESISTOR			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
SHEET 31		OF 68	

CHANGE by **Drawn Name** 27-Sep-2003

	1	2	3	4	5	6	7	8																																																									
A	BLANK								A																																																								
B									B																																																								
C									C																																																								
D									D																																																								
E									E																																																								
F	<table><tr><td colspan="4">CHANGE by</td><td colspan="2">1-Sep-2004</td><td colspan="2"></td></tr><tr><td colspan="4">Draver Name</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="4">SIZE</td><td colspan="2">SHEET</td><td colspan="2">8</td></tr><tr><td colspan="4">A3</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="4">CODE</td><td colspan="2">DOC. NUMBER</td><td colspan="2">REV</td></tr><tr><td colspan="4">CS</td><td colspan="2"></td><td colspan="2">AX1</td></tr><tr><td colspan="4"></td><td colspan="2">Model No</td><td colspan="2"></td></tr></table>								CHANGE by				1-Sep-2004				Draver Name								SIZE				SHEET		8		A3								CODE				DOC. NUMBER		REV		CS						AX1						Model No				F
CHANGE by				1-Sep-2004																																																													
Draver Name																																																																	
SIZE				SHEET		8																																																											
A3																																																																	
CODE				DOC. NUMBER		REV																																																											
CS						AX1																																																											
				Model No																																																													
	1	2	3	4	5	6	7	8																																																									

INVENTEC

TITLE
Title_Name
BLANK

SIZE
A3

CODE
CS

DOC. NUMBER
Model No

REV
AX1

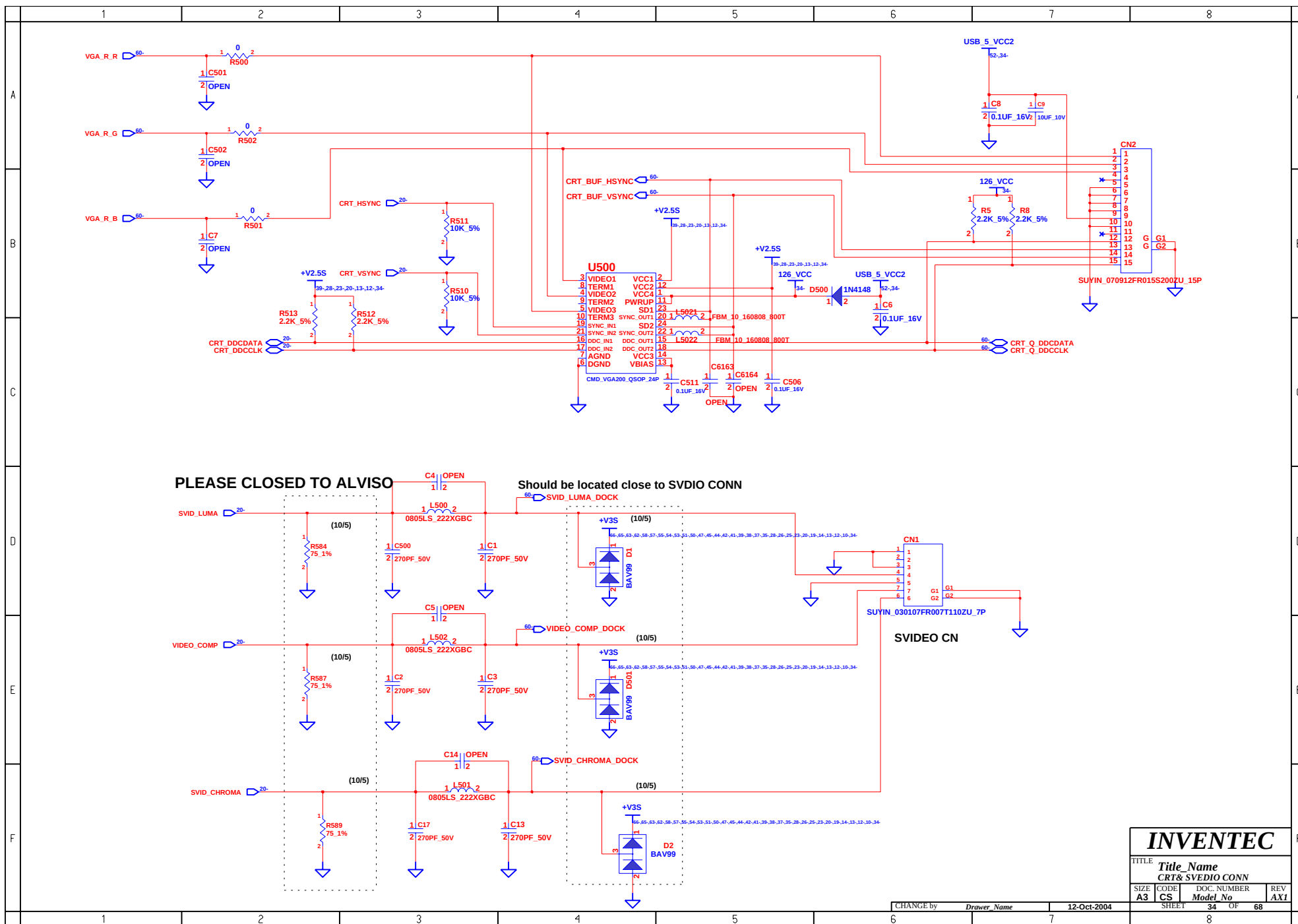
	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

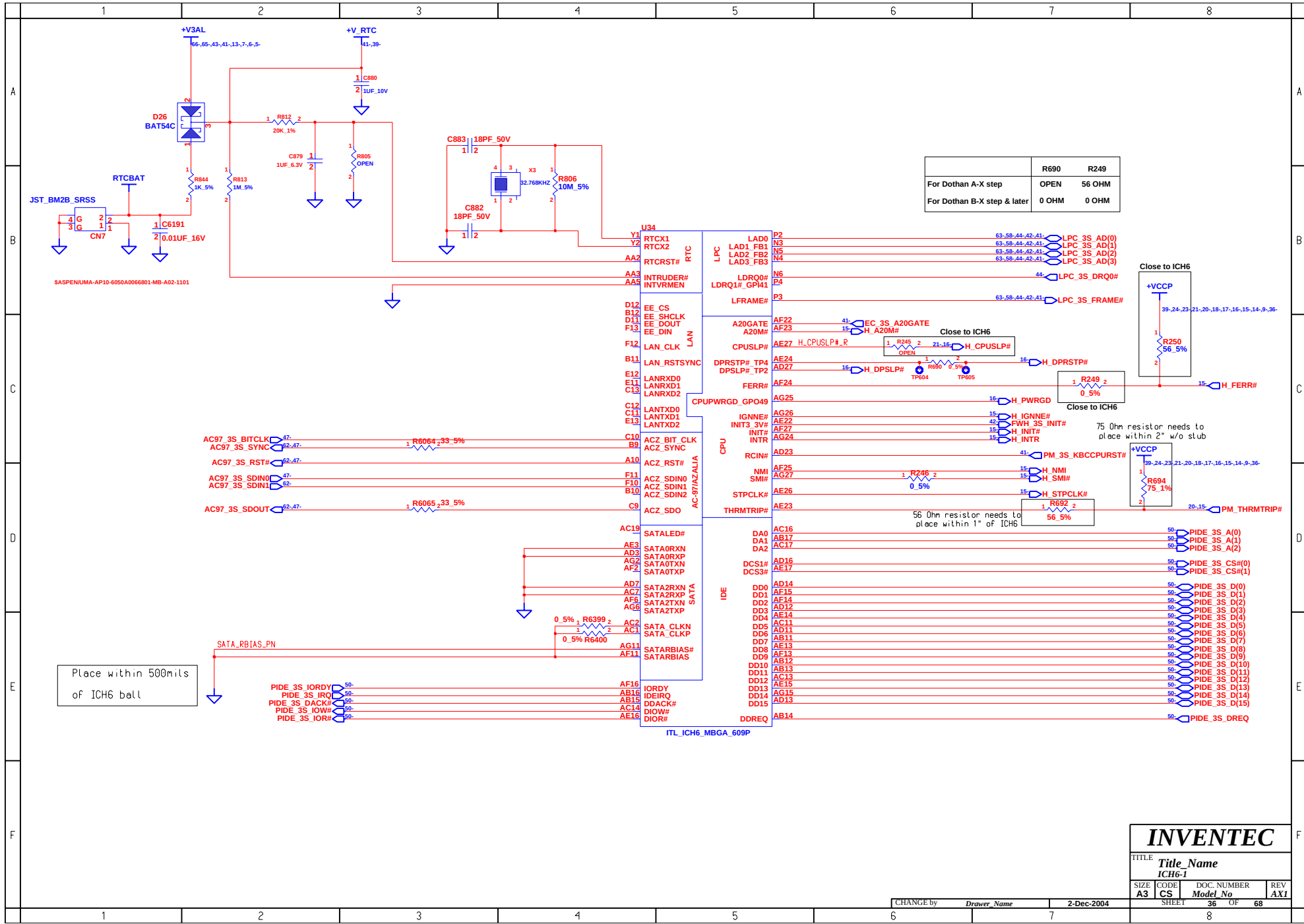
INVENTEC

TITLE <i>Title Name</i> VIDEO RAM-2			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV AX1

CHANGE by *Drawn Name* 1-Sep-2004

SHEET 33 OF 68





	R690	R249
For Dothan A-X step	OPEN	56 OHM
For Dothan B-X step & later	0 OHM	0 OHM

75 Ohm resistor needs to place within 2" w/o stub

56 Ohm resistor needs to place within 1" of ICH6

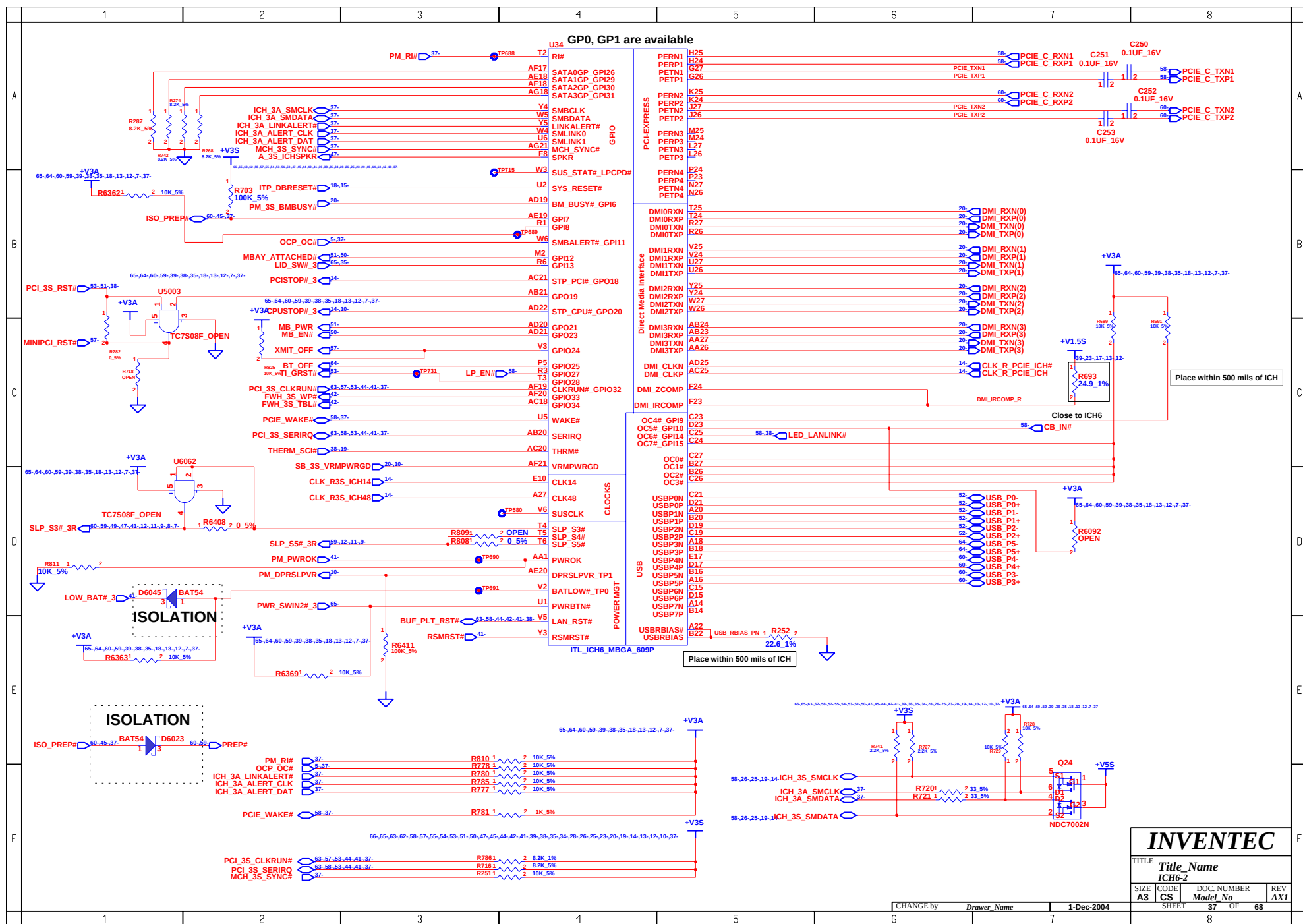
INVENTEC

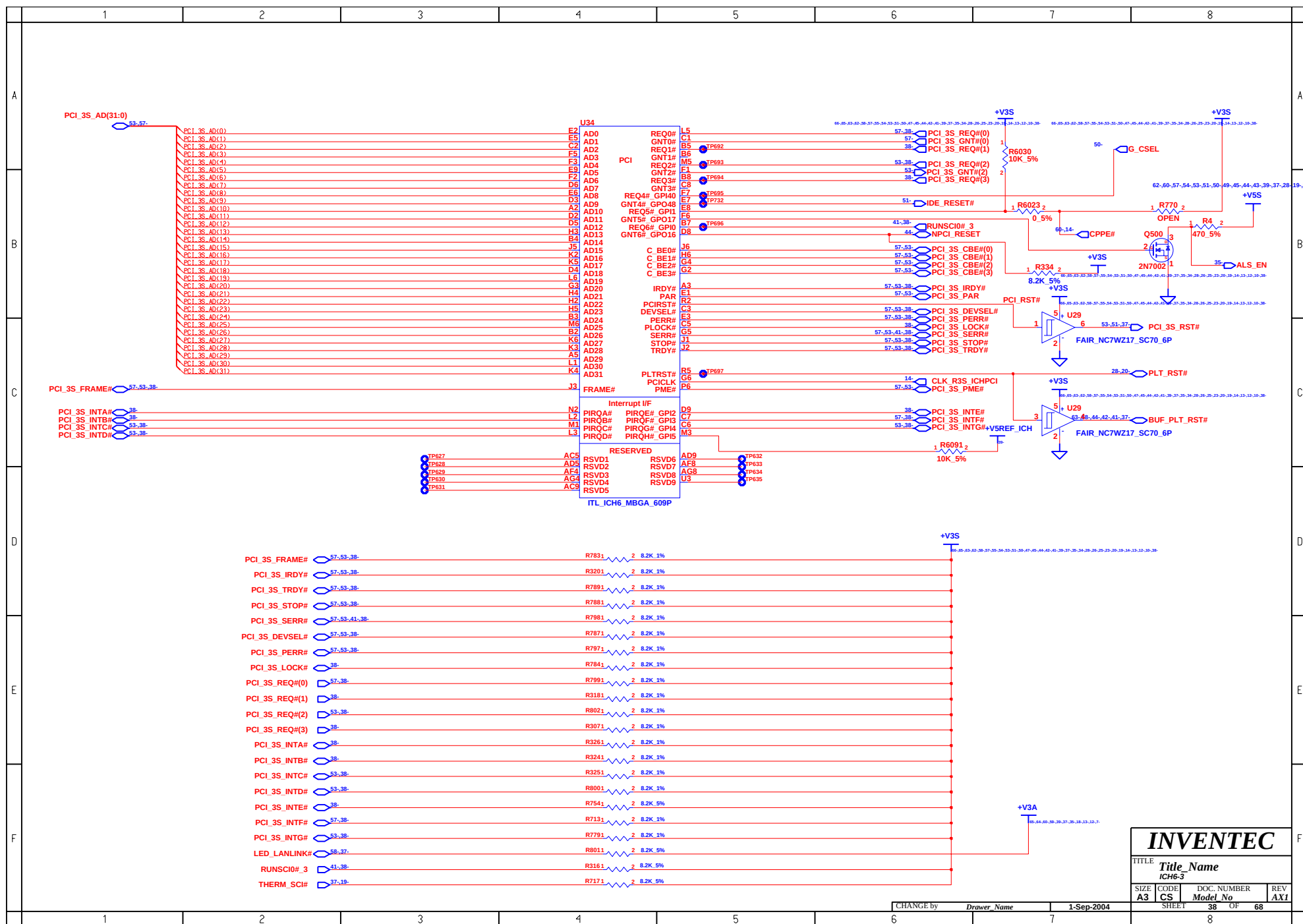
TITLE
Title Name
ICH6-1

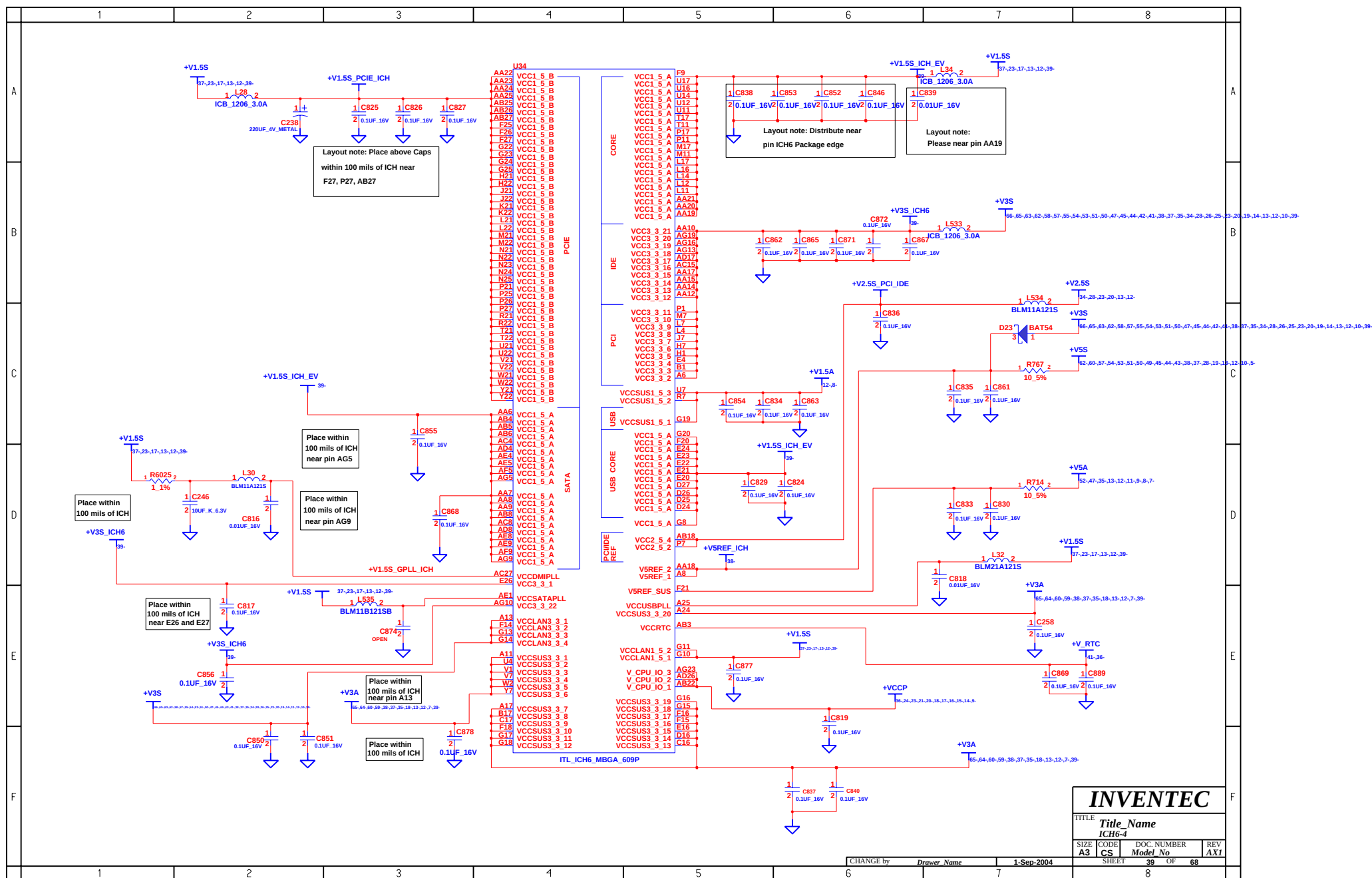
SIZE	CODE	CS	DOC. NUMBER	REV
A3	CS		Model No	AXI

CHANGE by
Drawer Name
2-Dec-2004

SHEET
36 OF 68







	1	2	3	4	5	6	7	8	
A									A
B									B
C									C
D									D
E									E
F									F

U34

E27

V6

Y27

Y26

Y23

W7

W25

W24

W23

W1

V4

V27

V26

V25

U24

U23

U19

T7

T26

T23

T16

T15

T14

T13

T12

R4

R25

R24

R23

P17

R16

R15

R14

R13

R12

R11

P22

P16

P15

P14

P13

P12

N7

N17

N16

N15

N14

N13

N12

N11

N1

M4

M27

M26

M23

M18

M15

M14

M13

M12

L25

L24

L23

L15

L13

K7

K27

K26

K23

K1

J5

J24

J23

H27

H26

H23

G9

G7

G24

G12

G1

VSS172

VSS171

VSS170

VSS169

VSS168

VSS167

VSS166

VSS165

VSS164

VSS163

VSS162

VSS161

VSS160

VSS159

VSS158

VSS157

VSS156

VSS155

VSS154

VSS153

VSS152

VSS151

VSS150

VSS149

VSS148

VSS147

VSS146

VSS145

VSS144

VSS143

VSS142

VSS141

VSS140

VSS139

VSS138

VSS137

VSS136

VSS135

VSS134

VSS133

VSS132

VSS131

VSS130

VSS129

VSS128

VSS127

VSS126

VSS125

VSS124

VSS123

VSS122

VSS121

VSS120

VSS119

VSS118

VSS117

VSS116

VSS115

VSS114

VSS113

VSS112

VSS111

VSS110

VSS109

VSS108

VSS107

VSS106

VSS105

VSS104

VSS103

VSS102

VSS101

VSS100

VSS99

VSS98

VSS97

VSS96

VSS95

VSS94

VSS93

VSS92

VSS91

VSS90

VSS89

VSS88

VSS87

F4

F22

F19

F17

E25

E19

E18

E15

E14

D7

D22

D20

D18

D13

D10

D1

C4

C22

C20

C18

C14

B25

B24

B23

B21

B19

B15

B13

AG7

AG3

AG22

AG20

AG17

AG14

AG12

AG1

AF7

AF3

AF26

AF12

AF10

AF1

AE7

AE6

AE25

AE21

AE2

AE12

AE11

AE10

AD6

AD24

AD2

AD18

AD15

AD10

AD1

AC3

AC6

AC26

AC24

AC23

AC22

AC15

AC10

AB9

AB7

AB2

AB19

AB10

AB1

AA4

AA16

AA13

AA11

A9

A7

A4

A26

A23

A21

A19

A15

A12

A1

ITL_ICH6_MBG_609P

CHANGE by

Drawer Name

1-Sep-2004

SIZE

A3

CODE

CS

DOC. NUMBER

Model No

REV

AX1

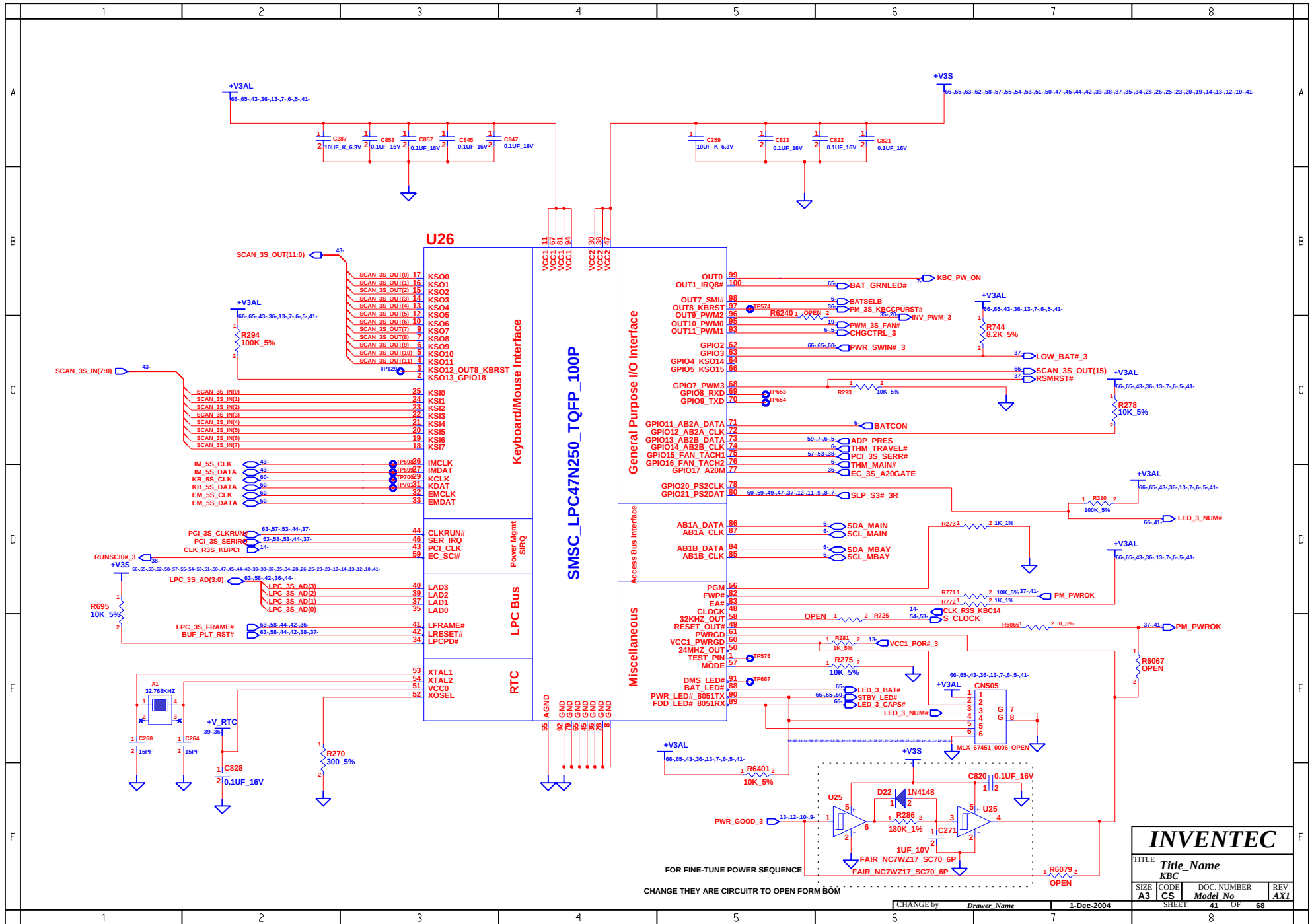
SHEET

40

OF

68

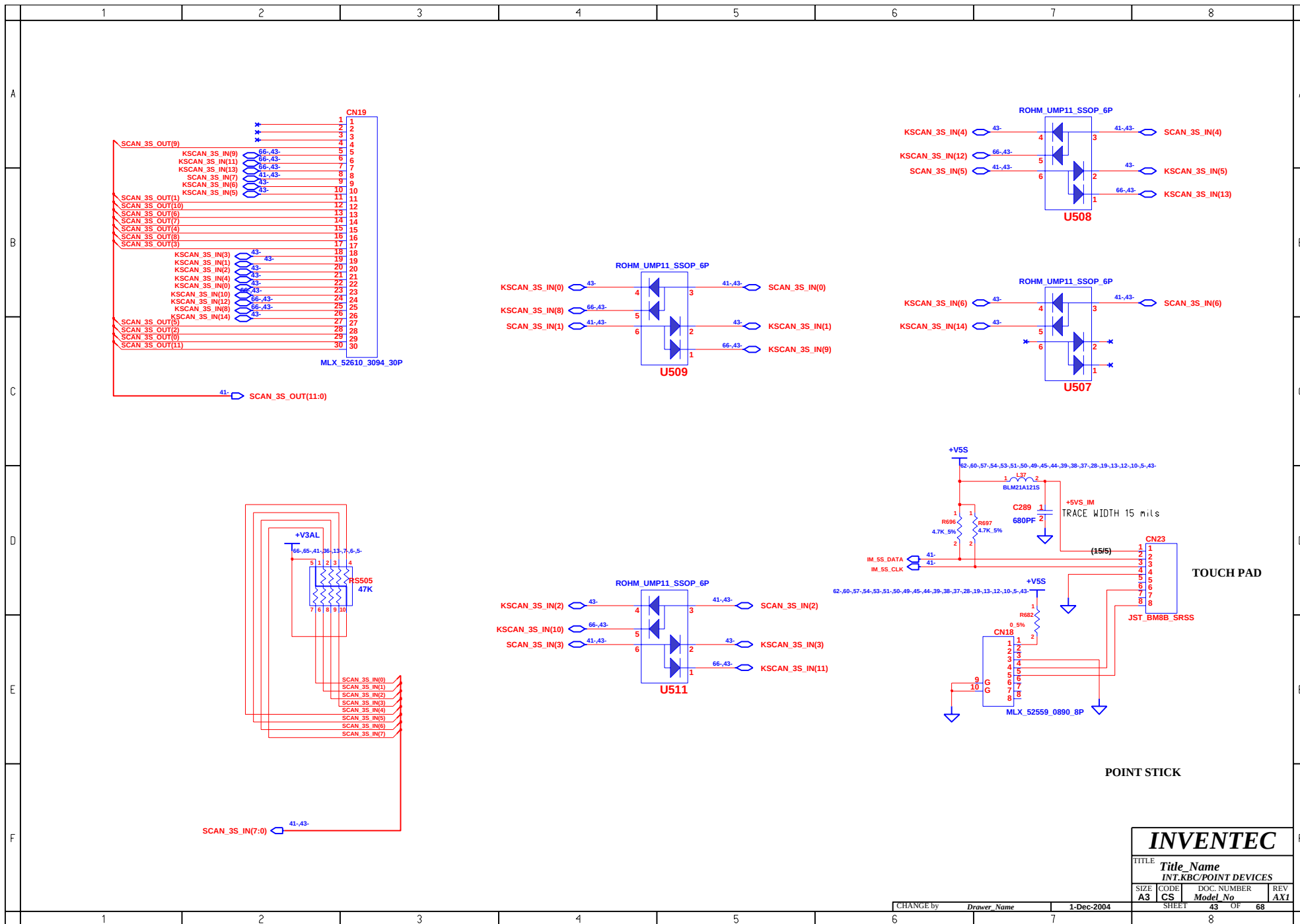
8



INVENTEC

TITLE			
KBC			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI
SHEET		41	OF 68

CHANGE by *Drawn Name* 1-Dec-2004

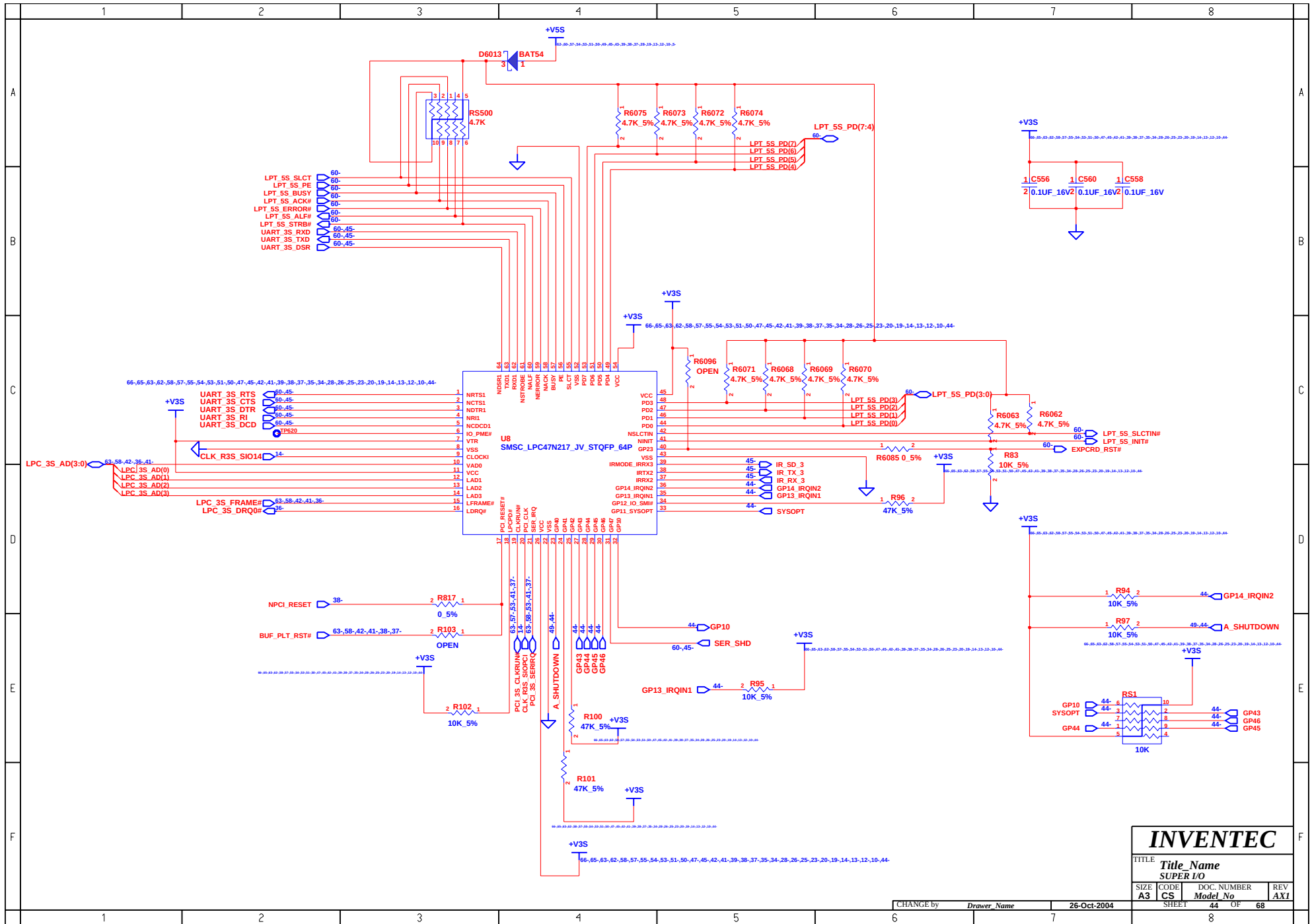


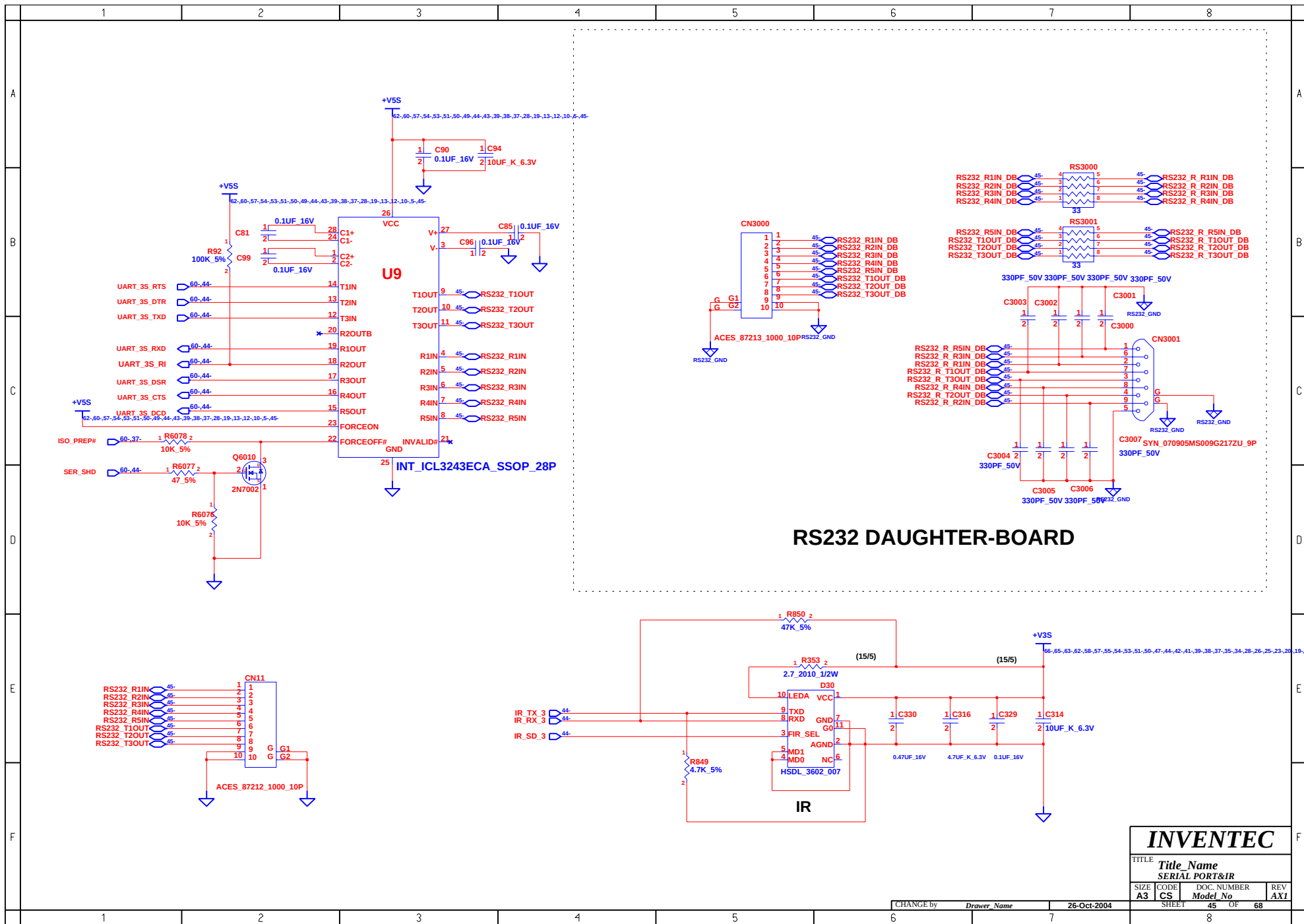
INVENTEC

TITLE			
Title Name			
INT.KBC/POINT DEVICES			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AXI

CHANGE by *Drawer Name* 1-Dec-2004

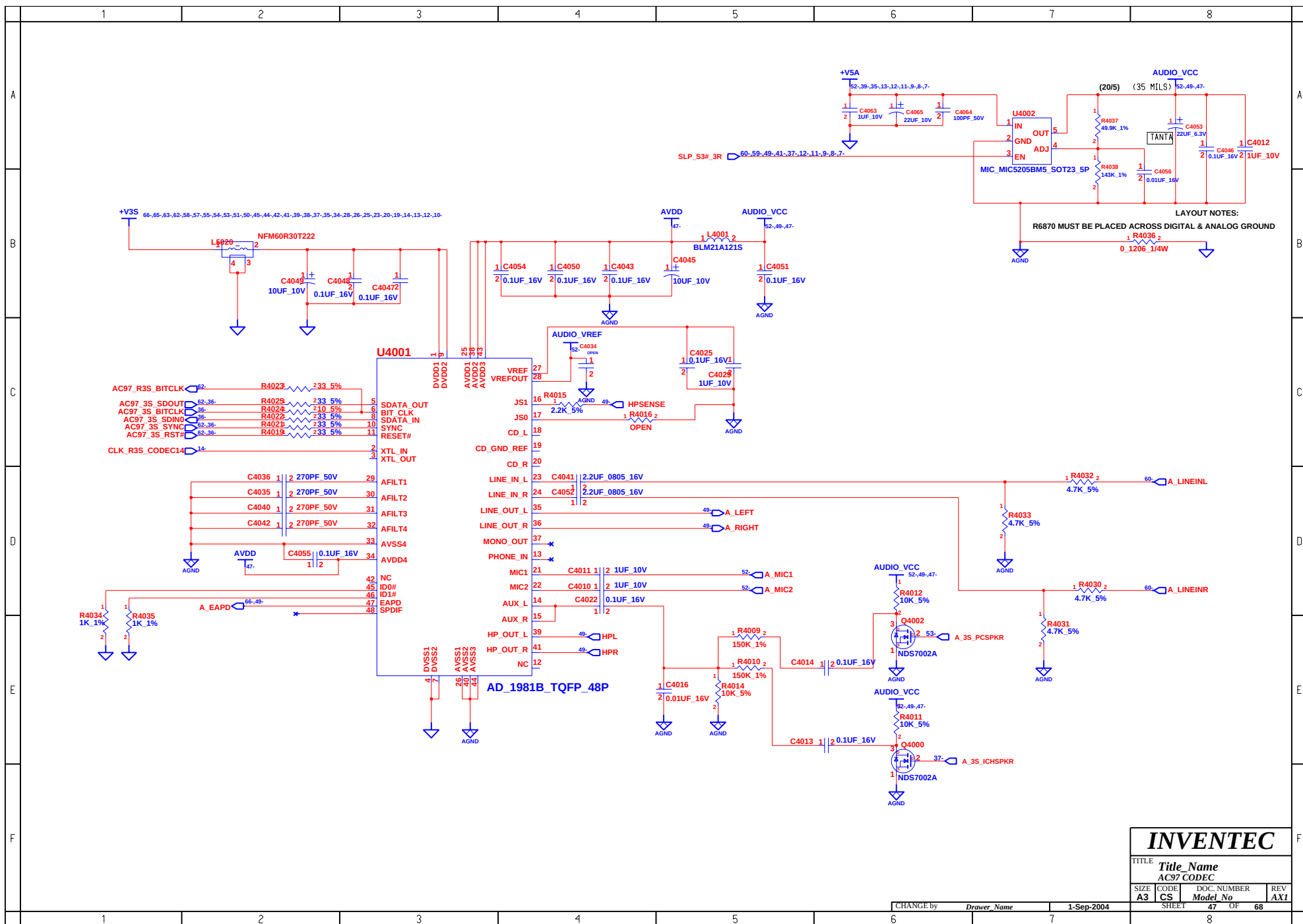
SHEET 43 OF 68

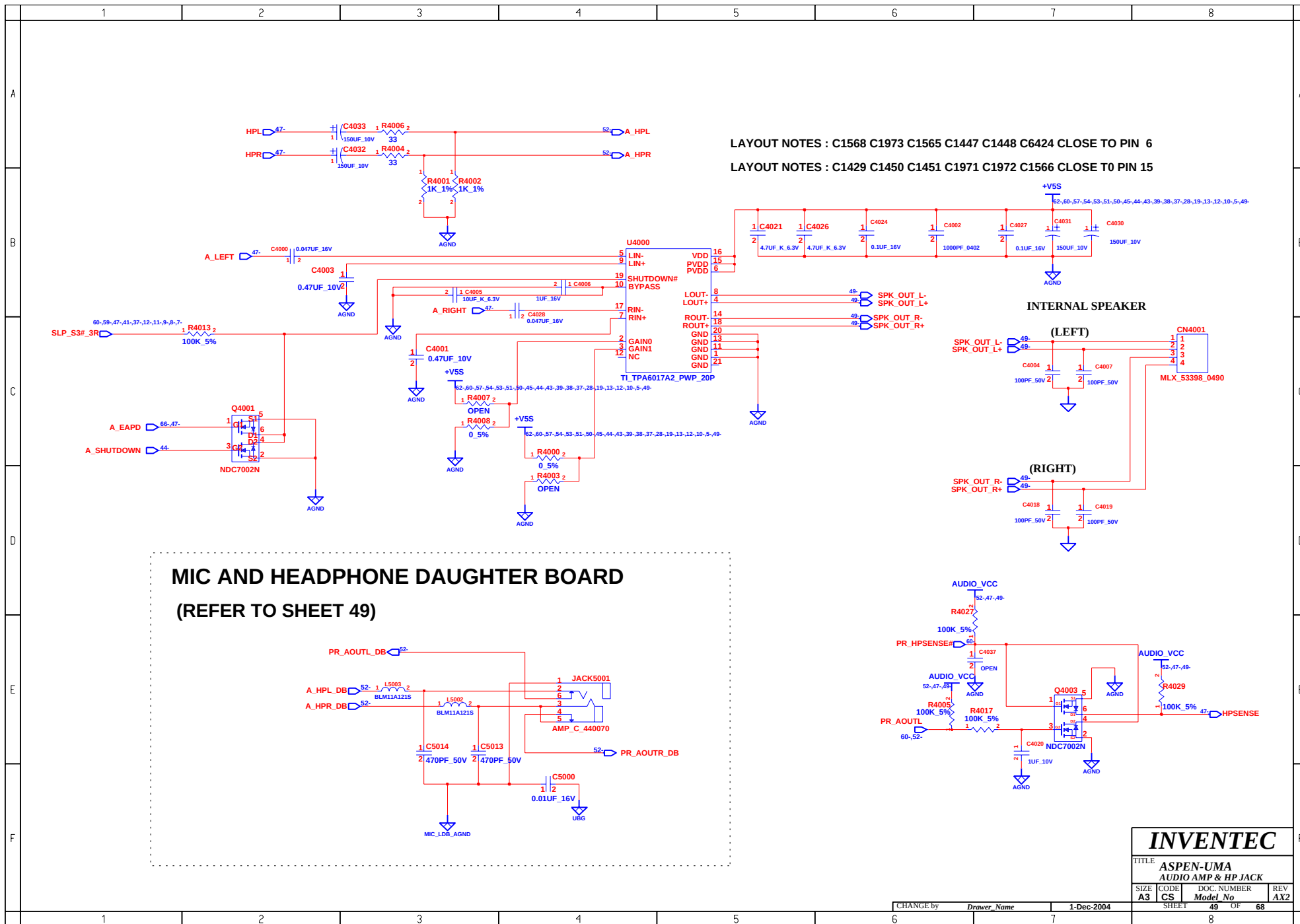


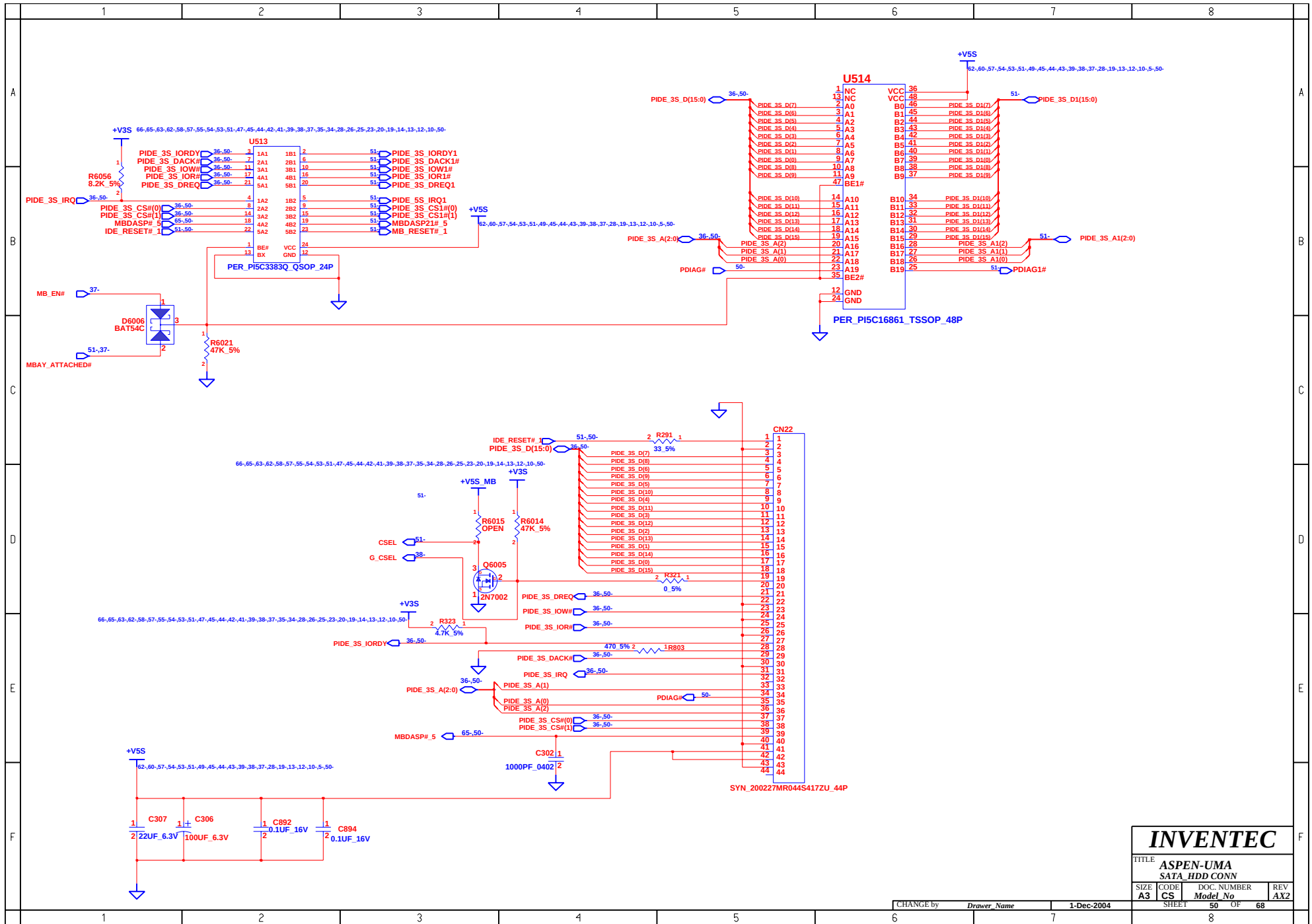


	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE <i>Title Name</i> PARALLER PORT			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX1
CHANGE by <i>Drawn Name</i>		1-Sep-2004	
SHEET 46		OF 68	



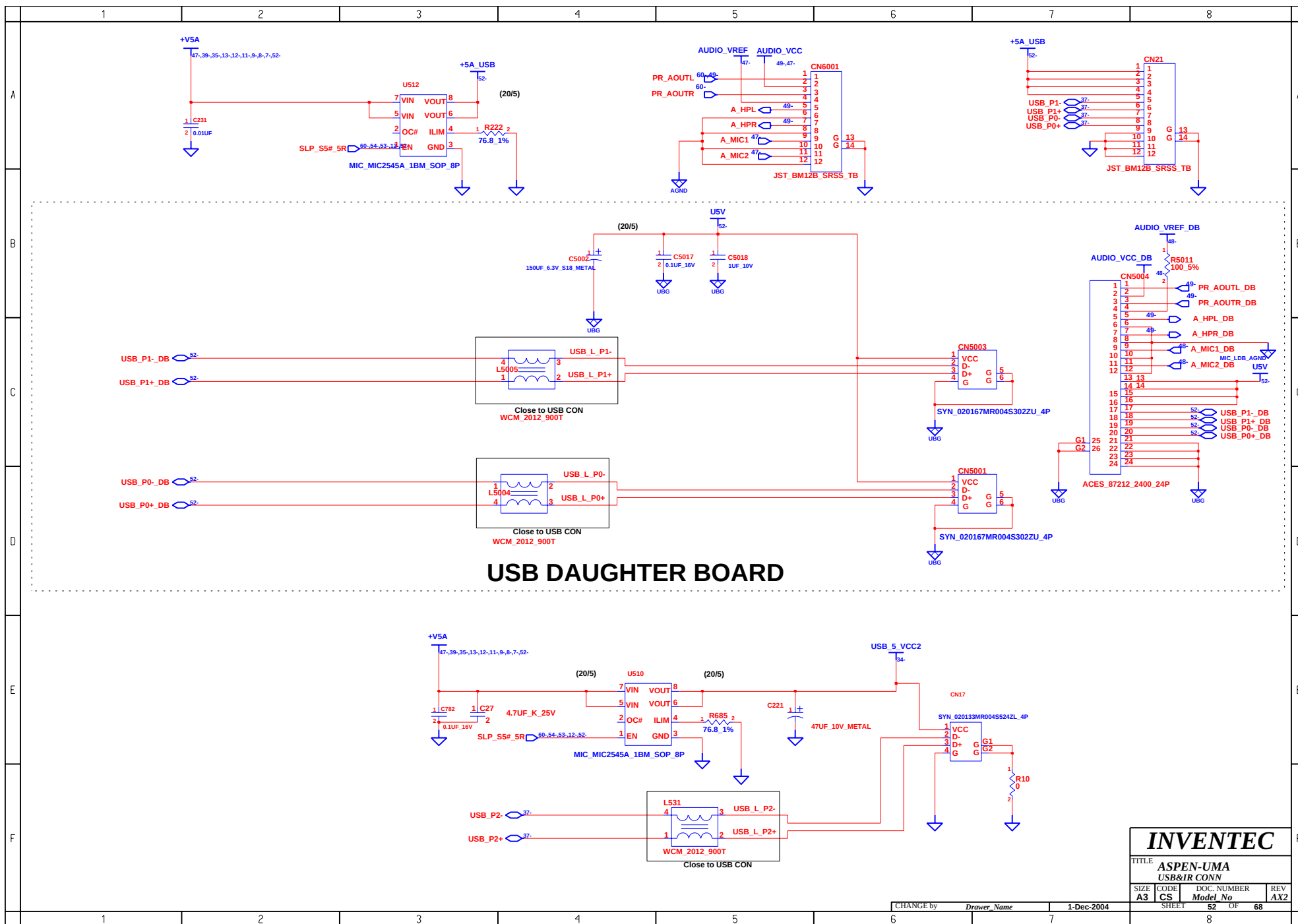




INVENTEC

TITLE ASPEN-UMA SATA_HDD CONN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
SHEET 50 OF 68			

CHANGE by *Drawer Name* 1-Dec-2004



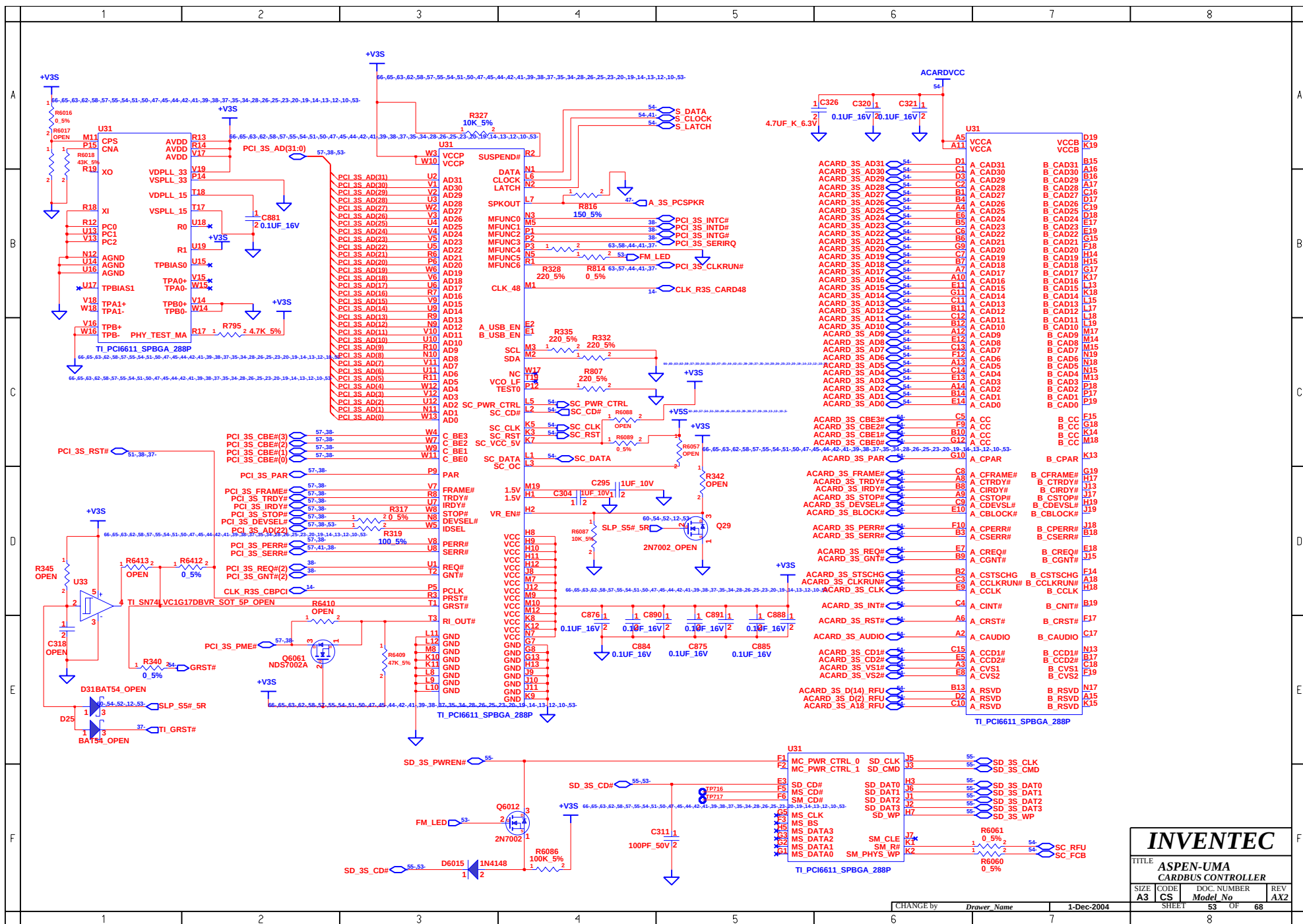
INVENTEC

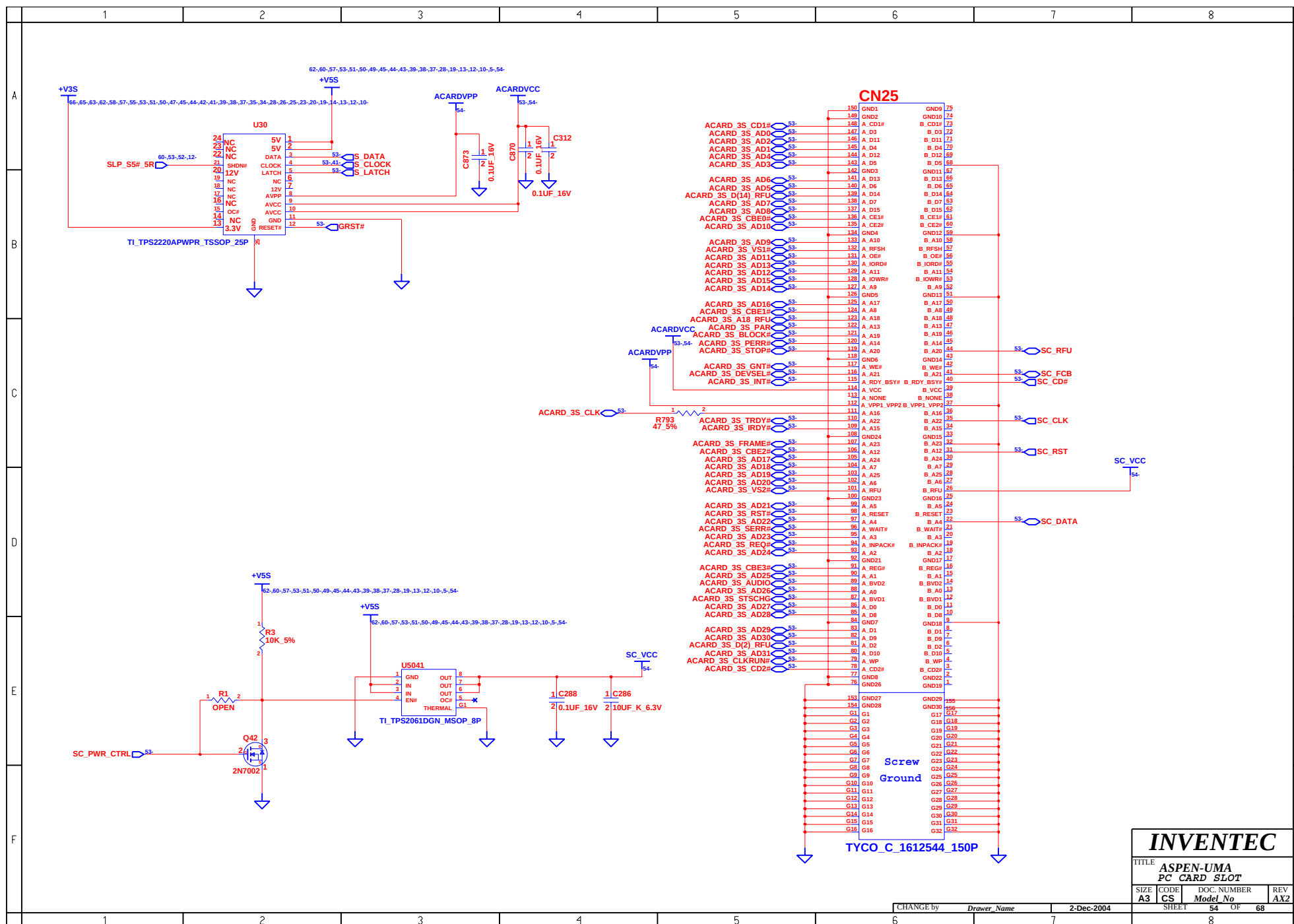
TITLE
ASPEN-UMA
USB&IR CONN

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 1-Dec-2004

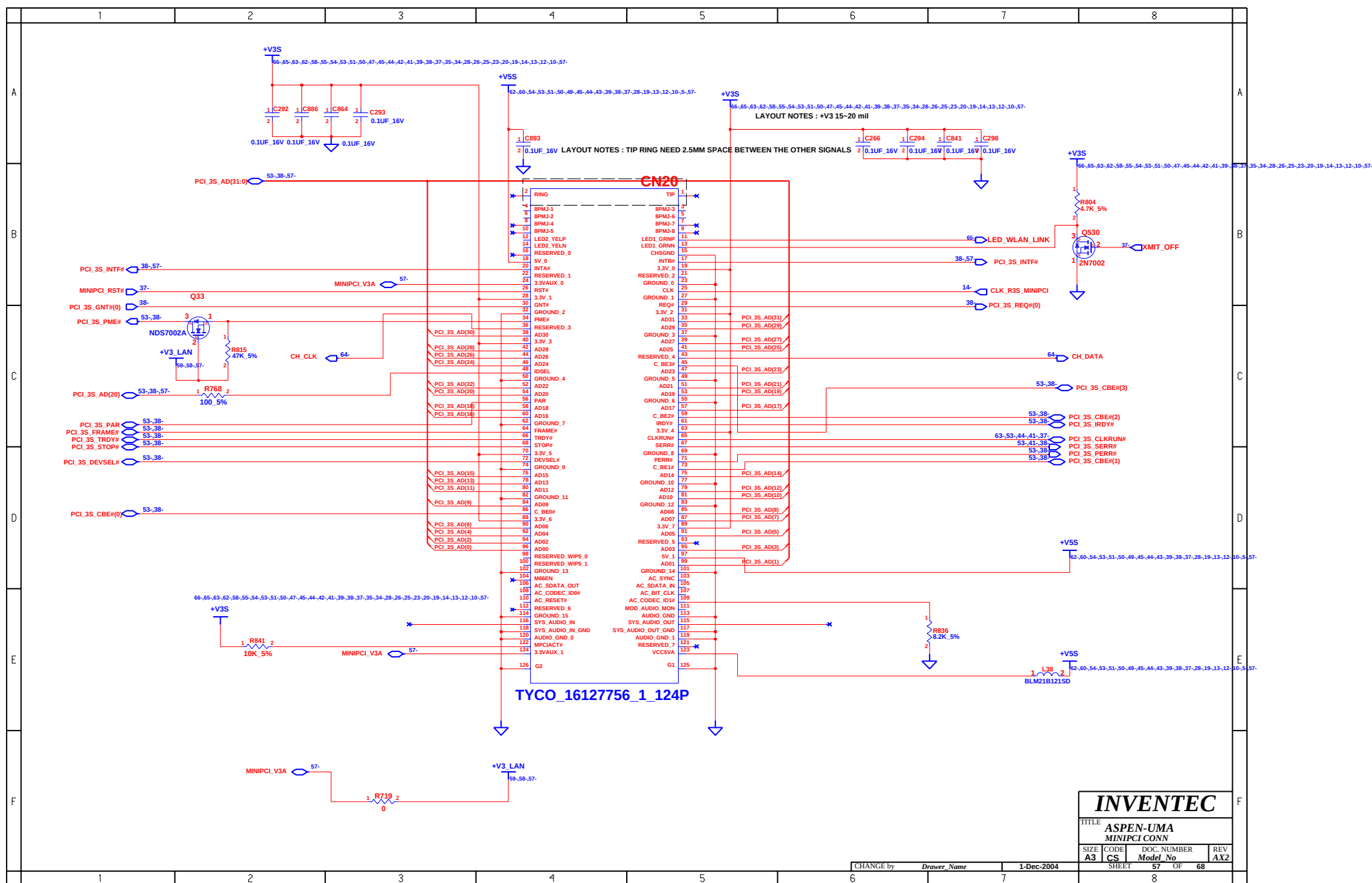
SHEET 52 OF 68

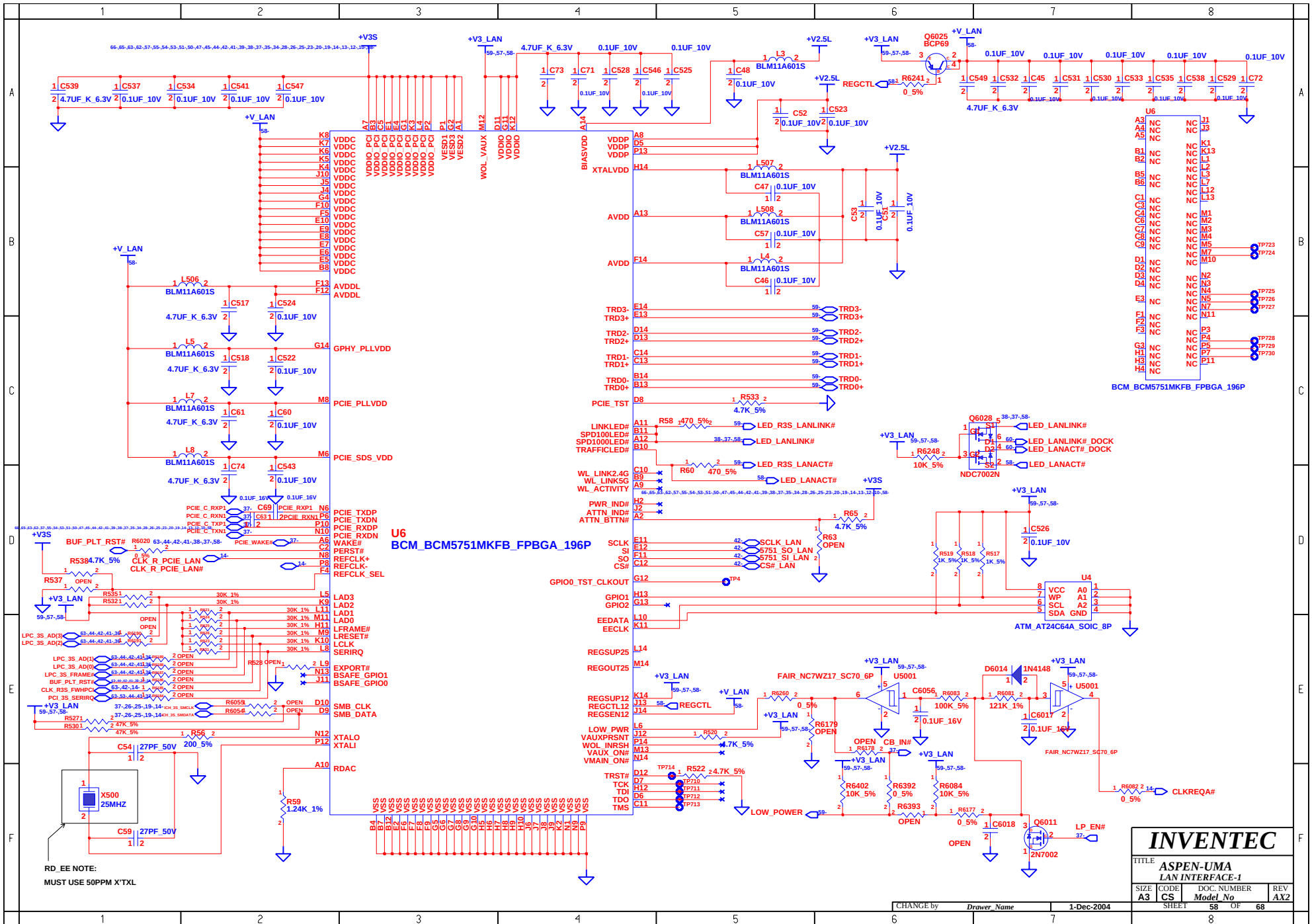


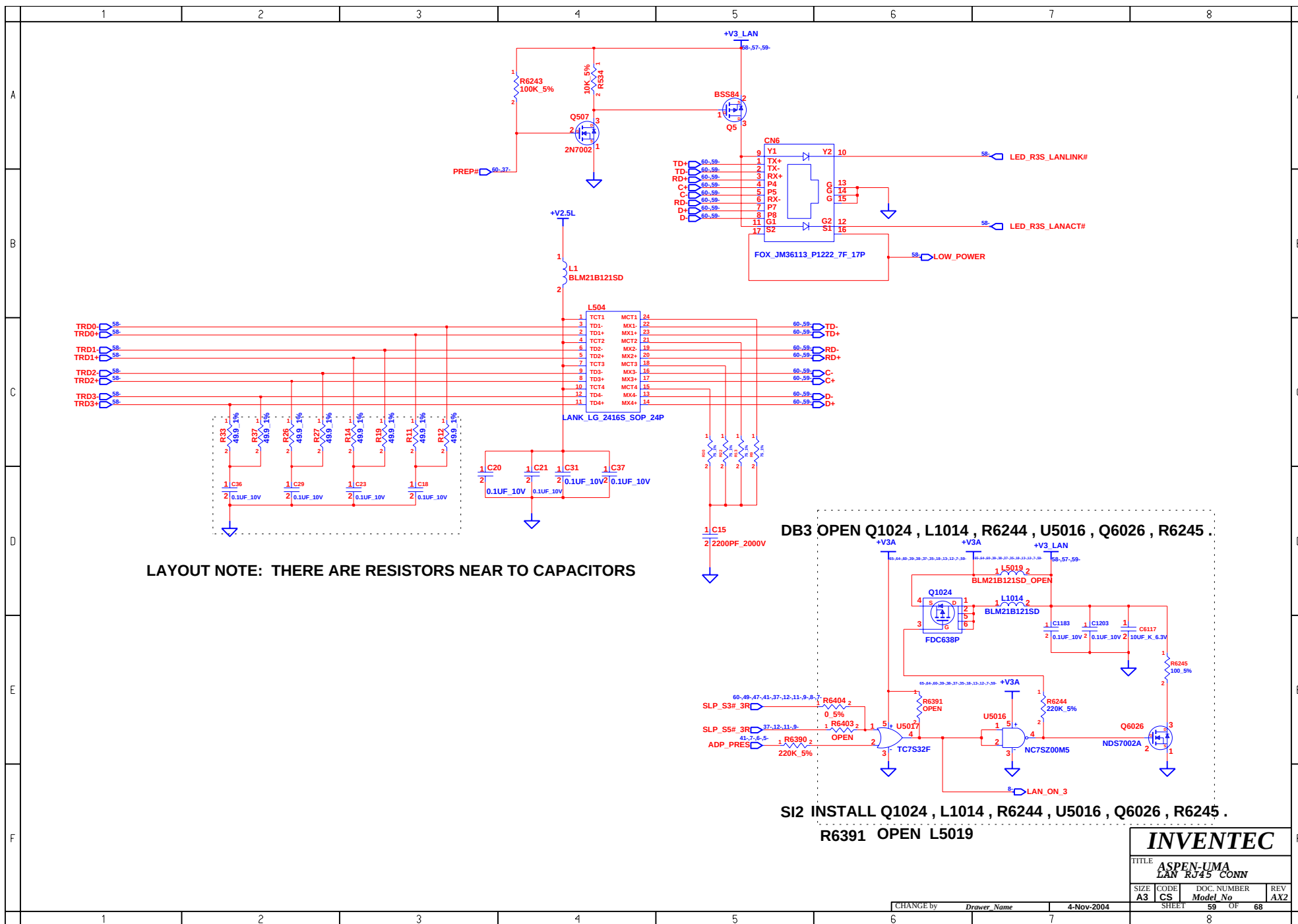


	1	2	3	4	5	6	7	8	
A	BLANK								A
B									B
C									C
D									D
E									E
F									F
	1	2	3	4	5	6	7	8	

INVENTEC			
TITLE ASPEN-UMA DAUGHTER BOARD CNN			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
CHANGE by Drafter Name		30-Aug-2004	SHEET 56 OF 68







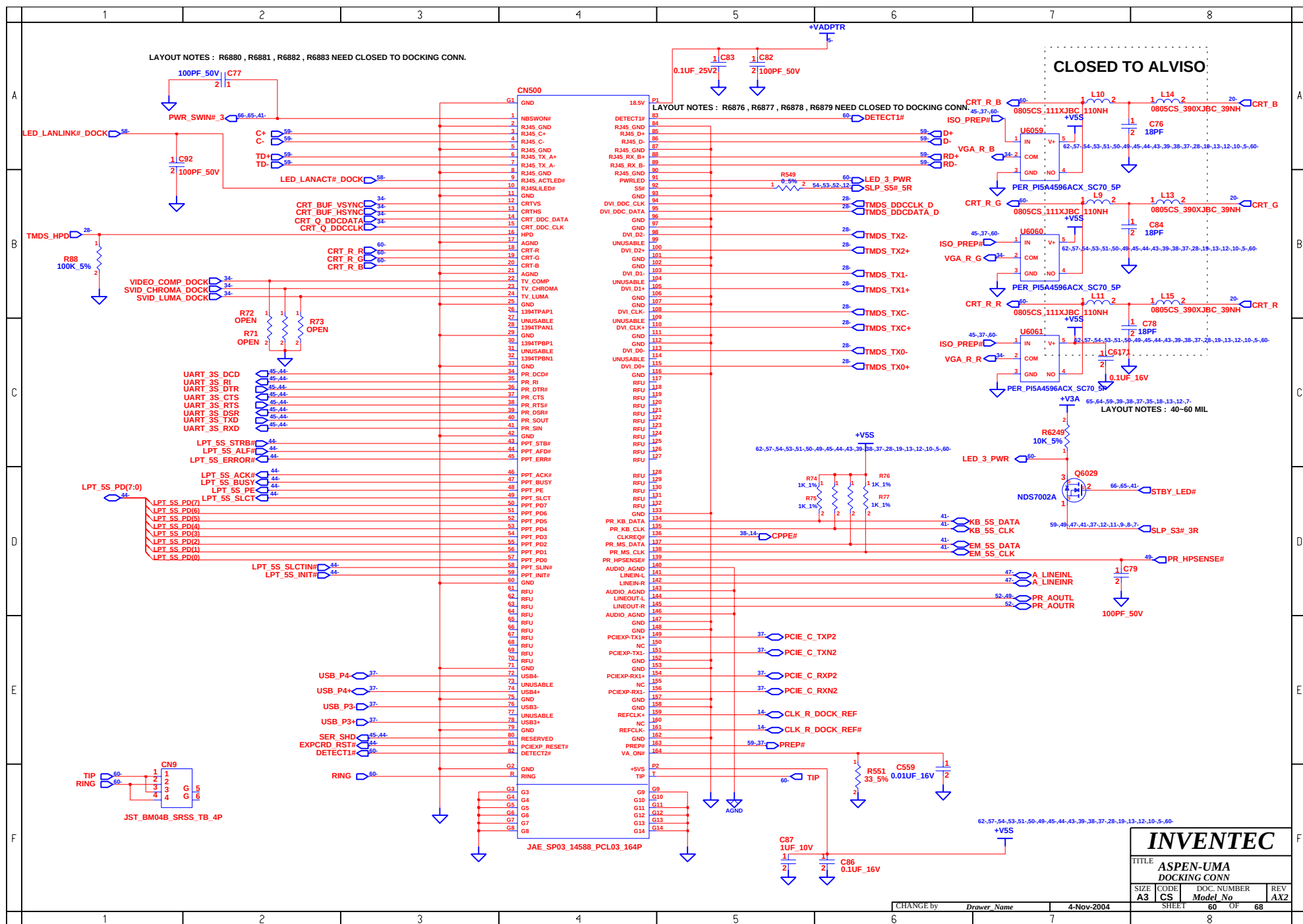
INVENTEC

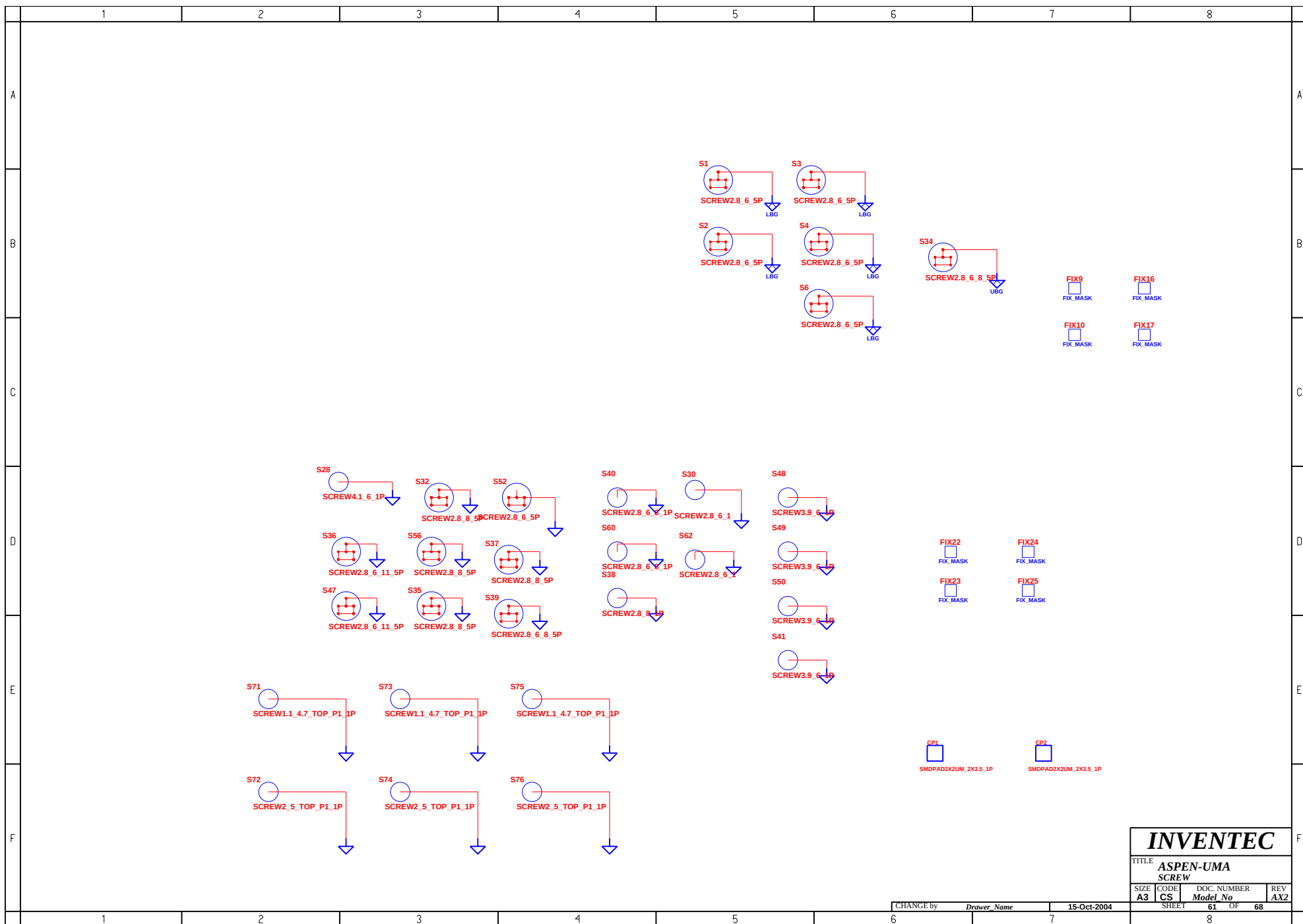
TITLE
**ASPEN-UMA
LAN RJ45 CONN**

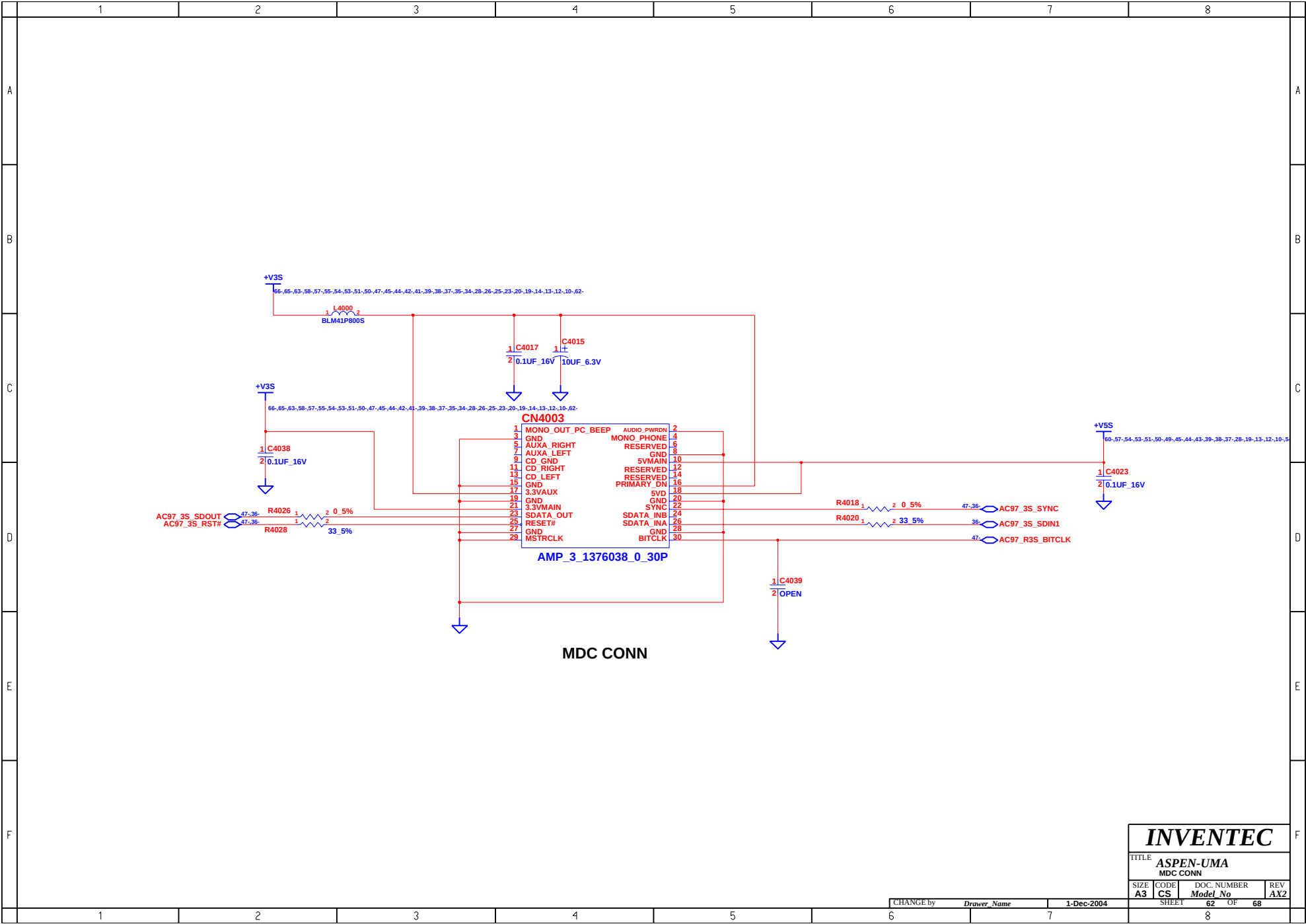
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by: *Drawn* Date: 4-Nov-2004

SHEET 59 OF 68





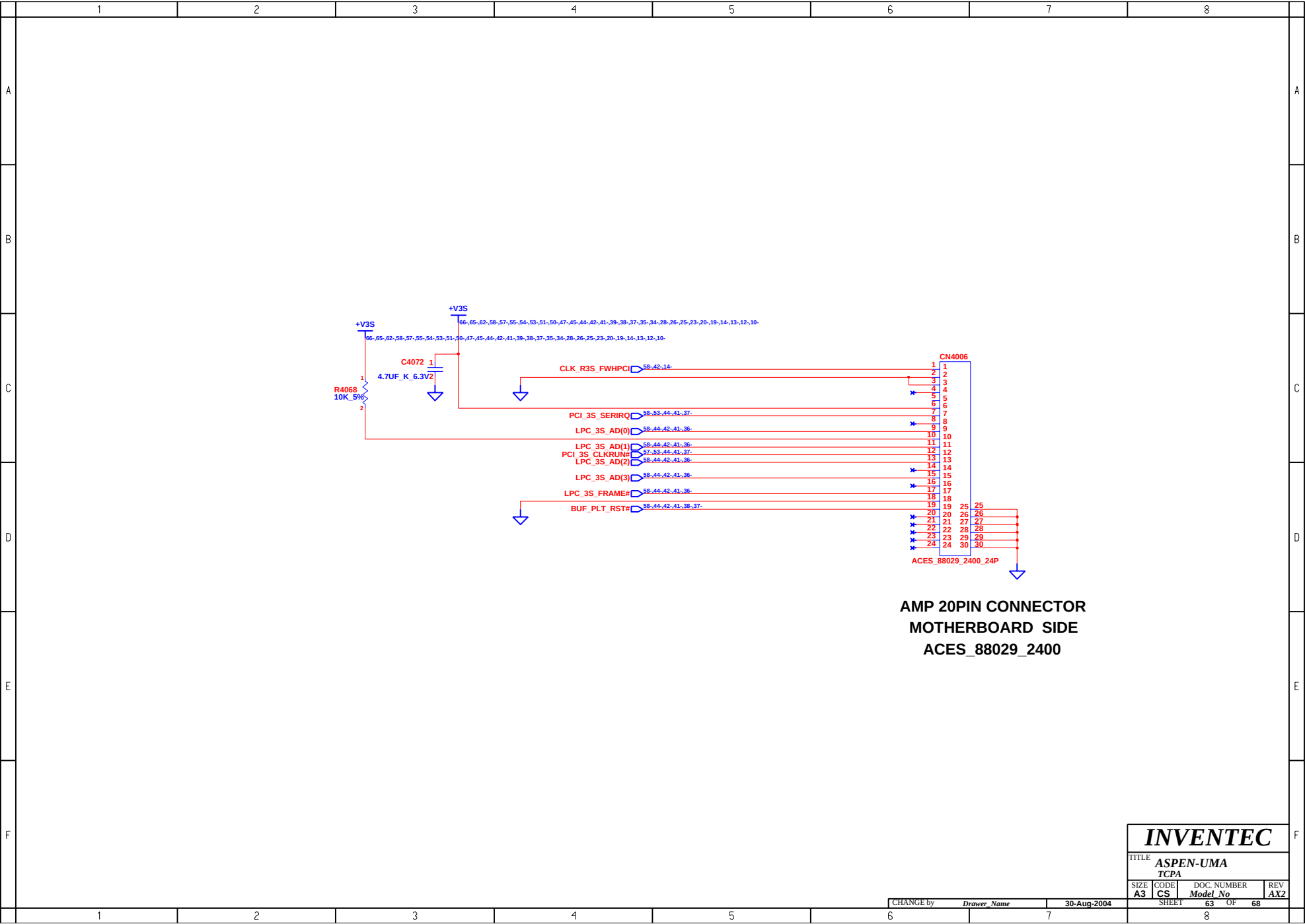


INVENTEC

TITLE			
ASPEN-UMA			
MDC CONN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	AX2

CHANGE by *Drawer Name* 1-Dec-2004

SHEET 62 OF 68



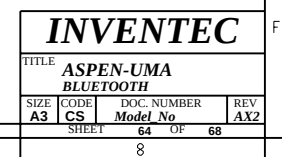
INVENTEC

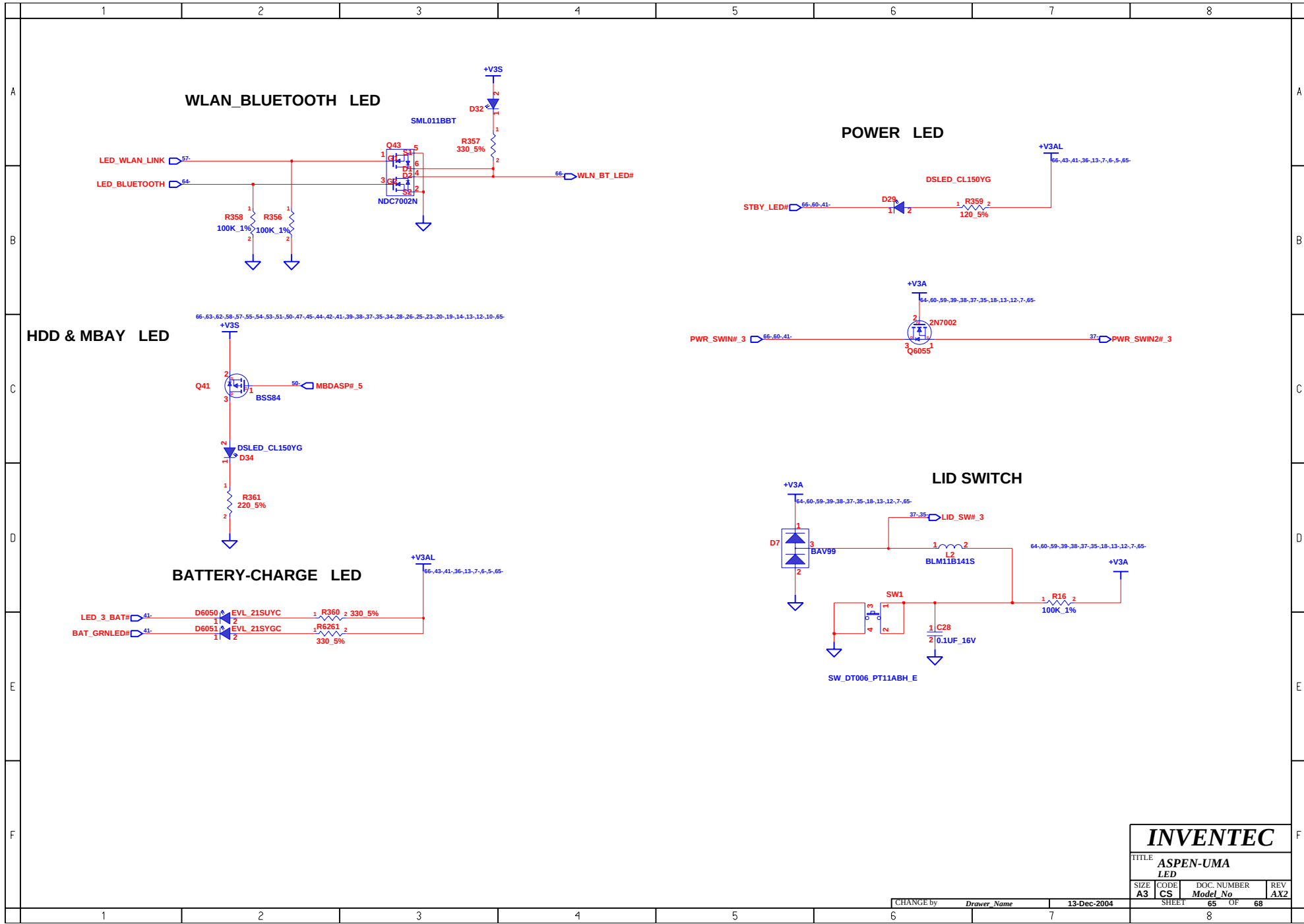
TITLE
ASPEN-UMA
TCPA

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
-------------------	-------------------	--------------------------------	-------------------

SHEET 63 OF 68

CHANGE by *Drawer Name* 30-Aug-2004





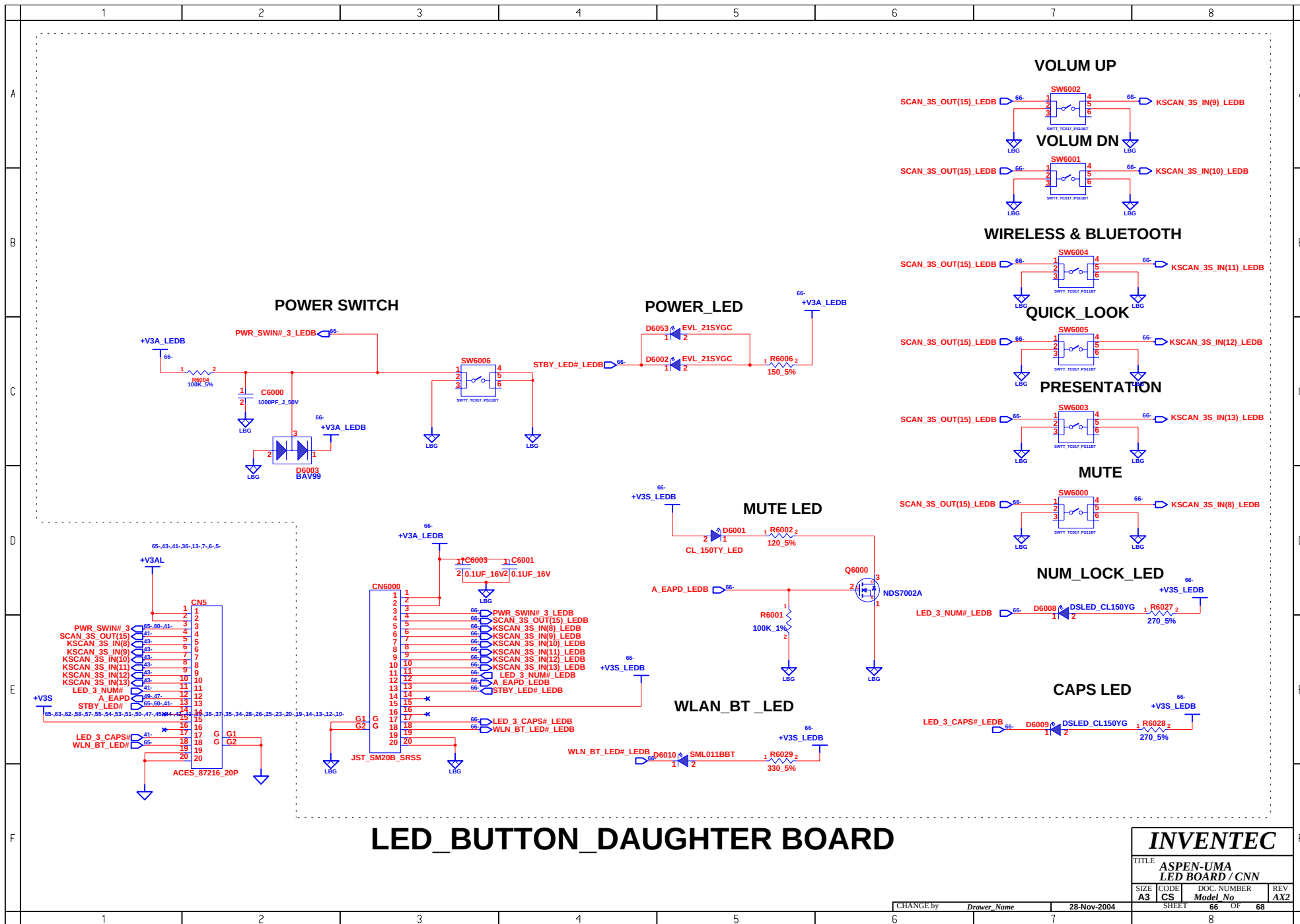
INVENTEC

TITLE
**ASPEN-UMA
LED**

SIZE A3	CODE CS	DOC. NUMBER Model No	REV AX2
-------------------	-------------------	--------------------------------	-------------------

CHANGE by *Drawn Name* 13-Dec-2004

SHEET 65 OF 68



	1	2	3	4	5	6	7	8																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																								
A	LCD CABLE <table><tr><td>1</td><td>1</td><td>2</td><td>2</td><td>3</td><td>3</td><td>4</td><td>4</td><td>5</td><td>5</td><td>6</td><td>6</td><td>7</td><td>7</td><td>8</td><td>8</td><td>9</td><td>9</td><td>10</td><td>10</td><td>11</td><td>11</td><td>12</td><td>12</td><td>13</td><td>13</td><td>14</td><td>14</td><td>15</td><td>15</td><td>16</td><td>16</td><td>17</td><td>17</td><td>18</td><td>18</td><td>19</td><td>19</td><td>20</td><td>20</td><td>21</td><td>21</td><td>22</td><td>22</td><td>23</td><td>23</td><td>24</td><td>24</td><td>25</td><td>25</td><td>26</td><td>26</td><td>27</td><td>27</td><td>28</td><td>28</td><td>29</td><td>29</td><td>30</td><td>30</td><td>31</td><td>31</td><td>32</td><td>32</td><td>33</td><td>33</td><td>34</td><td>34</td><td>35</td><td>35</td><td>36</td><td>36</td><td>37</td><td>37</td><td>38</td><td>38</td><td>39</td><td>39</td><td>40</td><td>40</td></tr><tr><td></td><td></td><td></td><td>+V3S</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>LCM DDCPCLK</td><td></td><td></td><td></td><td></td><td>LCM DDCPDATA</td><td></td><td></td><td>LVDS TXDL0+</td><td></td><td></td><td>LVDS TXDL1+</td><td></td><td></td><td>LVDS TXDL2+</td><td></td><td></td><td>LVDS TXDL2+</td><td></td><td></td><td>LVDS TXDL2+</td><td></td><td></td><td>GND</td><td></td><td></td><td>LVDS TXDU0+</td><td></td><td></td><td>LVDS TXDU0+</td><td></td><td></td><td>LVDS TXDU1+</td><td></td><td></td><td>LVDS TXDU1+</td><td></td><td></td><td>LVDS TXDU2+</td><td></td><td></td><td>LVDS TXDU2+</td><td></td><td></td><td>LVDS TXCU+</td><td></td><td></td><td>LVDS TXCU+</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>ALS EN</td><td></td><td></td><td>+V5A</td><td></td><td></td><td>+V5A</td><td></td><td></td><td>+V5A</td><td></td><td></td><td>INV_PWM3</td><td></td><td></td><td>+V3S</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td><td></td><td></td><td>GND</td></tr></table>								1	1	2	2	3	3	4	4	5	5	6	6	7	7	8	8	9	9	10	10	11	11	12	12	13	13	14	14	15	15	16	16	17	17	18	18	19	19	20	20	21	21	22	22	23	23	24	24	25	25	26	26	27	27	28	28	29	29	30	30	31	31	32	32	33	33	34	34	35	35	36	36	37	37	38	38	39	39	40	40				+V3S											LCM DDCPCLK					LCM DDCPDATA			LVDS TXDL0+			LVDS TXDL1+			LVDS TXDL2+			LVDS TXDL2+			LVDS TXDL2+			GND			LVDS TXDU0+			LVDS TXDU0+			LVDS TXDU1+			LVDS TXDU1+			LVDS TXDU2+			LVDS TXDU2+			LVDS TXCU+			LVDS TXCU+			GND			GND			ALS EN			+V5A			+V5A			+V5A			INV_PWM3			+V3S			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND
1	1	2	2	3	3	4	4	5	5	6	6	7	7	8	8	9	9	10	10	11	11	12	12	13	13	14	14	15	15	16	16	17	17	18	18	19	19	20	20	21	21	22	22	23	23	24	24	25	25	26	26	27	27	28	28	29	29	30	30	31	31	32	32	33	33	34	34	35	35	36	36	37	37	38	38	39	39	40	40																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
			+V3S											LCM DDCPCLK					LCM DDCPDATA			LVDS TXDL0+			LVDS TXDL1+			LVDS TXDL2+			LVDS TXDL2+			LVDS TXDL2+			GND			LVDS TXDU0+			LVDS TXDU0+			LVDS TXDU1+			LVDS TXDU1+			LVDS TXDU2+			LVDS TXDU2+			LVDS TXCU+			LVDS TXCU+			GND			GND			ALS EN			+V5A			+V5A			+V5A			INV_PWM3			+V3S			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND			GND																																																																																									

IEC / HP Aspen_UMA MV Schematic Changes								December 10, 2004
HP Confidential Information						Open Items		
Item #	Project	Component / Subsection	PG. #	Description of Requested Change	Date Open+F7 / Closed	Requested By / Owner	Status / Aspen	Aspen FBK/Date
1	UMA	V2.5L	8	Improve the RGB signal waveform on CRT monitor R6247 from 909k change to 1M_1% 60130B10049T	2004/11/17	IEC	Open	sch1117 done
2	UMA	ALVISO	23	Improve the S-Video & RGB signal waveform for S-Video & RGB monitor. 1.L5014 pin 1 C6059 change to 47UF capacitor (OXi cap). 2.L5015 pin 1 C6063 change to 47UF capacitor (OXi cap.) 3.C216 change to 47UF capacitor (OXi cap.) 4.Remove C6068.	2004/11/29	IEC	Open	sch1129 done
3	UMA	Power	11	Change U6064 +V0.9 limit regulator circuit for DDR2. Remove R640 , C734 , C195 , C735 , C196 , C193 add C6176 1uf , C6172 , C6173 , C6174 , C6175 10uf.	2004/11/29	HP	Open	sch1129 done
4	UMA	LED	66	1.Add LED D6053 EVL_21SYGC for machine ID recommend. 2.Change D6002 to EVL_21SYGC.	2004/11/29	HP	Open	sch1129 done
5	UMA	Switch Board	66	Change switch Board trace from Top side & Bottom side to internal layer a surface used GND to Cover it this change for safty ESD recommend.	2004/11/29	IEC	Open	sch1129 done
6	UMA	serial Board	45	Change Serial Board trace from Top side & Bottom side to internal layer a surface used GND to cover it this change for Safty ESD recommend.	2004/11/29	IEC	Open	sch1129 done
7	UMA	Power	7	Move D6048 , C6162 , R6283 placement this change for machine recommend.	2004/11/29	IEC	Open	sch1129 done
8	UMA	ICH-6	37	Change C882,C883 from 10pF to 18pF for RTC accuracy	2004/11/29	IEC	Open	sch1129 done
9	UMA	ALVISO ICH-6	23 23 36	For EMI recommend: 1.Add C6188,C6189,C6190 0.01uF_16V connect +V1.5S for EMI improve +V1.5S power plane. 2.Add C6185,C6186,C6187 0.01uF_16V connect +V1.8 for EMI improve +V1.8 power plane 3.Add C6191 0.01uF_16V connect CN7 RTCBAT to improve EMI noise.	2004/12/2	IEC	Open	sch1202 done
10	UMA	Power	11	Add two power via 30_16 on +V0.9S power rail.	2004/12/3	IEC	open	sch1203 done
11	UMA	Power	13	Aspen UMA uses a 2.5V rail,R6150 should be changed to 107K_1%, and R6152 should be changed to 57.6K_1%.	2004/12/10	HP	open	sch1210 done