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HSF Property:ROHS or Halogen-Free

BANDIT M2 HD+

MV BUILD

2013.01.04

2012/09/10	2012-ECO-017507	A
DATE	CHANGE NO.	REV

DRAWER				INVENTEC			
EE	DATE	POWER	DATE	TITLE			
xxx	21-OCT-2002	RAY CHEN	21-OCT-2002	MODEL PROJECT FUNCTION			
xxx	21-OCT-2002	RAY CHEN	21-OCT-2002	ULTRABOOK MAIN BOARD			
xxx	21-OCT-2002	RAY CHEN	21-OCT-2002	RESPONSIBLE			
xxx	21-OCT-2002	RAY CHEN	21-OCT-2002	RESPONSIBLE			
SIZE: A3				VER: A02	SIZE: A3	CODE: CS	DOC NUMBER: 1310000000-01-0
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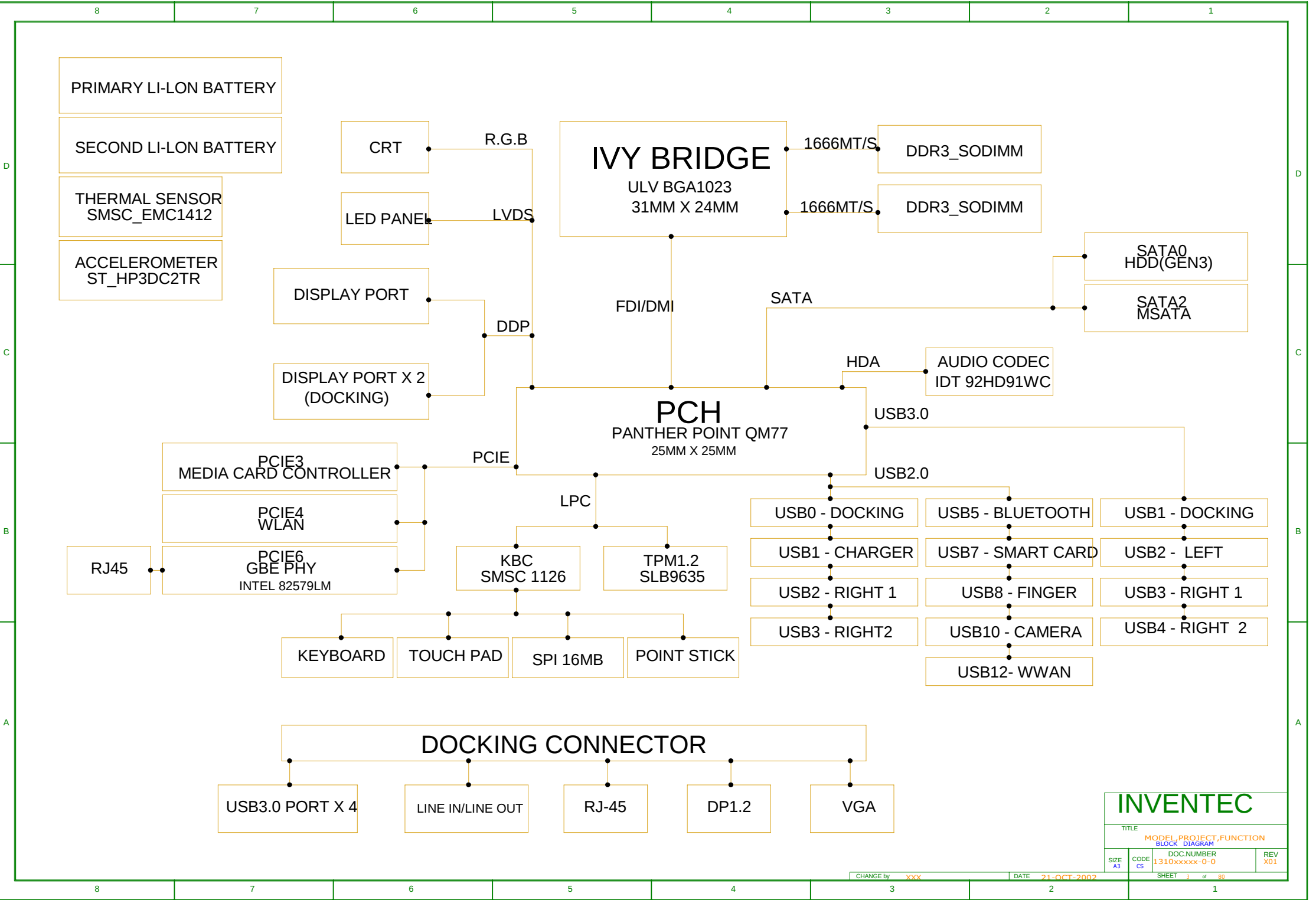
TABLE OF CONTENTS

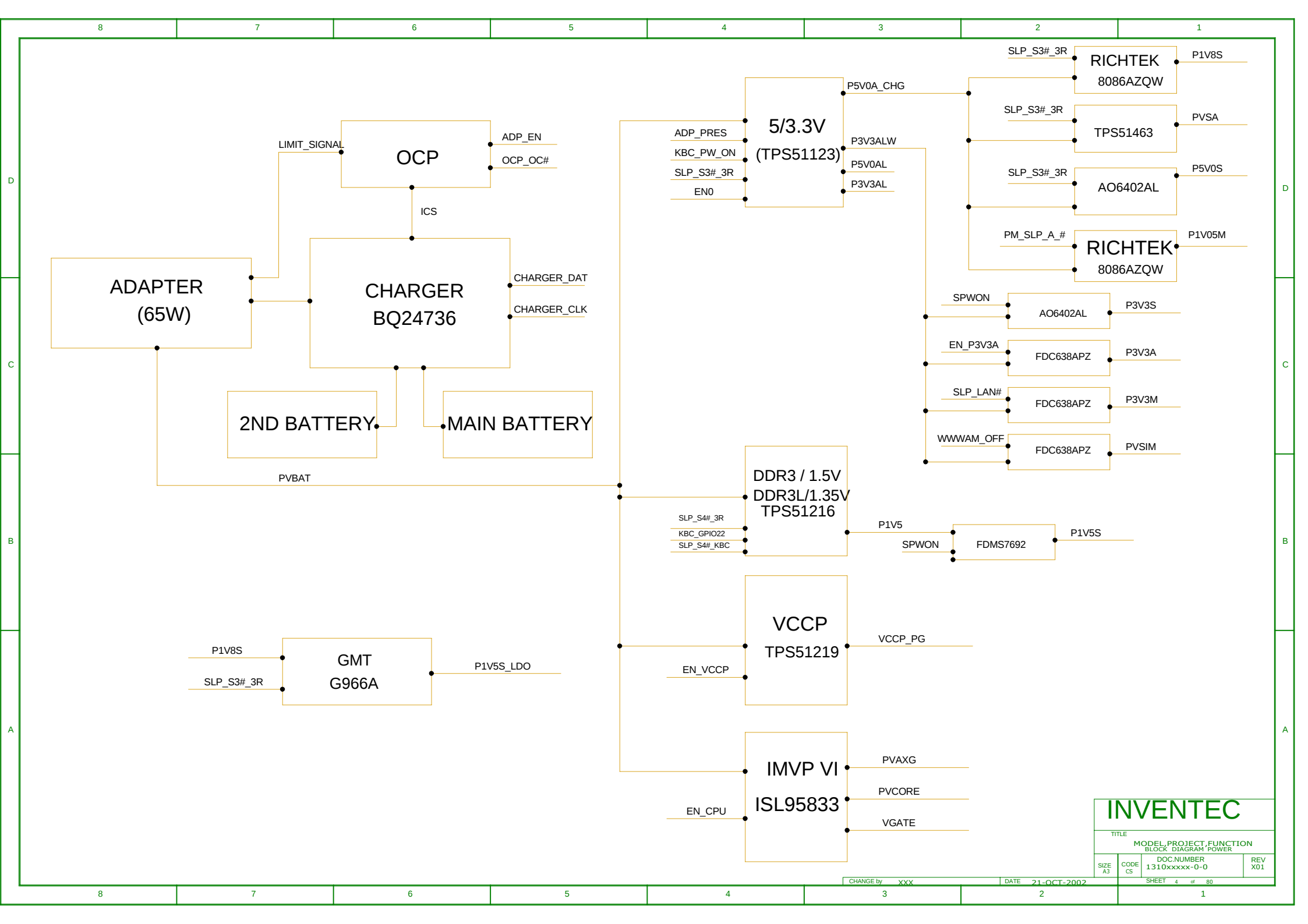
1. PROJECT NAME	23. IVY BRIDGE_1 (CLK,MISC,JTAG)	47. KEYBOARD
2. TABLE OF CONTENTS	24. IVY BRIDGE_2 (POWER)	48. TPM
3. BLOCK DIAGRAM	25. IVY BRIDGE_3 (DMI,DP,PEG,FDI)	49. LAN INTERFACE
4. SYSTEM POWER FLOW	26. IVY BRIDGE_4 (DDR3)	50. LAN RJ45 CNTR
5. SYSTEM POWER(CHARGER)	27. IVY BRIDGE_5 (GRAPHICS POWER)	51. WLAN
6. SYSTEM POWER(BATT SELECTOR)	28. IVY BRIDGE_6 (GND,RESERVED)	52. WWAN & SIM CARD
7. SYSTEM POWER(OCF)	29. DDR3_SO-DIMM0	53. DOCKING CNTR
8. SYSTEM POWER(P3V3A&P5V0A)	30. DDR3_SO-DIMM1	54. STICK POINT & B2B CNTR
8. SYSTEM POWER(P5V0A _CHG	31. PANTER POINT_1 (HDA,JTAG,SPI,SATA)	55. CODEC
10. SYSTEM POWER(P1V5)	32. PANTER POINT_2 (PCI-E,SMBUS,CLK)	56. EXT MIC AMP
11. SYSTEM POWER(P1V05M)	33. PANTER POINT_3 (DMI,FDI,GPIO)	57. JACK & USB3 CNTR
12. SYSTEM POWER(P1V05S)	34. PANTER POINT_4 (LVDS,DDI)	58. CARD READER
13. SYSTEM POWER(P1V8S)	35. PANTER POINT_5 (PCI,USB,NVRAM)	59. BUTTON LED
14. SYSTEM POWER(PVSA)	36. PANTER POINT_6 (GPIO,VSS_NCFT,RSVD)	60. SMART VARD & LED DB
15. SYSTEM POWER(PVCORE1)	37. PANTER POINT_7 (POWER)	61. MIC DB
16. SYSTEM POWER(PVCORE2)	38. PANTER POINT_8 (POWER)	62. SCREW
17. POWER TEST POINT	39. PANTER POINT_9 (GND)	63. EMI & RF SOLUTION
18. POWER PAD	40. CRT / DISPLAY PORT CNTR	64. SYSTEM SEQUENCE
19. POWER(SLEEP)	41. LCM & WEBCAM	
20. POWER(SEQUENCE)	42. SATA & MSATA CNTR	
21. XDP CONN	43. USB CNTR & USB CHARGER	
22. FAN & THERMAL	44. FINGER PRINTER CNTR	
	45. ACCELEMETOR	
	46. KBC / SPI	

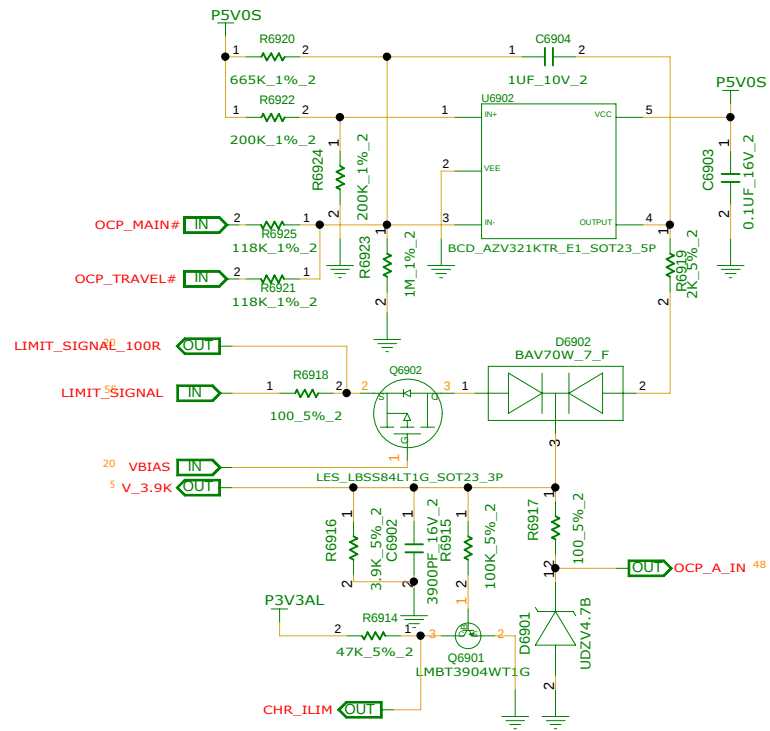
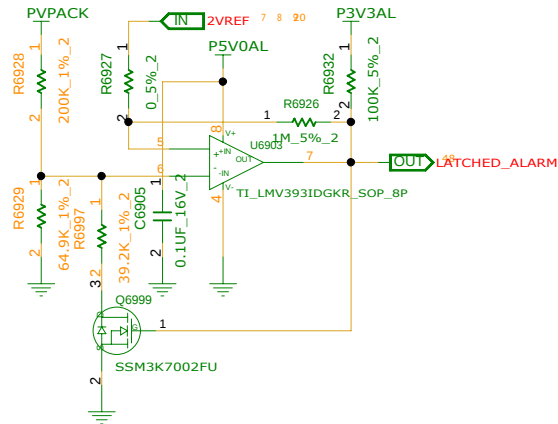
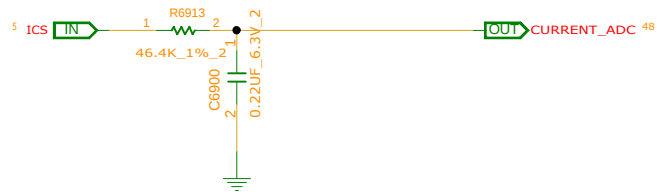
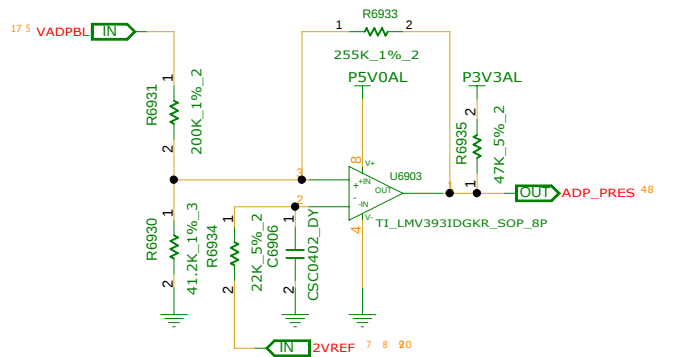
INVENTEC

TITLE
MODEL PROJECT FUNCTION
TABLE OF CONTENTS

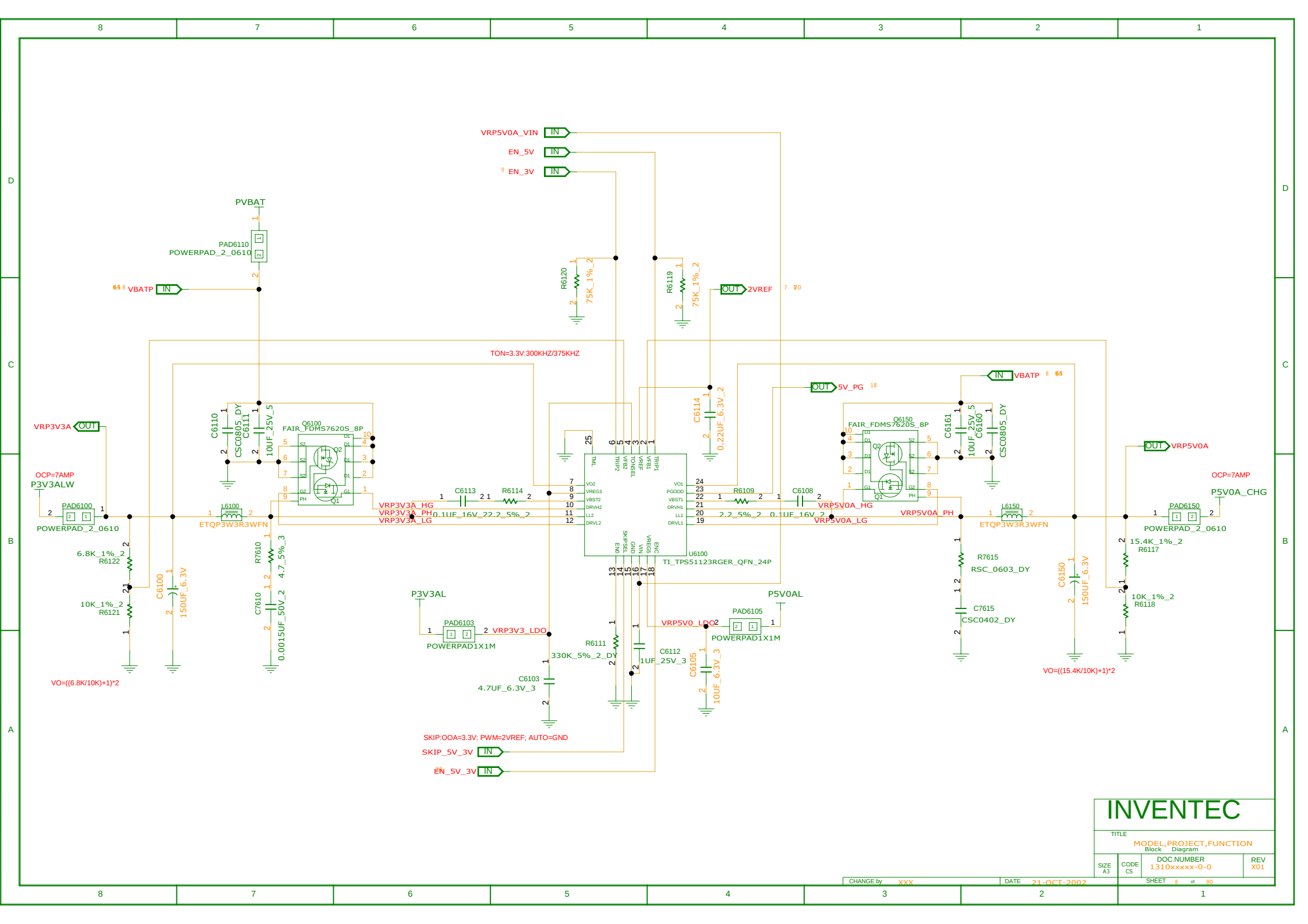
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CHANGE by XXX		DATE 21-OCT-2002	SHEET 2 of 80







INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 7 of 80			



INVENTEC

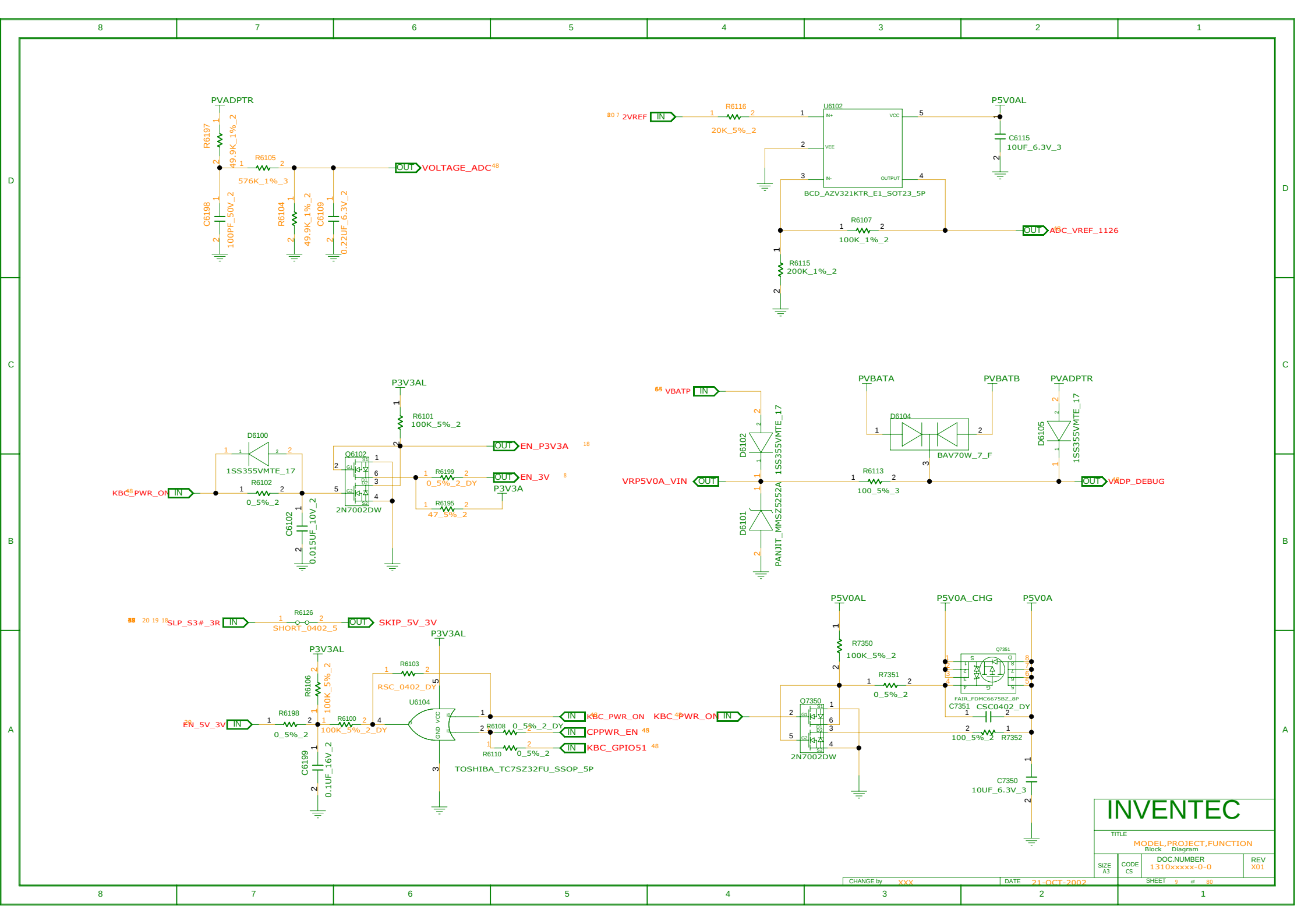
TITLE
MODEL,PROJECT,FUNCTION

Doc. Number
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REV
X01

CHANGE by XXX DATE 21-OCT-2002

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INVENTEC

TITLE MODEL,PROJECT,FUNCTION

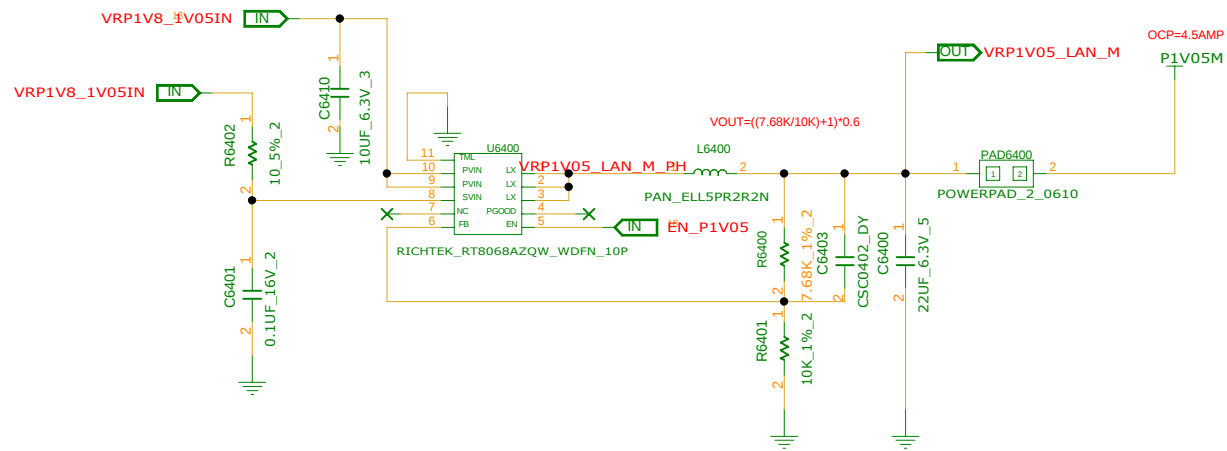
Block Diagram

DOC NUMBER 1310xxxxx-0-0

REV X01

SHEET 8 of 80

CHANGE by XXX DATE 21-OCT-2002



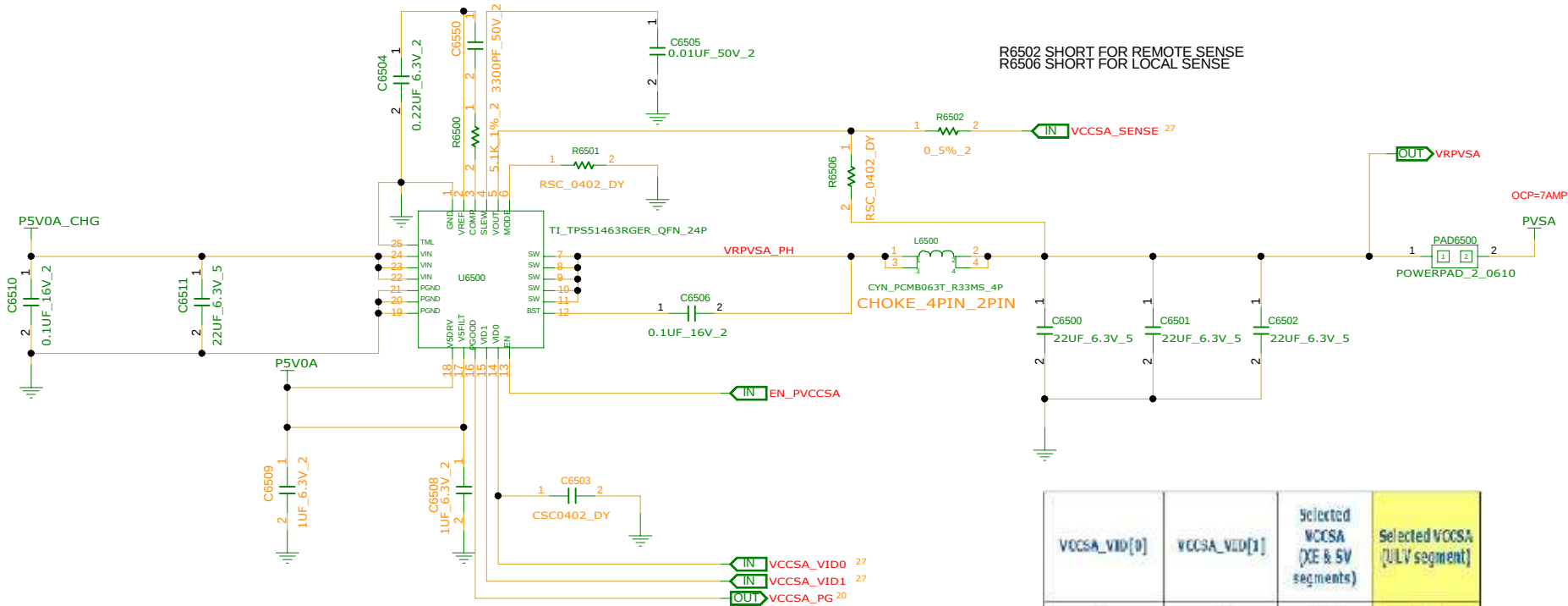
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TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

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VCCSA_VID[0]	VCCSA_VID[1]	Selected VCCSA (XE & 5V segments)	Selected VCCSA (ULV segment)
0	0	0.9 V	0.9 V
0	1	0.8 V	0.85 V
1	0	0.725 V	0.775 V
1	1	0.675 V	0.75 V

INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

SIZE A3

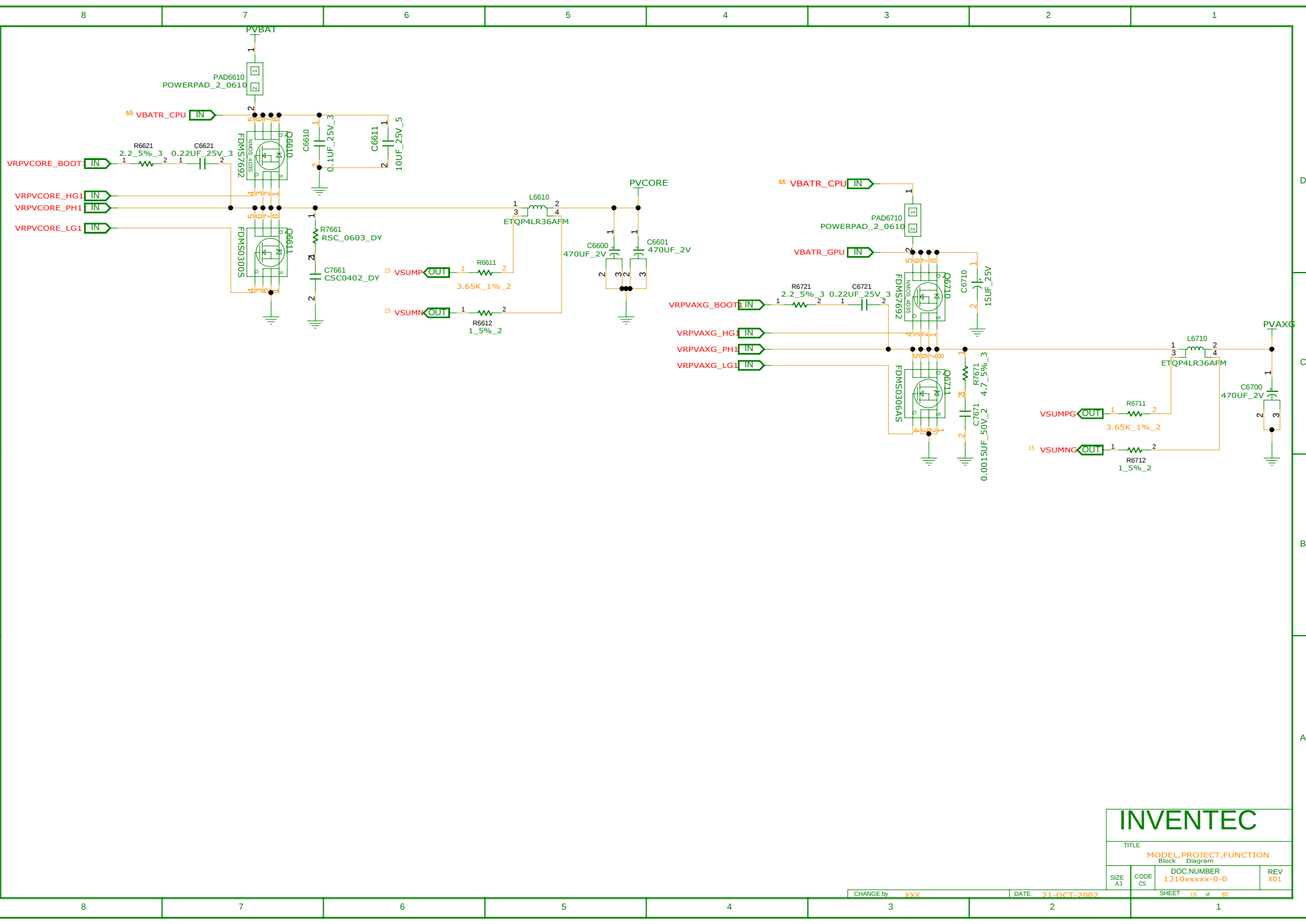
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DOC NUMBER 1310xxxxx-0-0

REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 14 of 80



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

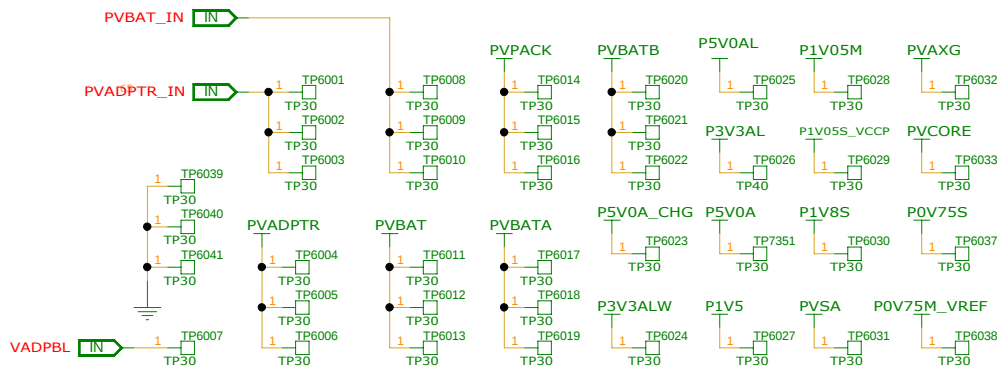
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CODE
CS

DOC NUMBER
1310xxxxx-0-0

REV
X01

SHEET 16 of 80



INVENTEC

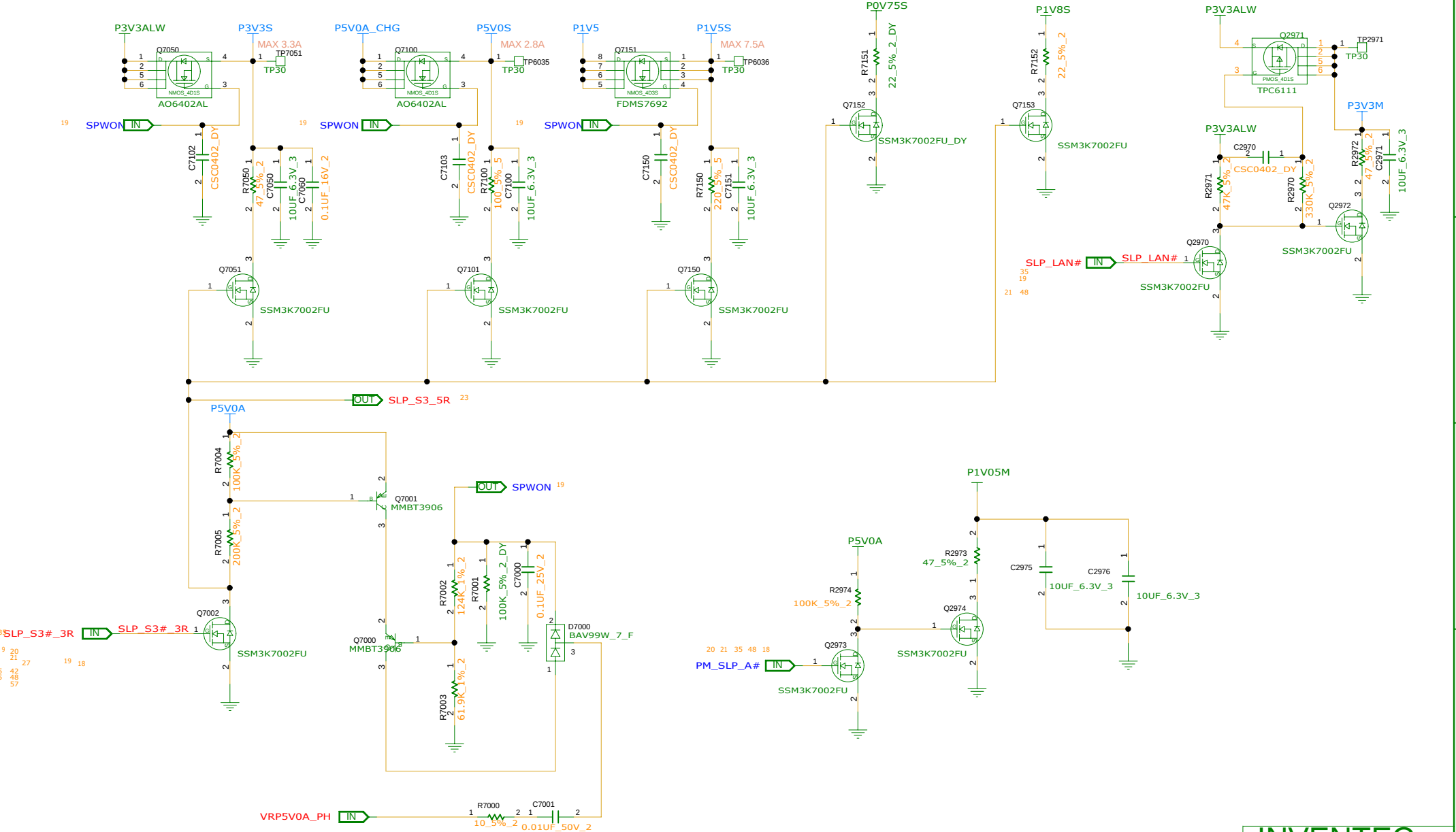
TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by XXX DATE 21-OCT-2002

SHEET 17 of 80

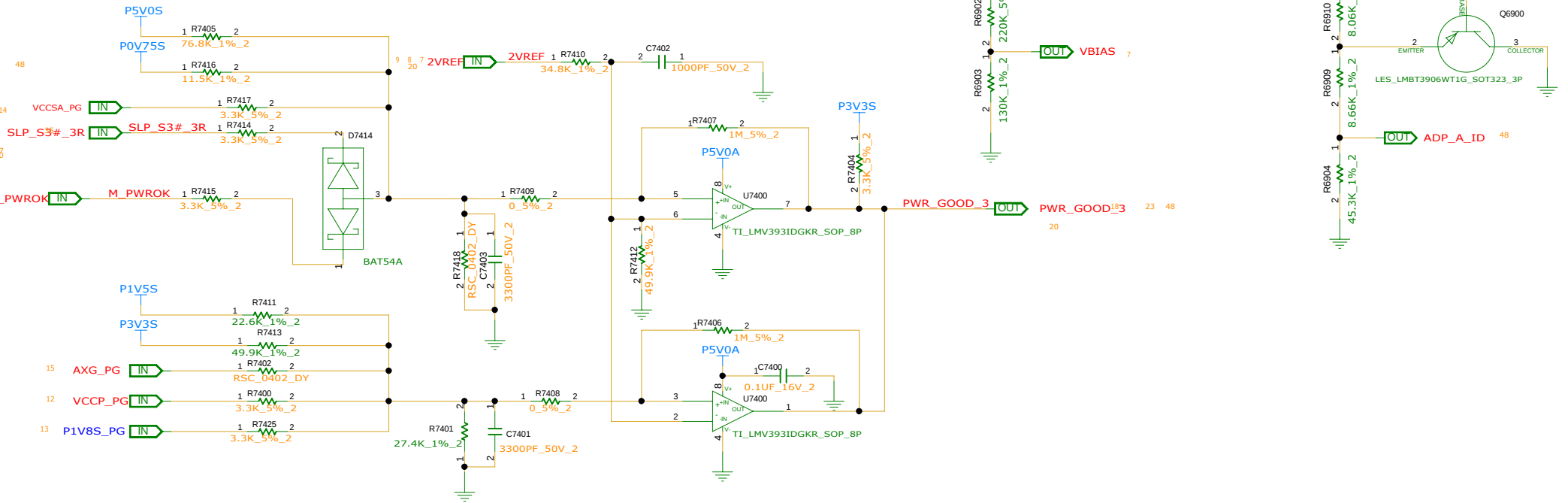
REFERENCE NUMER : 7000~7350



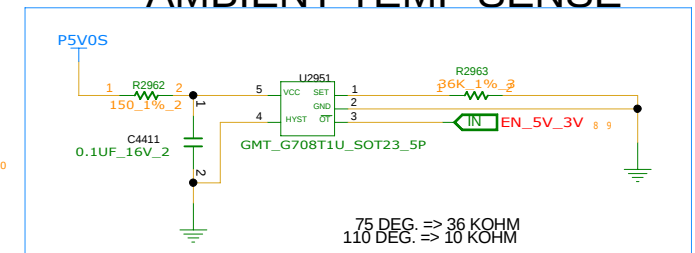
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INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
POWER(SLEEP)			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 19 of 80			

REFERENCE NUMER : 7400~7450



AMBIENT TEMP SENSE



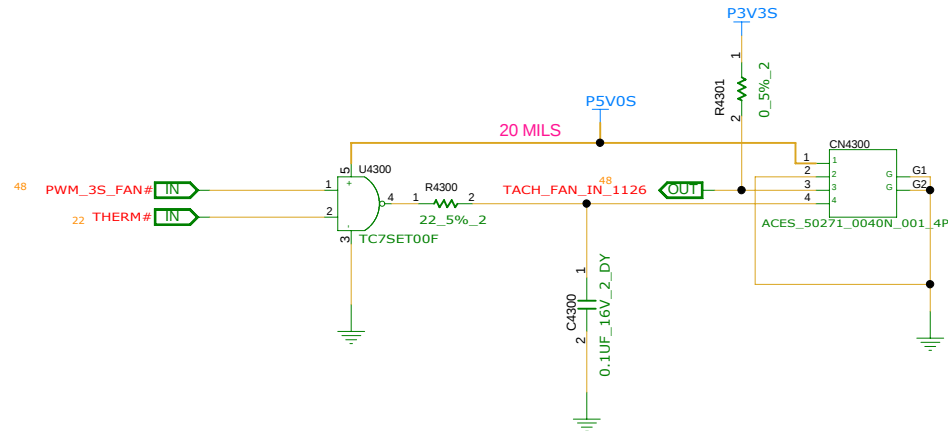
75 DEG. => 36 KOHM
110 DEG. => 10 KOHM

INVENTEC

TITLE			
MODEL PROJECT FUNCTION POWER (SEQUENCE)			
SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
SHEET 20 of 80			

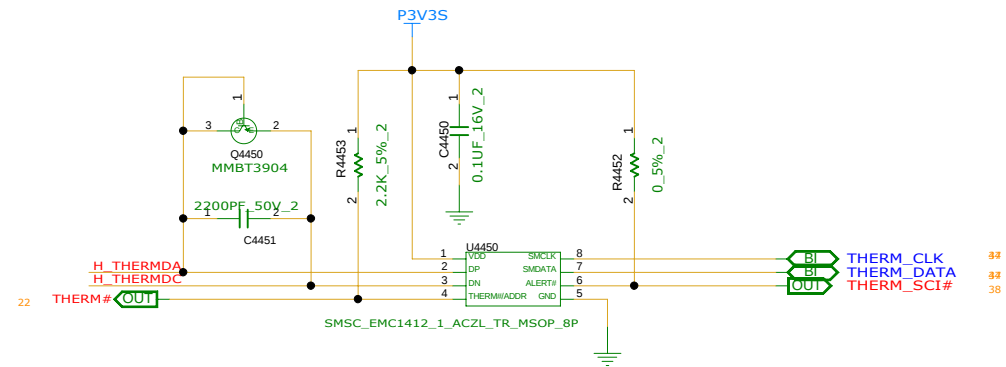
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REFERENCE NUMBER:4400~4349



REFERENCE NUMBER:4450~4499

THERM SENSOR



REFERENCE NUMBER:4411~4449

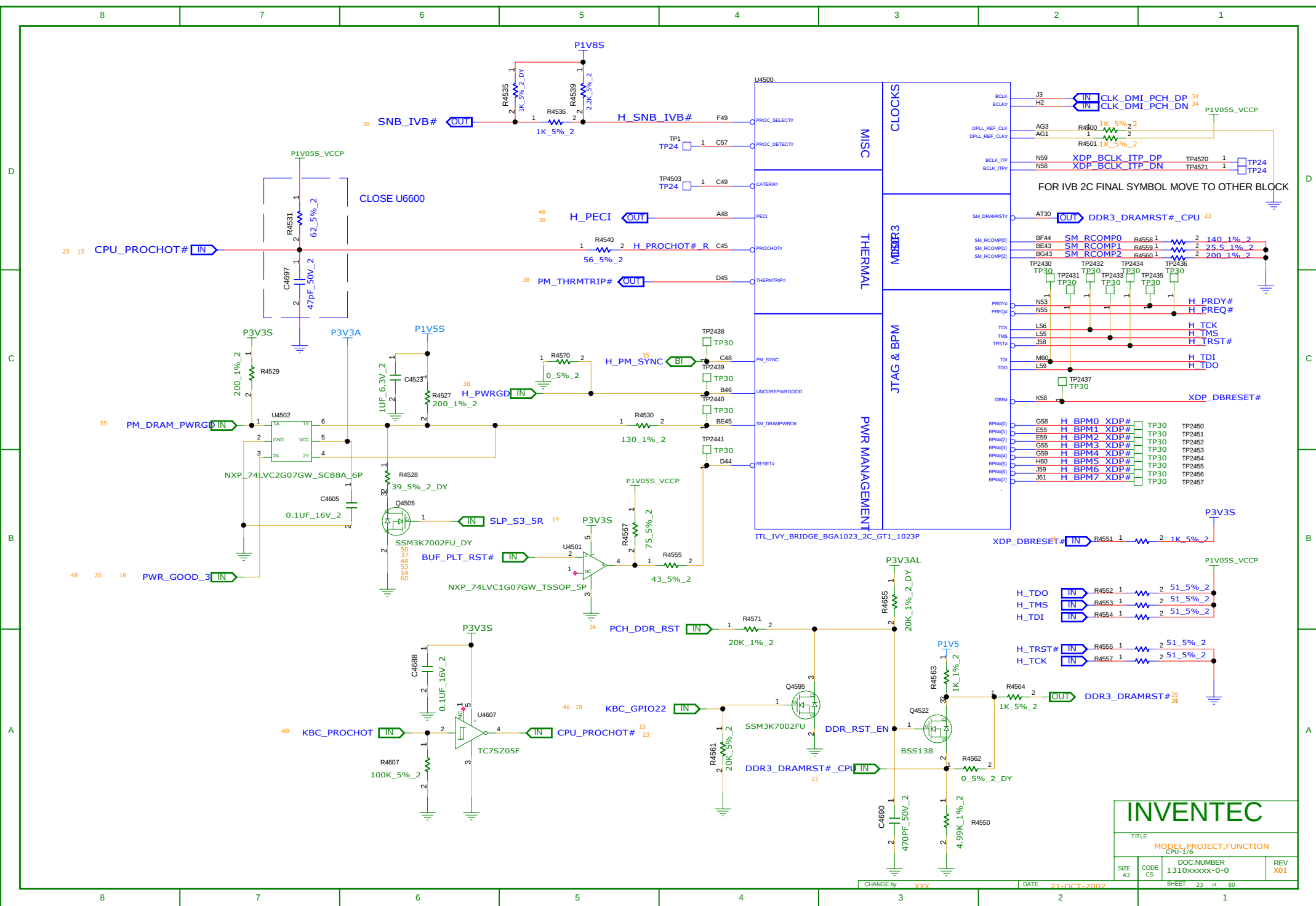
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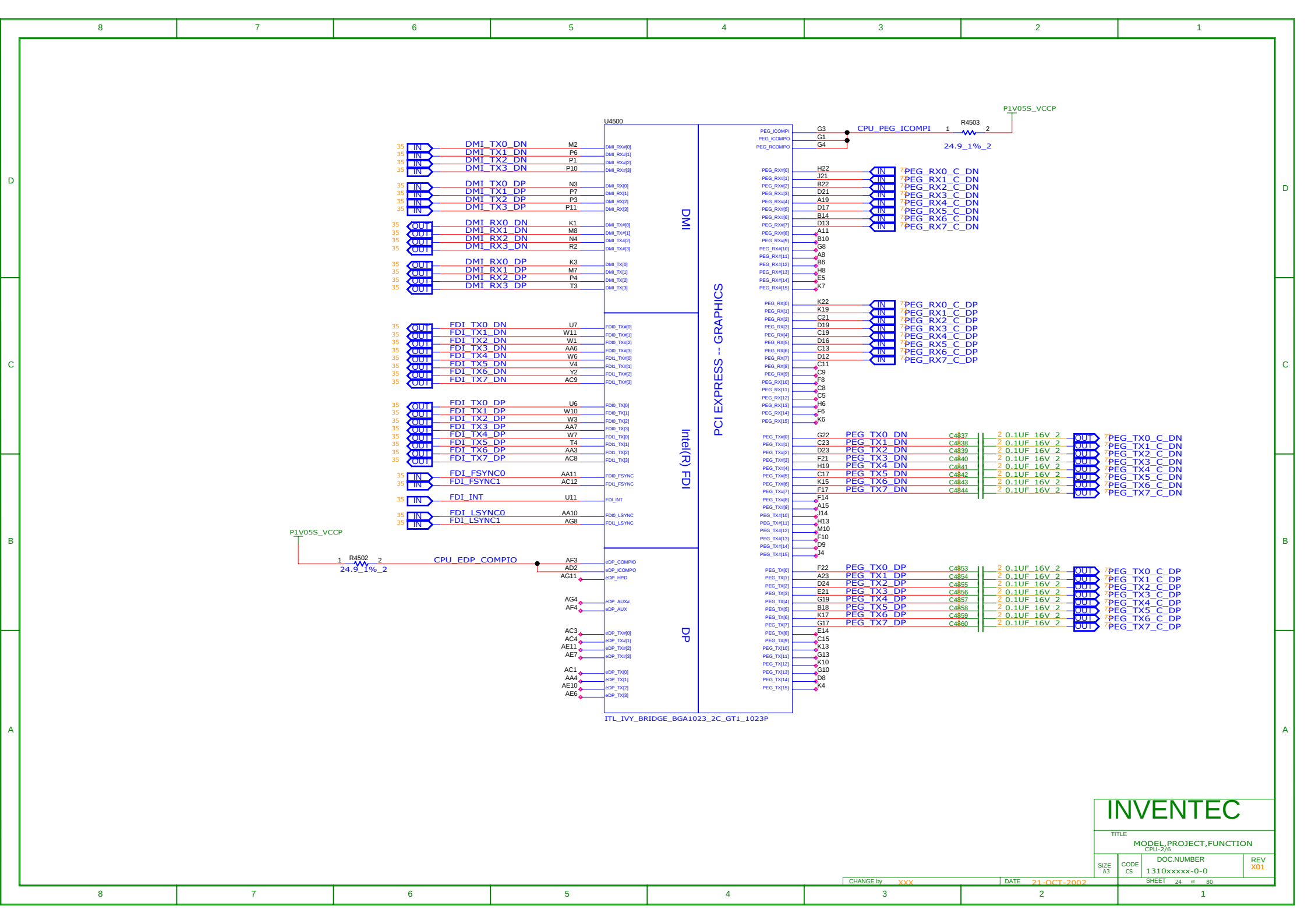
TITLE MODEL PROJECT,FUNCTION
FAN & THERMAL

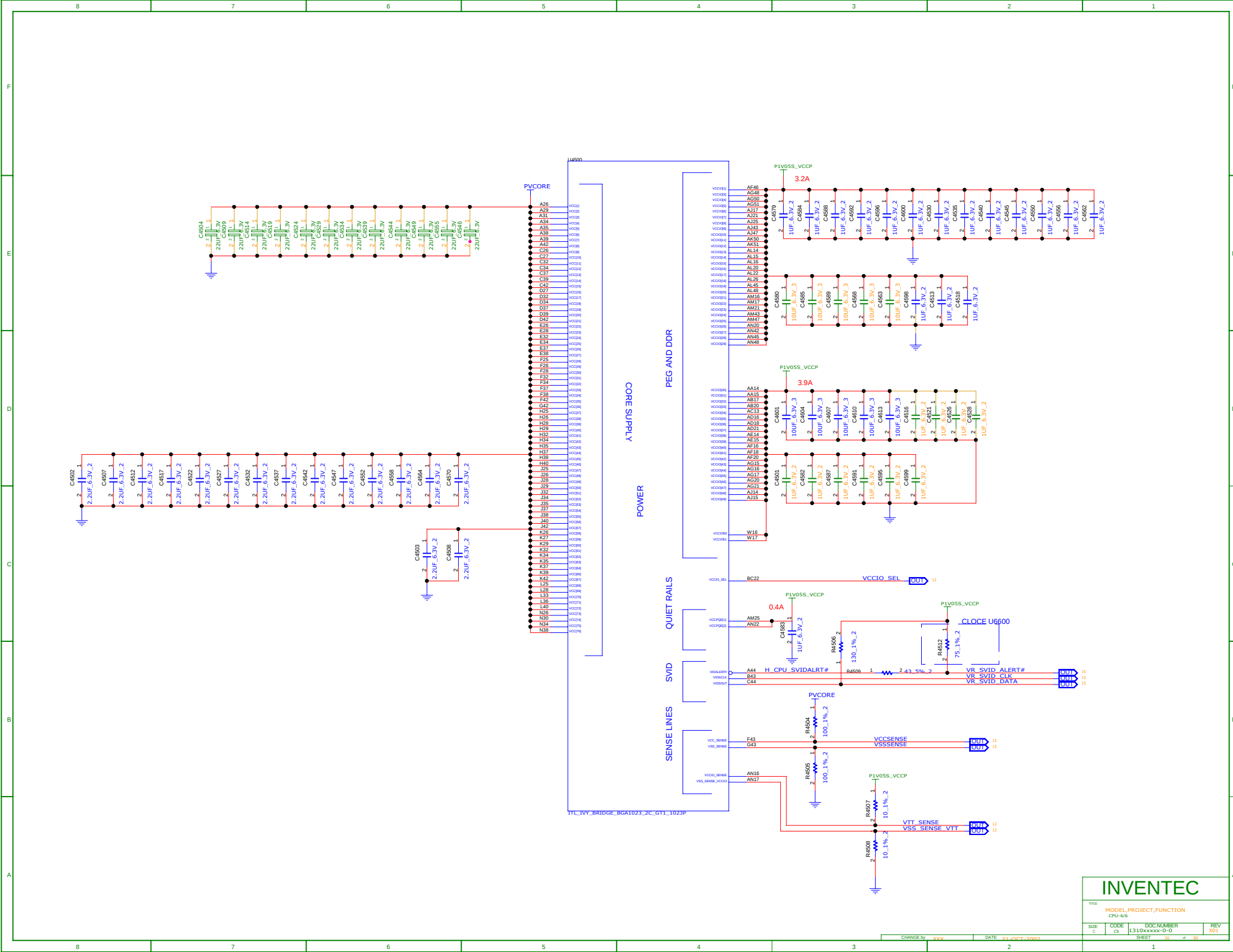
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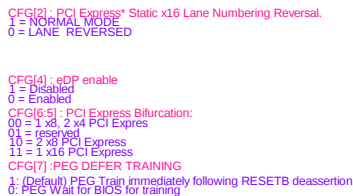
CHANGE by XXX DATE 21-OCT-2002

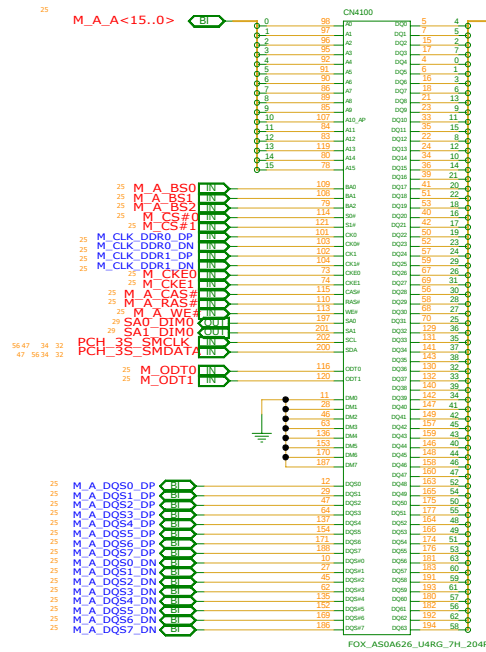
SHEET 22 of 80







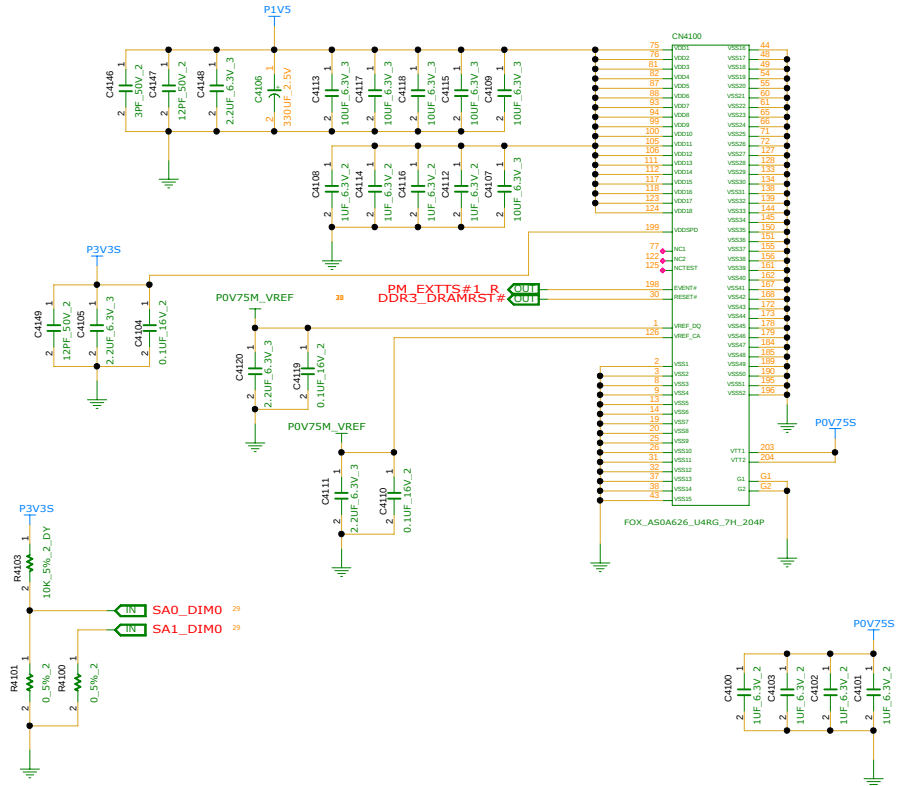




NOTE:

IF SA0_DIM0=0, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA0
SO-DIMMA TS ADDRESS IS 0X30

IF SA0_DIM0=1, SA1_DIM0=0
SO-DIMMA SPD ADDRESS IS 0XA2
SO-DIMMA TS ADDRESS IS 0X32



REFERENCE NUMBER:4100-4299

INVENTEC

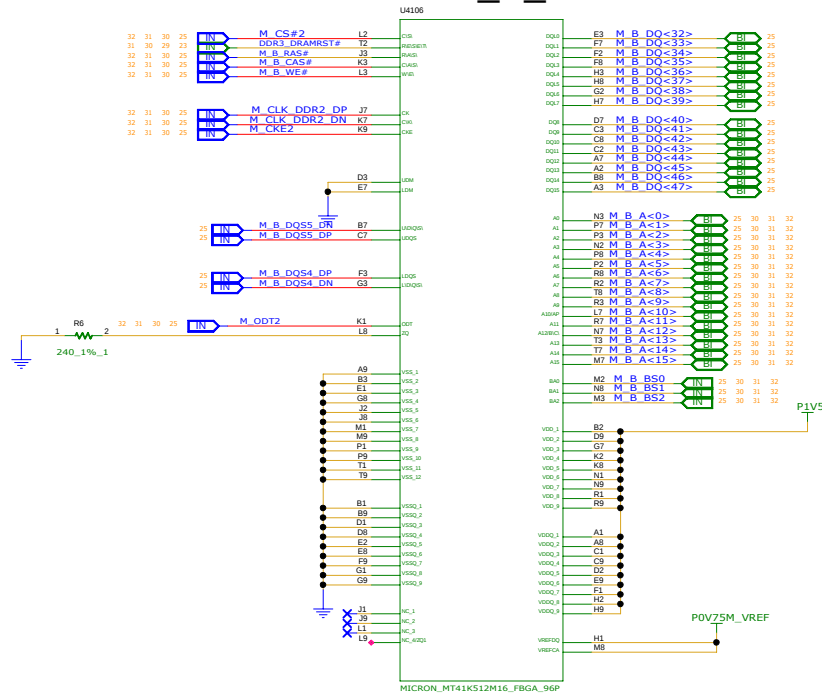
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MODEL,PROJECT,FUNCTION			
DDR3_SO-DIMM0			
SIZE	CODE	DOC NUMBER	REV
C	CS	13100XXXX-0-0	201
SHEET		24	of 68

CHANGE BY: xxx DATE: 21-OCT-2002

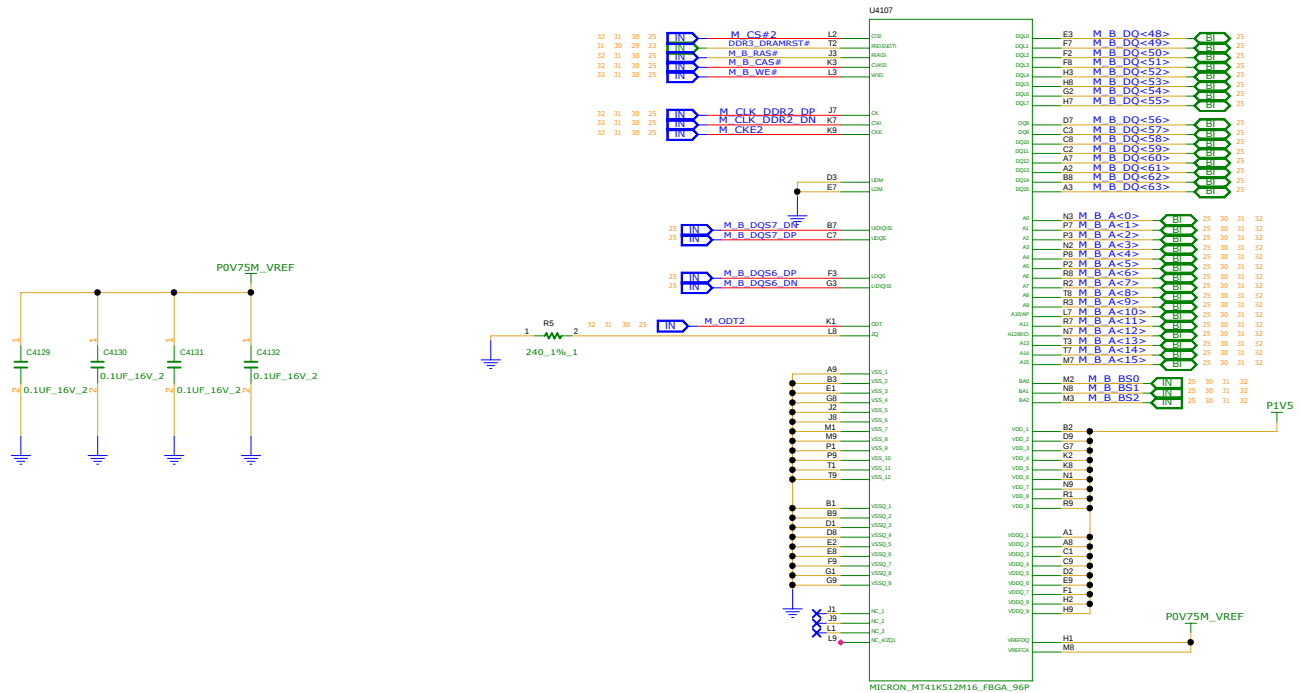


TITLE			
MODEL,PROJECT,FUNCTION 35_DDR3L-2			
SIZE C	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 30 of 80			

DDR3L_B_2



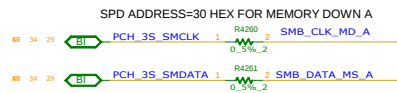
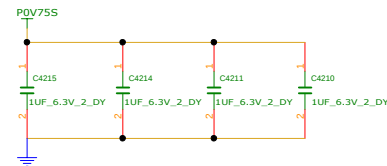
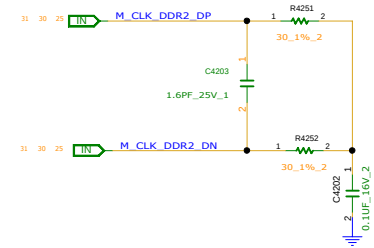
DDR3L_B_3



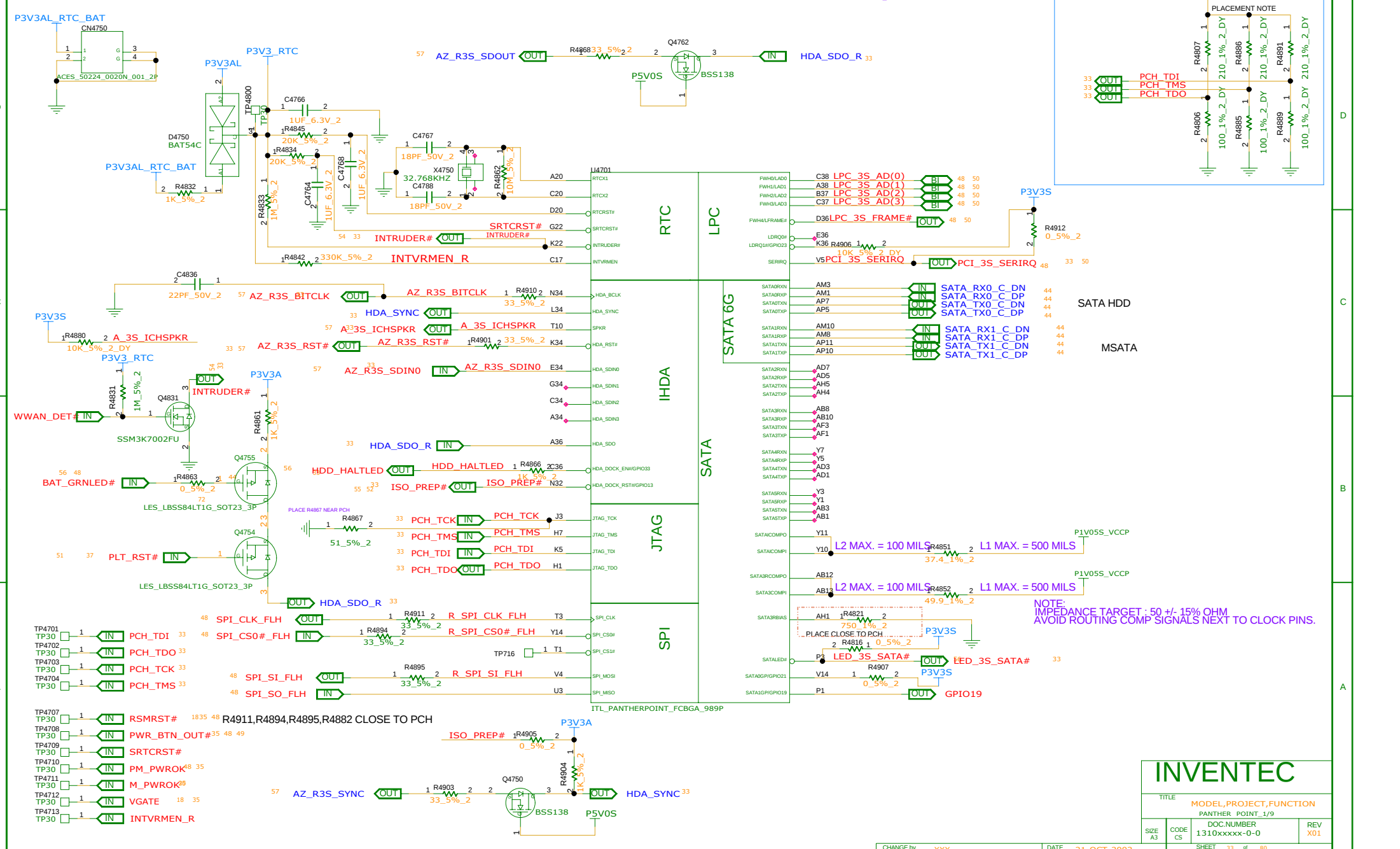
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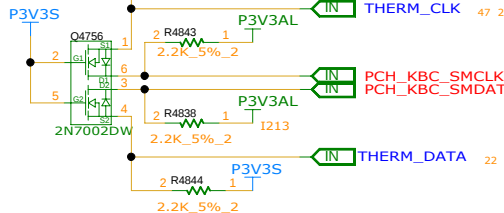
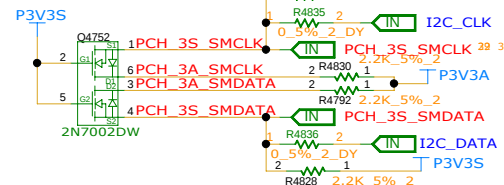
TITLE			
MODEL,PROJECT,FUNCTION			
36_DDR3L-3			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310XXXXX-0-0	201
SHEET		31	of 68

CHANGE by: xxx DATE: 21-OCT-2002



REFERENCE NUMER : 4700~4949

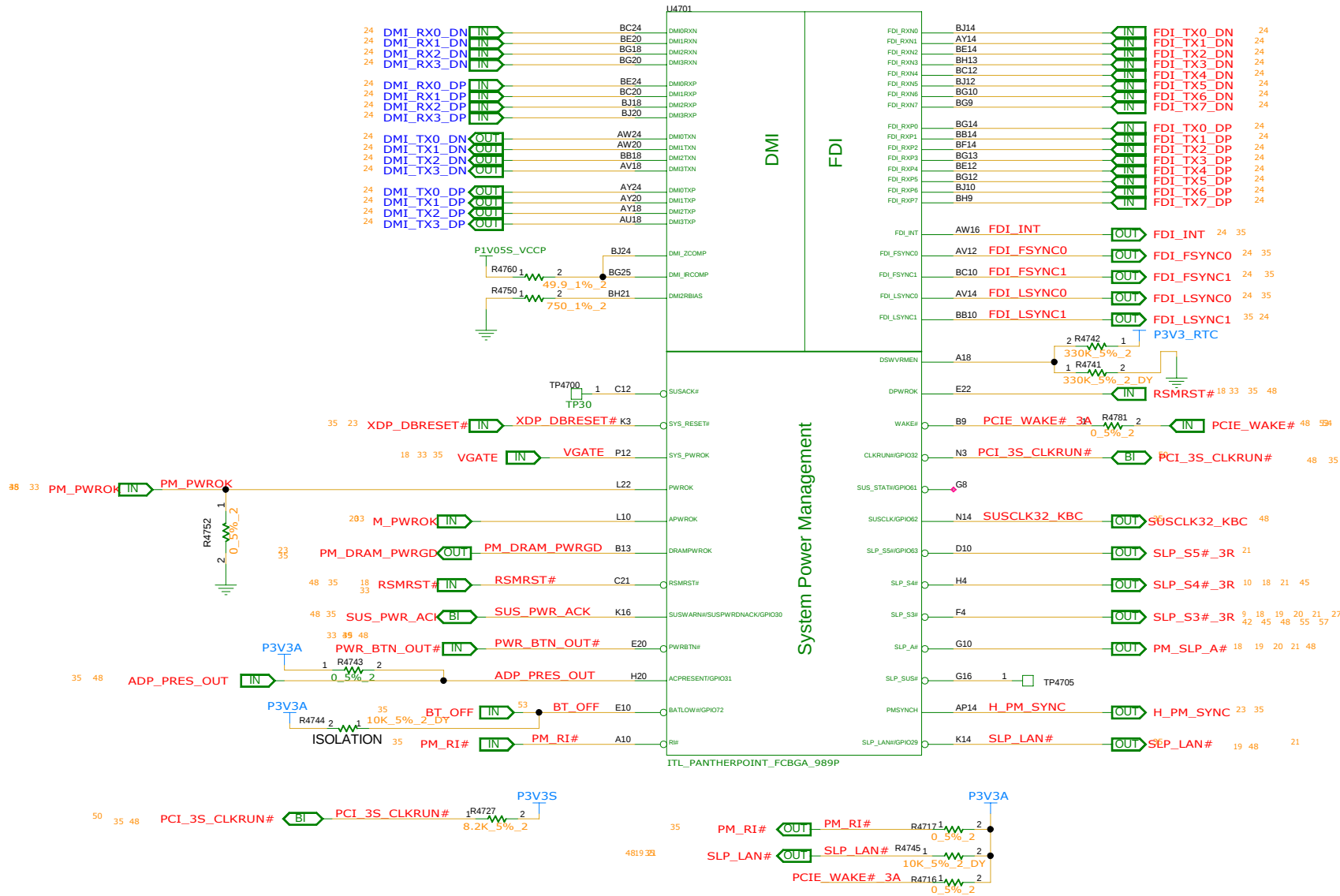




REFERENCE NUMER : 4700~4949

NOTE:

- 1.SLP_SUS AND SUSACK# ARE NC IF DSW IS NOT SUPPORTED
- 2.DPWROK SHOULD CONNECT TO RSMRST# IF DSW NOT SUPPORTED
- 3.PCH_DPWROK PULL UP TO P3V3S ENABLES DSW WUPPORT. NO INSTALL R4752 TO DISABLE DSW



INVENTEC

TITLE
MODEL PROJECT FUNCTION
PANTHER POINT_3/9

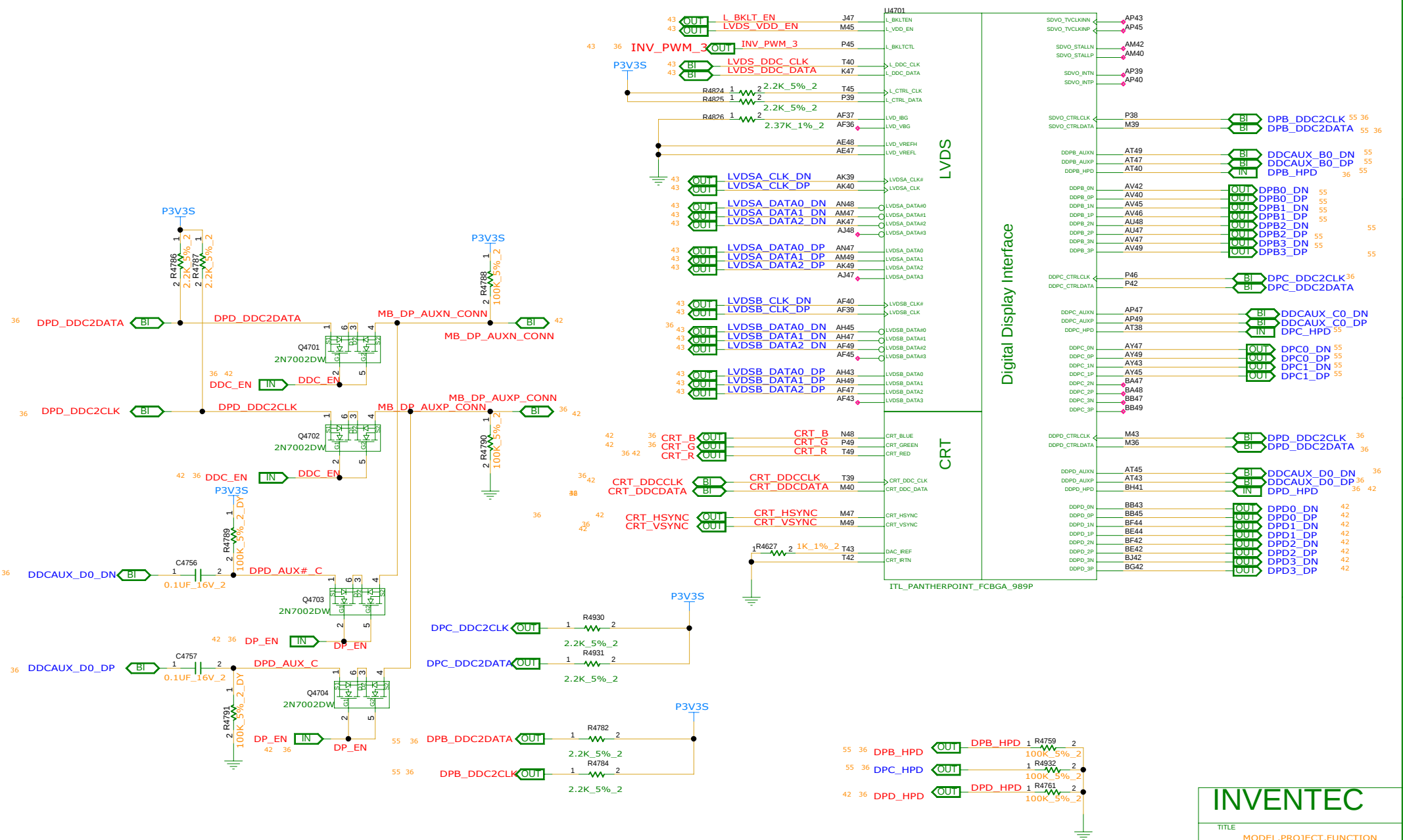
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REV
X01

CHANGE by XXX DATE 21-OCT-2002

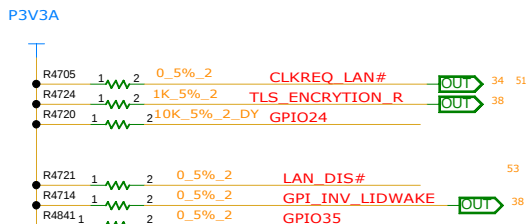
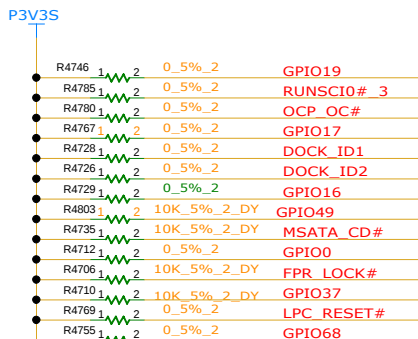
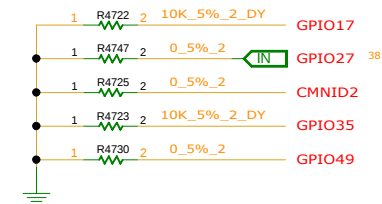
SHEET 35 of 80

REFERENCE NUMER : 4700~4949

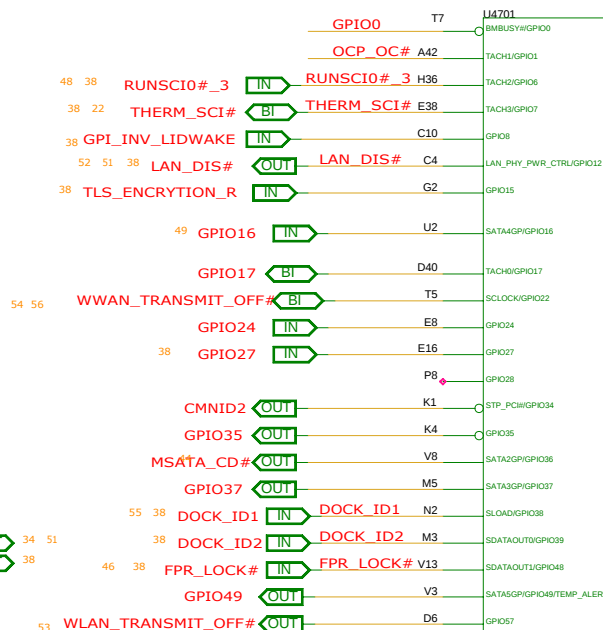


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
PANTHER POINT_4/9			
SIZE A3		DOC NUMBER	REV
CODE CS		1310xxxxx-0-0	X01
SHEET 36 of 80			

REFERENCE NUMBER : 4700~4949



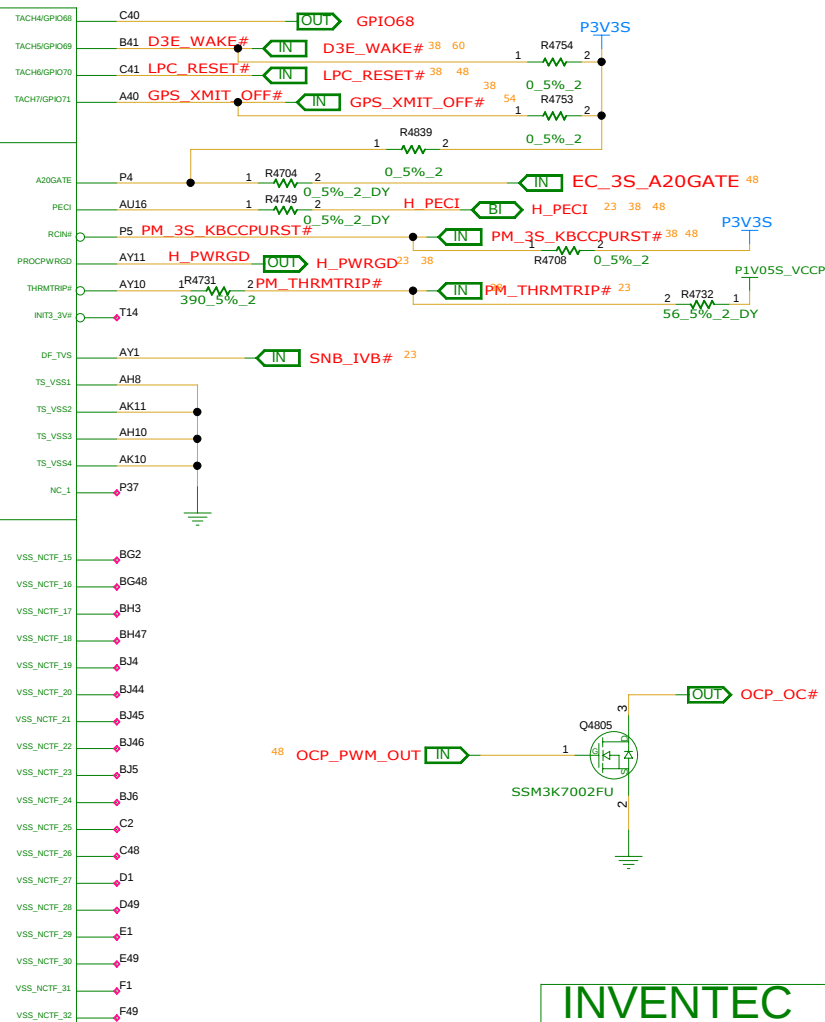
GPIO17	GPIO35	GPIO49	GPIO50	
R4767	R4841	R4803	R4758	H
R4722	R4723	R4730	R4719	L
0	1	0	0	SI 1
0	1	0	1	SI 1B
0	1	1	0	SI 2
1	0	0	0	PV 1
1	1	0	0	MV



GPIO

CPU/MISC

NCTF

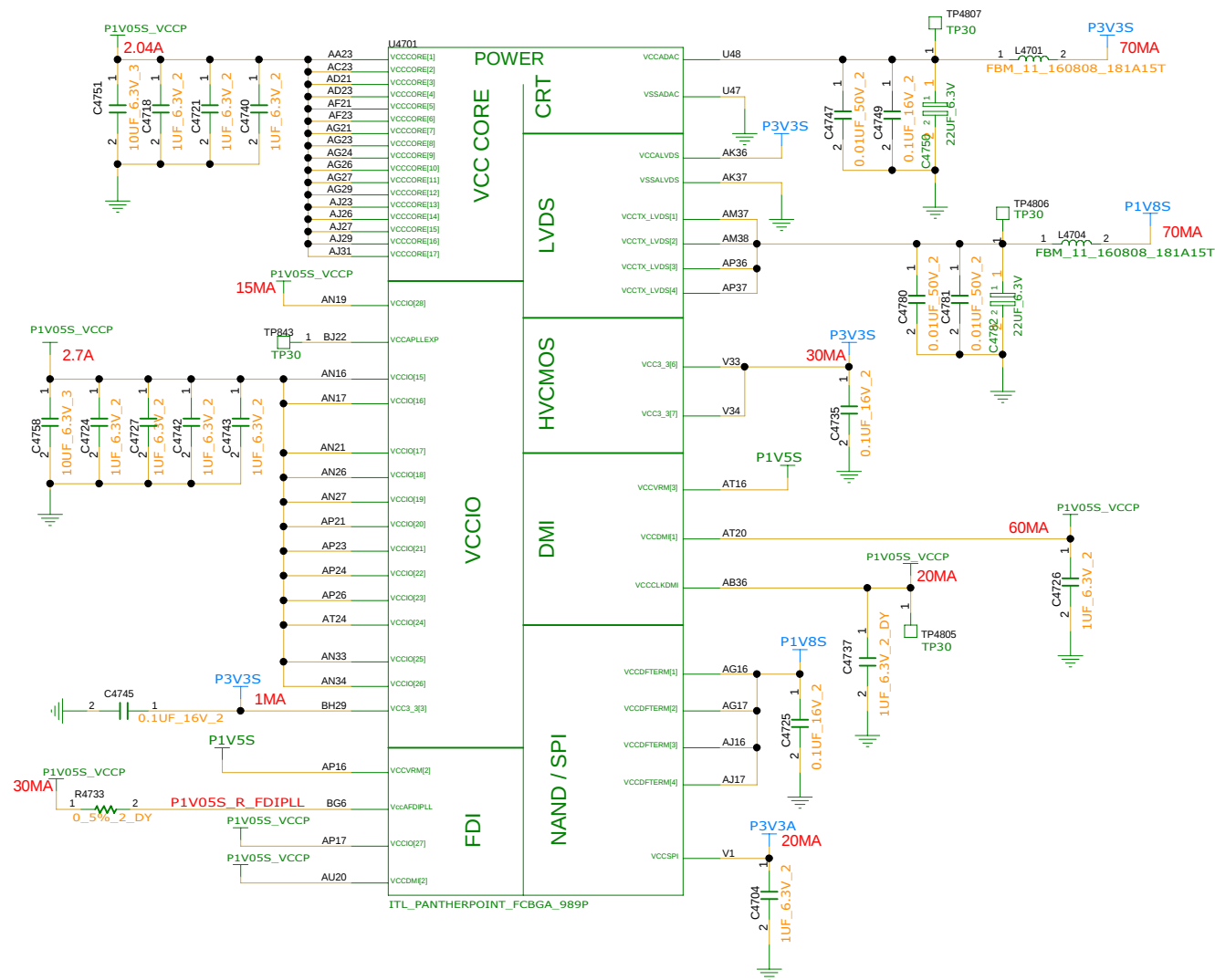


INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
PANTHER POINT_6/9			
SIZE	CODE	DOC NUMBER	REV
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REFERENCE NUMER : 4700~4949



INVENTEC

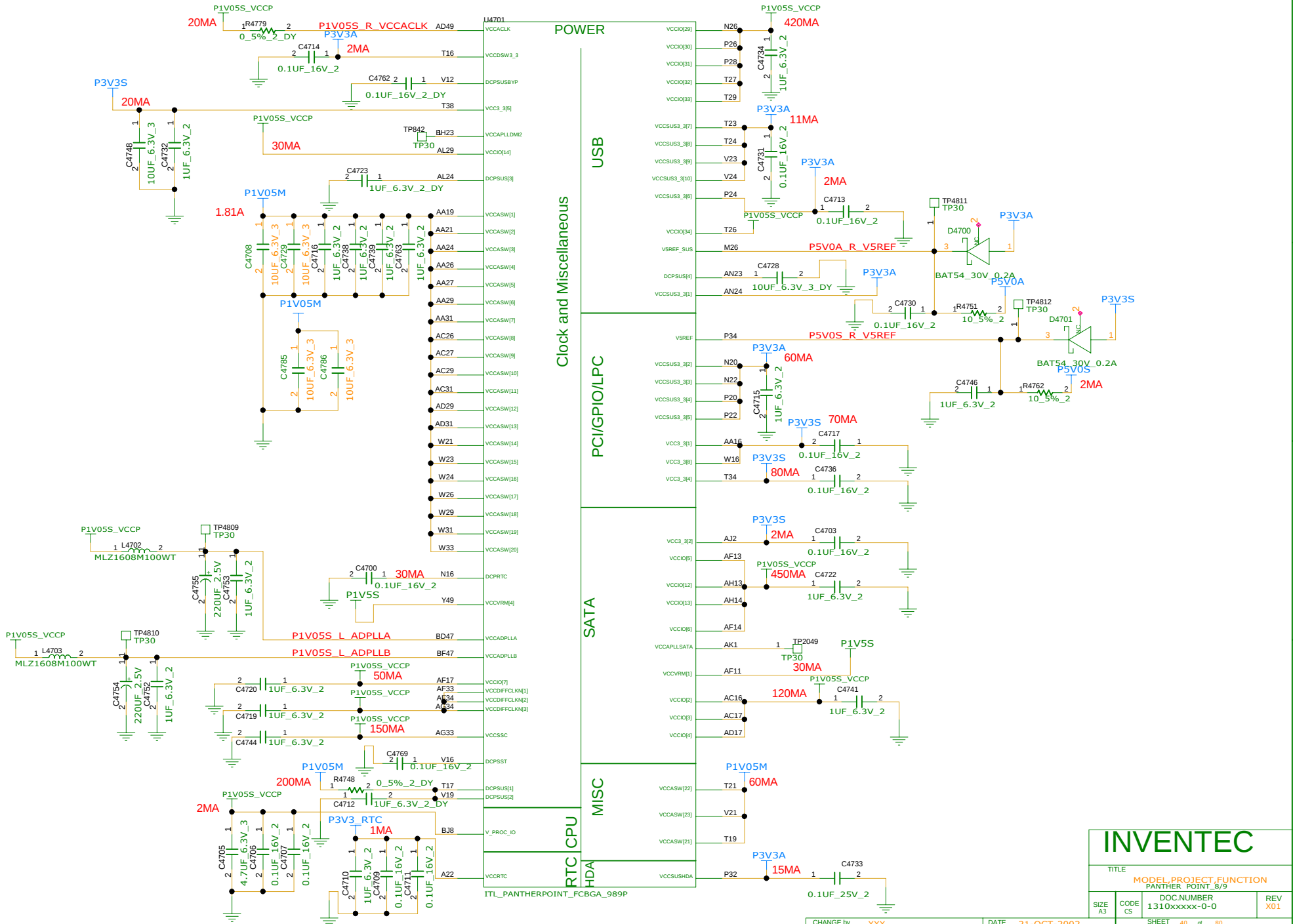
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	PANTHER POINT 7/9

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CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 39 of 80

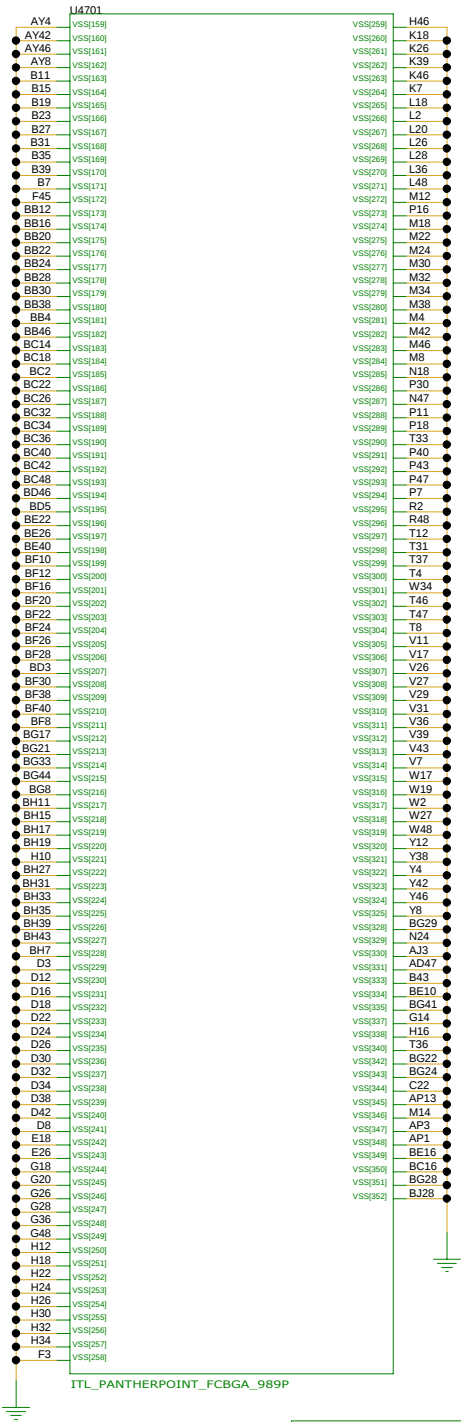
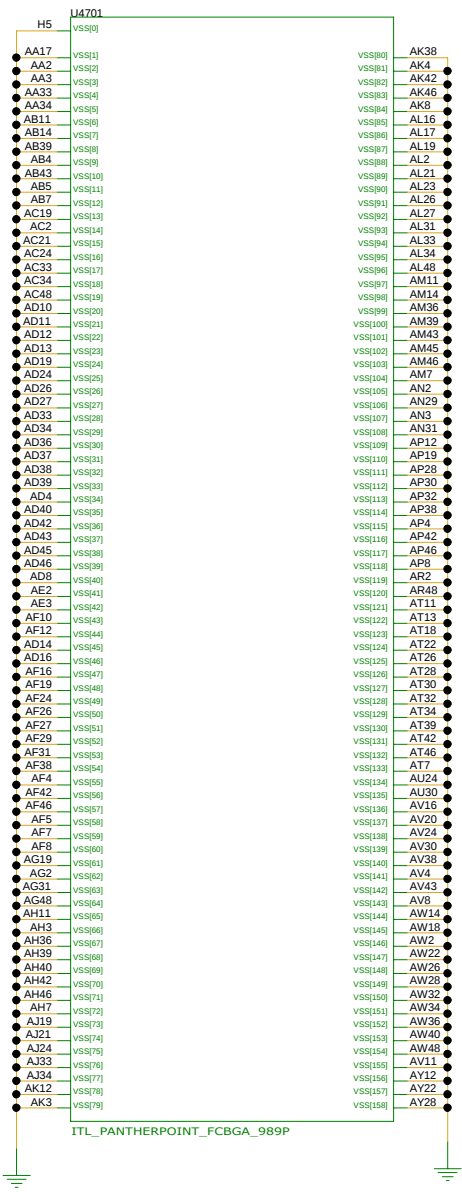
REFERENCE NUMER : 4700~4949



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
PANTHER POINT 8/9			
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SHEET 40 of 80			

REFERENCE NUMBER : 4700~4949

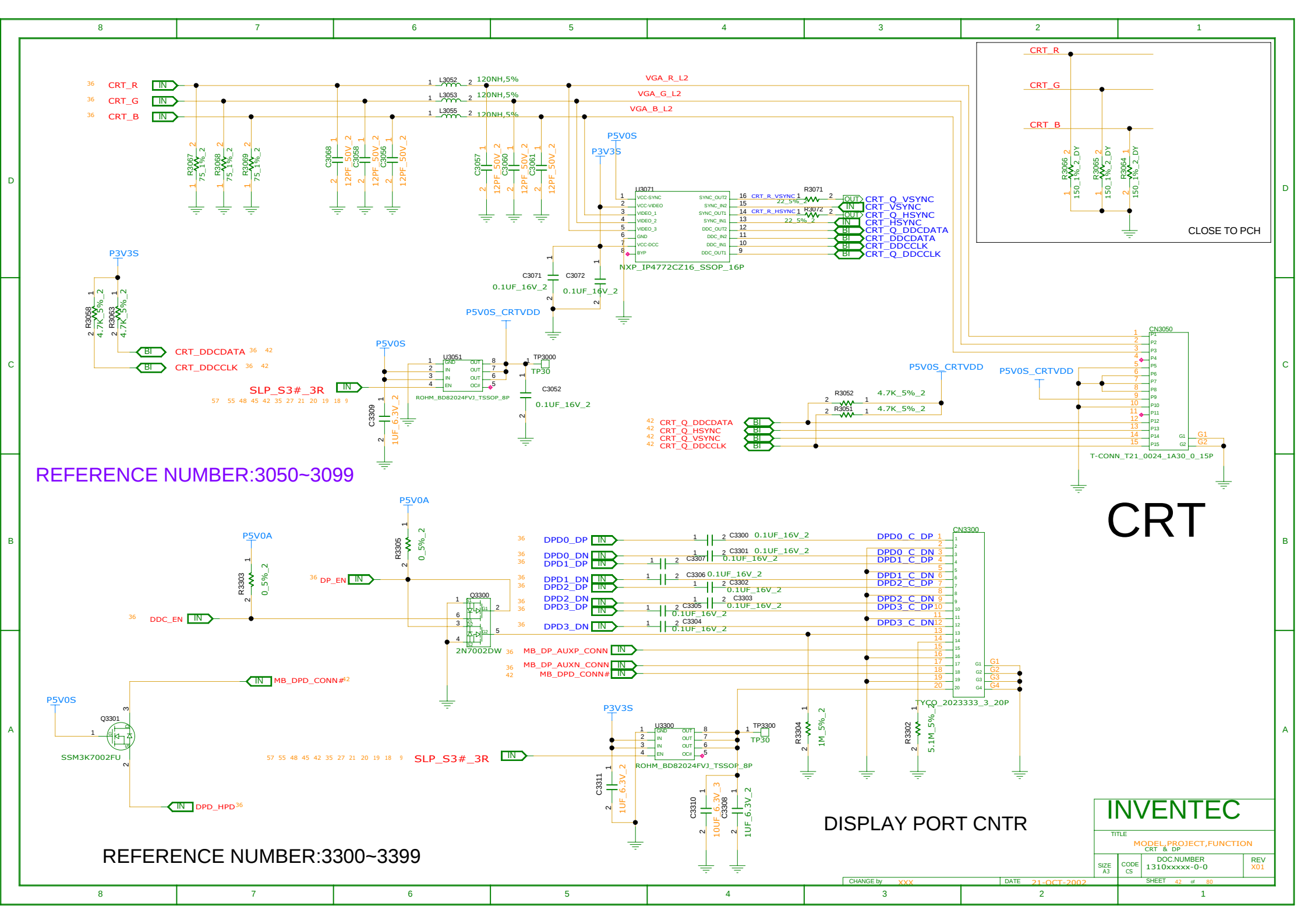


INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
PANTHER POINT 3/9

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

SHEET 41 of 80



REFERENCE NUMBER:3050~3099

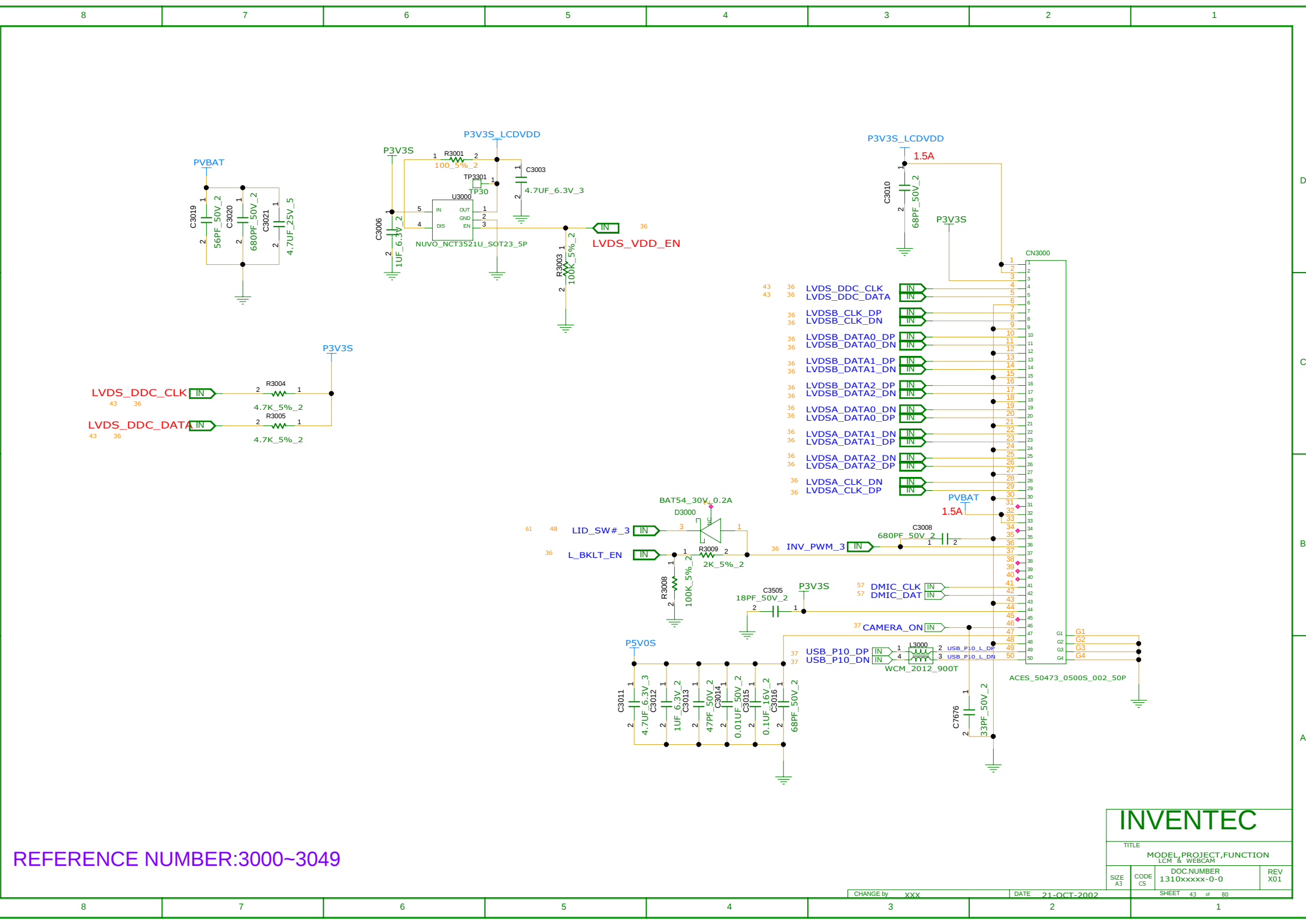
CRT

REFERENCE NUMBER:3300~3399

DISPLAY PORT CNTR

INVENTEC

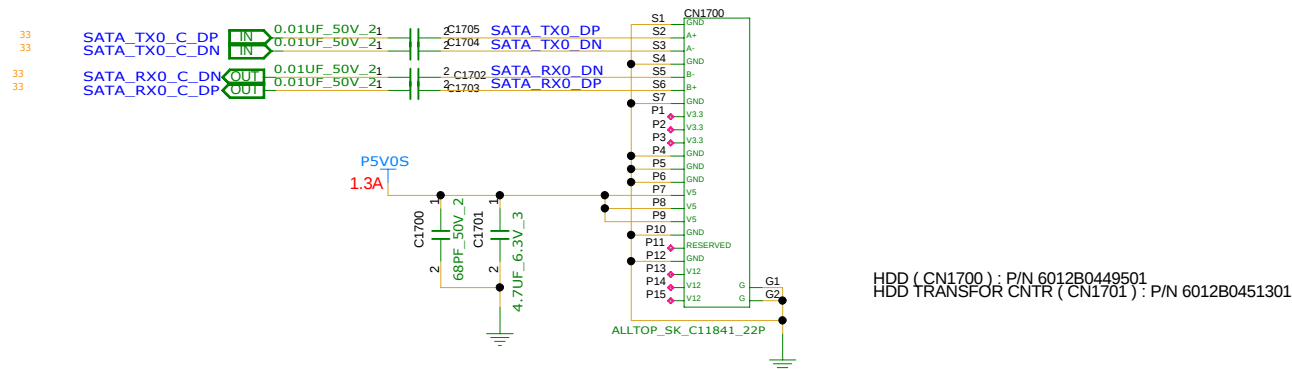
TITLE			
MODEL,PROJECT,FUNCTION			
CRT & DP			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxx-0-0	REV X01
		SHEET 42 of 80	



REFERENCE NUMBER:3000~3049

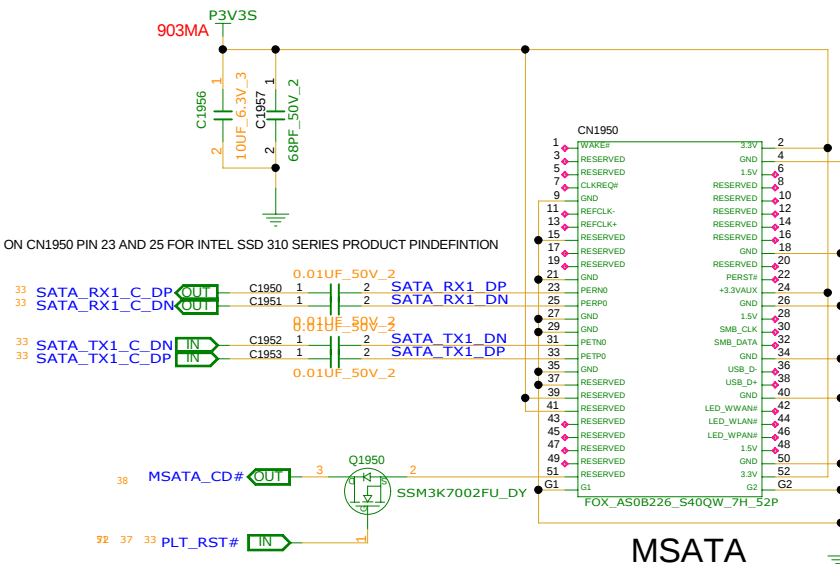
INVENTEC			
TITLE			
MODEL PROJECT,FUNCTION			
LCM & WEBCAM			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 43 of 80			

REFERENCE NUMBER:1700~1749



SATA HDD

NOTE : SWAP SATA_RXP2 AND SATA_RXN2 ON CN1950 PIN 23 AND 25 FOR INTEL SSD 310 SERIES PRODUCT PINDEFINTION



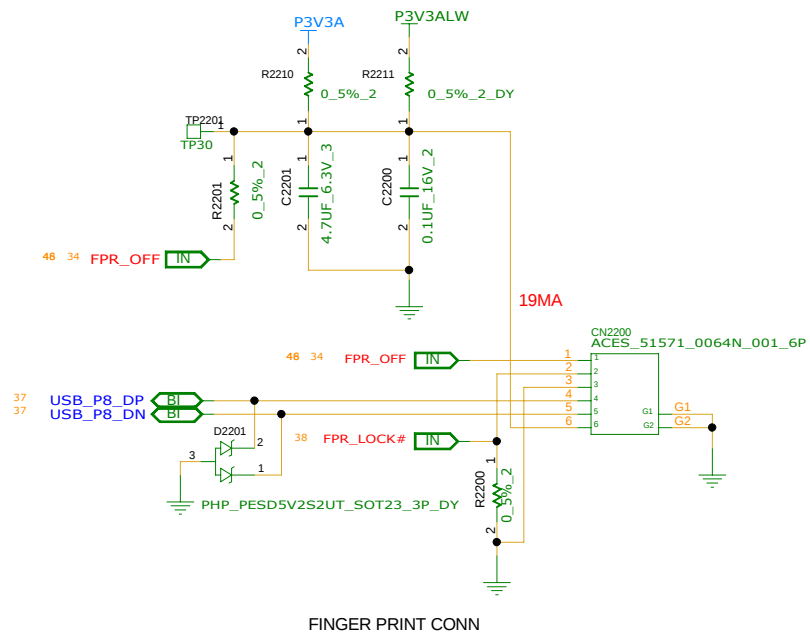
MSATA

REFERENCE NUMBER:1950~1999

INVENTEC			
TITLE			
MODEL PROJECT FUNCTION SATA HDD & M-SATA CNTR			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01

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REFERENCE:2200~2249

INVENTEC

TITLE
MODEL PROJECT FUNCTION
FINGER PRINTER CNTR

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

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TITLE

MODEL, PROJECT, FUNCTION
KBC & SPI

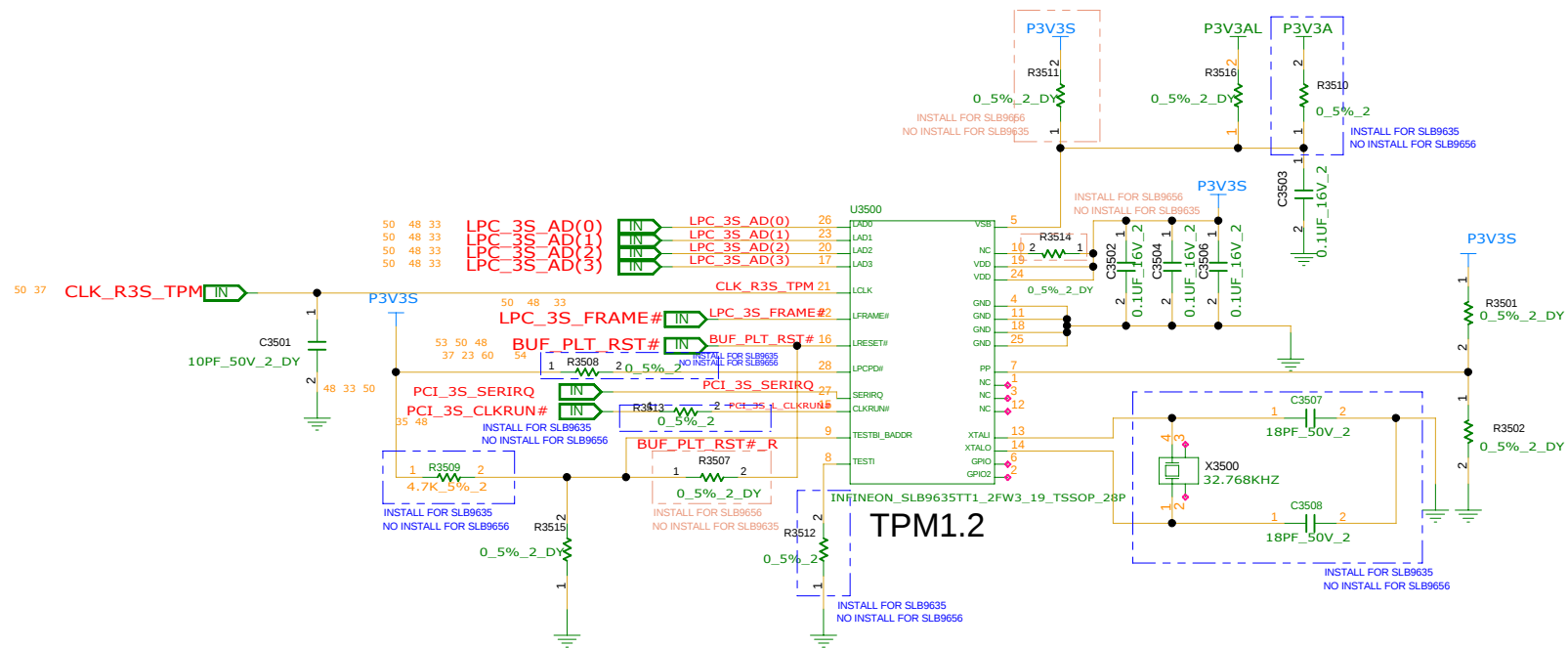
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CLASIFICACION			

A



CHANGE by	XXX	DATE	21-OCT-2002
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REFERENCE NUMER : 3500~3549

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

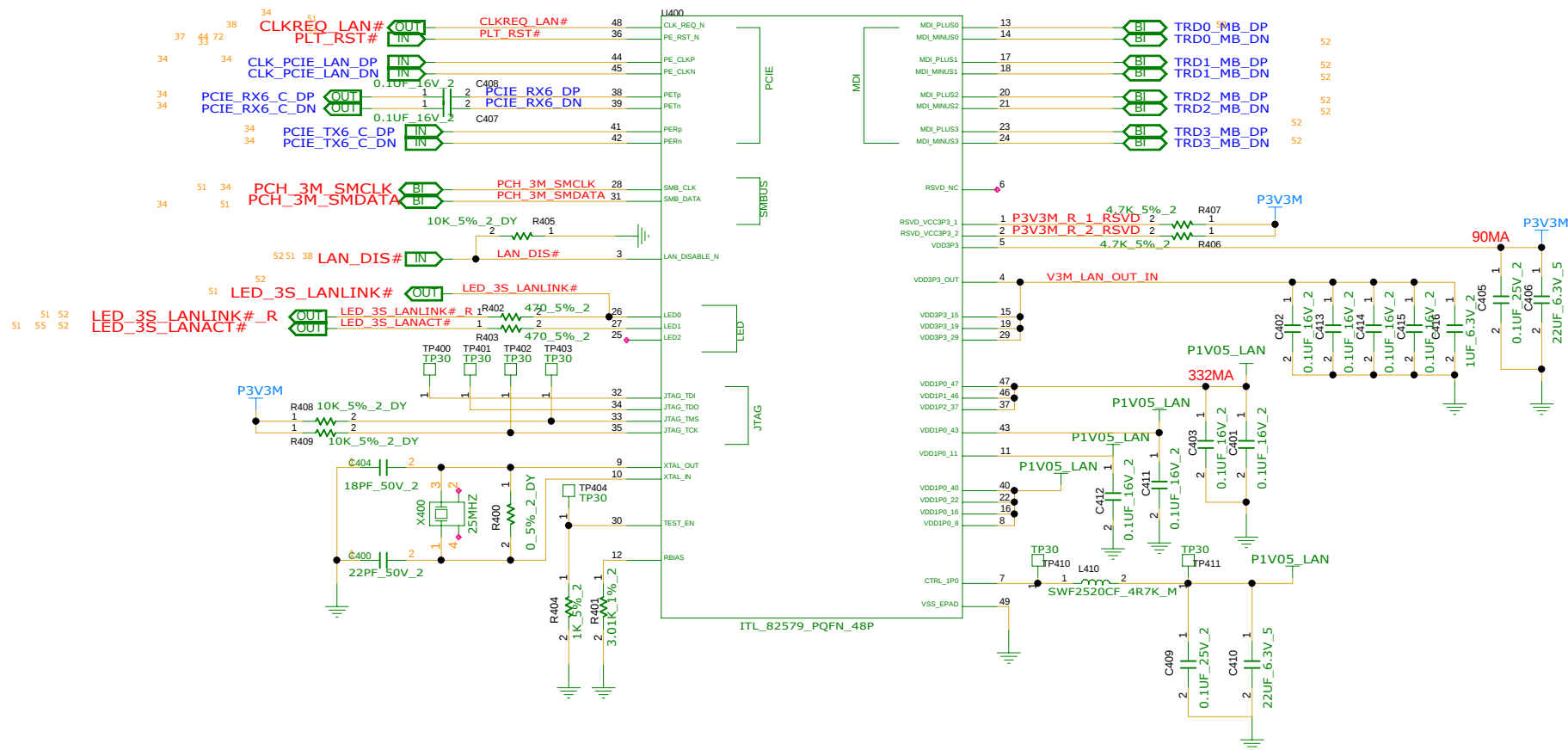
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REV
X01

CHANGE by XXX DATE 21-OCT-2002

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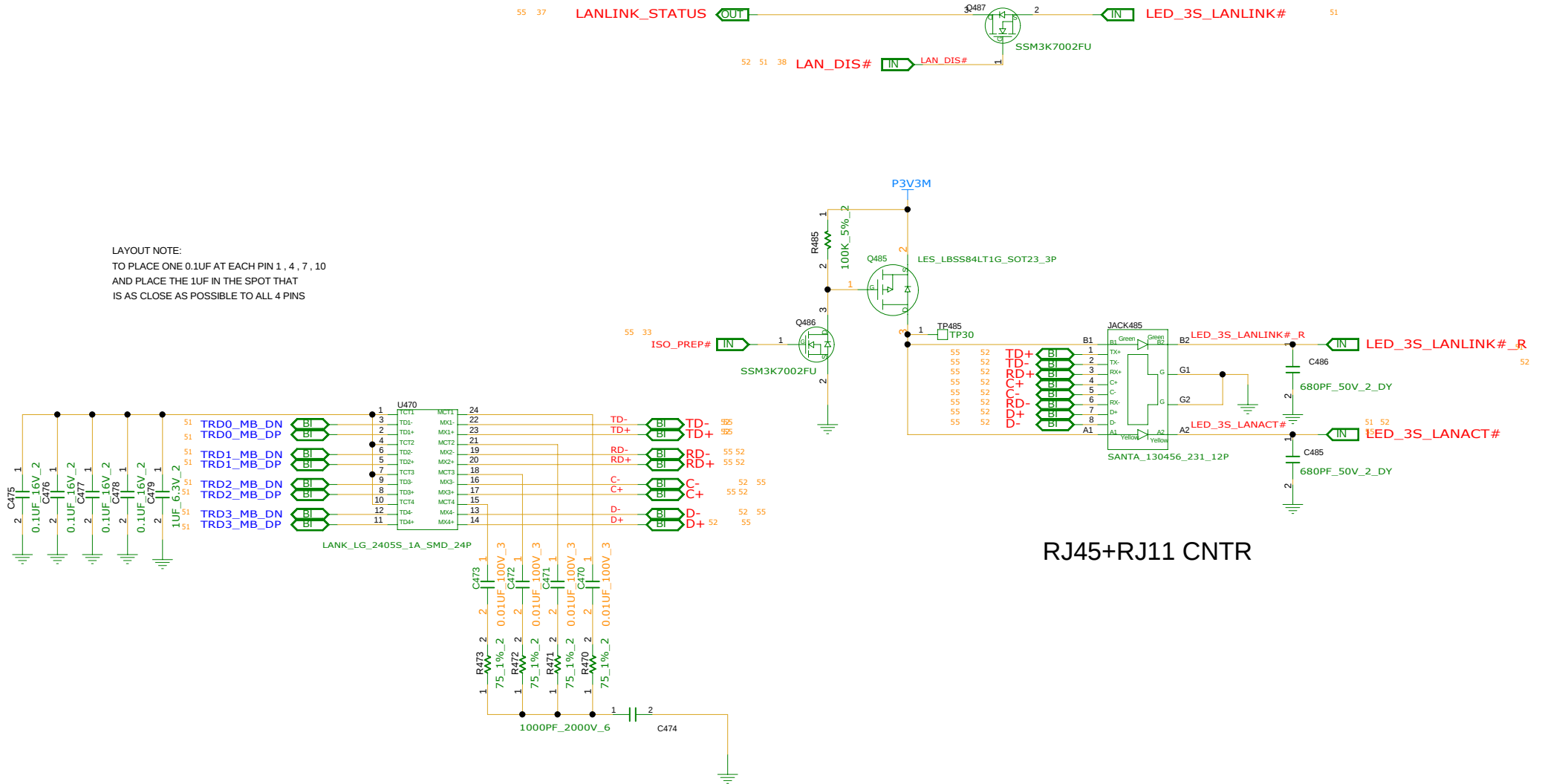
REFERENCE NUMER : 400~469



INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
LAN			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET 51 of 80			

REFERENCE NUMER : 470~499

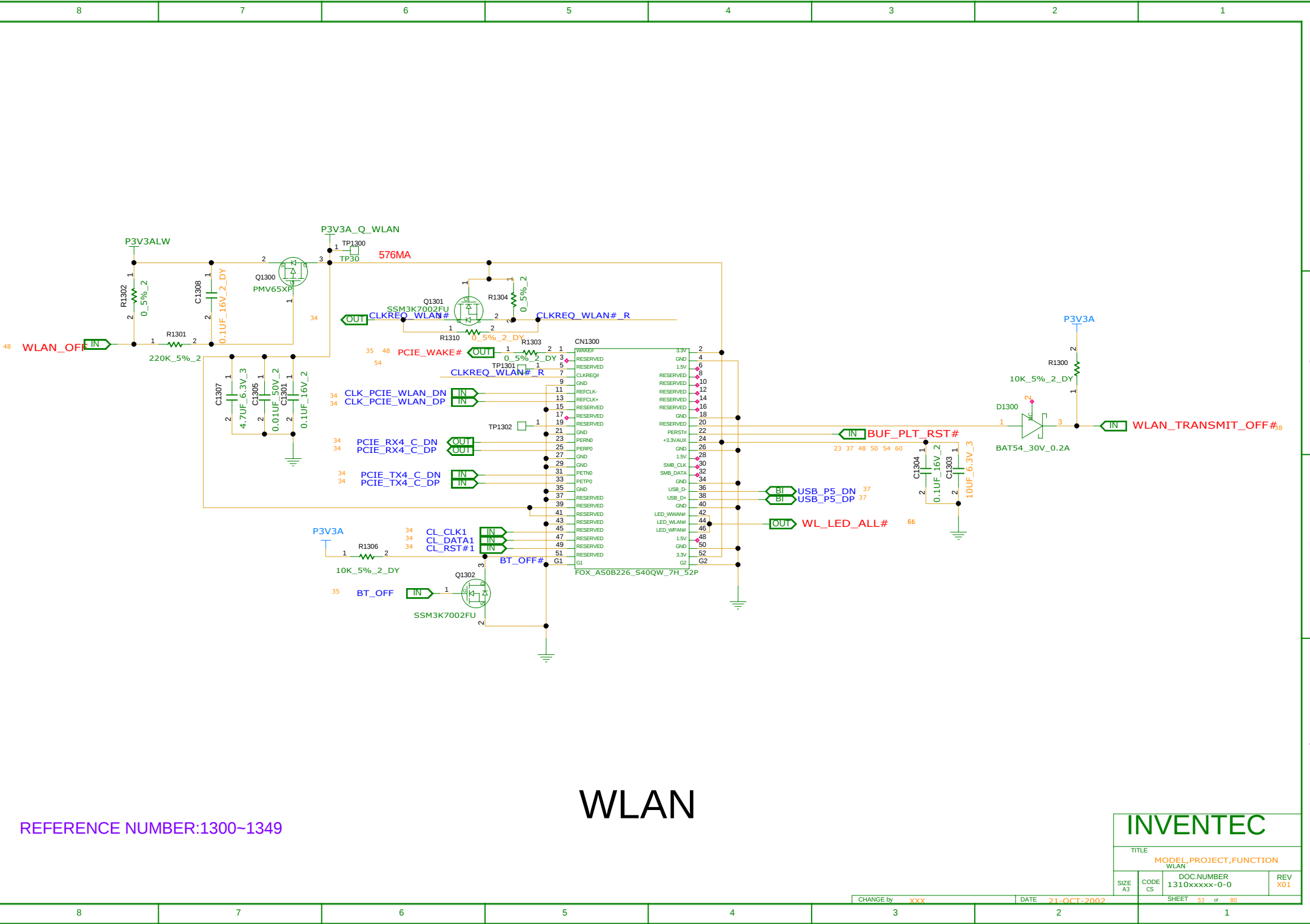
LAYOUT NOTE:
TO PLACE ONE 0.1UF AT EACH PIN 1 , 4 , 7 , 10
AND PLACE THE 1UF IN THE SPOT THAT
IS AS CLOSE AS POSSIBLE TO ALL 4 PINS



RJ45+RJ11 CNTR

INVENTEC

TITLE			
MODEL PROJECT,FUNCTION			
LAN RJ45+RJ11 CNTR			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 52 of 80			



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
WLAN

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 53 of 80

SIM CARD

WWAN

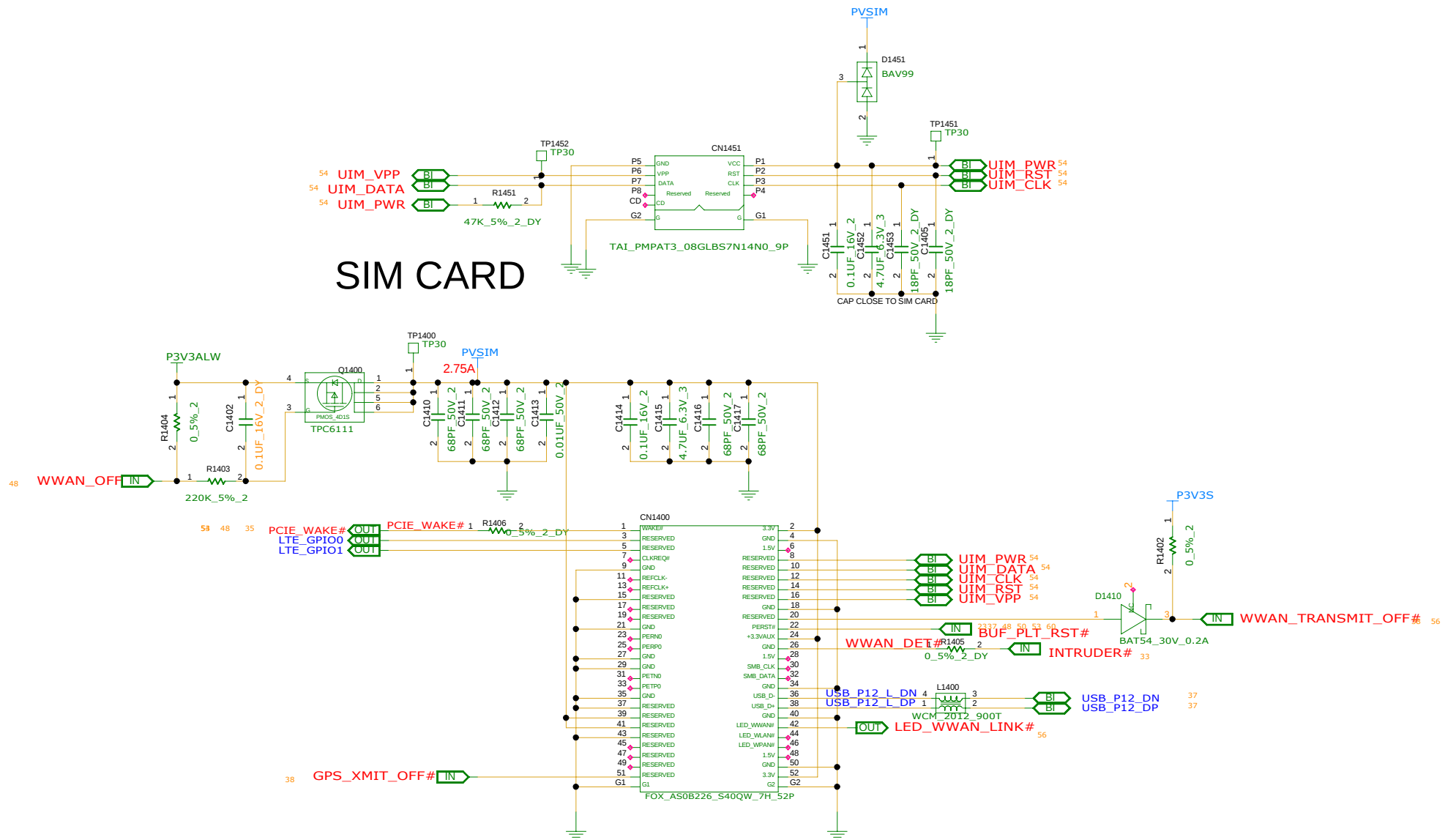
REFERENCE NUMBER:1400~1499

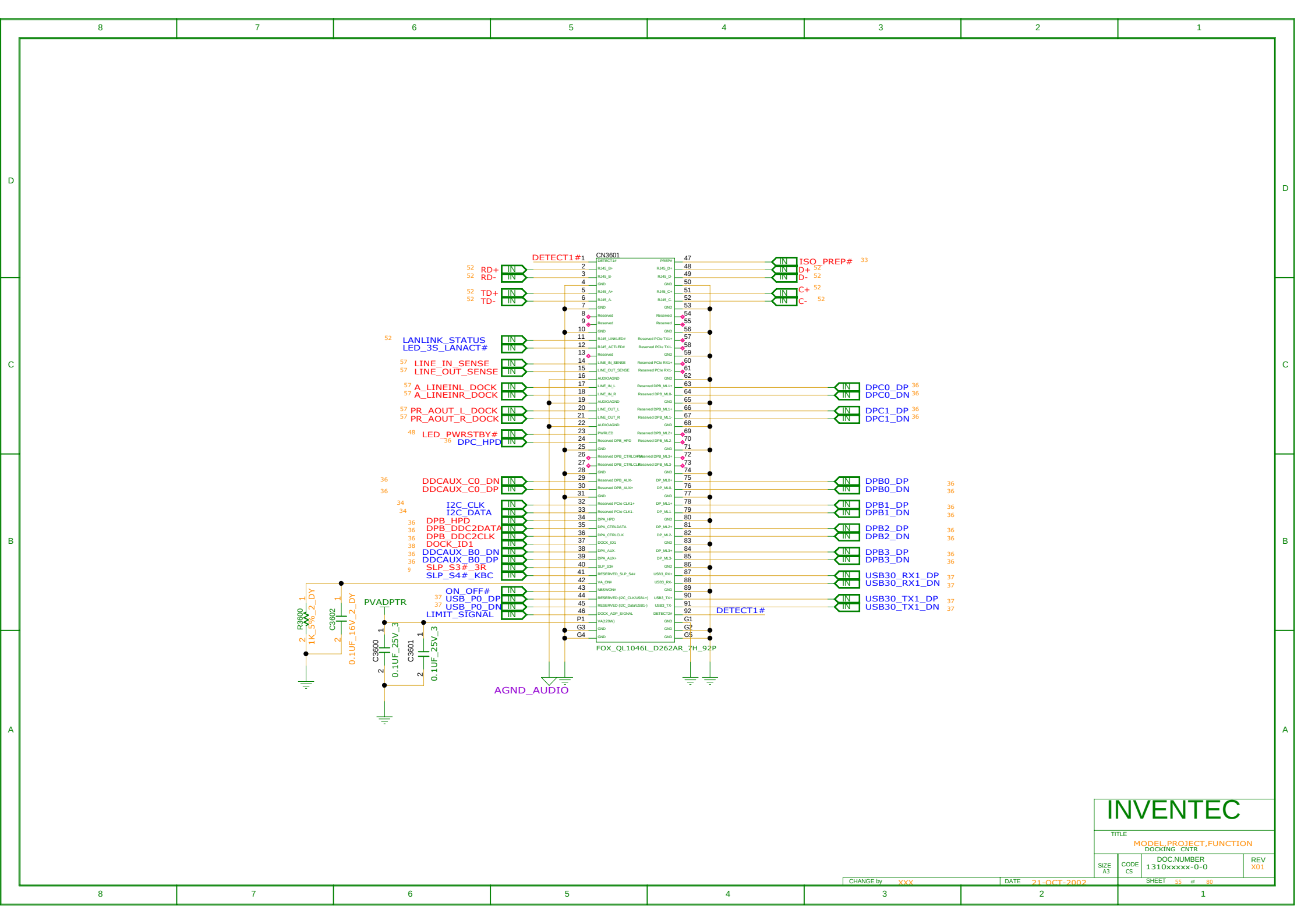
INVENTEC

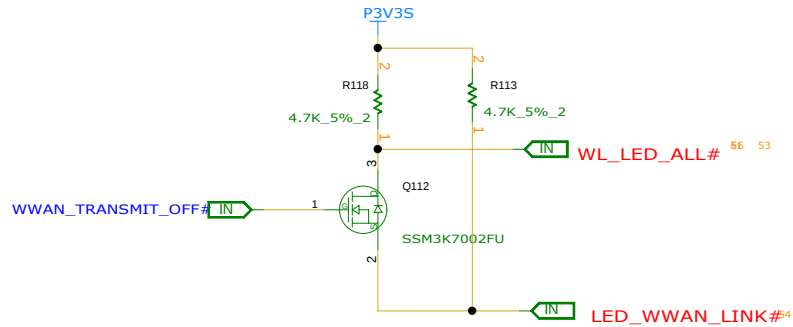
TITLE
MODEL PROJECT FUNCTION
WWAN & SIM CARD

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
------------	------------	----------------------------	------------

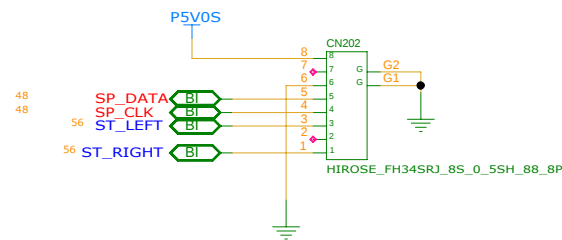
CHANGE by XXX DATE 21-OCT-2002 SHEET 54 of 80



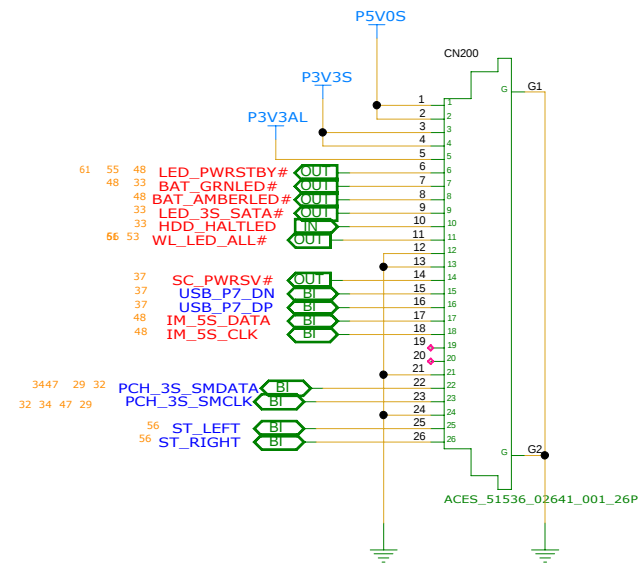




WLAN_WWAN_BLUETOOTH_LED



POINT STICK

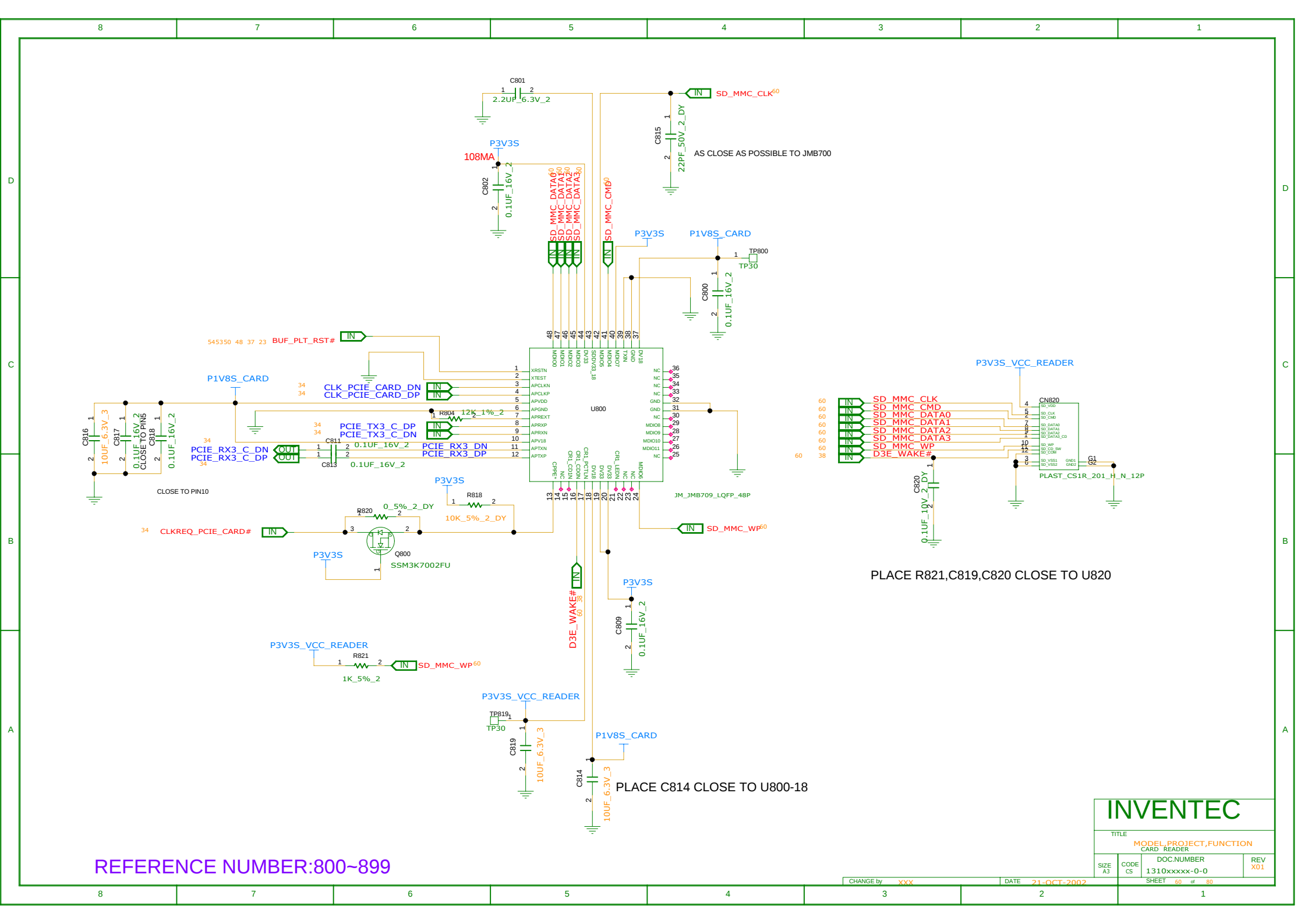


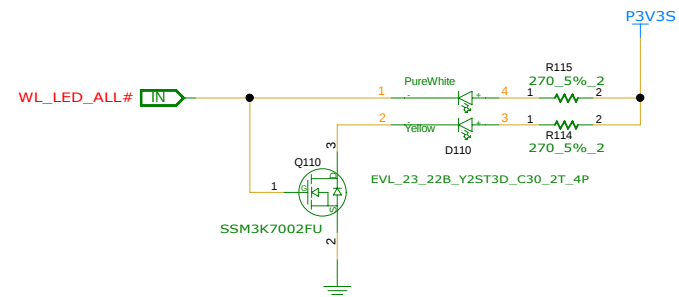
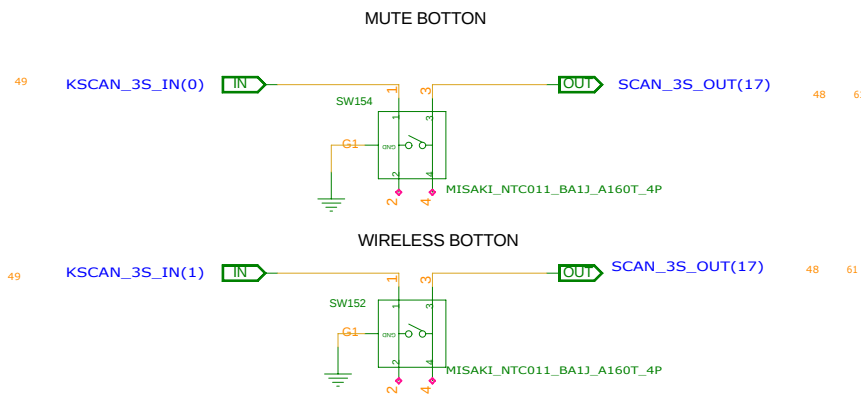
SMART CARD

INVENTEC

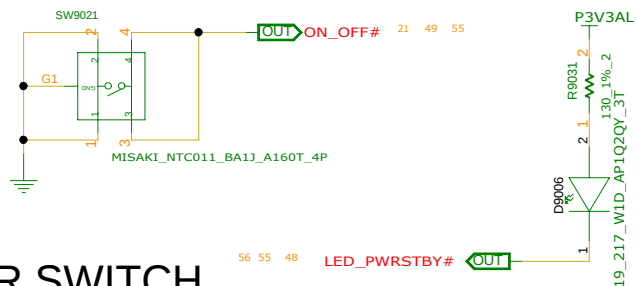
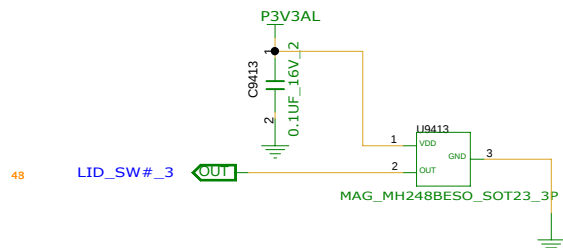
REFERENCE NUMBER:100~199

TITLE			
MODEL PROJECT FUNCTION STICK POINT & B2B CNTR			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 56 of 80			

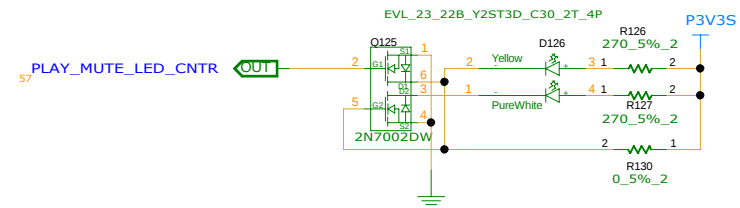




WIRELESS/BLUETOOTH LED



POWER SWITCH



MUTE LED

INVENTEC

TITLE MODEL PROJECT,FUNCTION

SIZE CODE DOC NUMBER REV
A3 CS 1310xxxxx-0-0 X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 61 of 80

SMART CARD DOUGHTER BOARD

REFERENCE NUMBER:9400~9499

TOUCHPAD

SATA LED & HDD-HALTED LED

WIRELESS LED

BATTERY LED

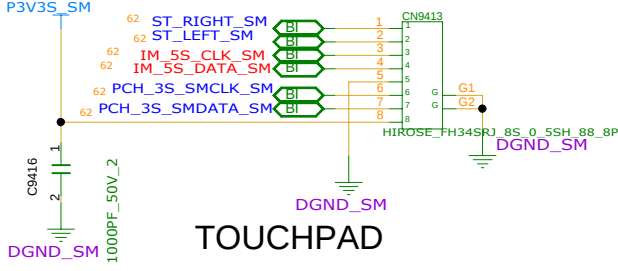
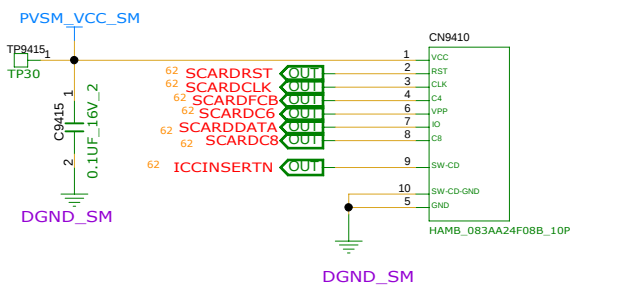
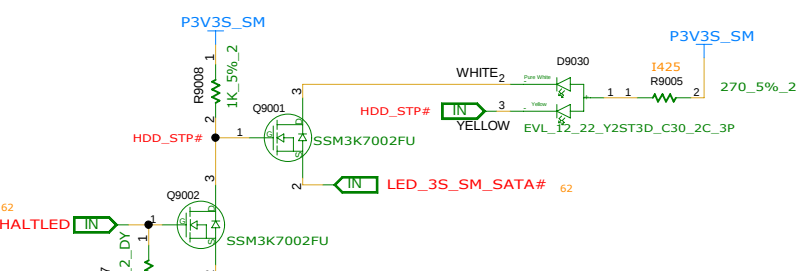
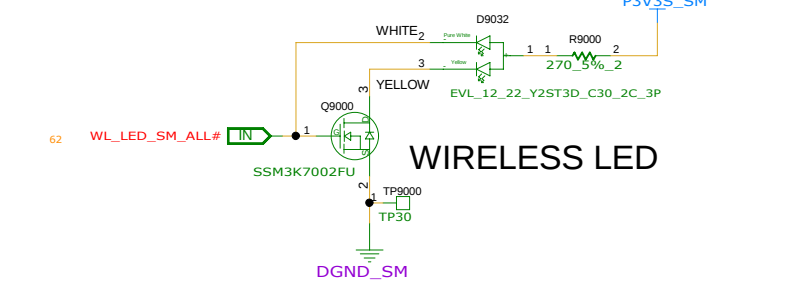
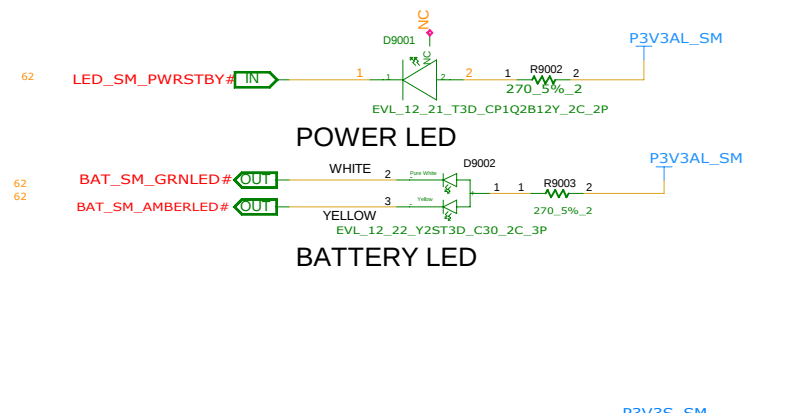
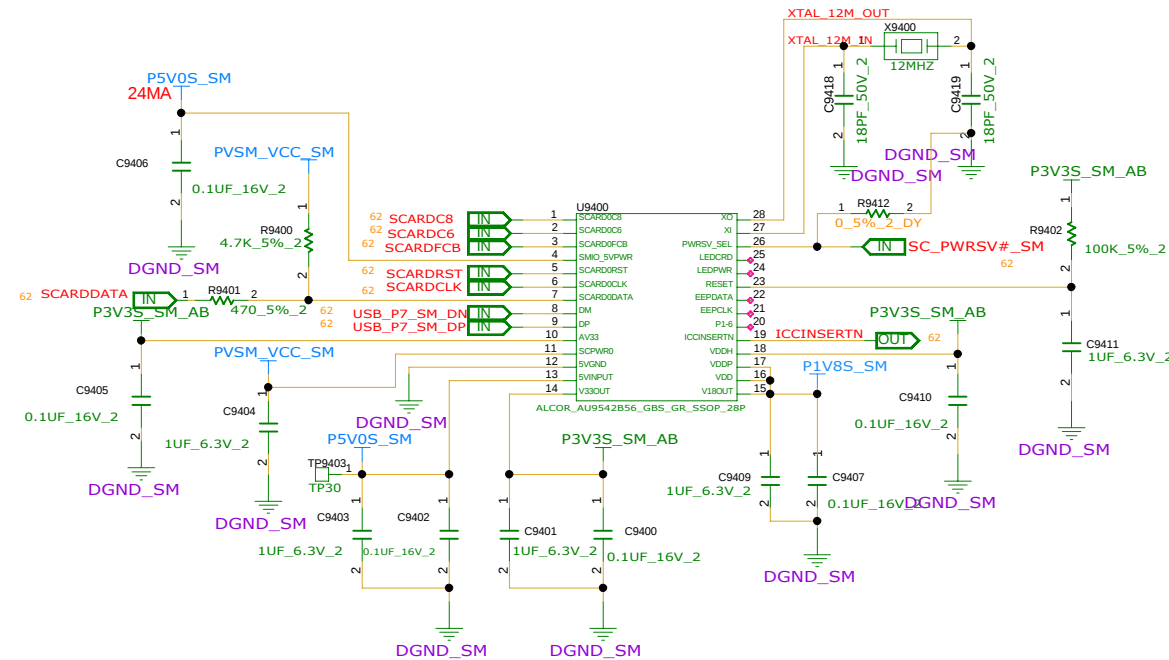
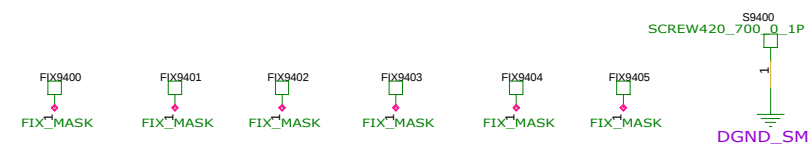
POWER LED

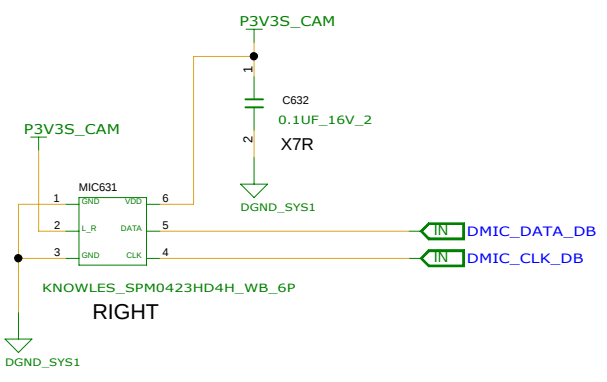
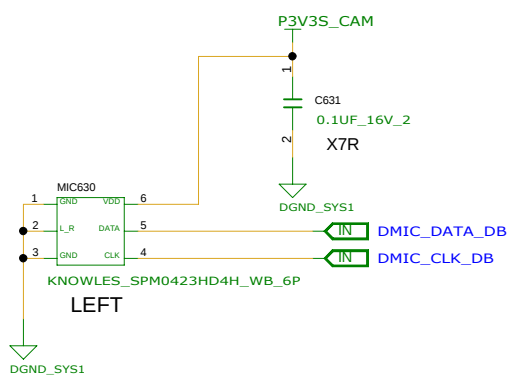
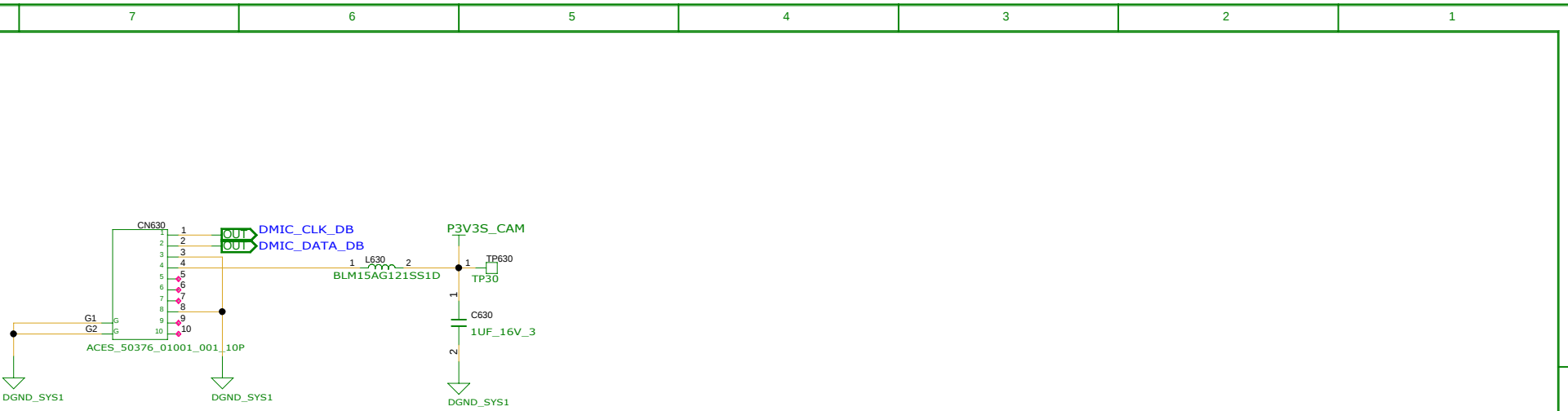
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
SMART CARD DB			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		62 of 80	

DATE 21-OCT-2002

CHANGE by XXX

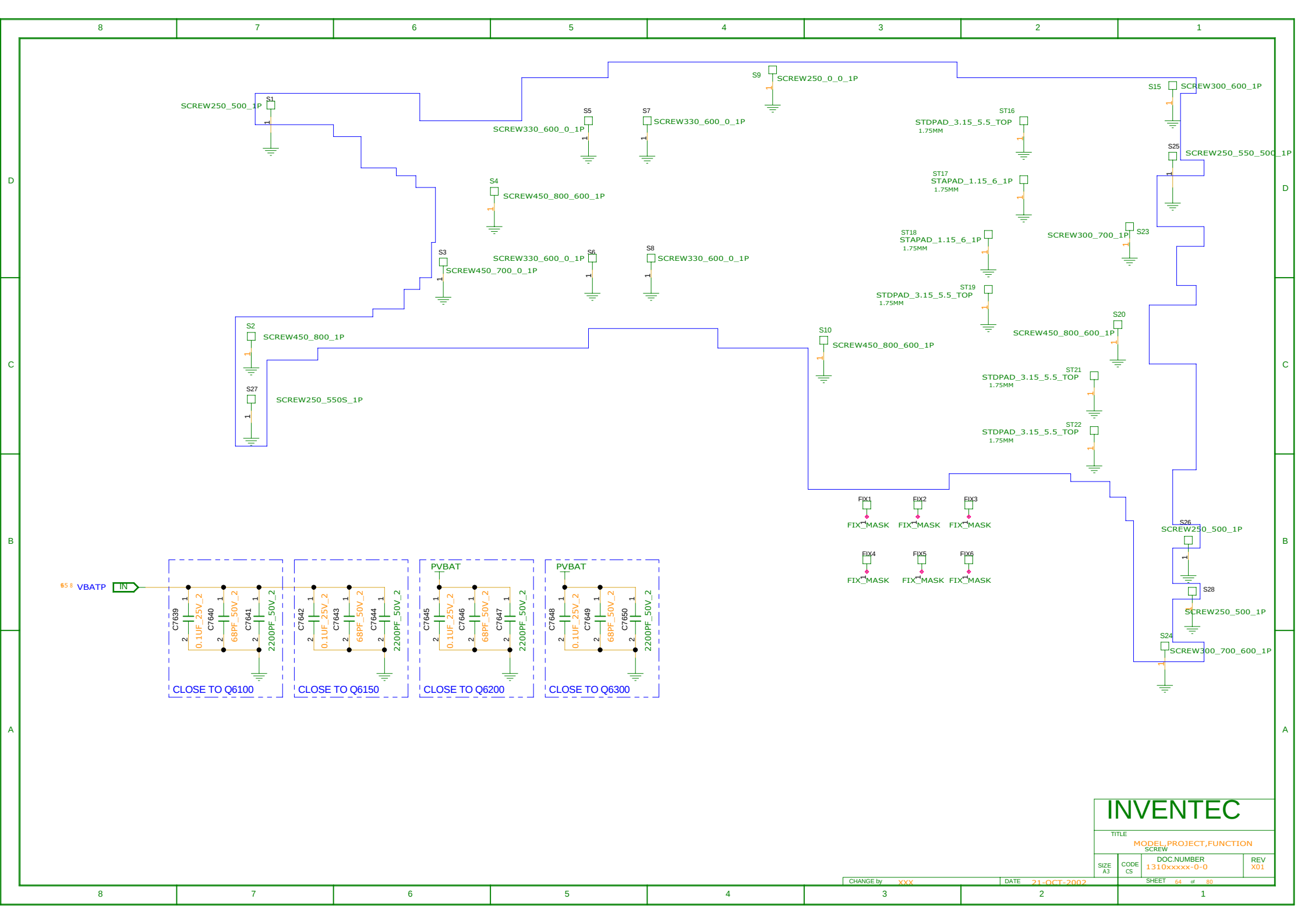




REFERENCE NUMBER:630-639
MIC DOUGHTER BOARD

FIX630
FIX631
FIX632

INVENTEC			
TITLE MODEL PROJECT,FUNCTION MIC DB			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET 63 of 80			



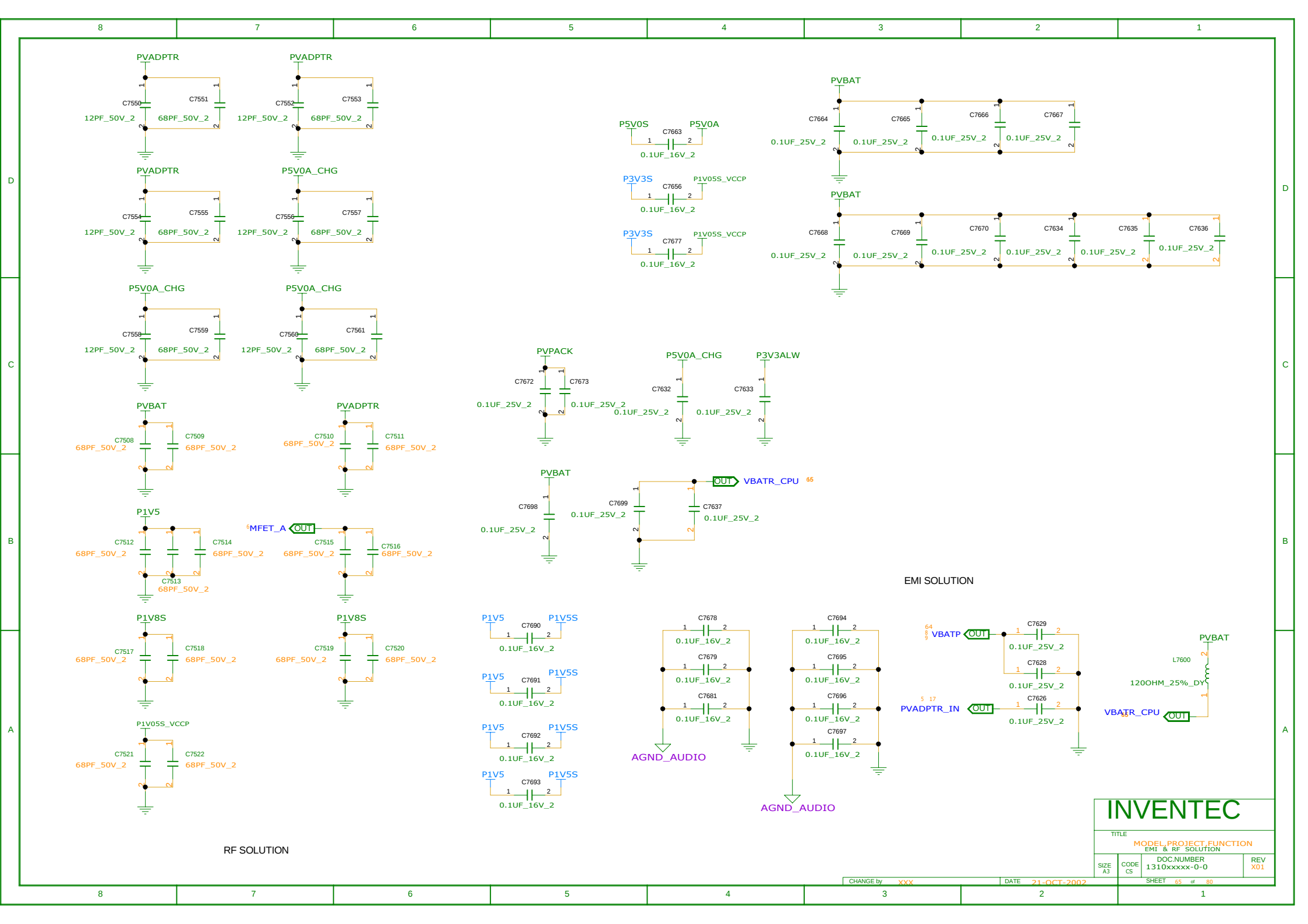
INVENTEC

TITLE
MODEL PROJECT,FUNCTION
SCREW

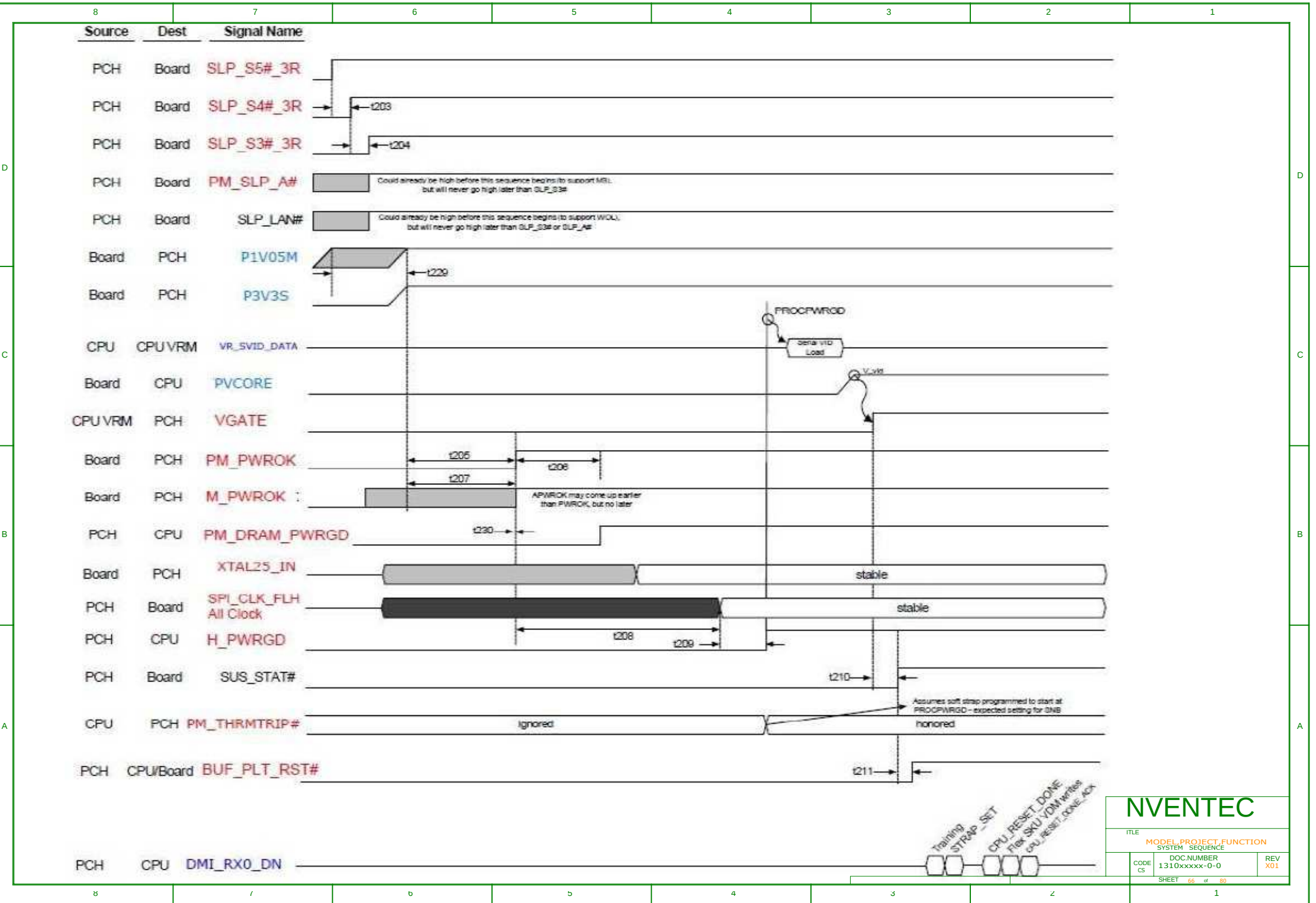
SIZE CODE DOC NUMBER REV
A3 CS 1310xxxx-0-0 X01

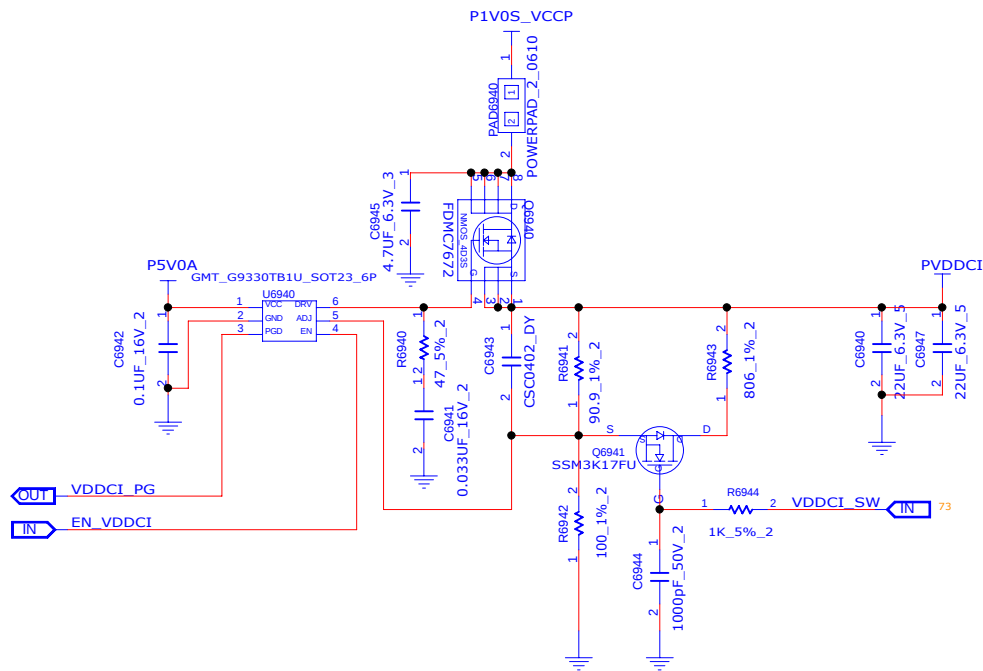
CHANGE by XXX DATE 21-OCT-2002

SHEET 64 of 80



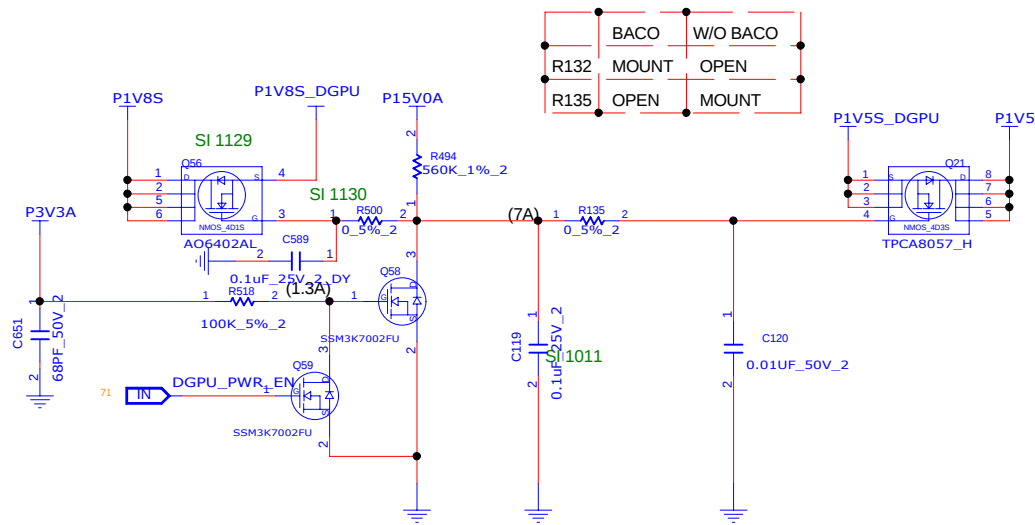
INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
EMI & RF SOLUTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 65 of 80			



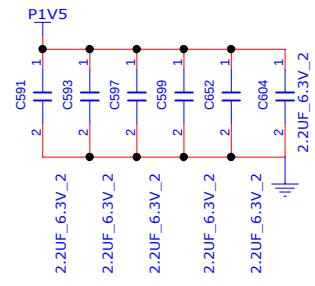


OCP=8AMP
 HIGH : 0.908V = 0.5 [1 + ((R6941/R6943) / R6942)]
 LOW : 0.954V=0.5 (1+ R6941/R6942)

INVENTEC			
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01
SHEET 68 of 80			

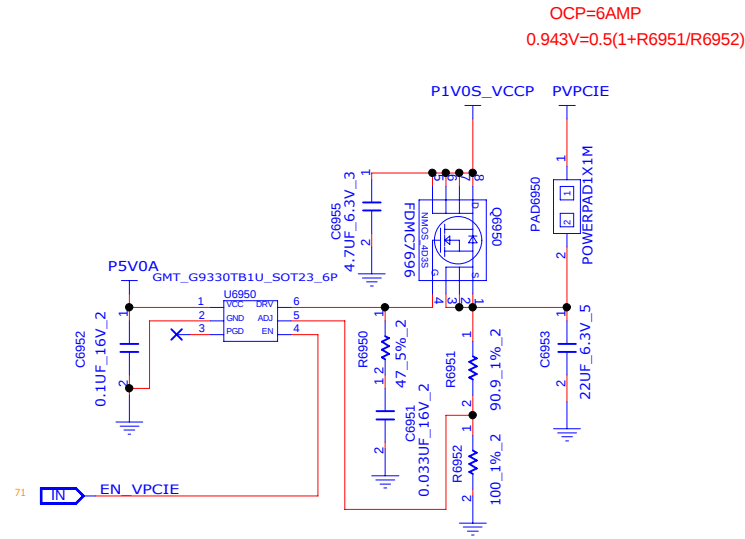


	BACO	W/O BACO
R132	MOUNT	OPEN
R135	OPEN	MOUNT



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

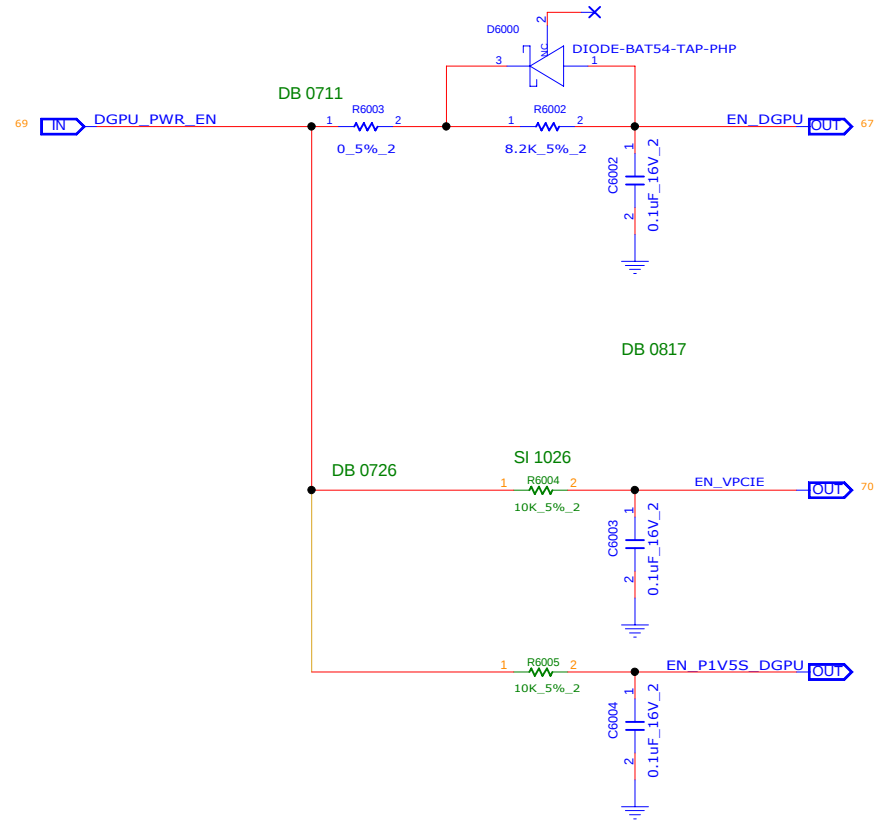
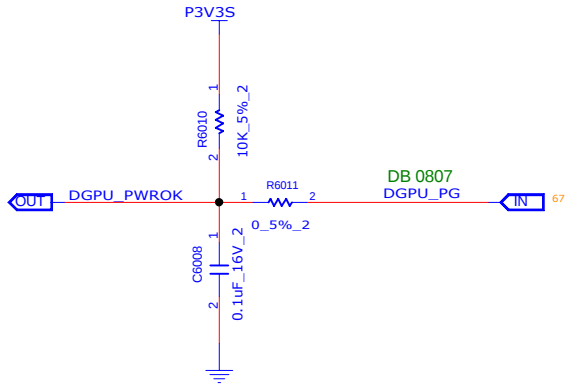
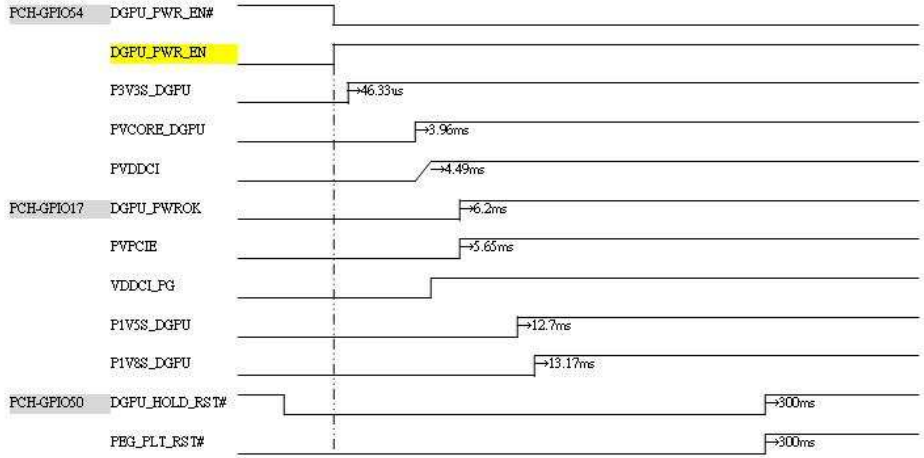


INVENTEC

TITLE
 MODEL,PROJECT,FUNCTION
 Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002 SHEET 70 of 80

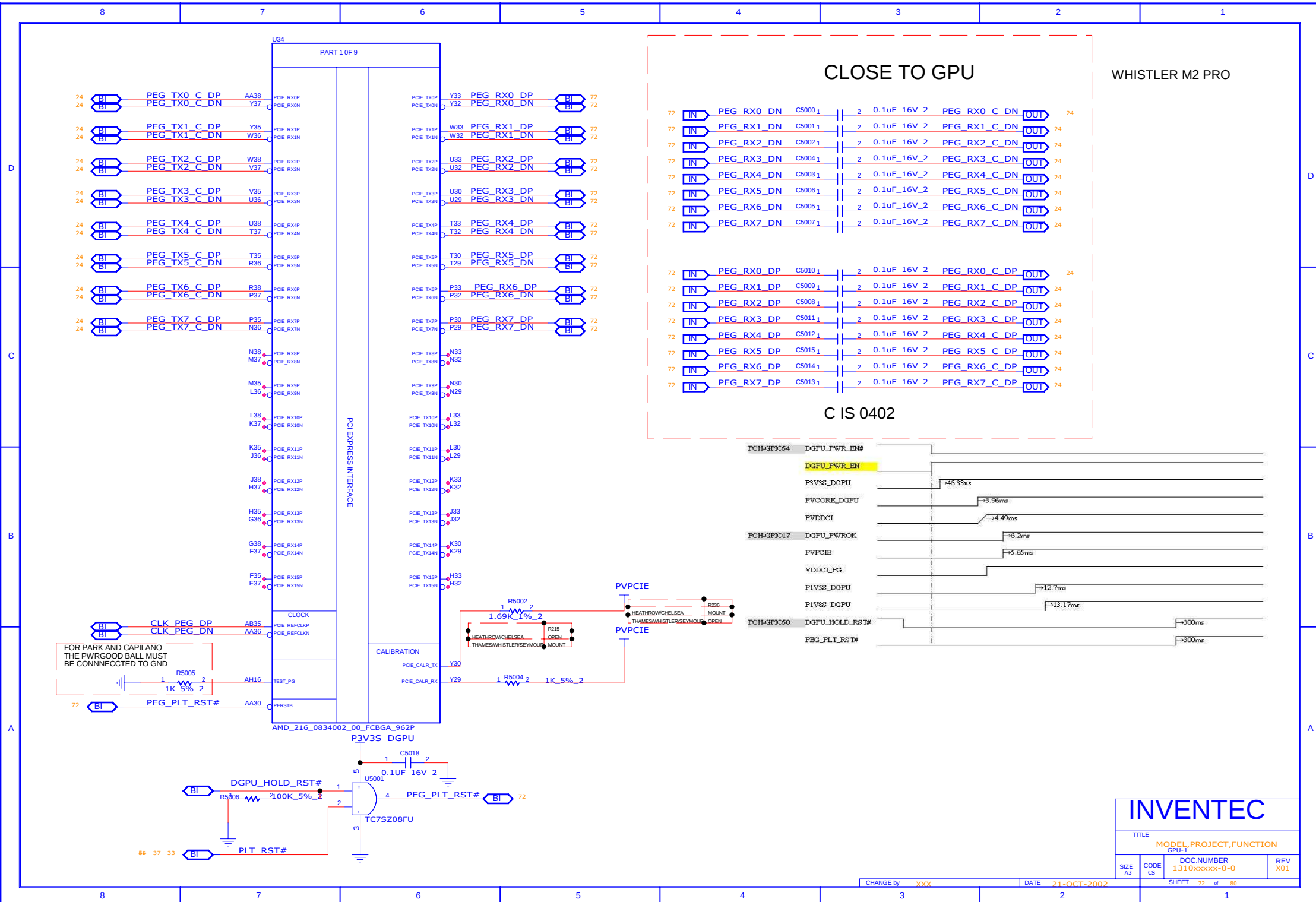


INVENTEC

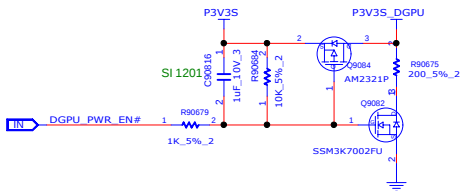
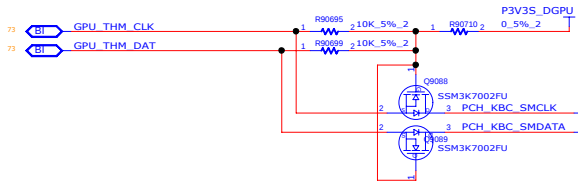
TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

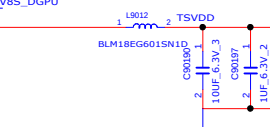
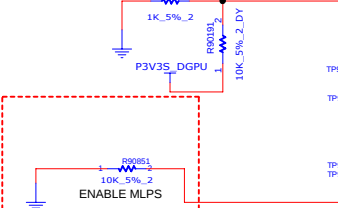
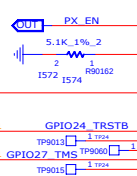
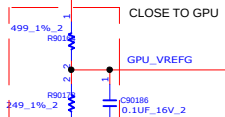
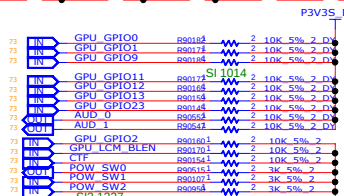
SHEET 21 of 80



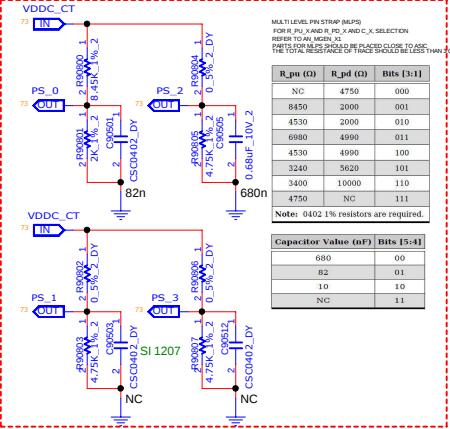
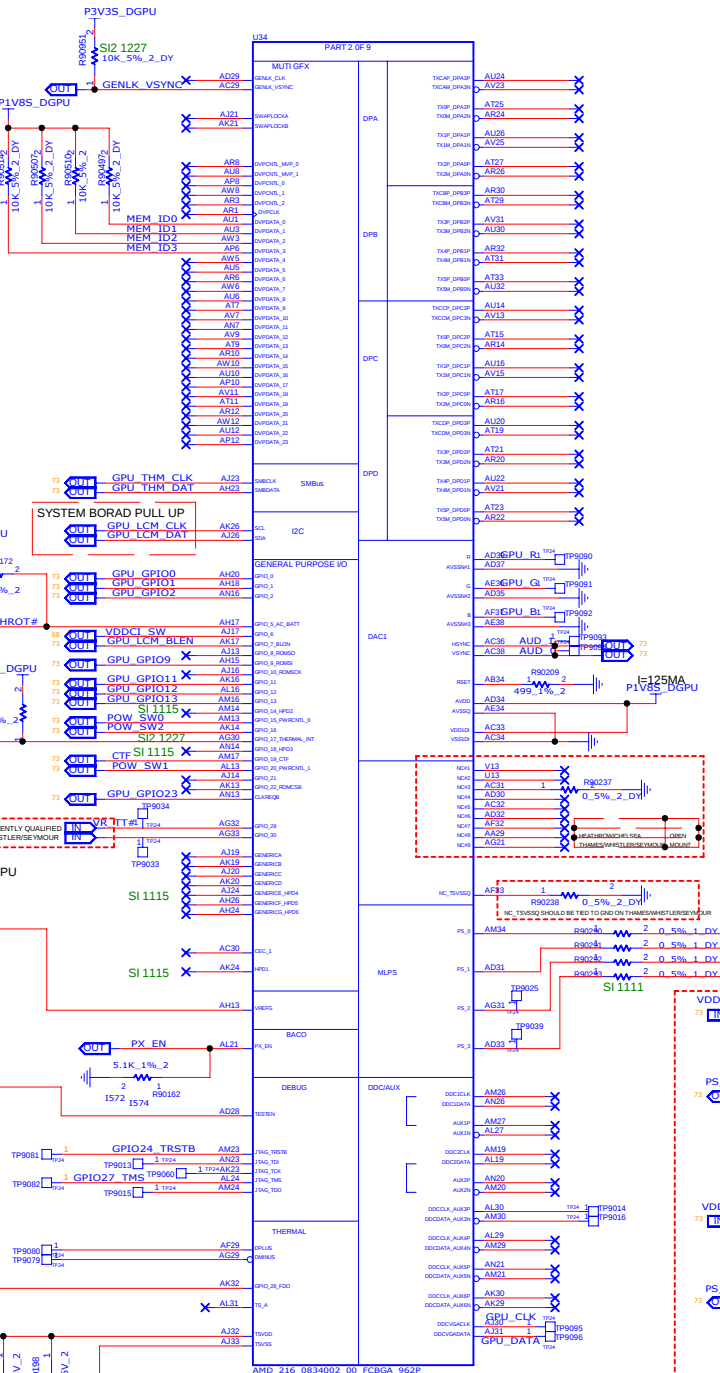
MEM_ID3 (R514)	MEM_ID2 (R507)	MEM_ID1 (R510)	MEM_ID0 (R497)	VENDOR	Die Ver	IEC P/N
0	0	0	0	SAMSUNG(2GB) (K4G20325FC-HC04)	C	6019B0842201
0	0	0	1	SAMSUNG(2GB) (K4G20325FD-FC04) MV NEW	D	6019B0971801
0	0	1	0	HYNIX (2GB)(DEFAULT) (H5GQ2424MFR-T2C)	VEGA	6019B0843001
0	0	1	1	HYNIX (2GB) (H5GQ2424MFR-T2C) MV NEW	A	6019B0971701
0	1	0	0	ELPIDA (2GB) NONE	NONE	NONE



IF GPIO_22_EN = 0, THEN GPIO[13:11] DEFINES THE PRIMARY MEMORY APERTURE SIZE.			
GPIO_13	GPIO_12	GPIO_11	MEMORY APERTURE SIZE
0	0	1	2GB / 1GB (DEFAULT)
1	0	0	RESERVED



THAMES/WHISTLER/SEYMOUR ONLY
DO NOT INSTALL FOR HEATHROW/CHELSEA
NC, TSVS00 SHOULD BE TIED TO GND ON THAMES/WHISTLER/SEYMOUR
PS_0 SHOULD BE TIED TO GND ON THAMES/WHISTLER/SEYMOUR



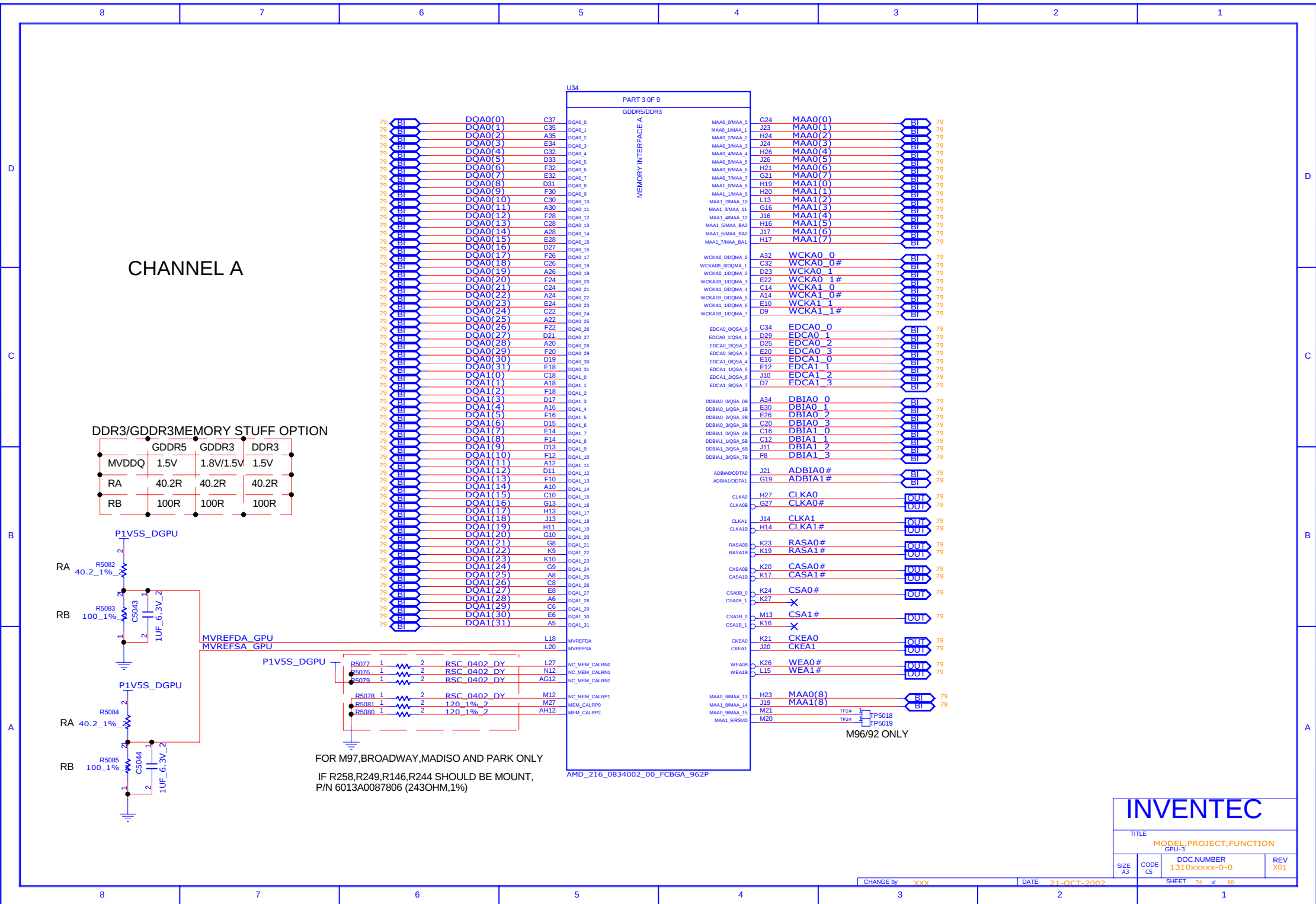
R_pin (G)	R_pin (G)	Bits (3-1)
NC	4750	000
8450	2000	001
4530	2000	010
6980	4990	011
4530	4990	100
3240	5620	101
3400	10000	110
4750	NC	111

Note: 0402 1% resistors are required.

Capacitor Value (nF)	Bits (15-1)
680	00
82	01
10	10
NC	11

INVENTEC

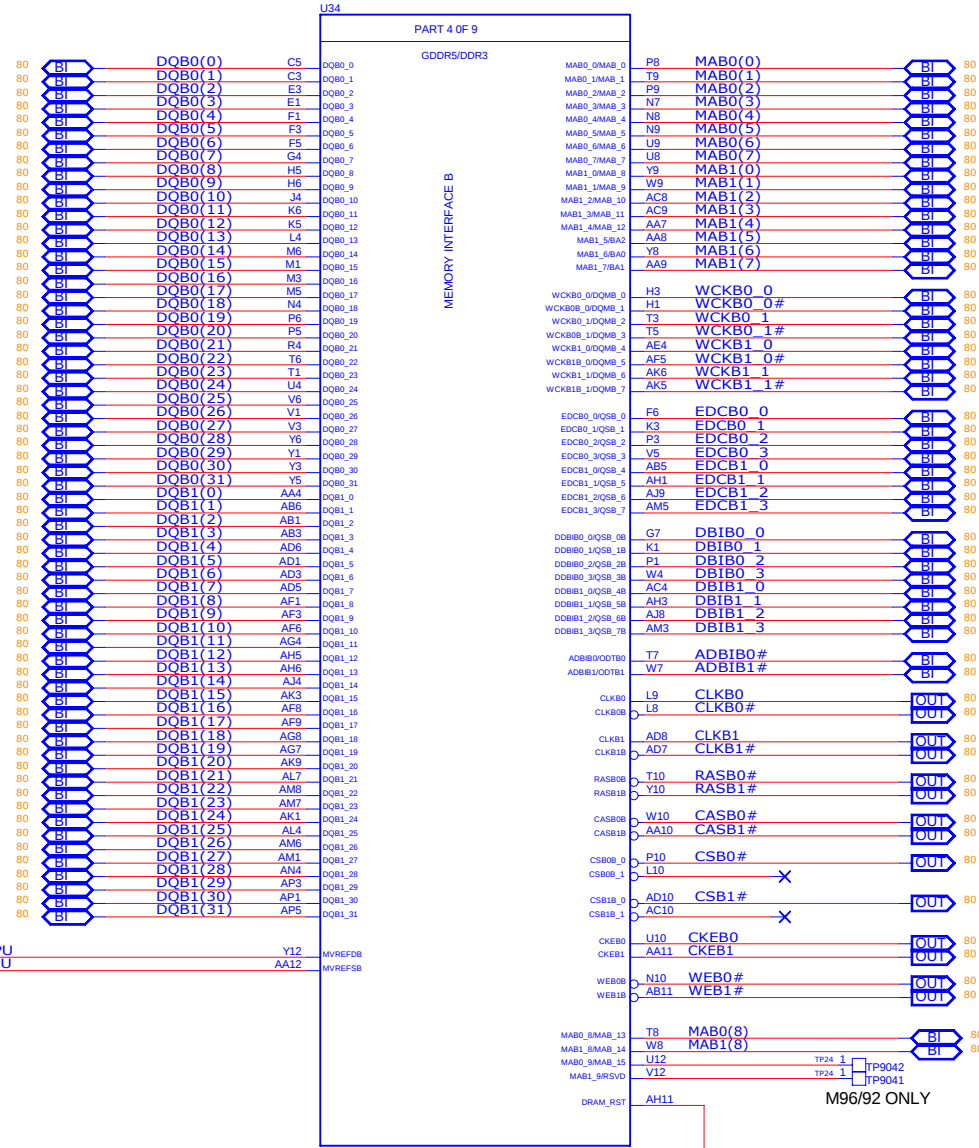
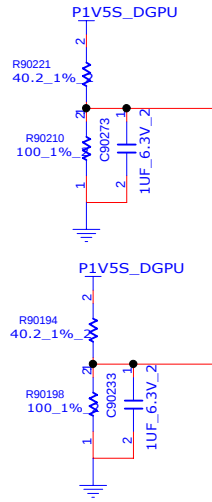
MODEL/PROJECT/FUNCTION			
GPU-2			
SIZE	CODE	DOC NUMBER	REV
C	IS	131DXXXXX-0-0	X01



CHANNEL B

DDR3/GDDR3MEMORY STUFF OPTION

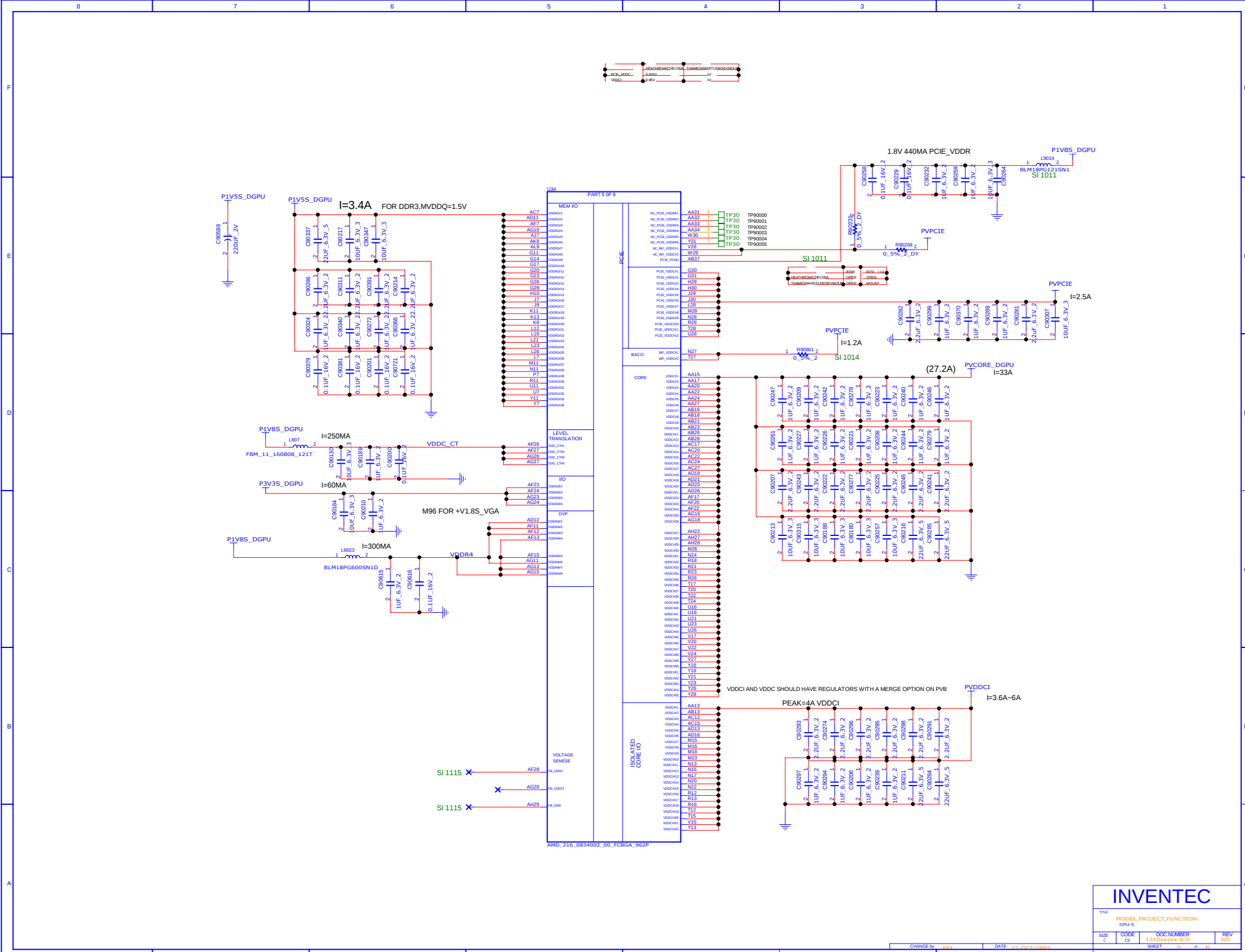
	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
RA	40.2R	40.2R	40.2R
RB	100R	100R	100R

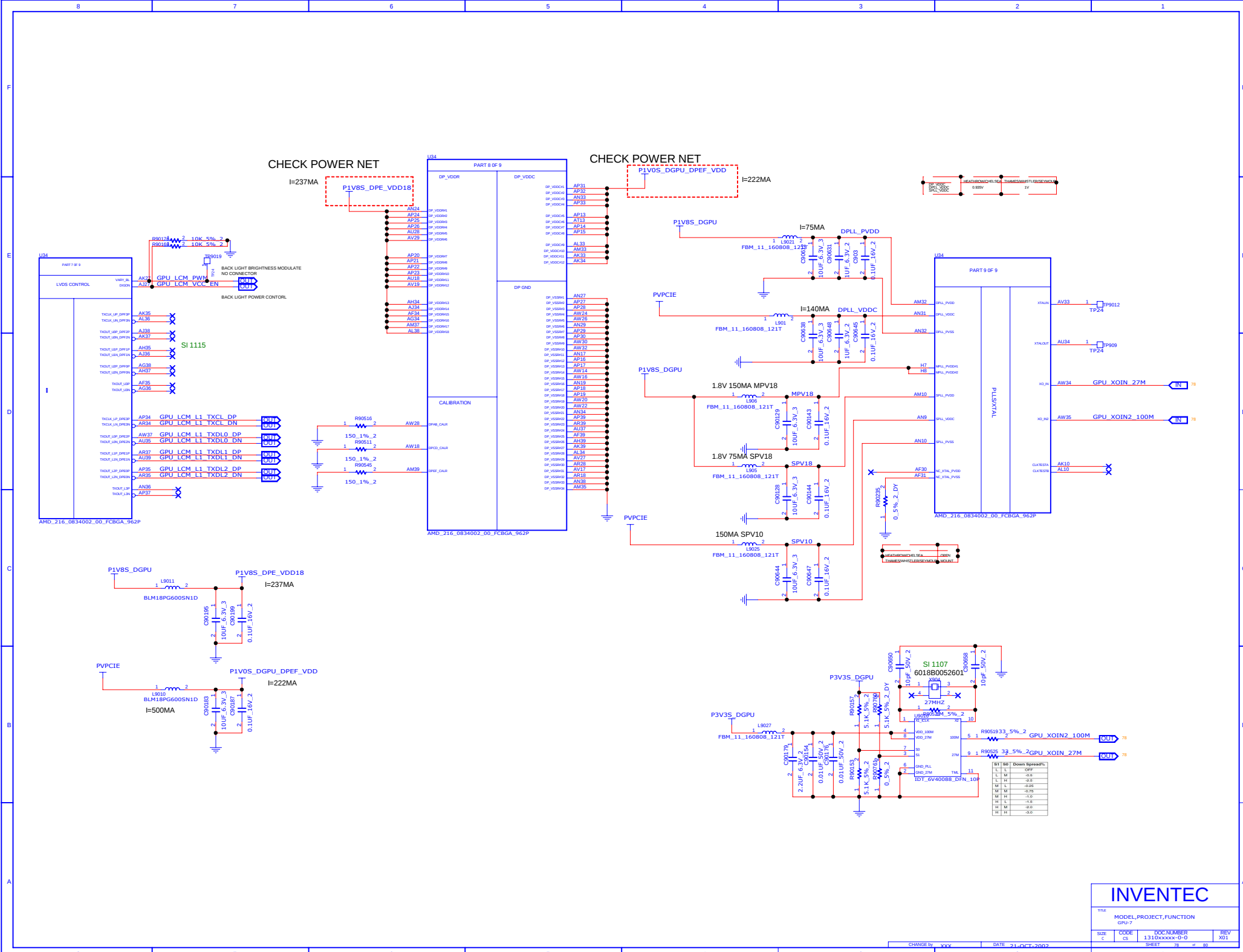


Place all these components vry close to GPU (Within 25mm)
and keep all component close to each other (within 5mm) except Rser2
* This basic topology should be used for DDR3/GDDR3/GDDR5
These Capacitors and Resistor values are an example only.
The Series R and || Cap values will depend on the DRAM load and will have to be
calculated for different Memory , DRAM Load and board to pass Reset Signal Spec.

INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

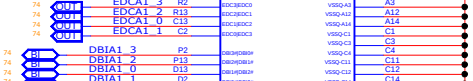
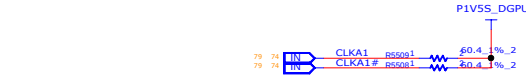




CHANNEL A MEMORY

VENDER	DENSITY	VENDER PN	IEC PN
SAMSUNG	64MX32	K4G20325FD-FC04	6019B0971801
HYNIX	64MX32	H5GQ2H24AFR-T2C	6019B0971701
SAMSUNG	64MX32	K4G20325FC-HC04	6019B0842201
HYNIX	64MX32	H5GQ2H24MFR-T2C	6019B0843001

MV 0313



INVENTEC

MODEL,PROJECT,FUNCTION

VRAM1 & VRAM2

SIZE

CODE

DOC NUMBER

REV

1310DXXXXX-0-0

201

CHANGE BY: VXX DATE: 21-OCT-2007

SHEET 24 OF 24

