

DDD Discrete GDDR2

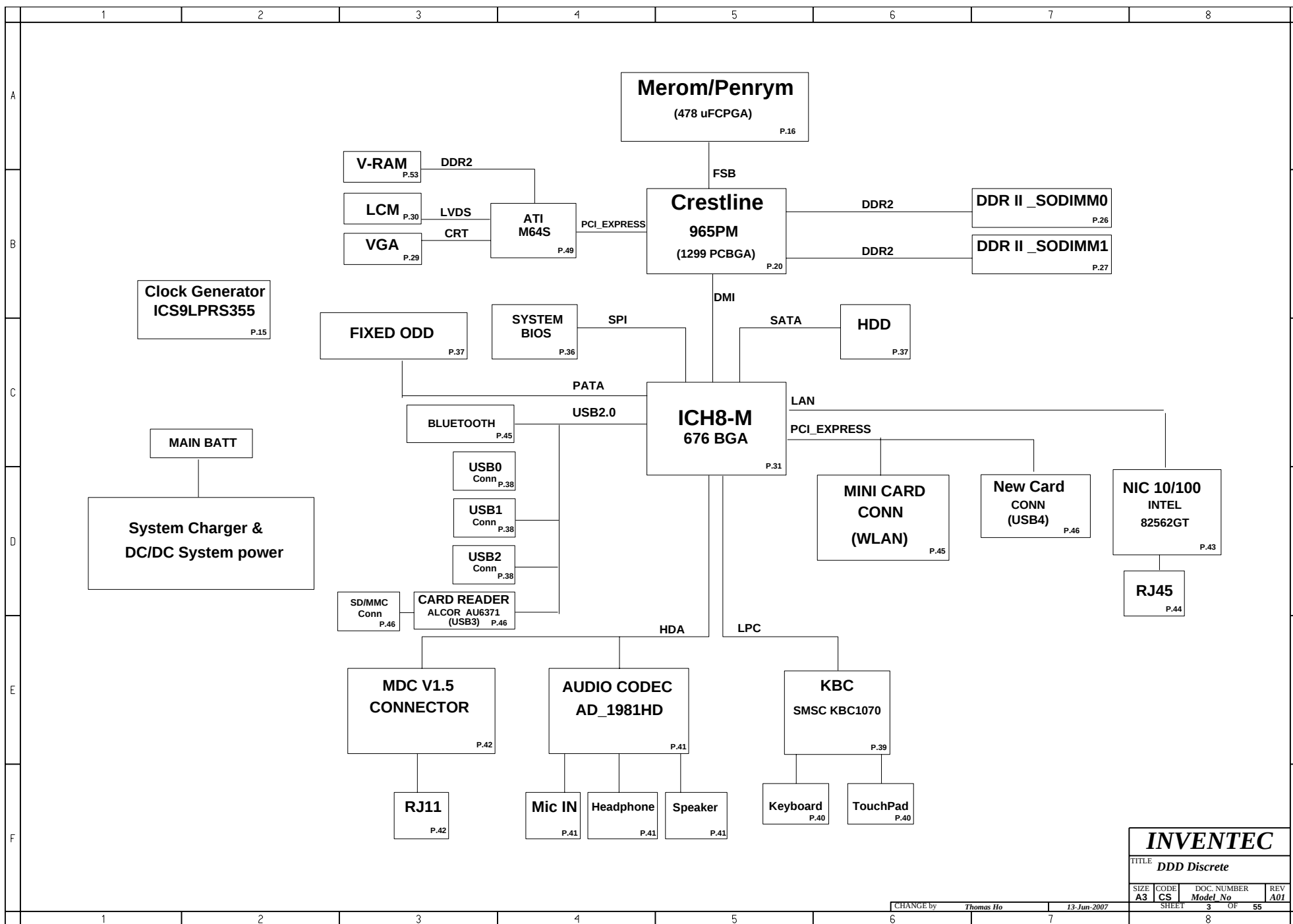
PV Build

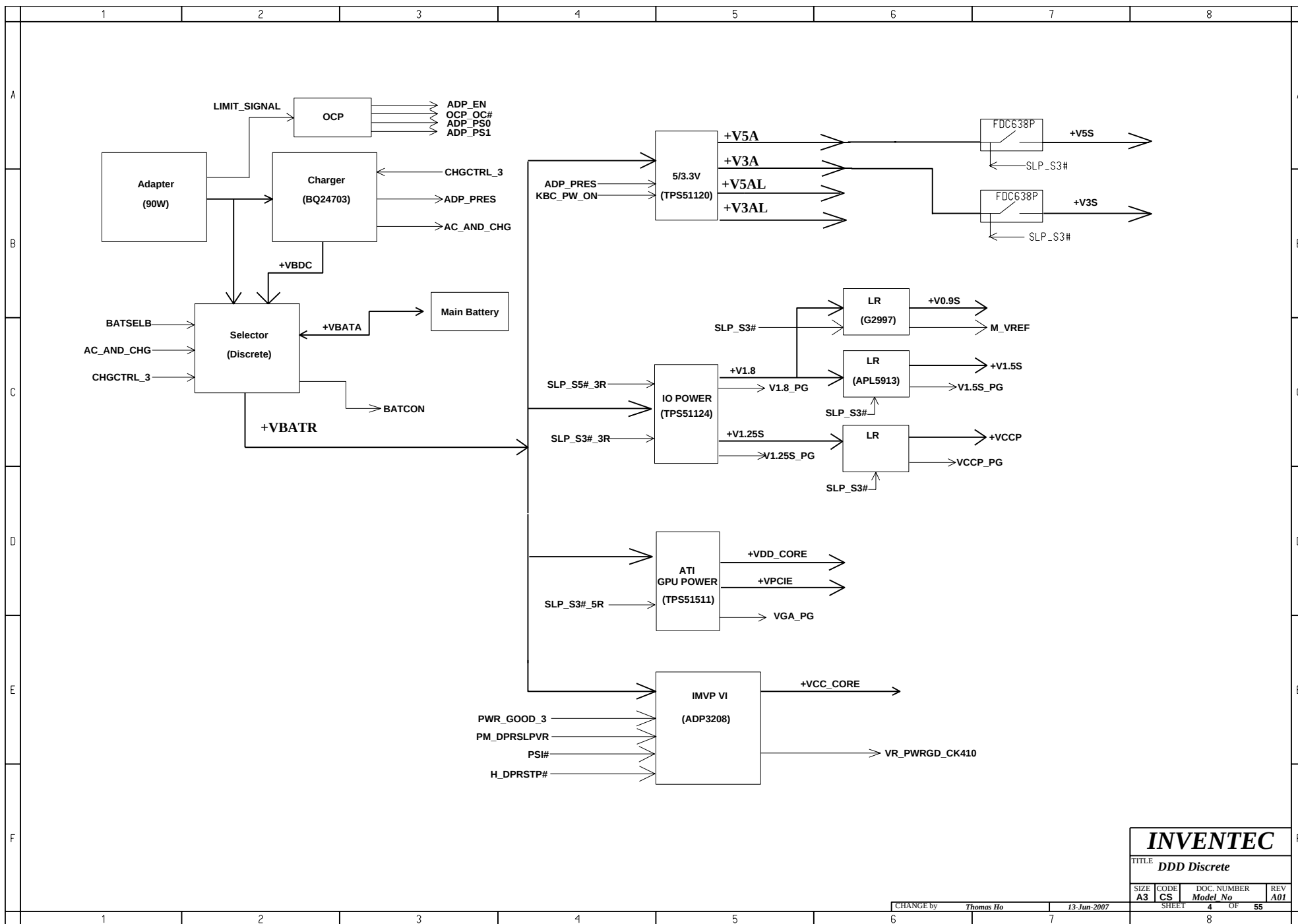
2007.06.15

DATE	CHANGE NO.	REV

DRAWER	EE	DATE	POWER	DATE	INVENTEC		
DESIGN					TITLE		
CHECK					DDD Discrete		
RESPONSIBLE							
SIZE = 3					SIZE	CODE	DOC. NUMBER
FILE NAME : XXXX-XXXXXX-XX					A3	CS	Model No
P/N	XXXXXXXXXXXX				SHEET	1	OF 55

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	9- GRAPHIC POWER(+VGFX_CORE)		19- THERMAL&FAN CONTROLLER		40- KB&TP CONN											
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						INVENTEC TITLE DDD Discrete <table><tr><td>SIZE</td><td>CODE</td><td>DOC. NUMBER</td><td>REV</td></tr><tr><td>A3</td><td>CS</td><td>Model No</td><td>A01</td></tr></table>			SIZE	CODE	DOC. NUMBER	REV	A3	CS	Model No	A01
SIZE	CODE	DOC. NUMBER	REV													
A3	CS	Model No	A01													
CHANGE by Thomas Ho						13-Jun-2007										
						SHEET 2 OF 55										
	1	2	3	4	5	6	7	8								

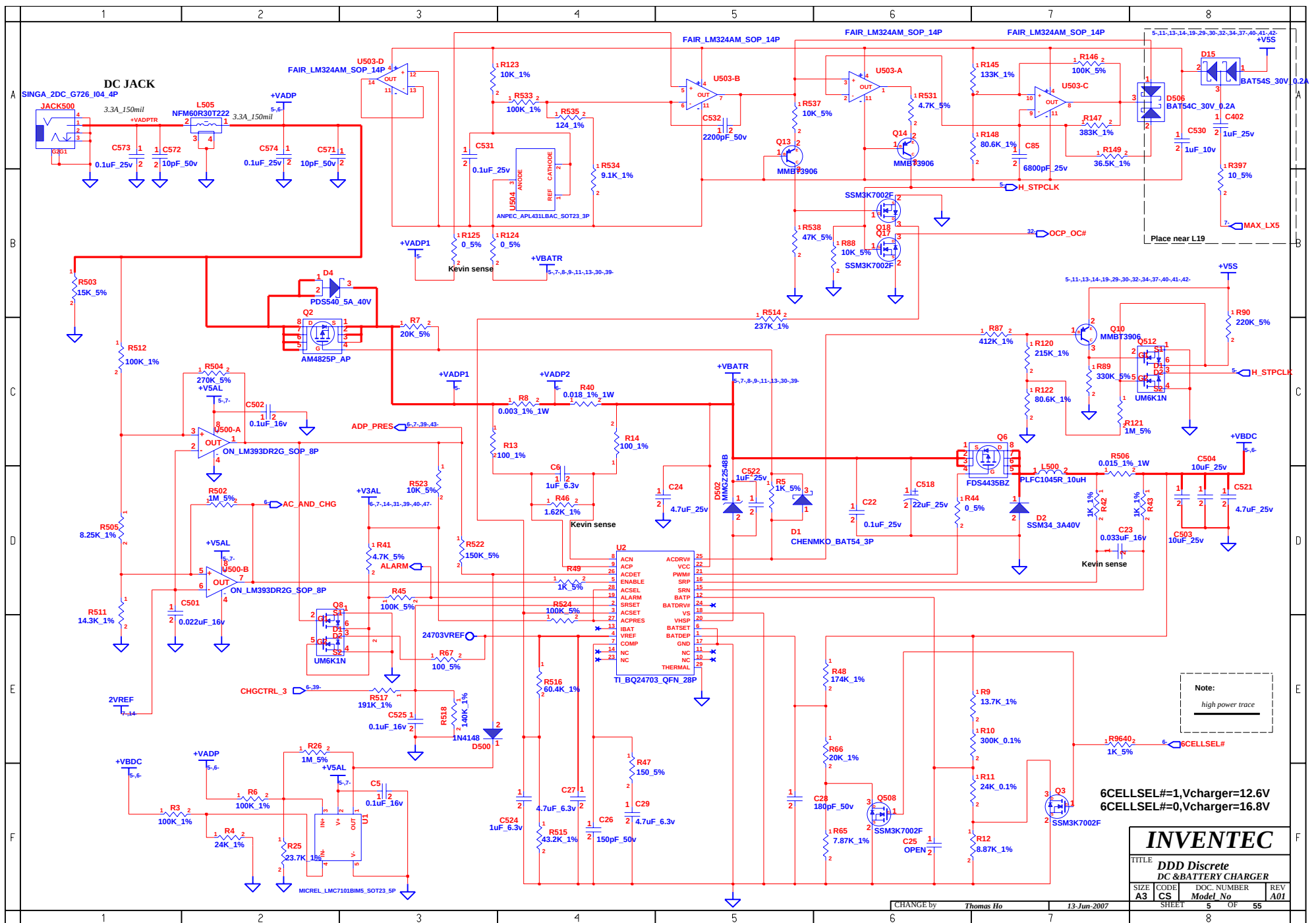


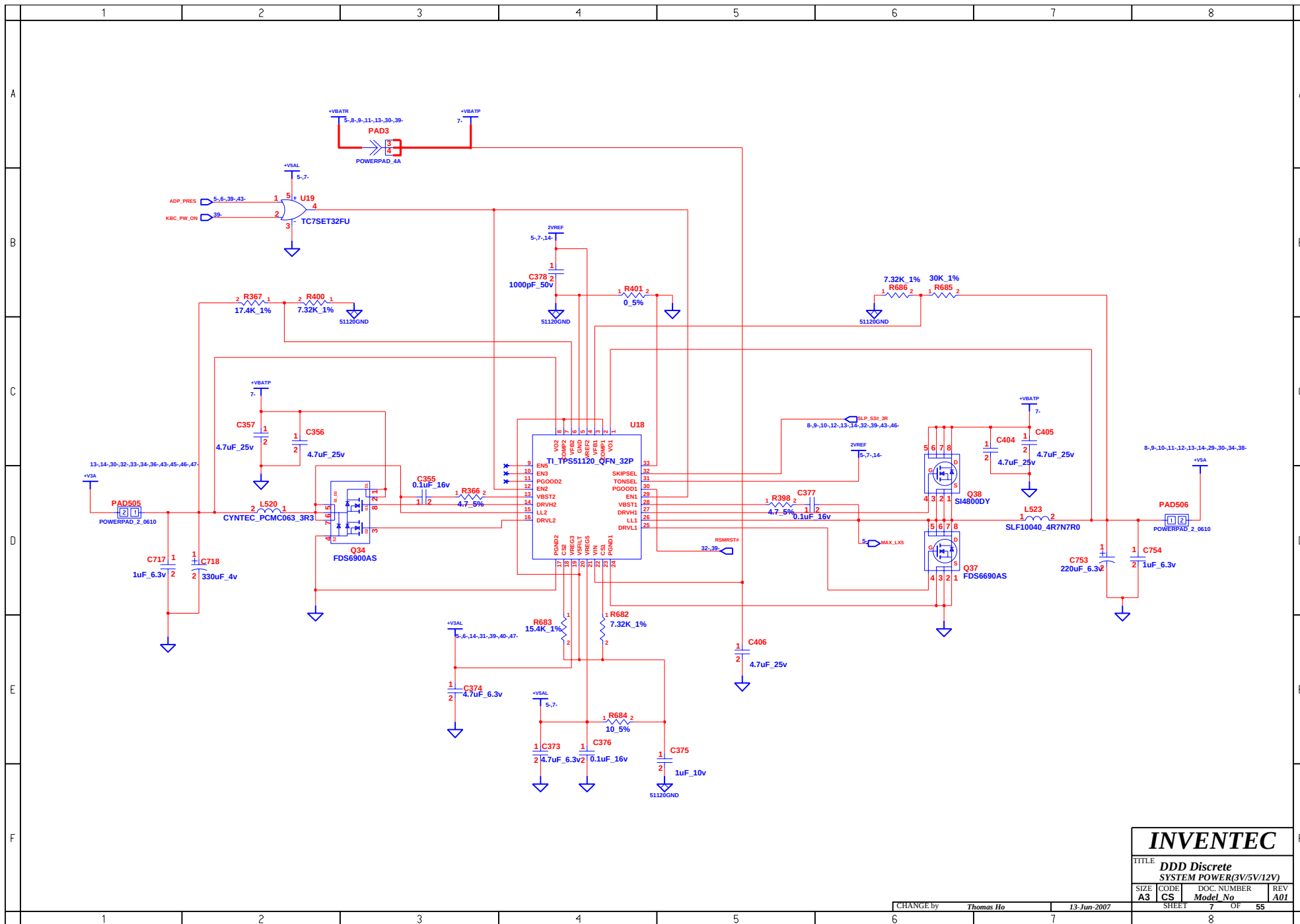


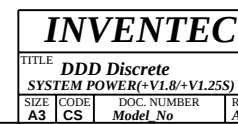
INVENTEC			
TITLE			
DDD Discrete			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

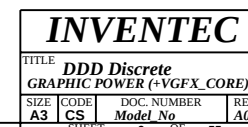
CHANGE by Thomas Ho 13-Jun-2007

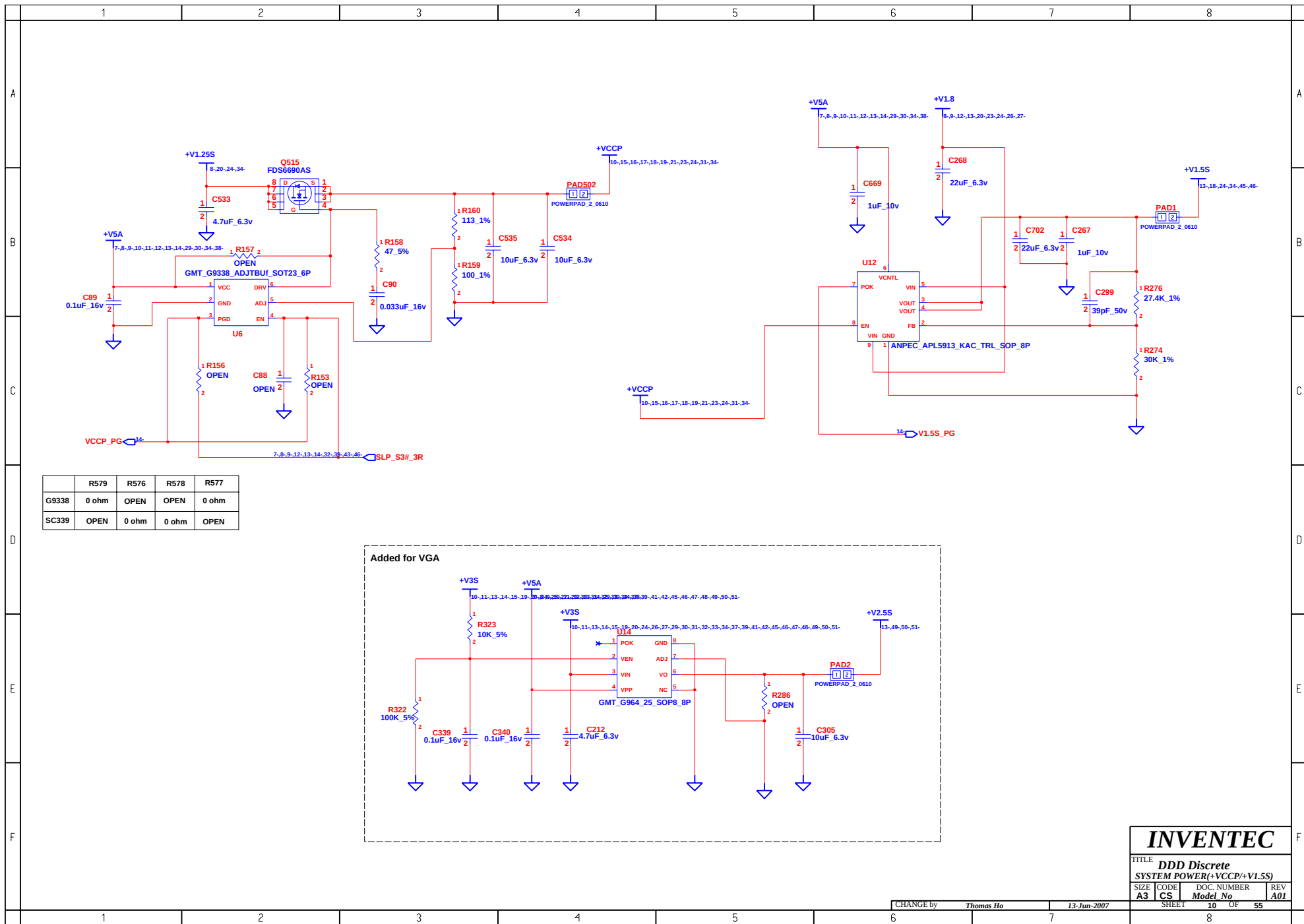
SHEET 4 OF 55

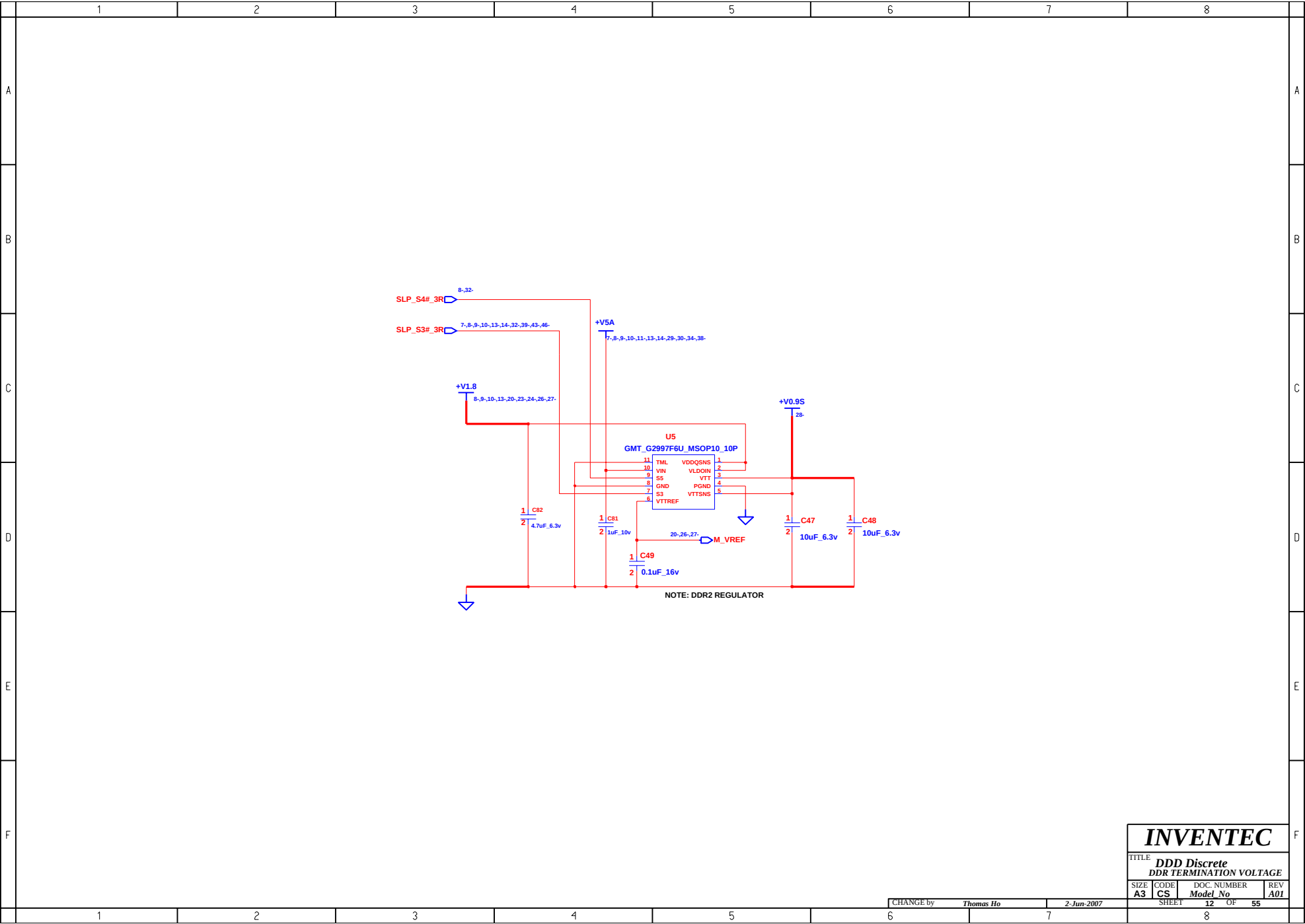


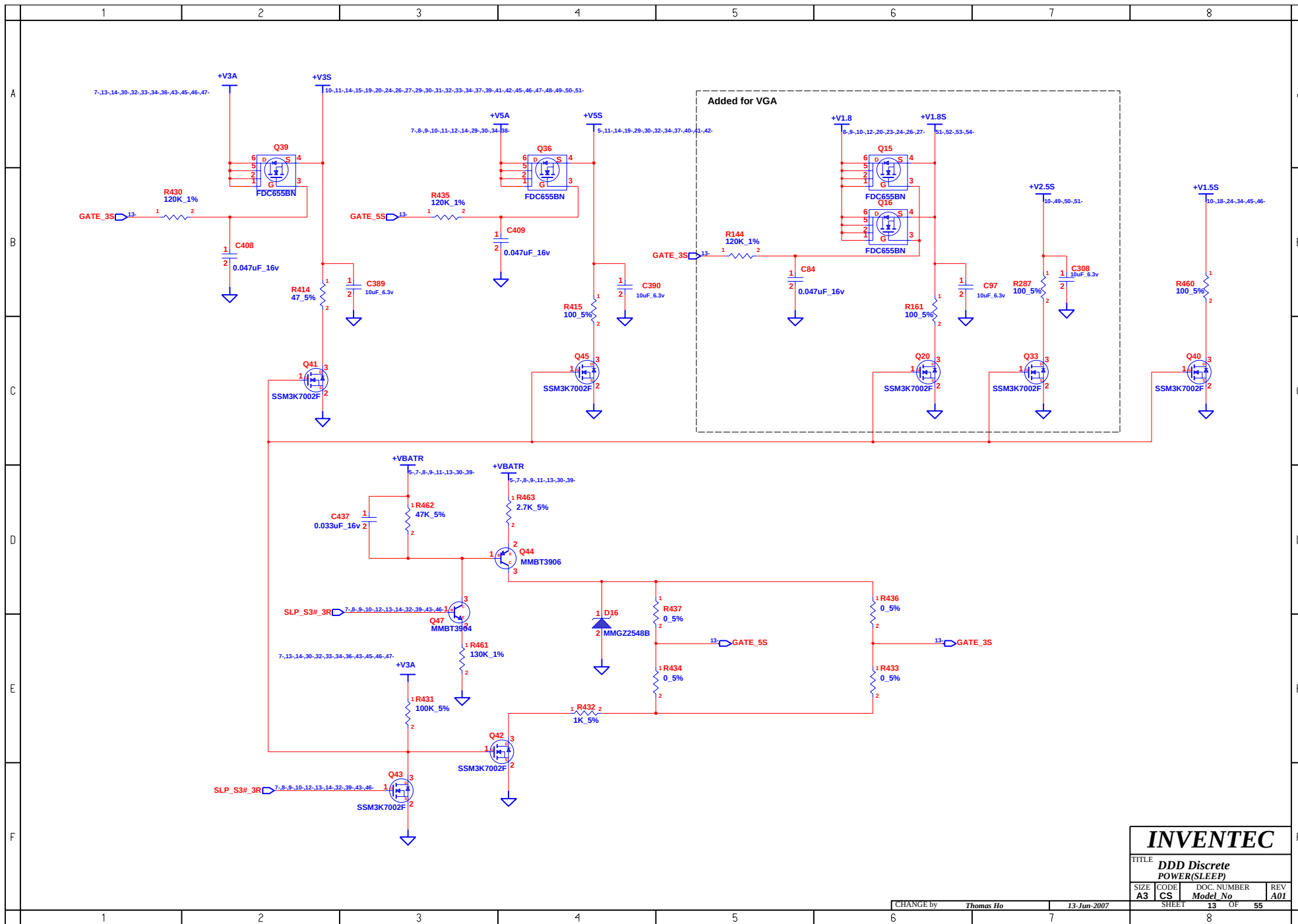


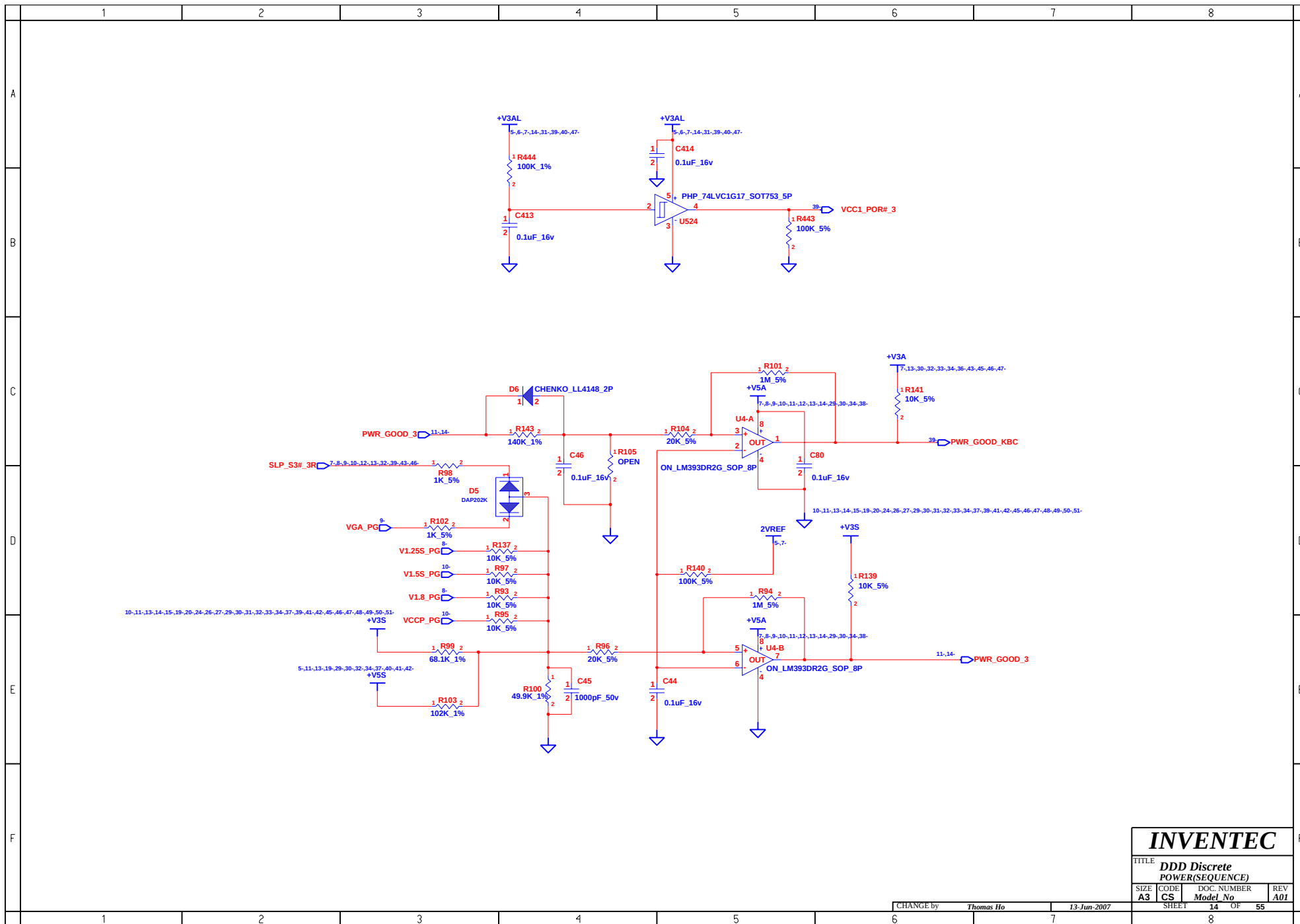






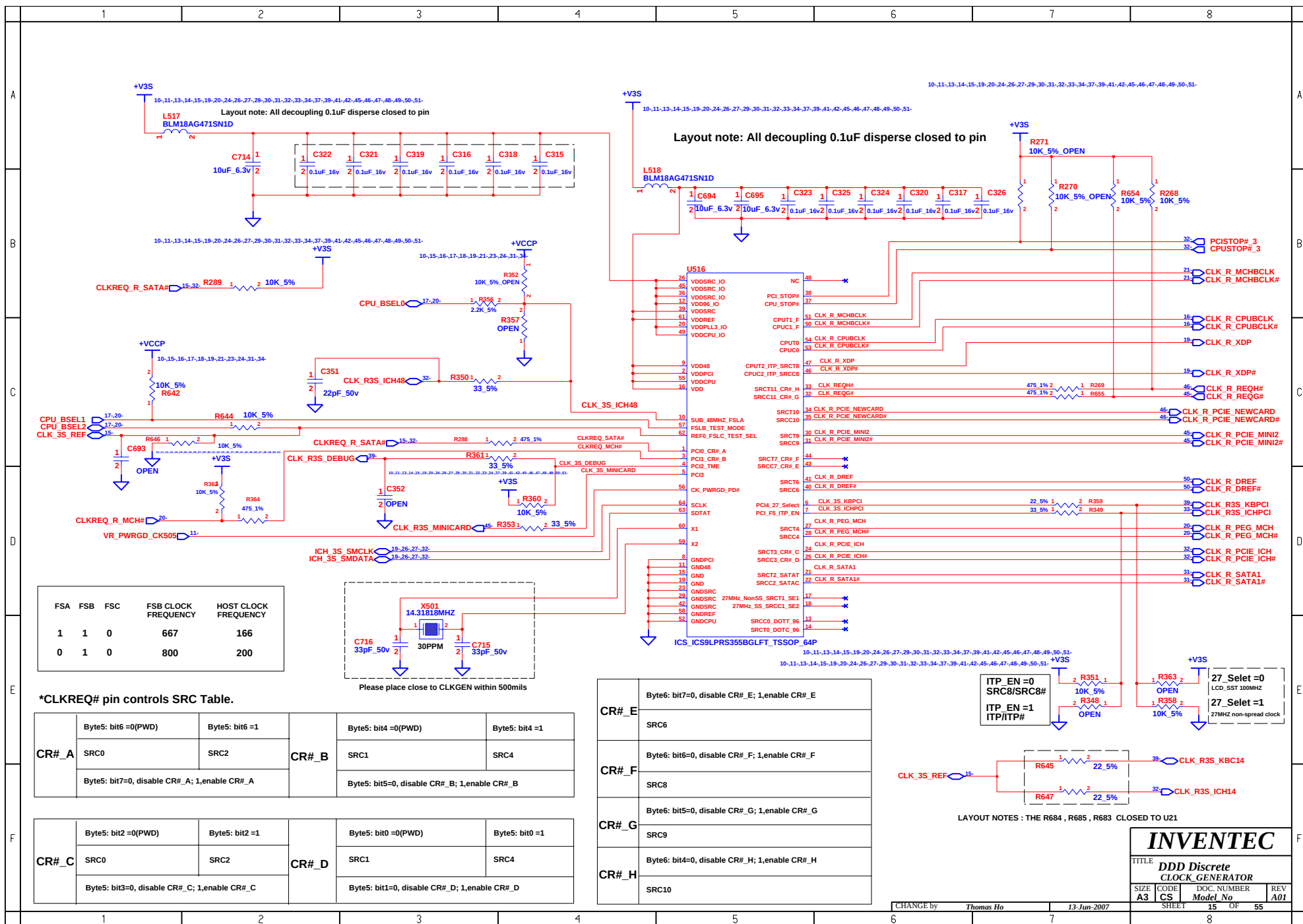


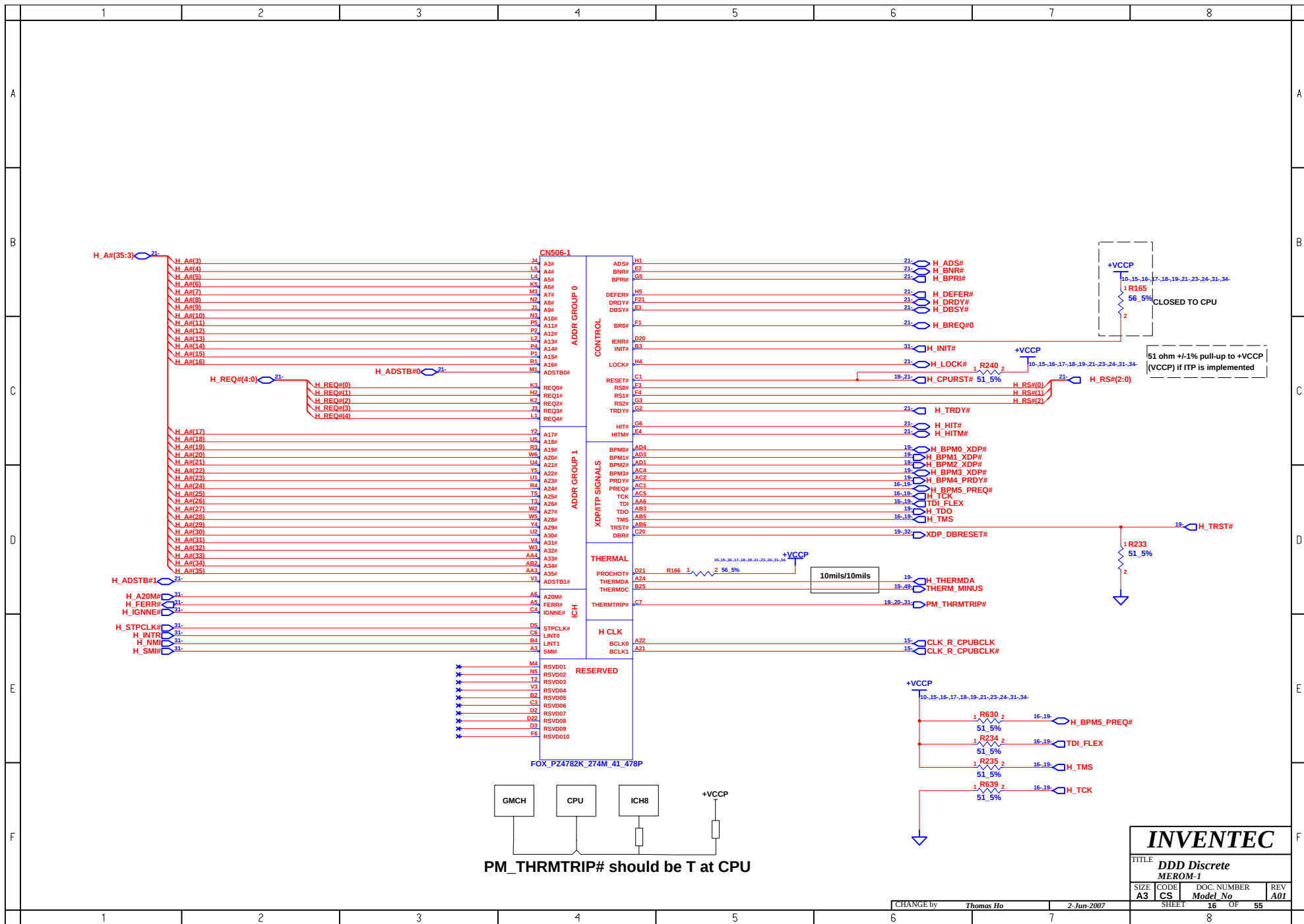


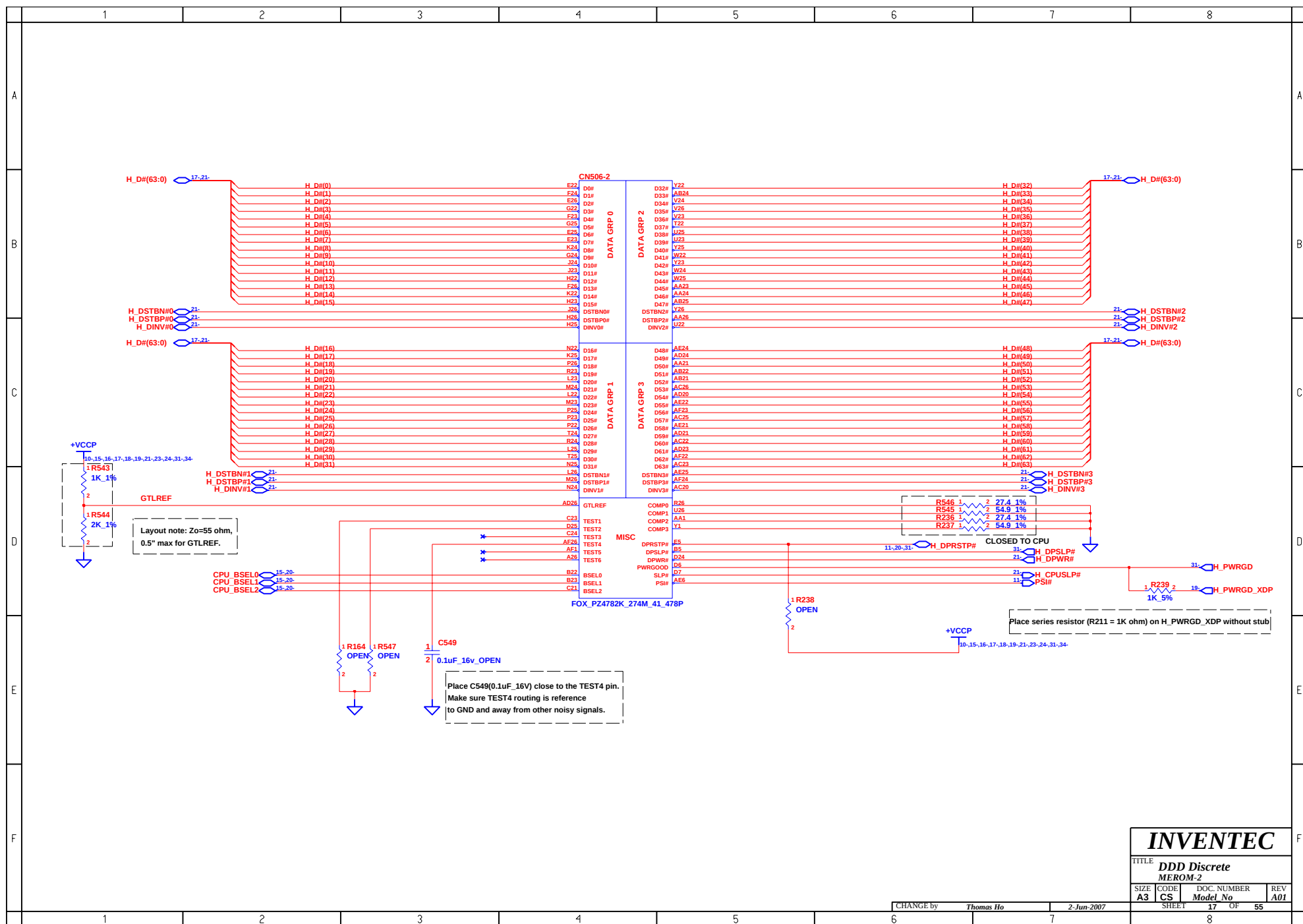


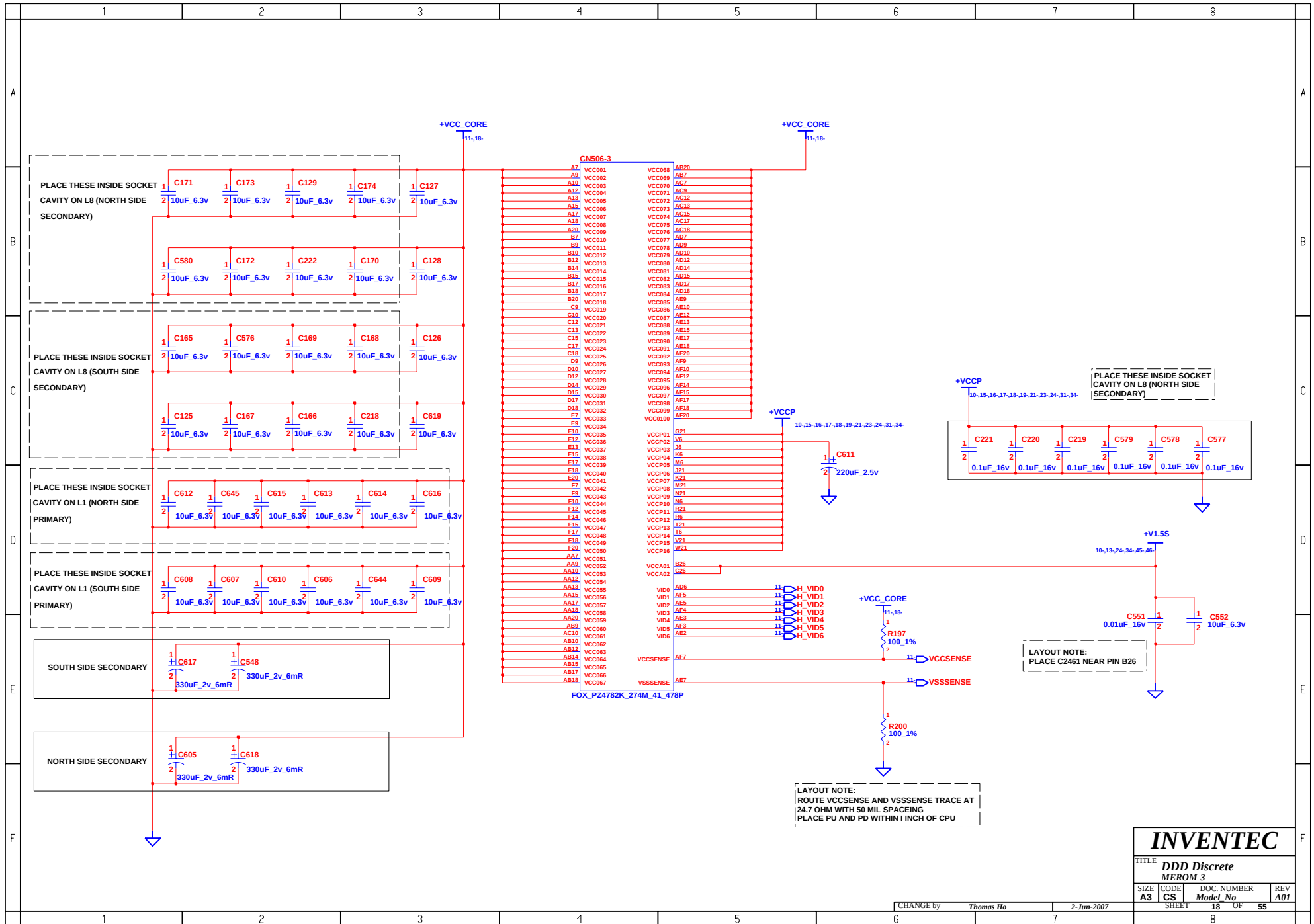
INVENTEC			
TITLE DDD Discrete POWER(SEQUENCE)			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
SHEET 14 OF 55			

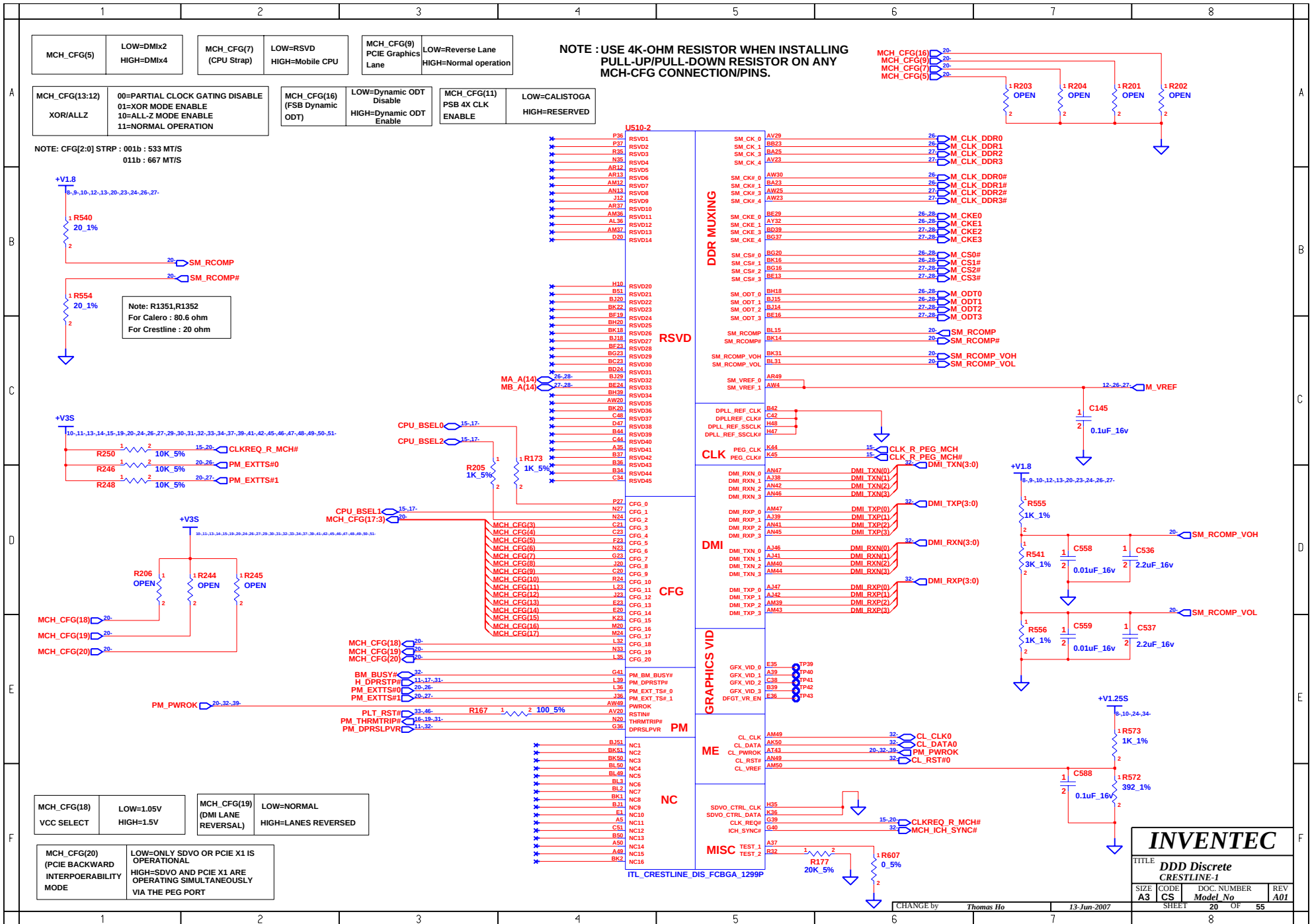
CHANGE by Thomas Ho 13-Jun-2007

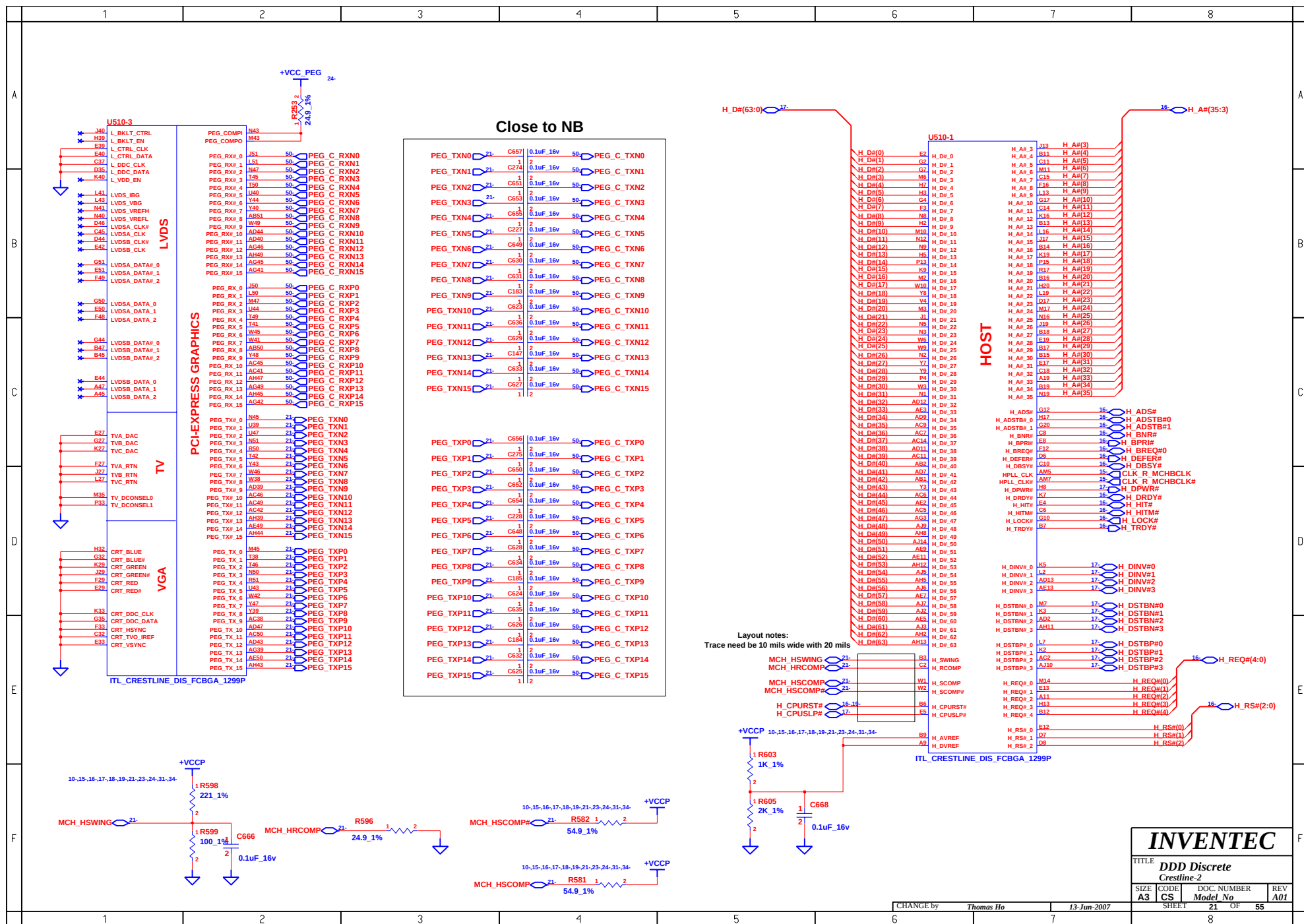


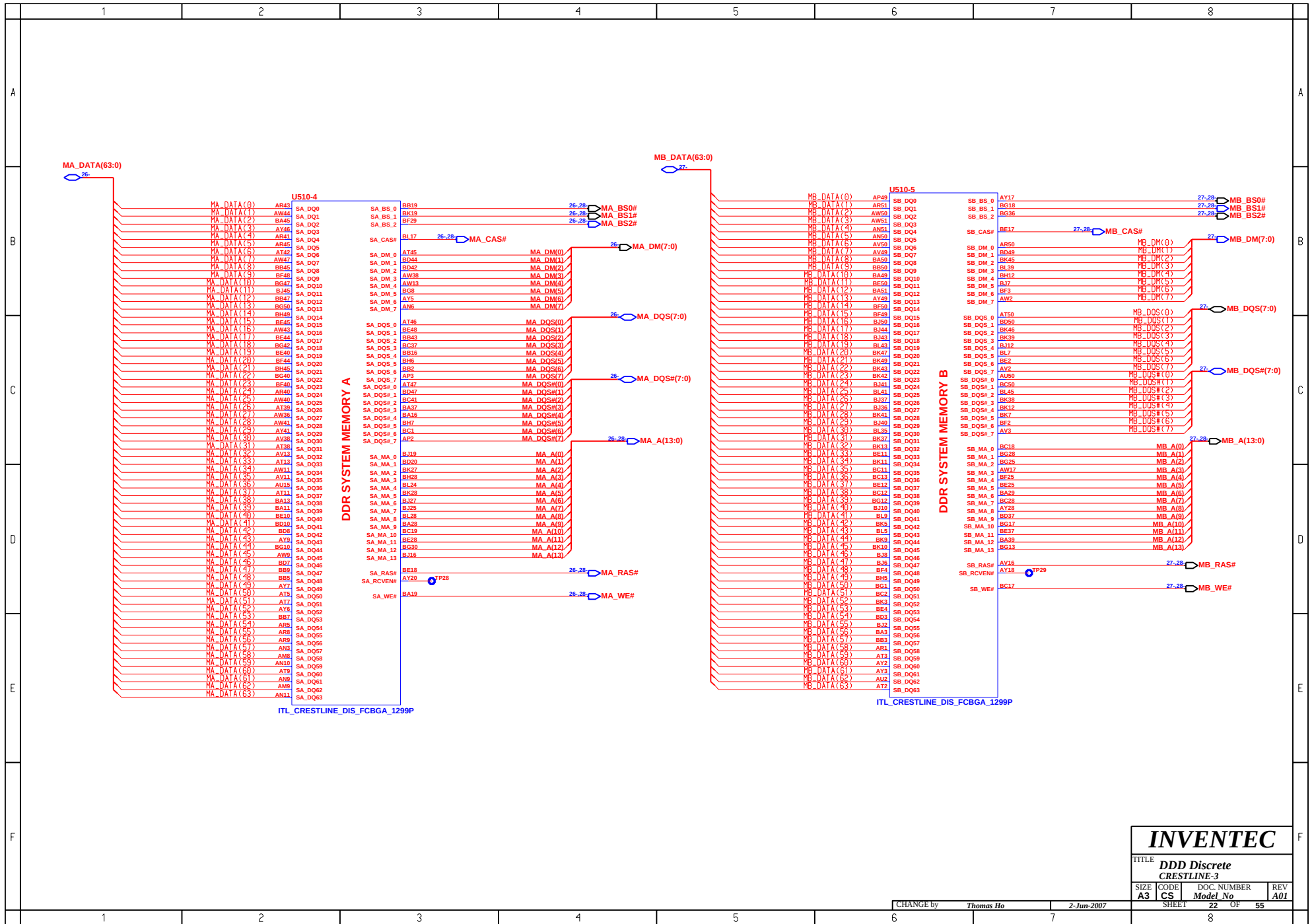






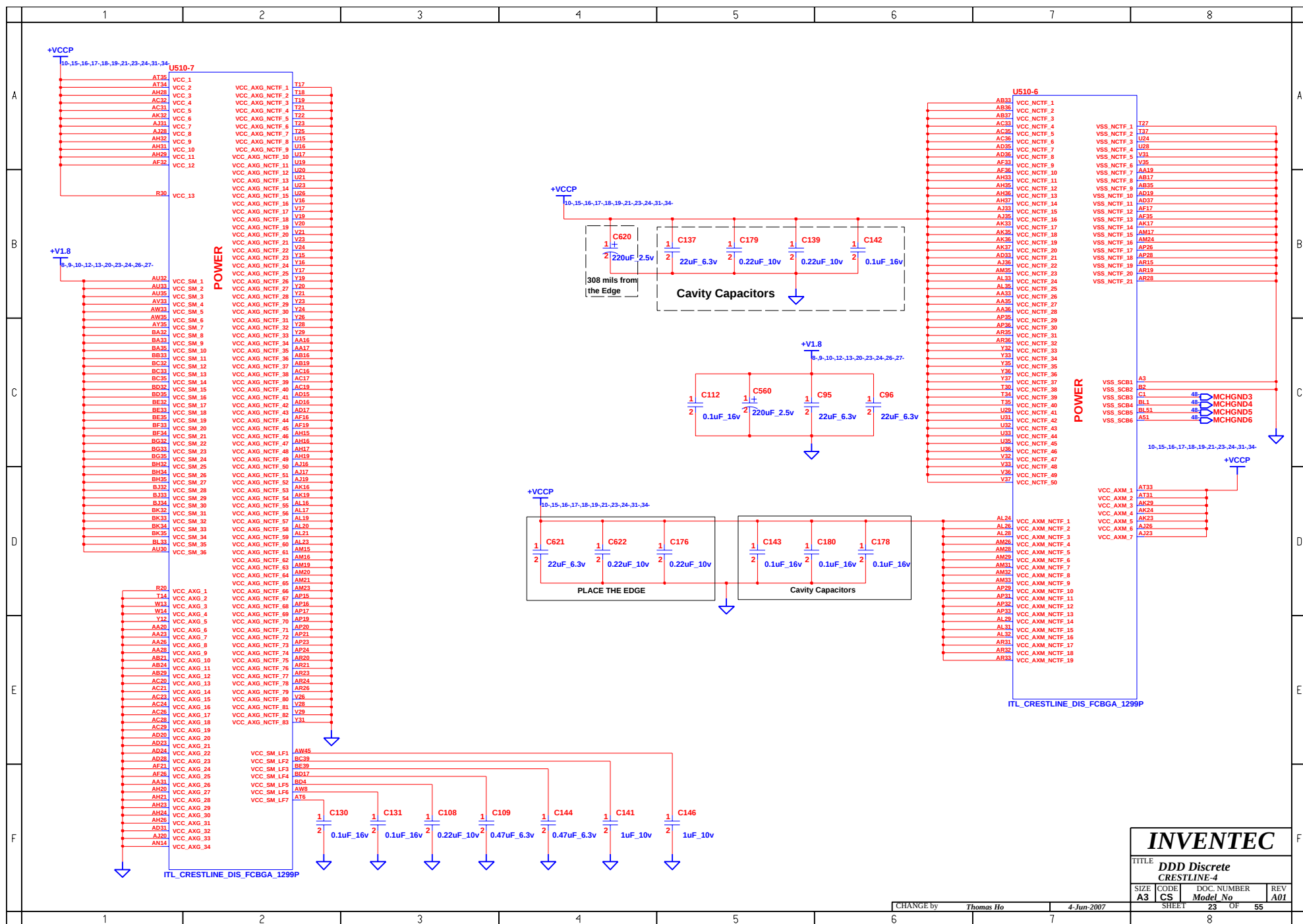


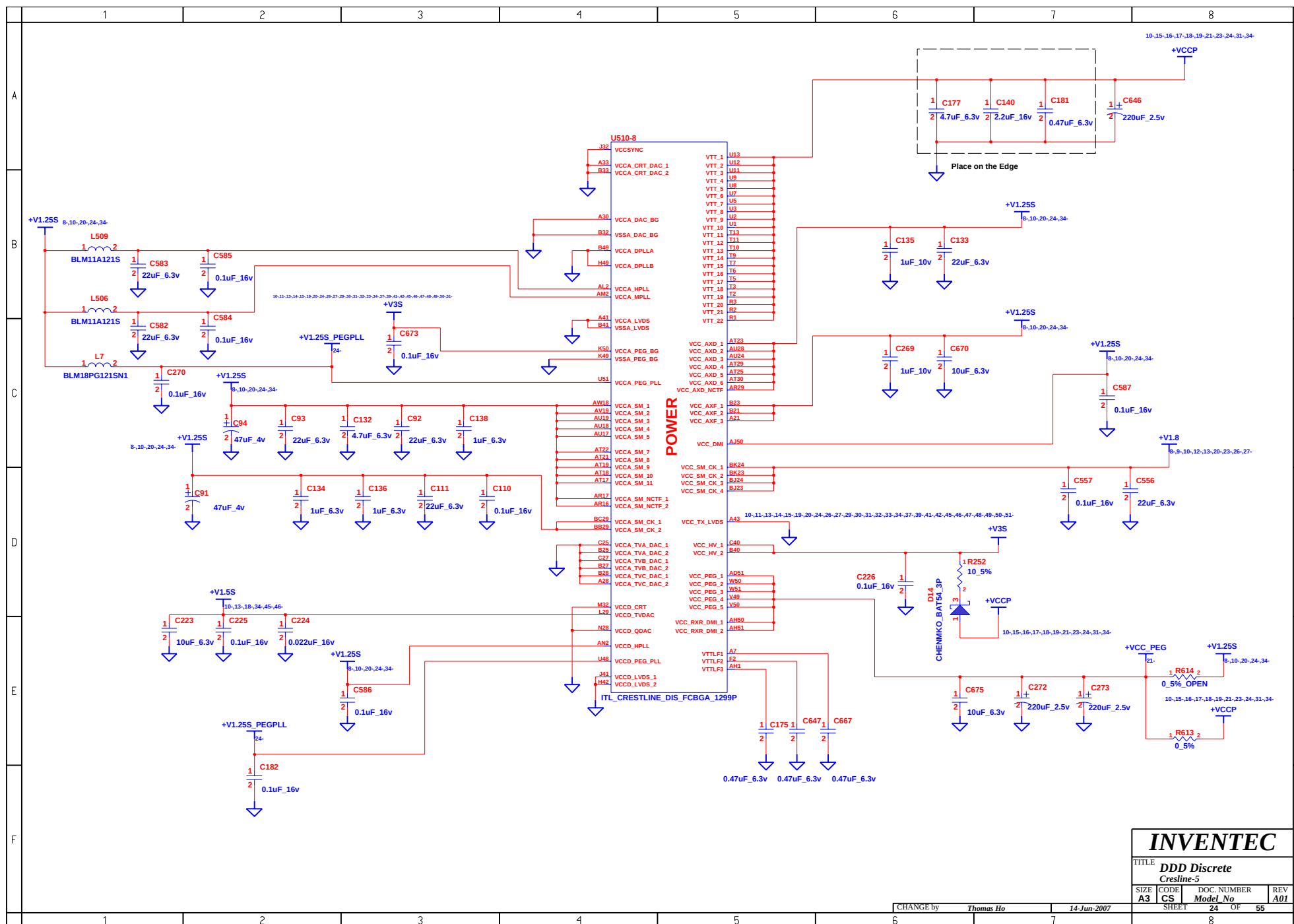


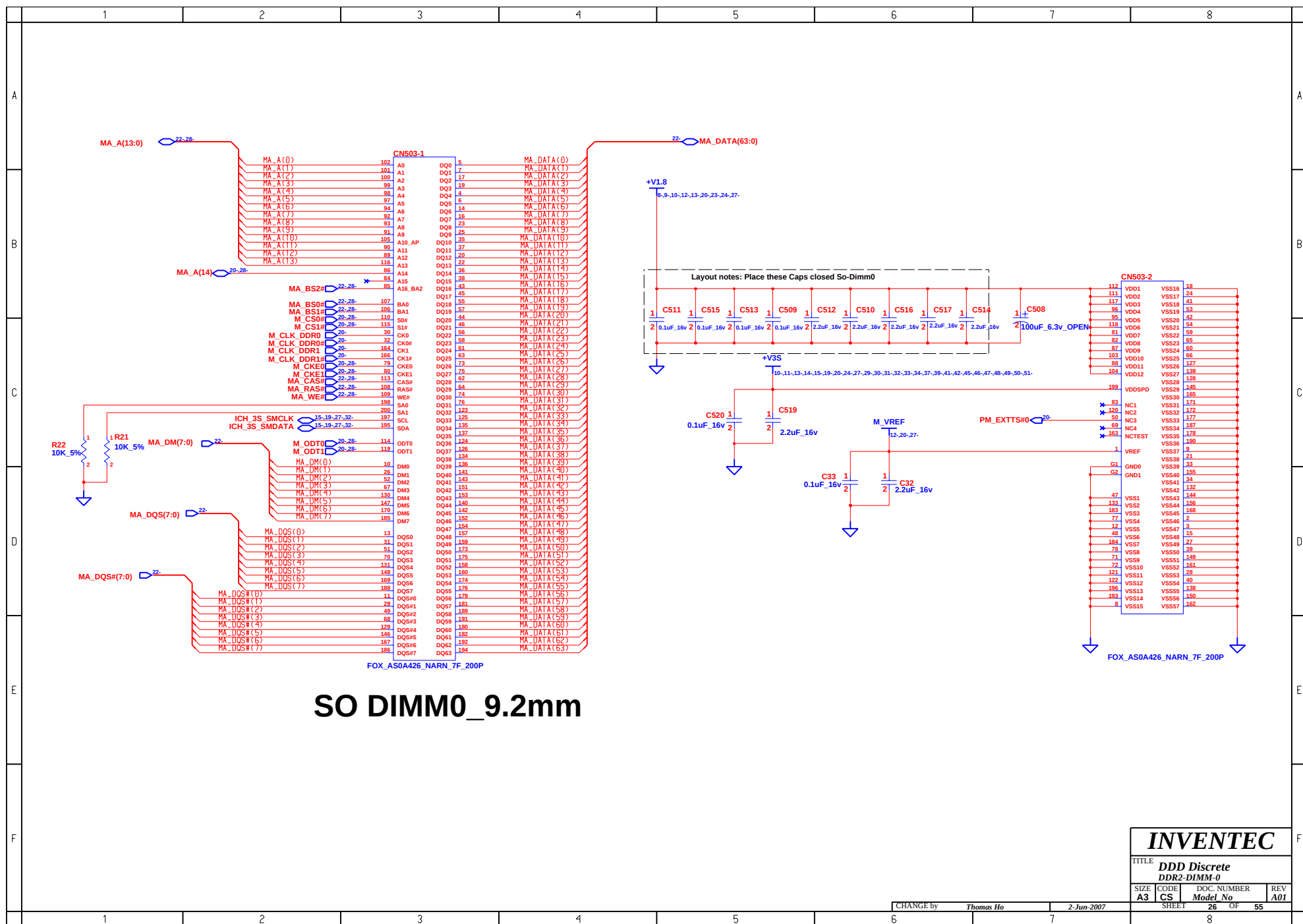


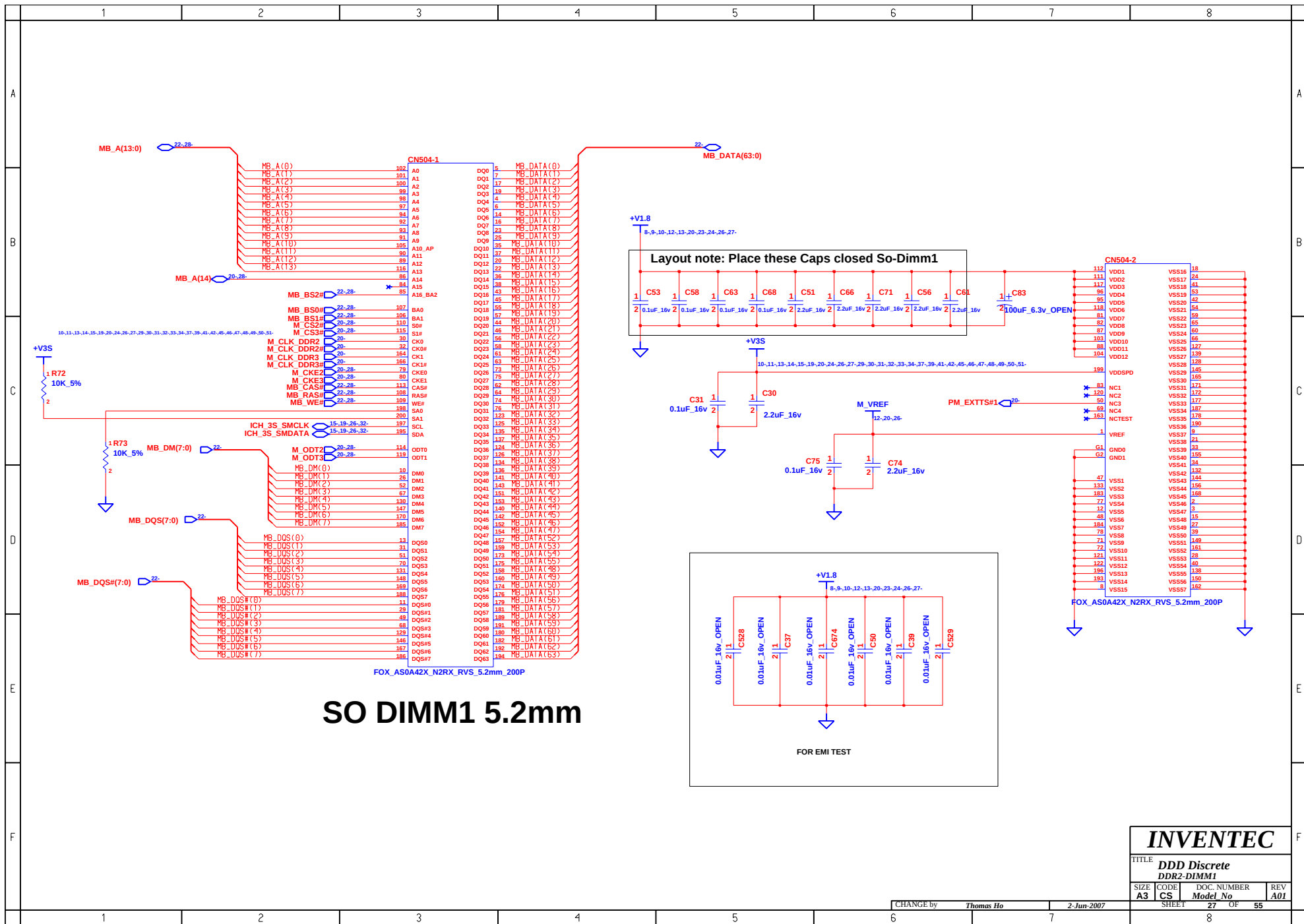
INVENTEC

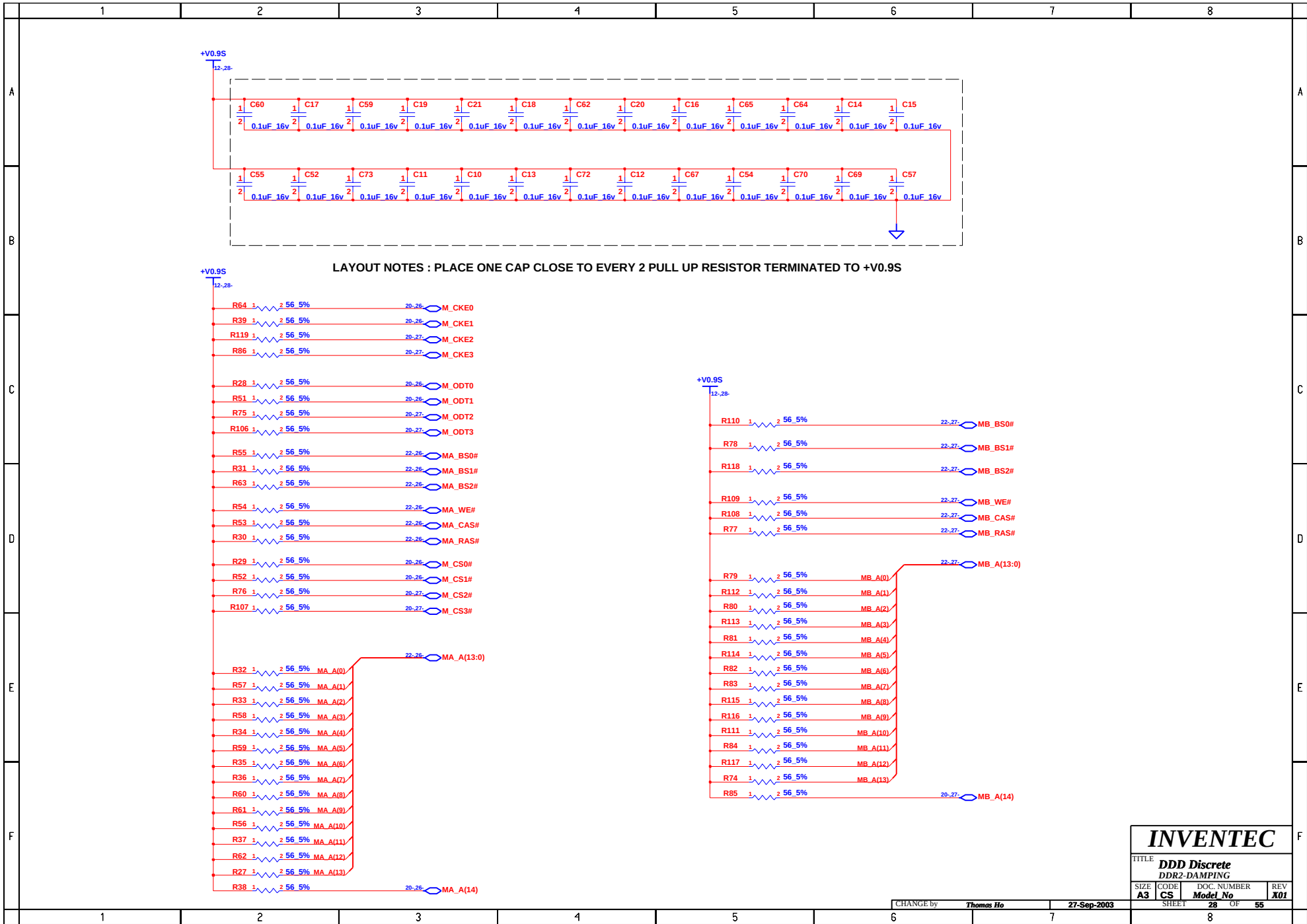
TITLE			
DDD Discrete			
CRESTRINE-3			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01
CHANGE by		2-Jun-2007	
Thomas Ho			
SHEET		22	OF 55

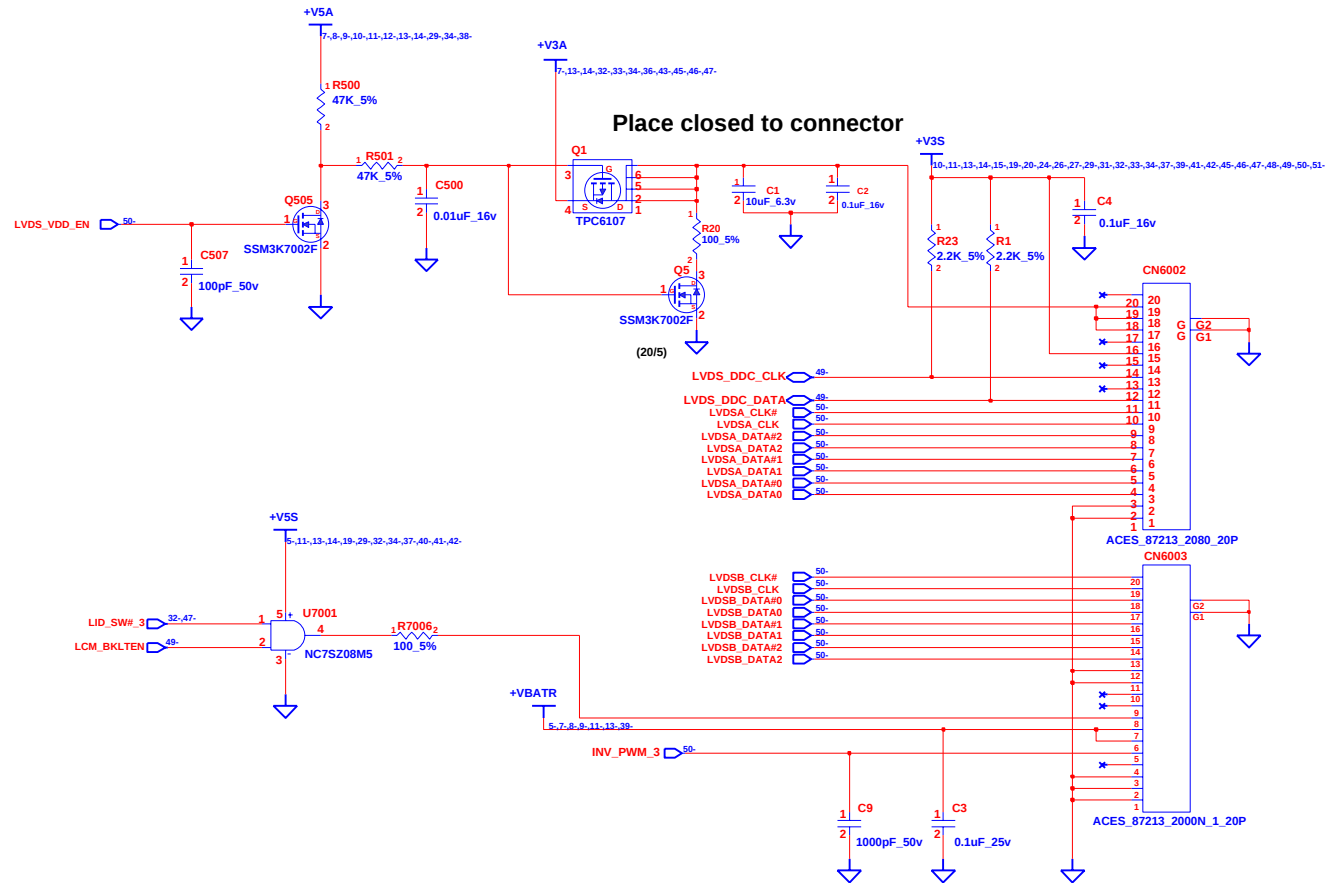




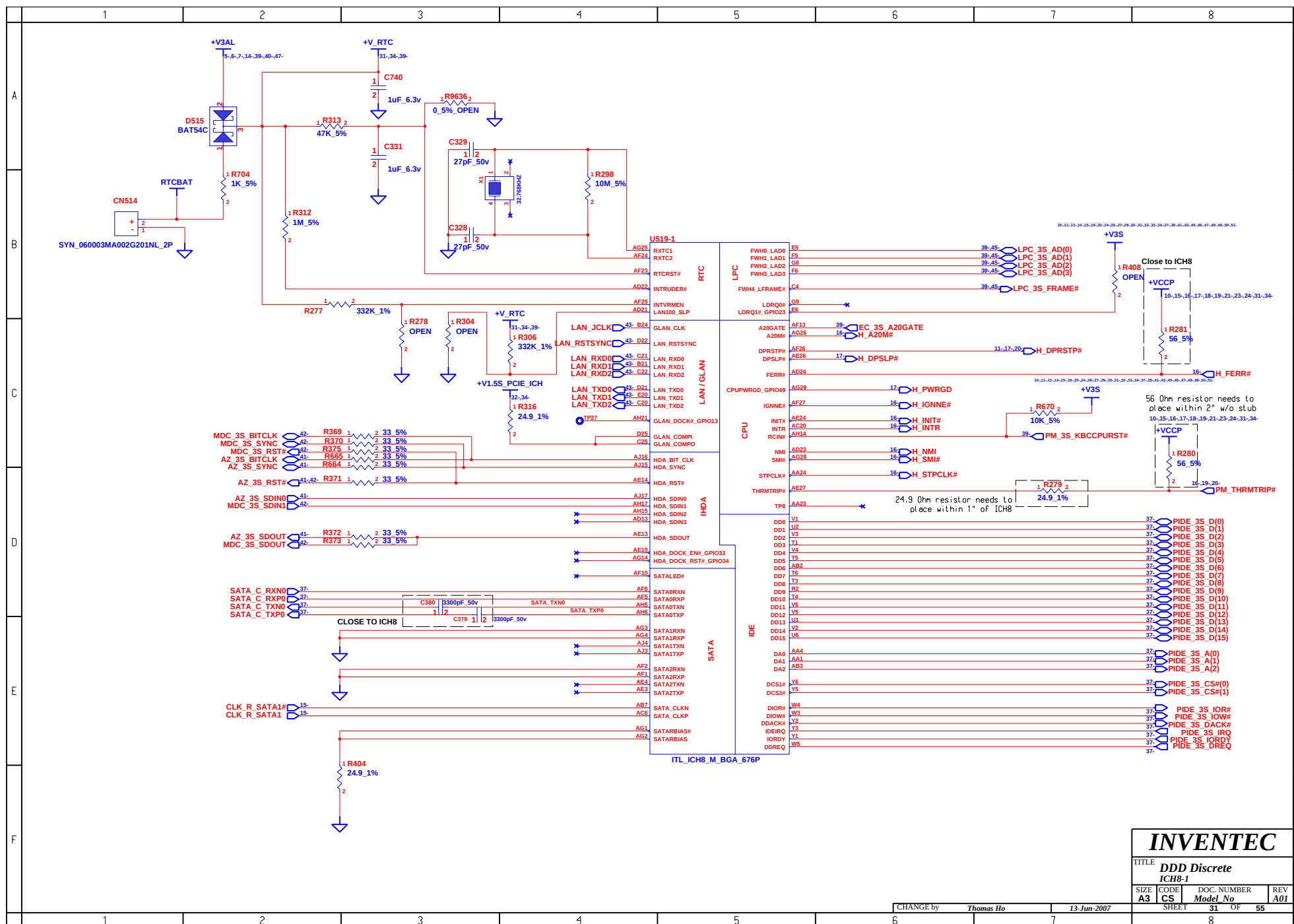


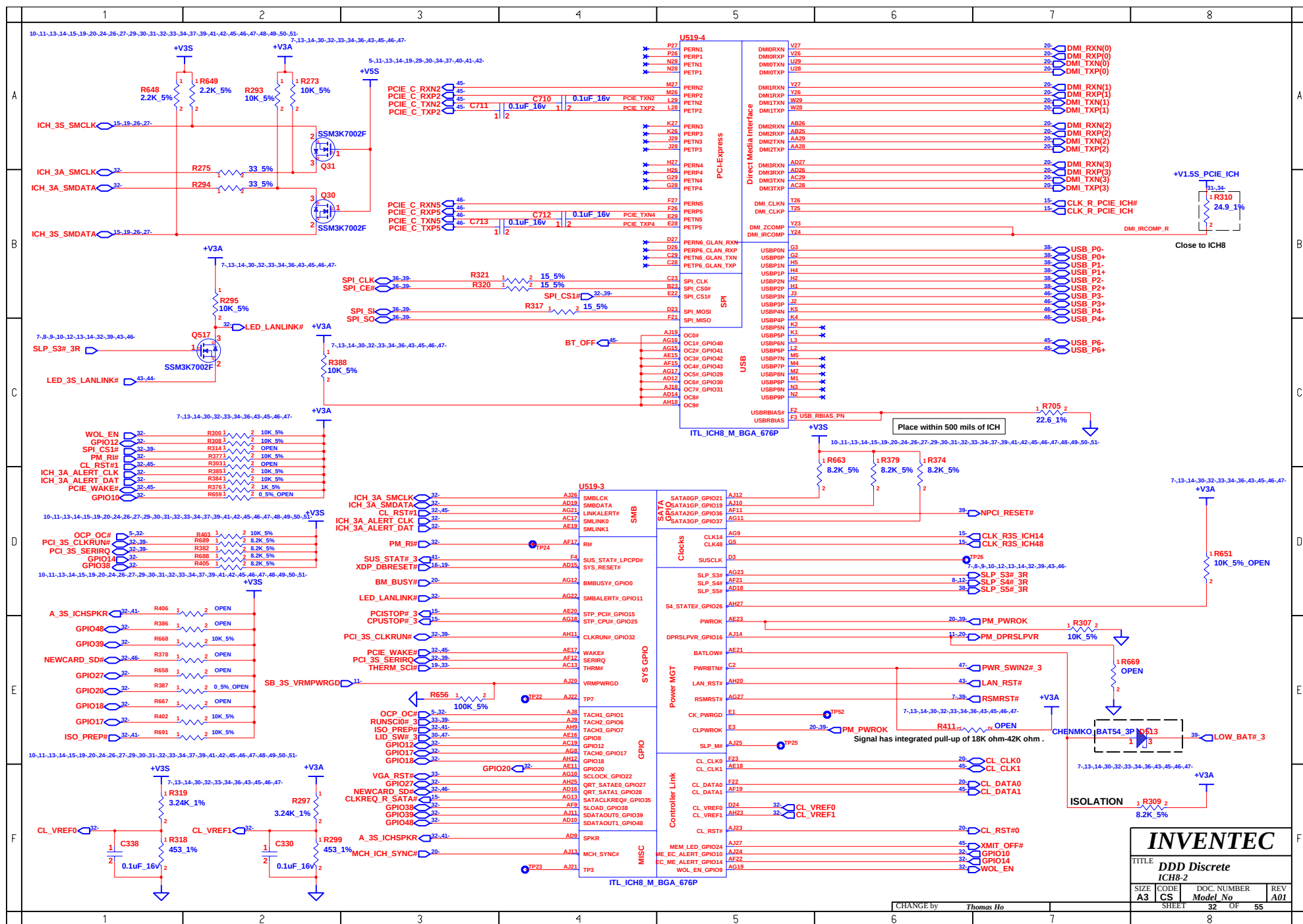






INVENTEC			
TITLE DDD Discrete LCM Conn			
SIZE A3	CODE CS	DOC. NUMBER Model No	REV A01
CHANGE by Thomas Ho		13-Jun-2007	
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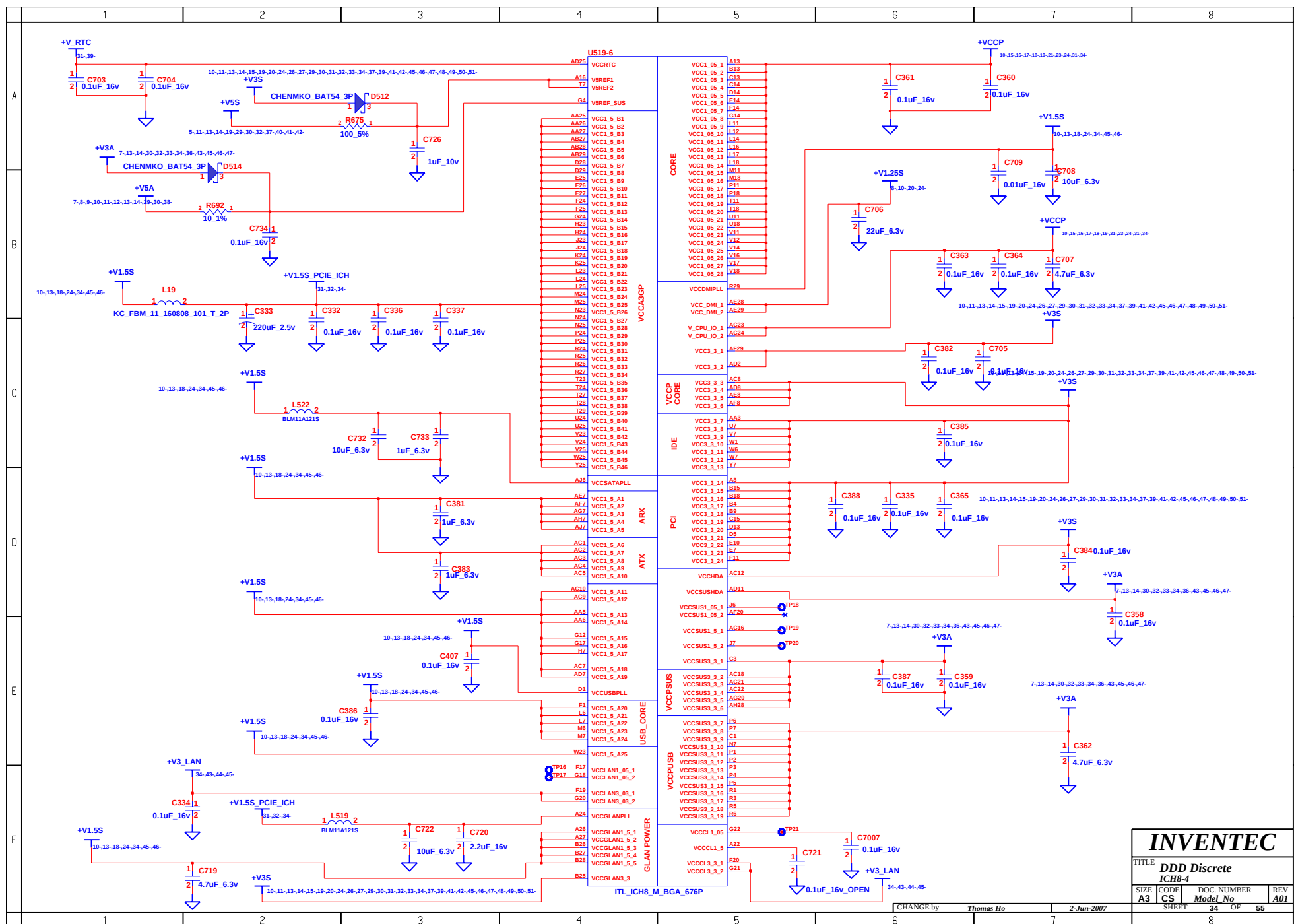
353

354

355

356

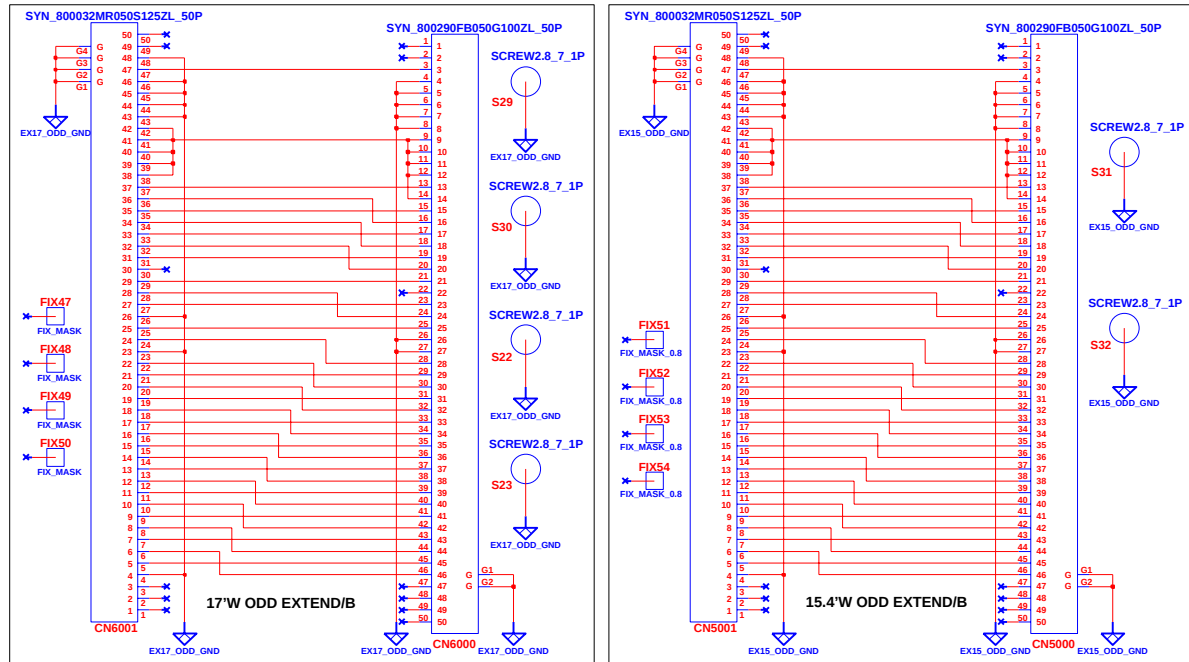
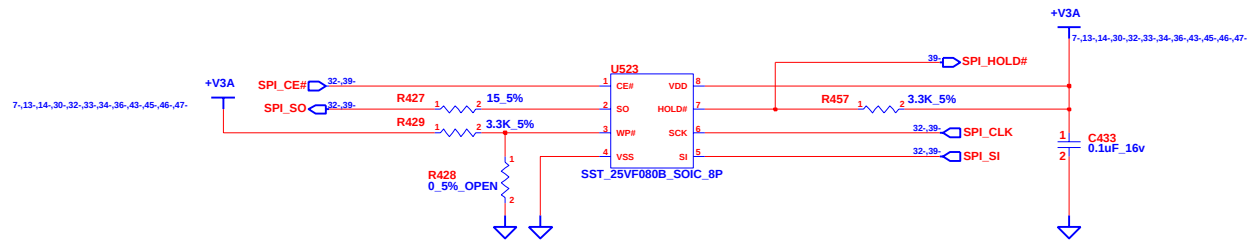
<



	1	2	3	4	5	6	7	8
A								
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D								
E								
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CHANGE by				Thomas Ho		4-Jun-2007	
SHEET				35		OF 55	

TITLE			
DDD Discrete			
ICH8-5			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01



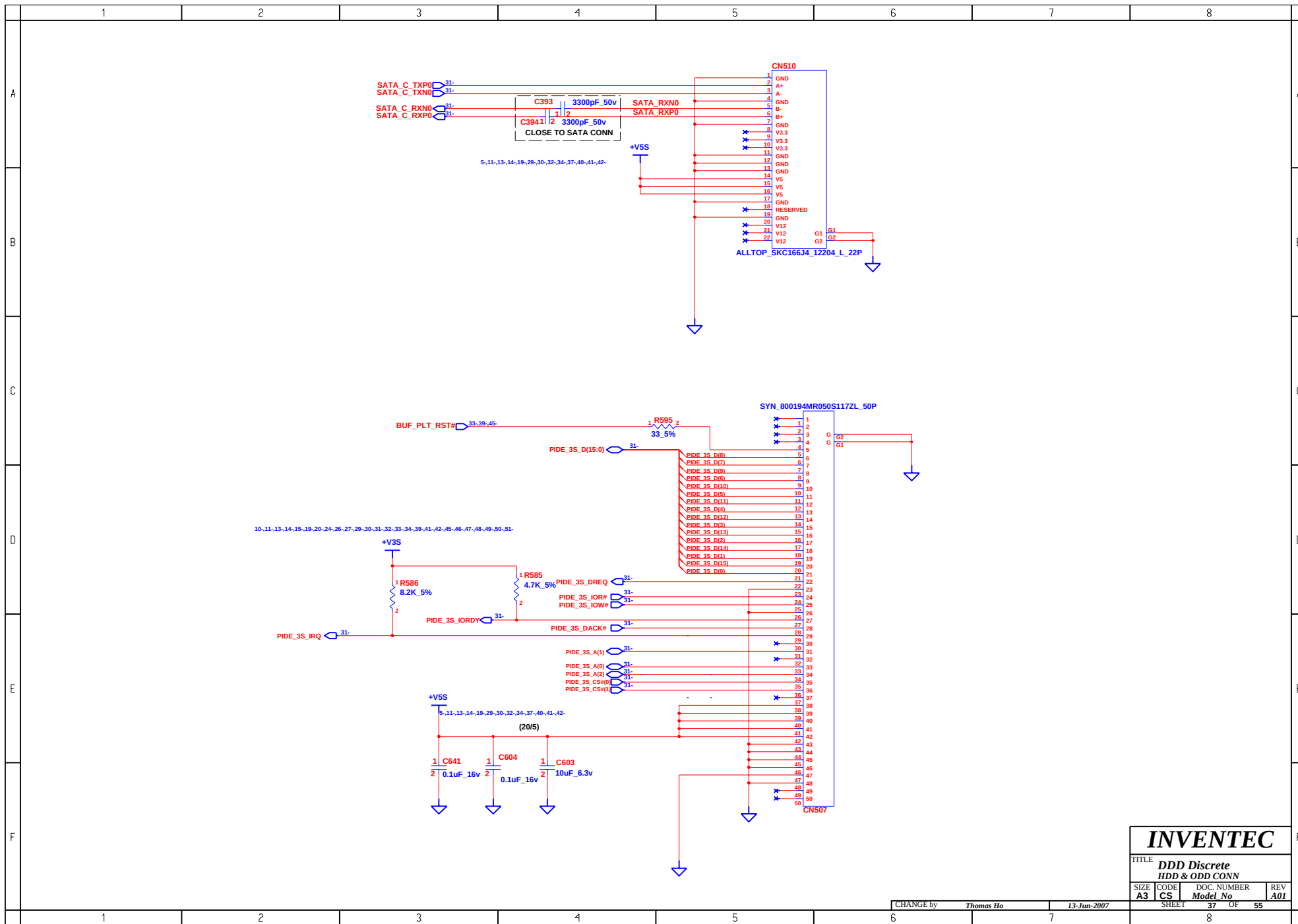
INVENTEC

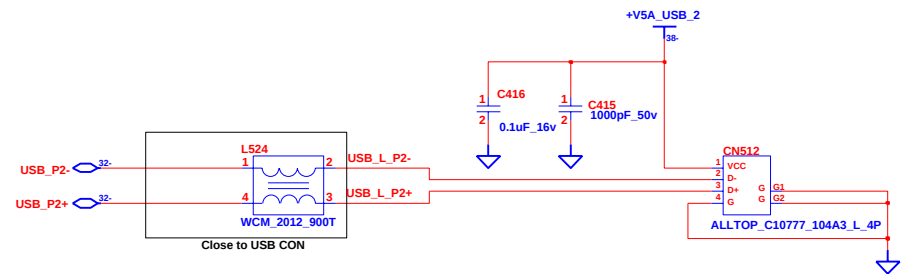
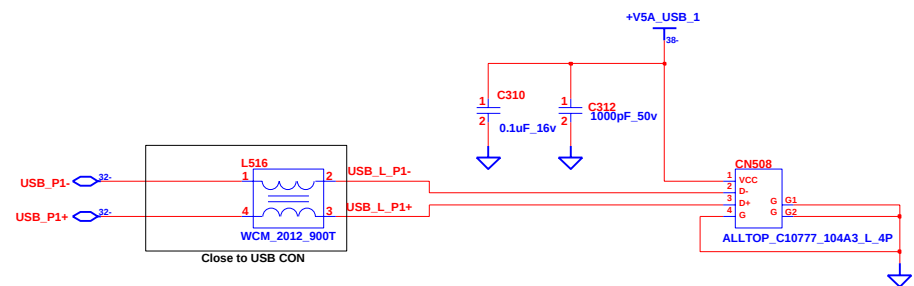
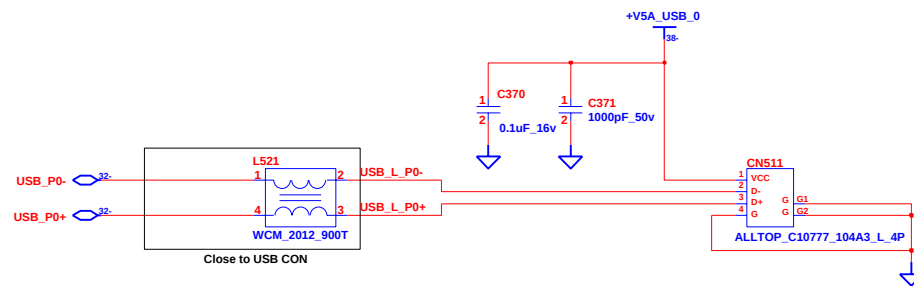
TITLE
DDD Discrete
SYSTEM BIOS&ODD EXT/B

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 13-Jun-2007

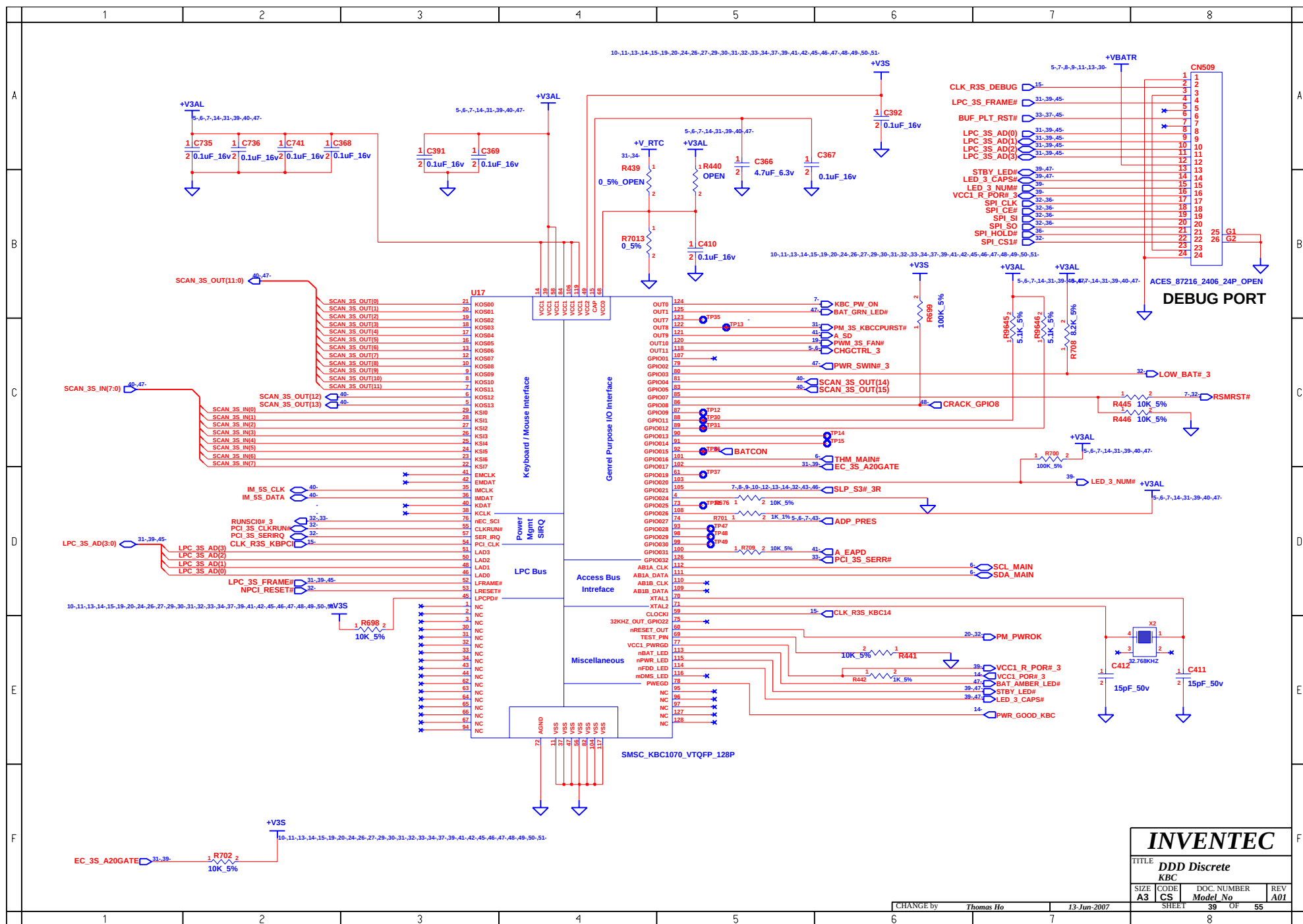
SHEET 36 OF 55

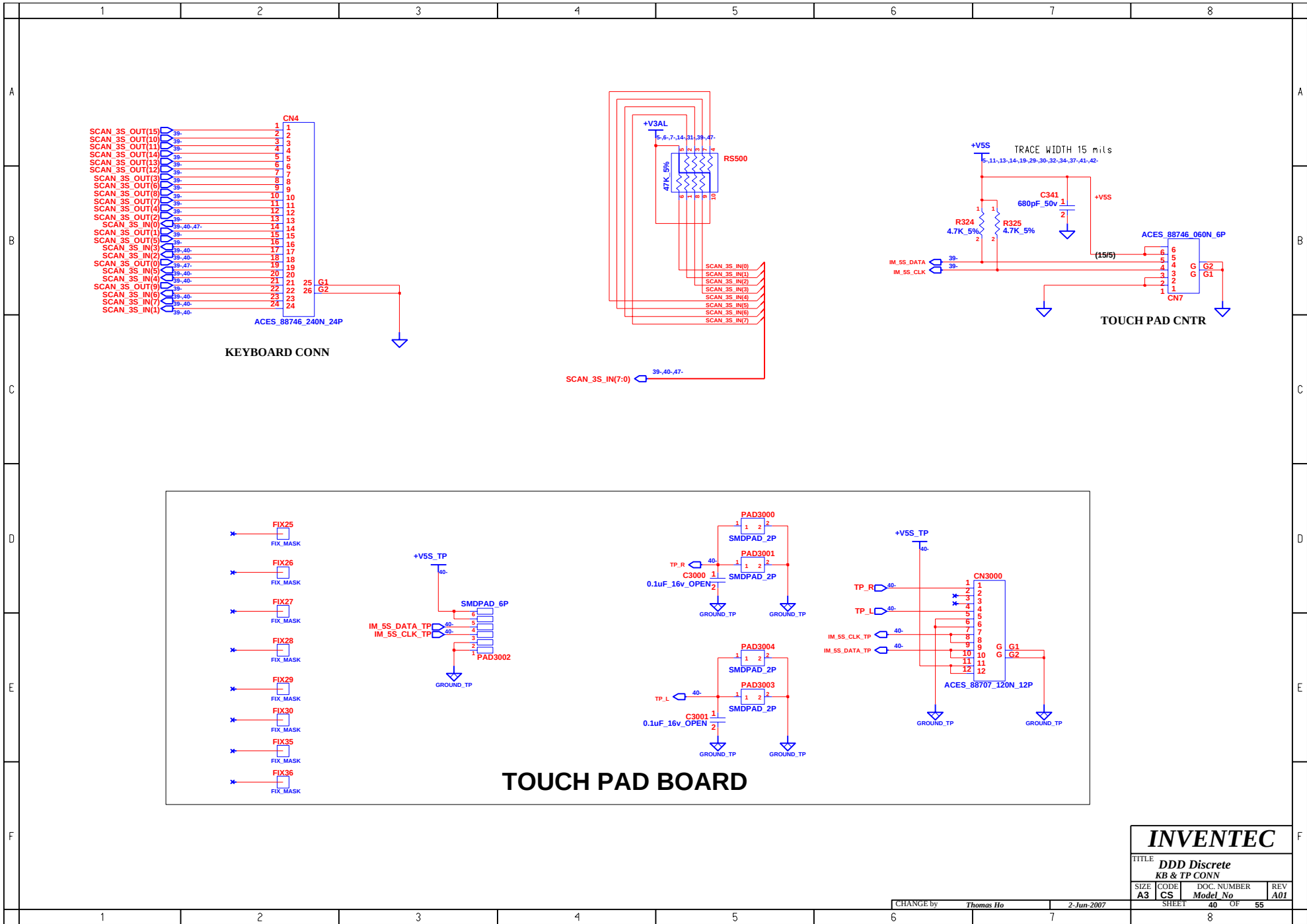


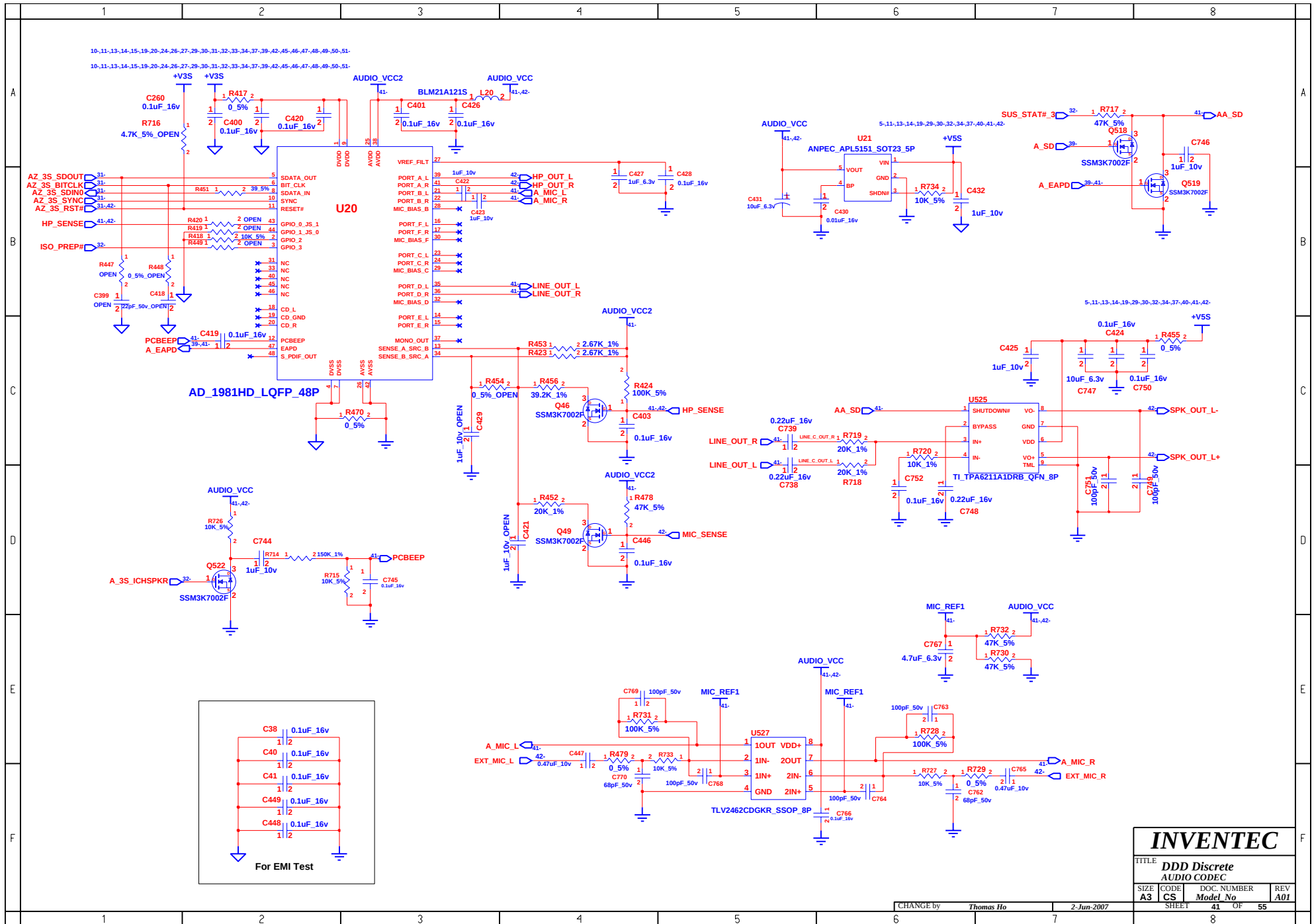


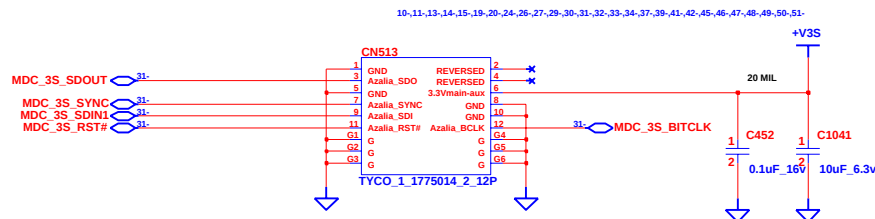
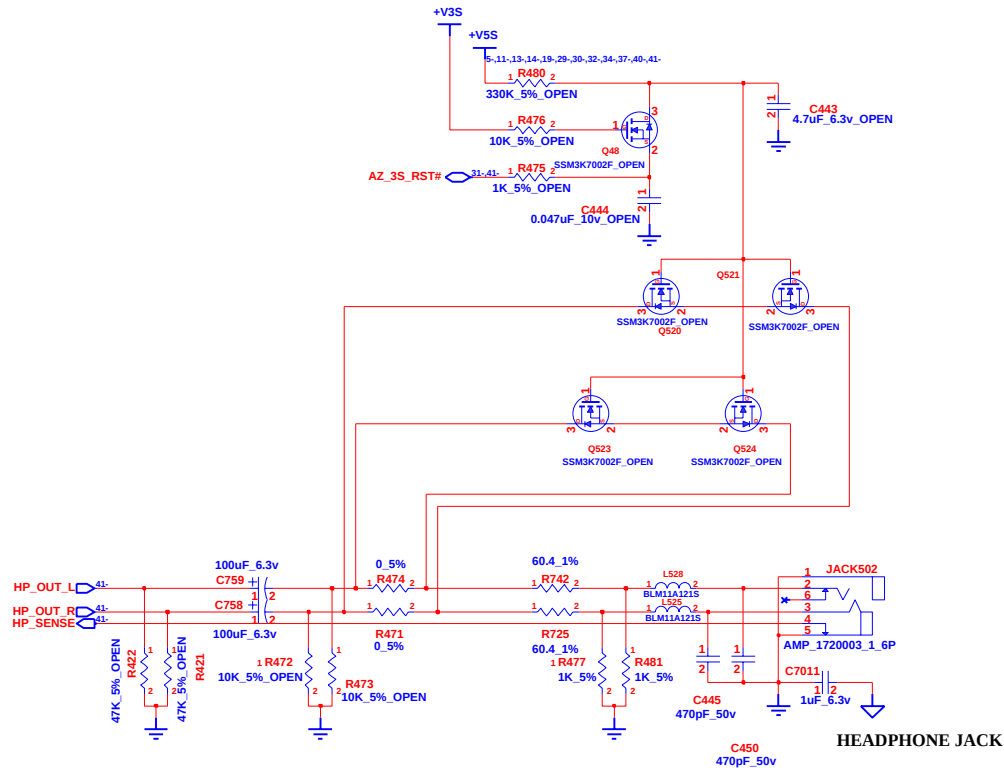
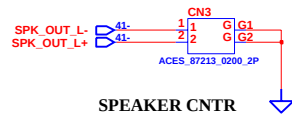
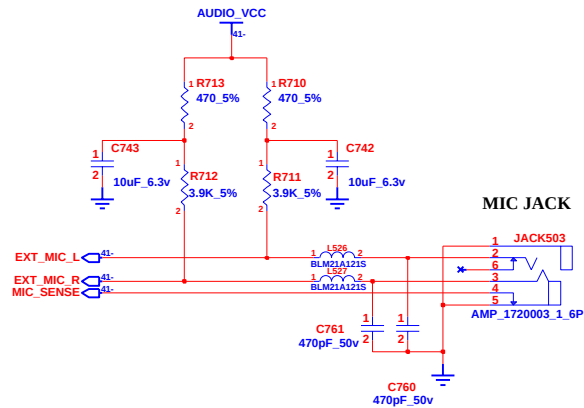
CHANGE by	<i>Thomas Ho</i>	<i>2-Jun-2007</i>
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INVENTEC			
TITLE DDD Discrete USB CONN			
SIZE A3	CODE CS	DOC. NUMBER <i>Model No</i>	REV A01
SHEET		38	OF 55









MDC CNTR

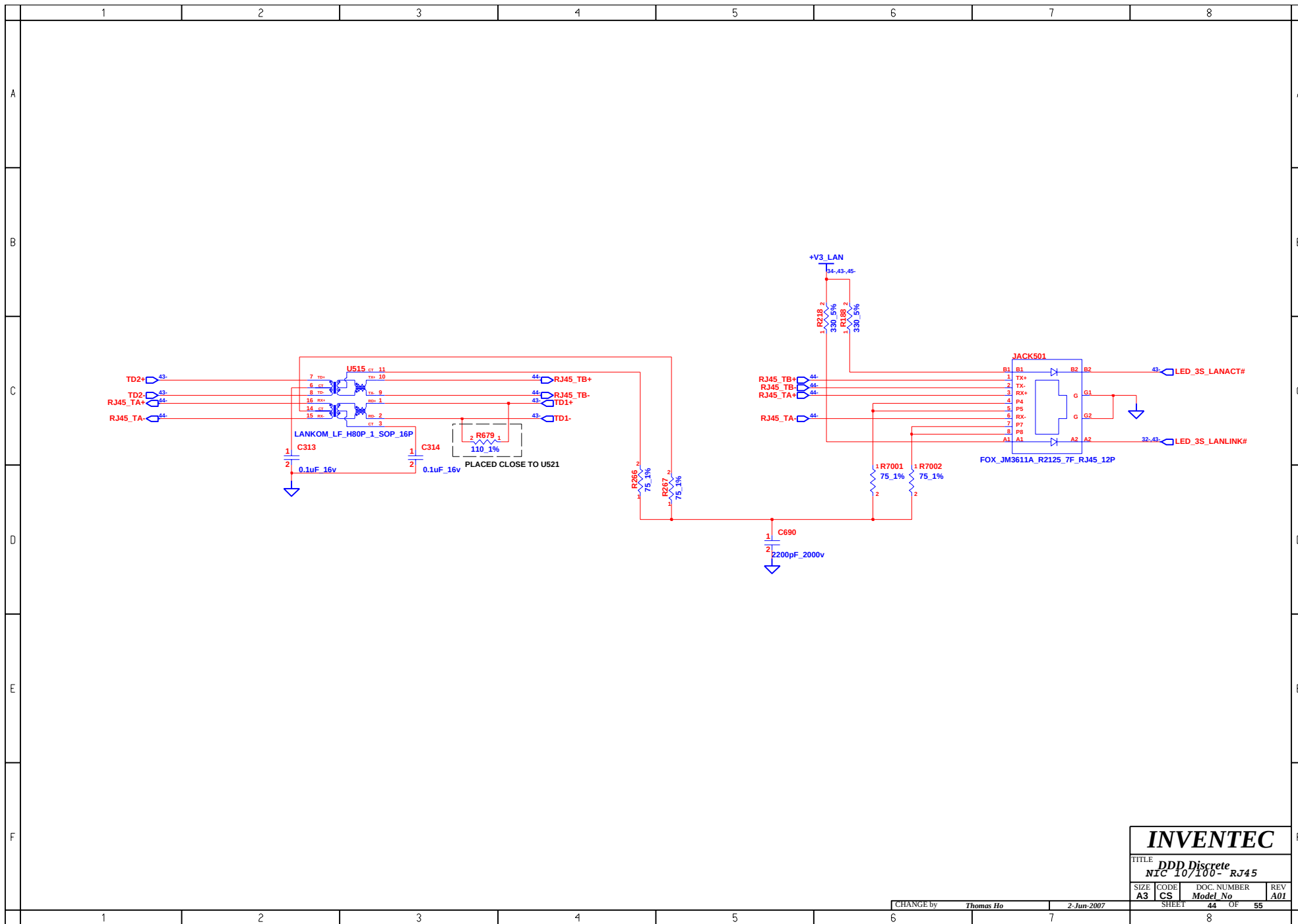
INVENTEC

TITLE
DDD Discrete
MDC CNTR

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 4-Jun-2007

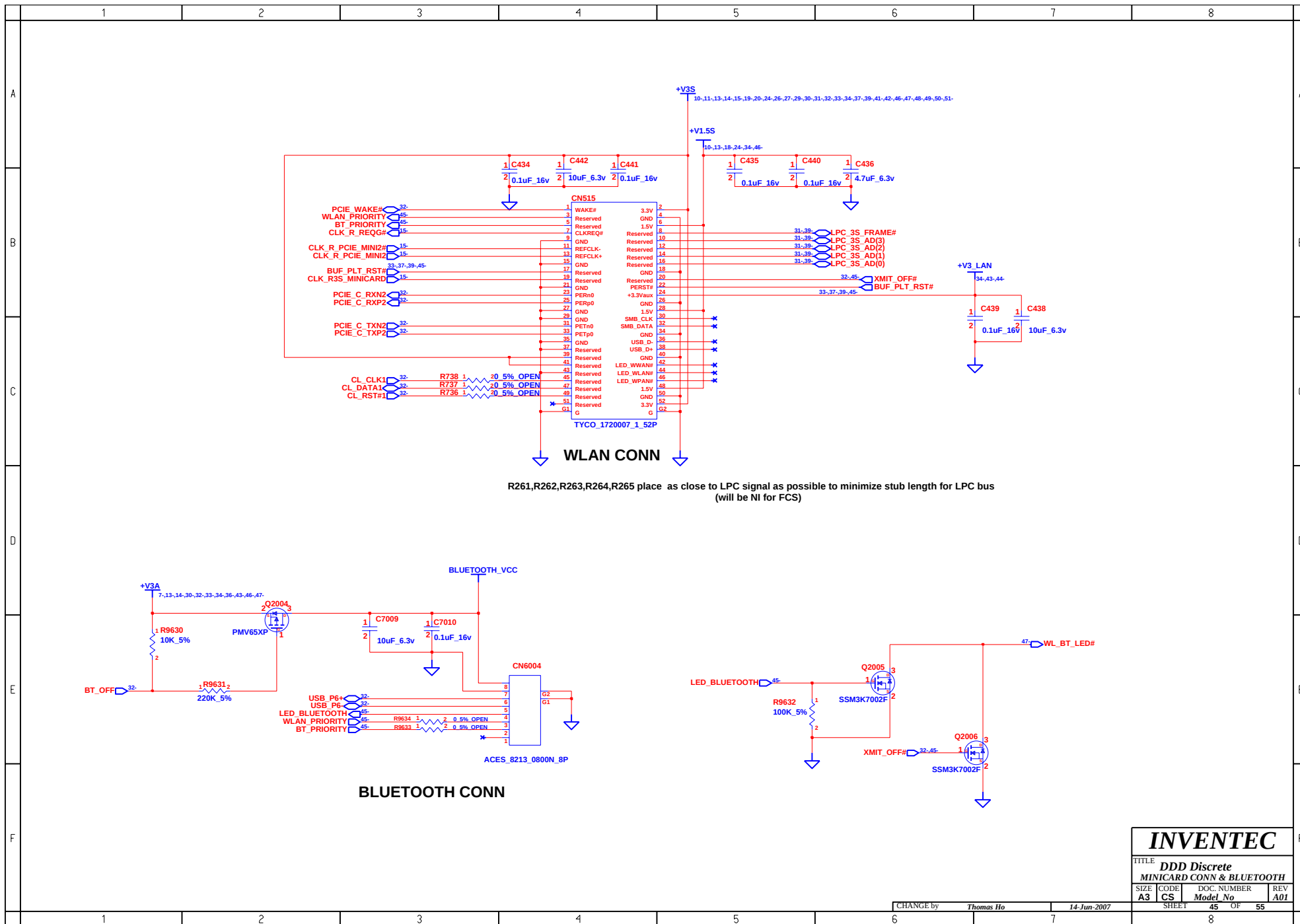
SHEET 42 OF 55

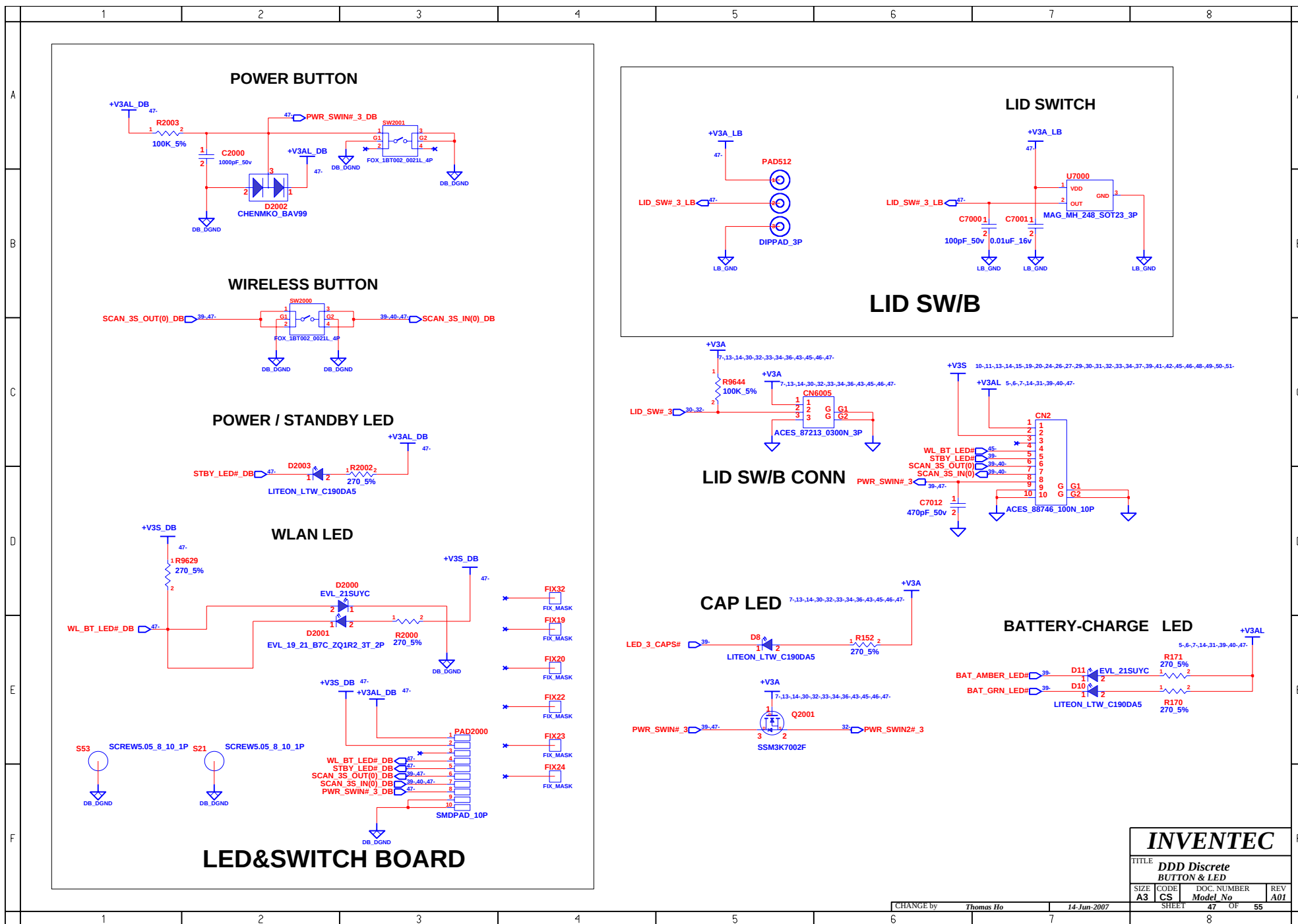


INVENTEC			
TITLE <i>DDP Discrete</i>			
NIC 10/100 - RJ45			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01
SHEET 44		OF 55	

CHANGE by Thomas Ho 2-Jun-2007

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INVENTEC

TITLE			
DDD Discrete			
BUTTON & LED			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 14-Jun-2007

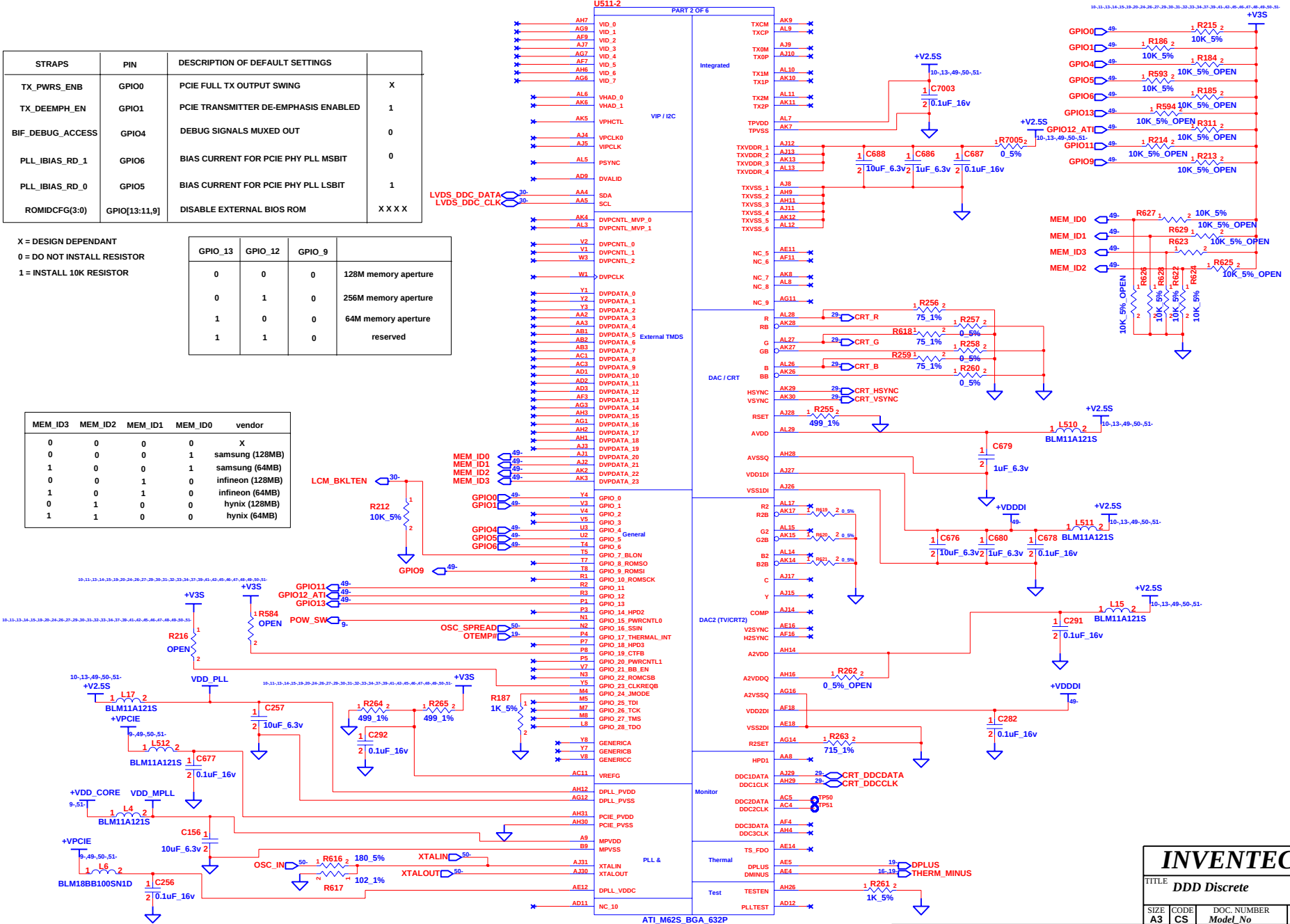
SHEET 47 OF 55

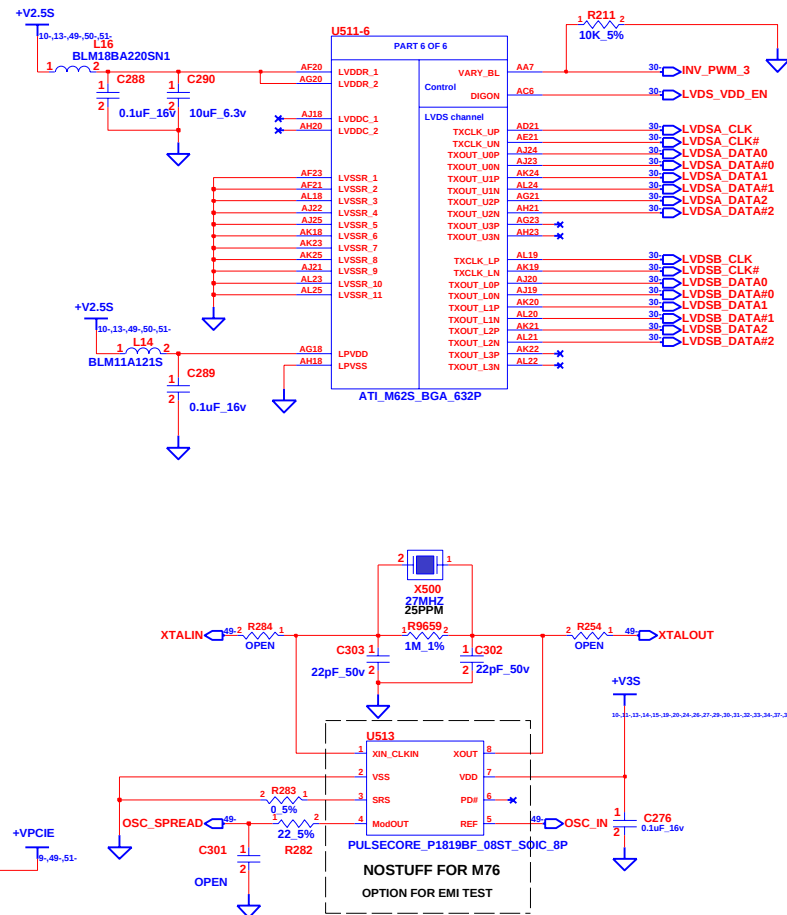
STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	X
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	1
BIF_DEBUG_ACCESS	GPIO4	DEBUG SIGNALS MUXED OUT	0
PLL_IBIAS_RD_1	GPIO6	BIAS CURRENT FOR PCIE PHY PLL MSBIT	0
PLL_IBIAS_RD_0	GPIO5	BIAS CURRENT FOR PCIE PHY PLL LSBIT	1
ROMIDCFG(3:0)	GPIO[13:11,9]	DISABLE EXTERNAL BIOS ROM	X X X X

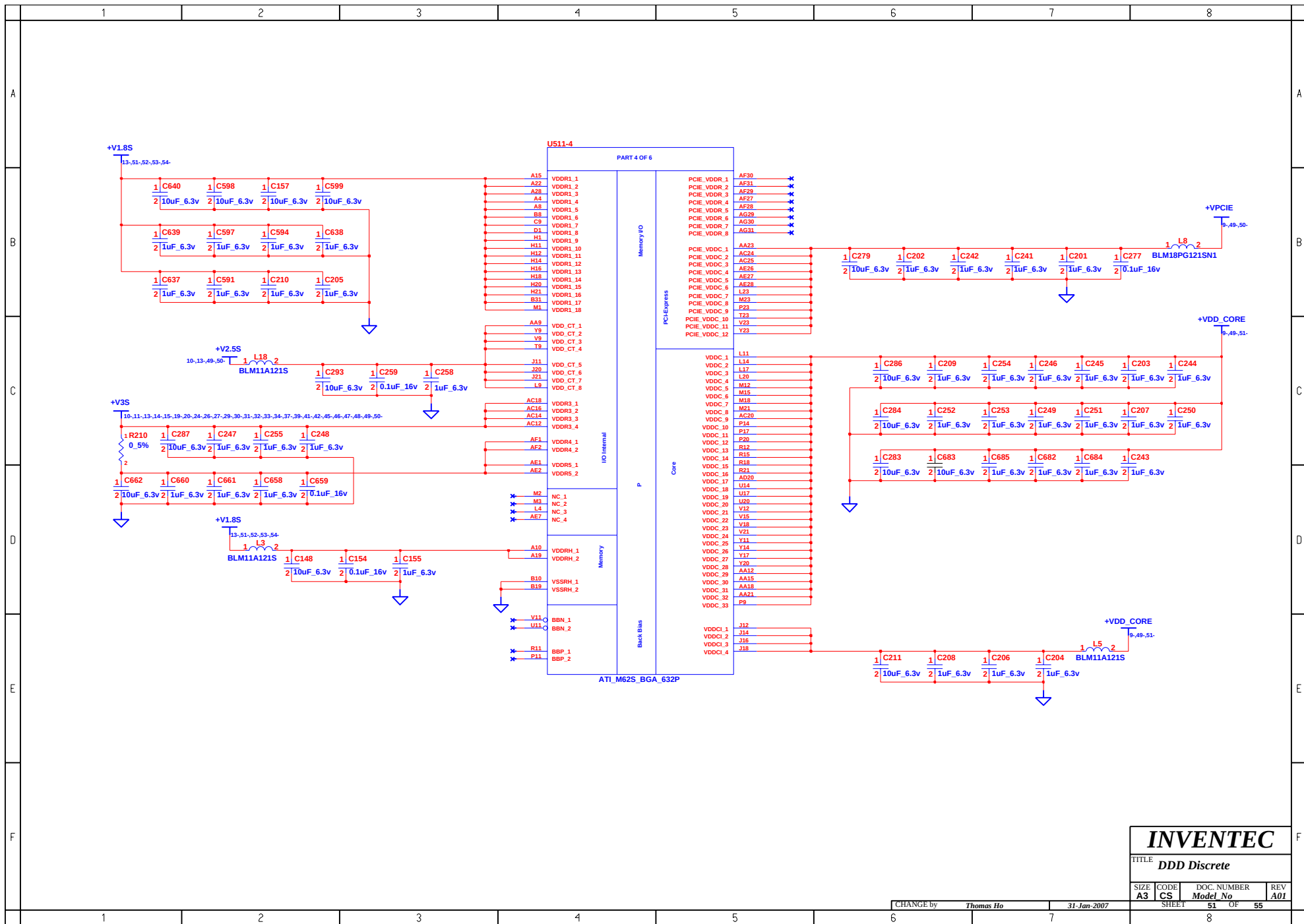
X = DESIGN DEPENDANT
0 = DO NOT INSTALL RESISTOR
1 = INSTALL 10K RESISTOR

GPIO_13	GPIO_12	GPIO_9	
0	0	0	128M memory aperture
0	1	0	256M memory aperture
1	0	0	64M memory aperture
1	1	0	reserved

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	vendor
0	0	0	0	X
0	0	0	1	samsung (128MB)
1	0	0	1	samsung (64MB)
0	0	1	0	infineon (128MB)
1	0	1	0	infineon (64MB)
0	1	0	0	hynix (128MB)
1	1	0	0	hynix (64MB)







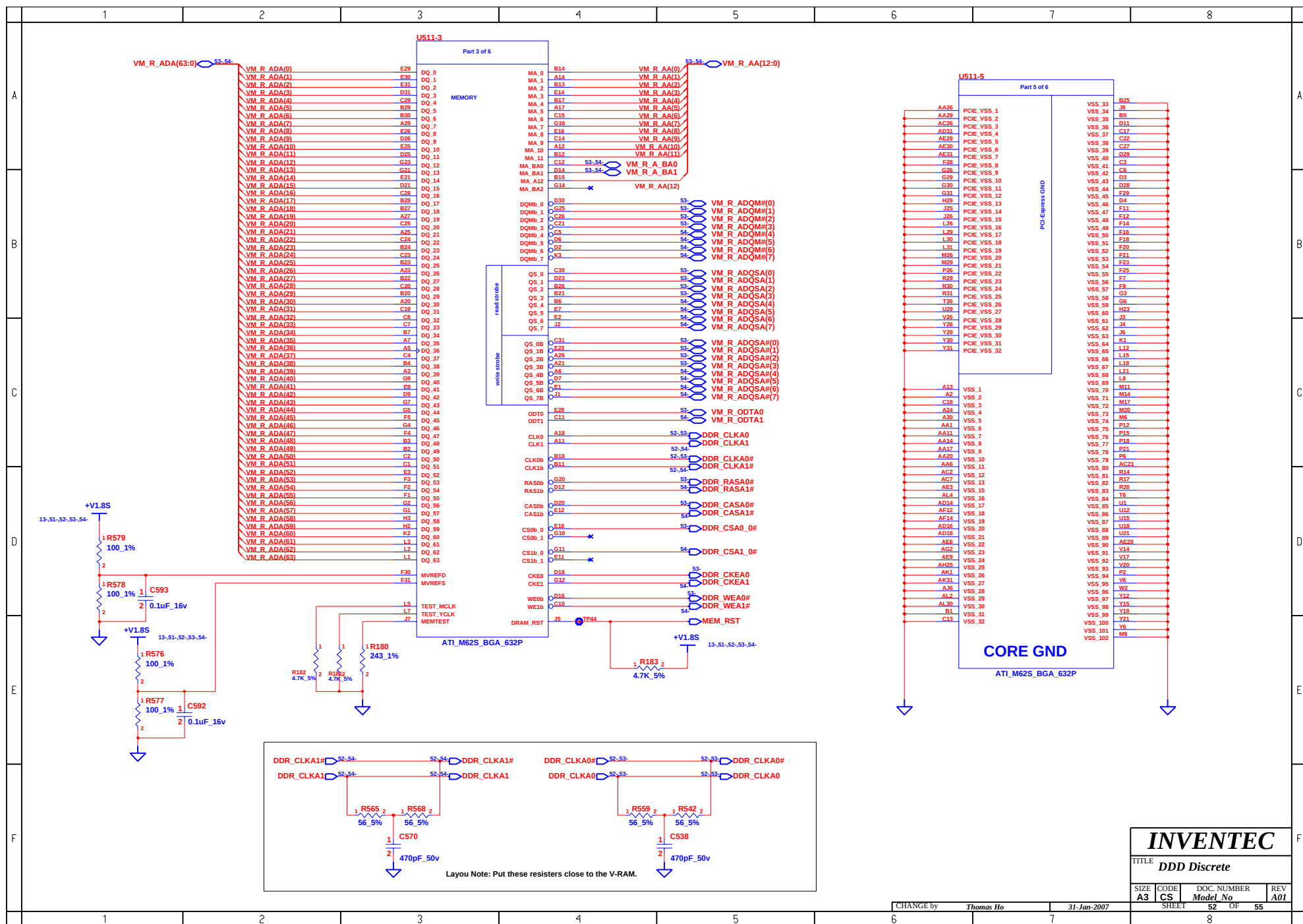
INVENTEC

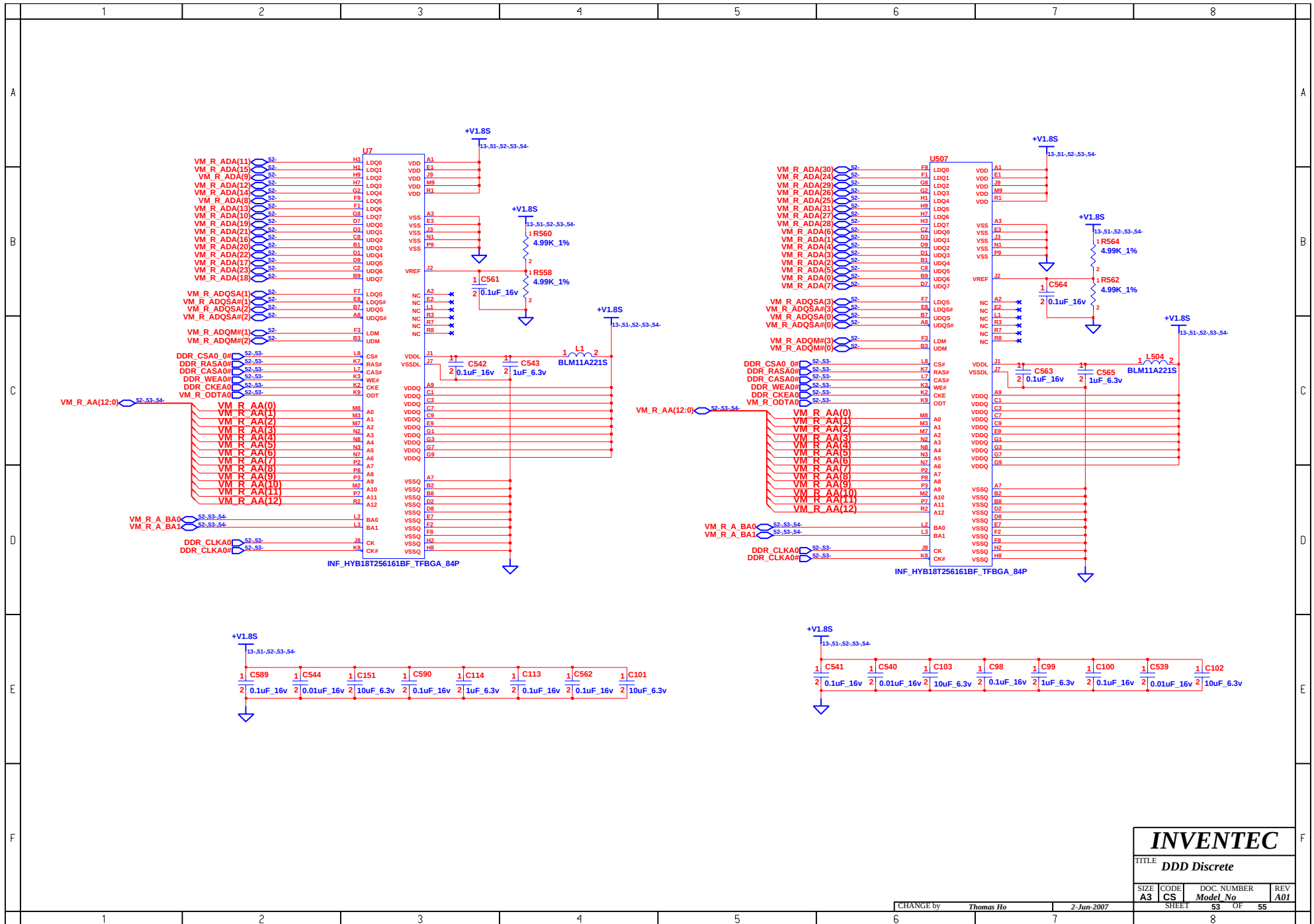
TITLE
DDD Discrete

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 31-Jan-2007

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INVENTEC

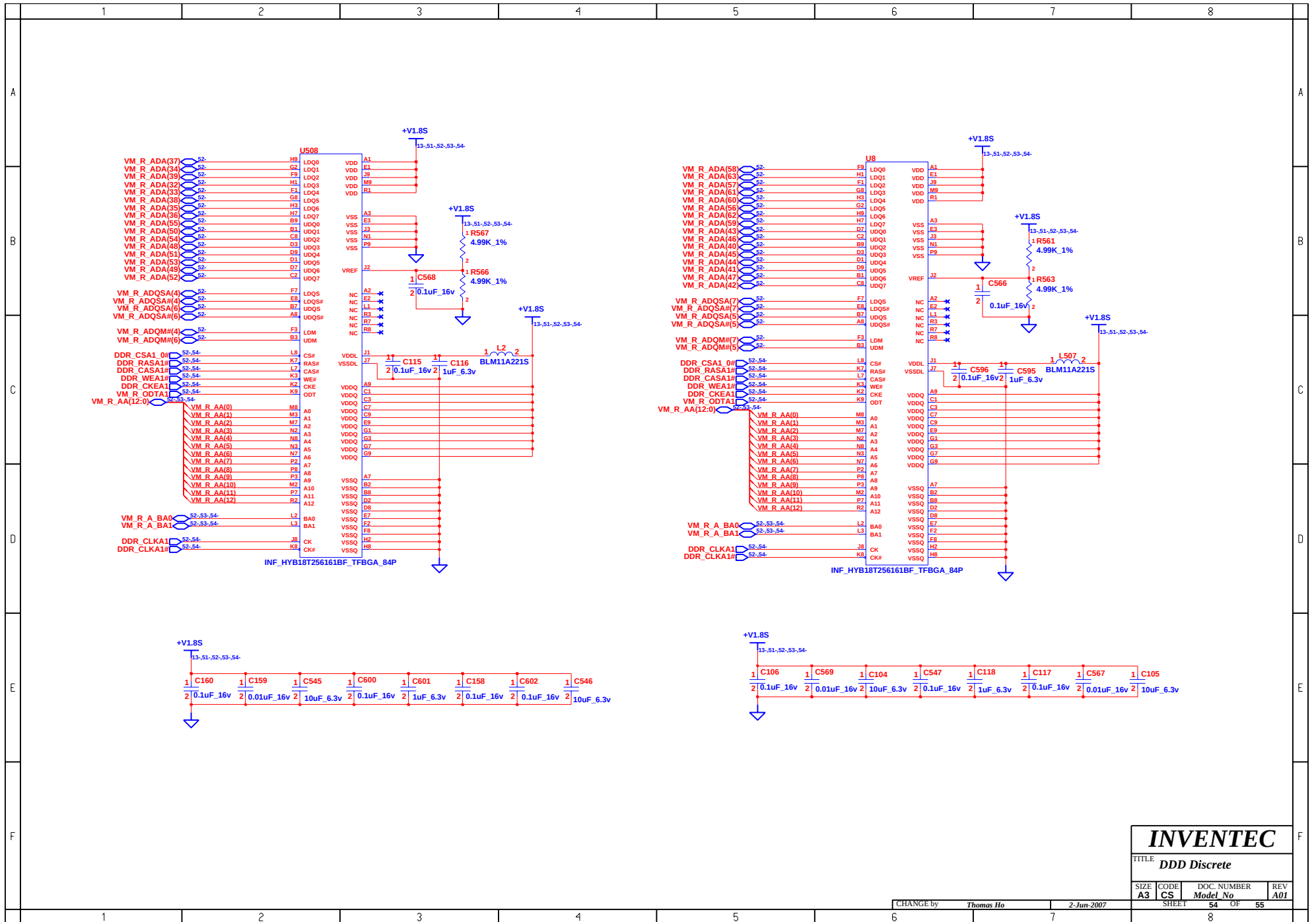
TITLE
DDD Discrete

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho

2-Jun-2007

SHEET 53 OF 55



INVENTEC

TITLE
DDD Discrete

SIZE	CODE	DOC. NUMBER	REV
A3	CS	Model No	A01

CHANGE by Thomas Ho 2-Jun-2007

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