

HSF Property:ROHS or Halogen-Free

2012.03.23

		EE	DATE	POWER	DATE	INVENTEC	
DRAWER		LOCKEN	2012.03.23	ENGIN	2012.03.23		
CHECKIN		LOCKEN	2012.03.23	ENGIN	2012.03.23		
RECALL		RECALL	2012.03.23	BY	2012.03.23		
RESPONSIBLE		RECALL	2012.03.23	BY	2012.03.23		
TITLE PROJECT/PROJECT FUNCTION DAKAR10F/IG MAIN BOARD							
SIZE=		VER:		SIZE	CODE	DOC NUMBER	REV
FILE NAME:		R/N		CS	CS	1310A049001-Q-MTR	X02
✓		✓		✓	✓	✓	✓

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D

C

B

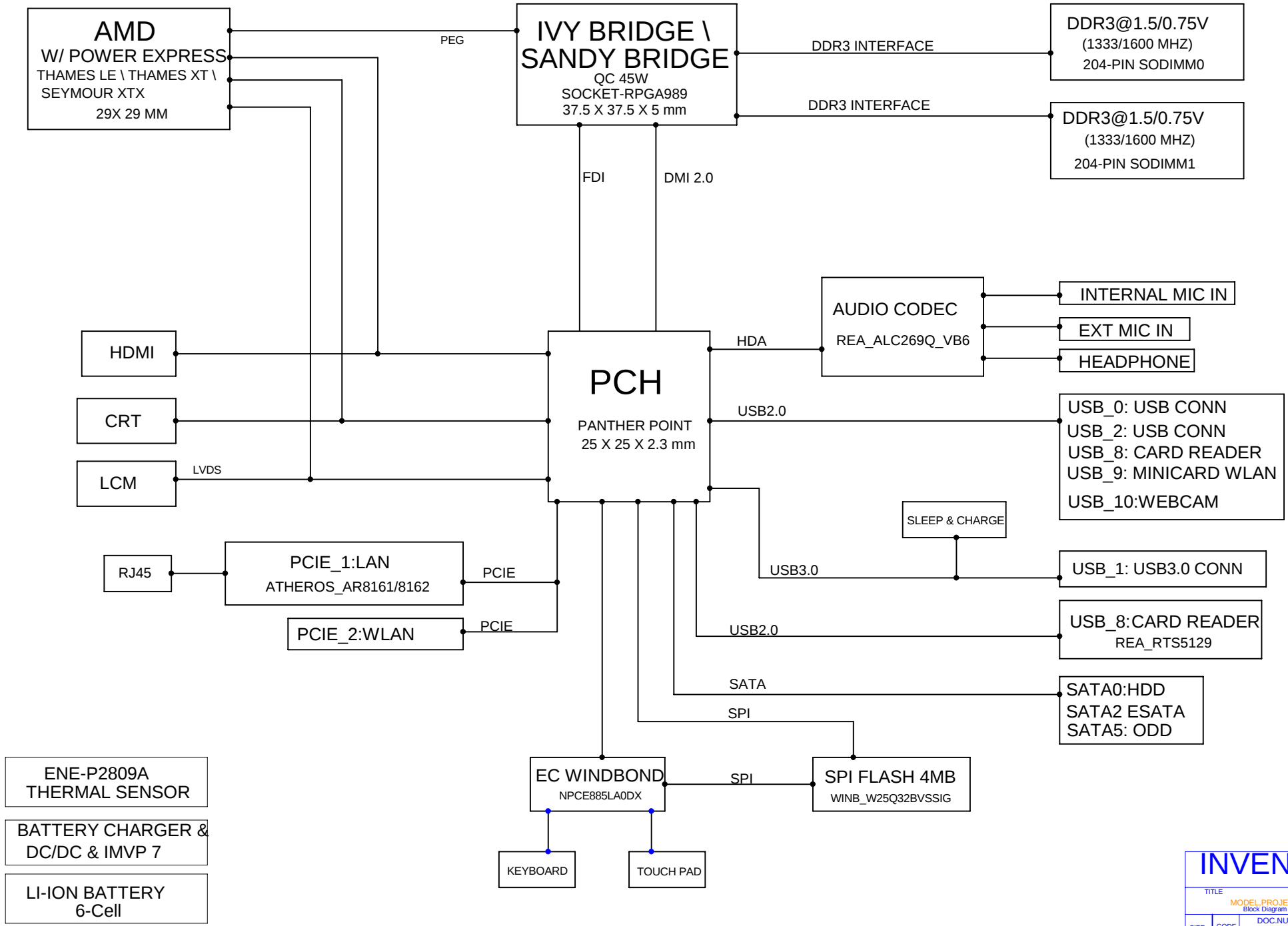
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D

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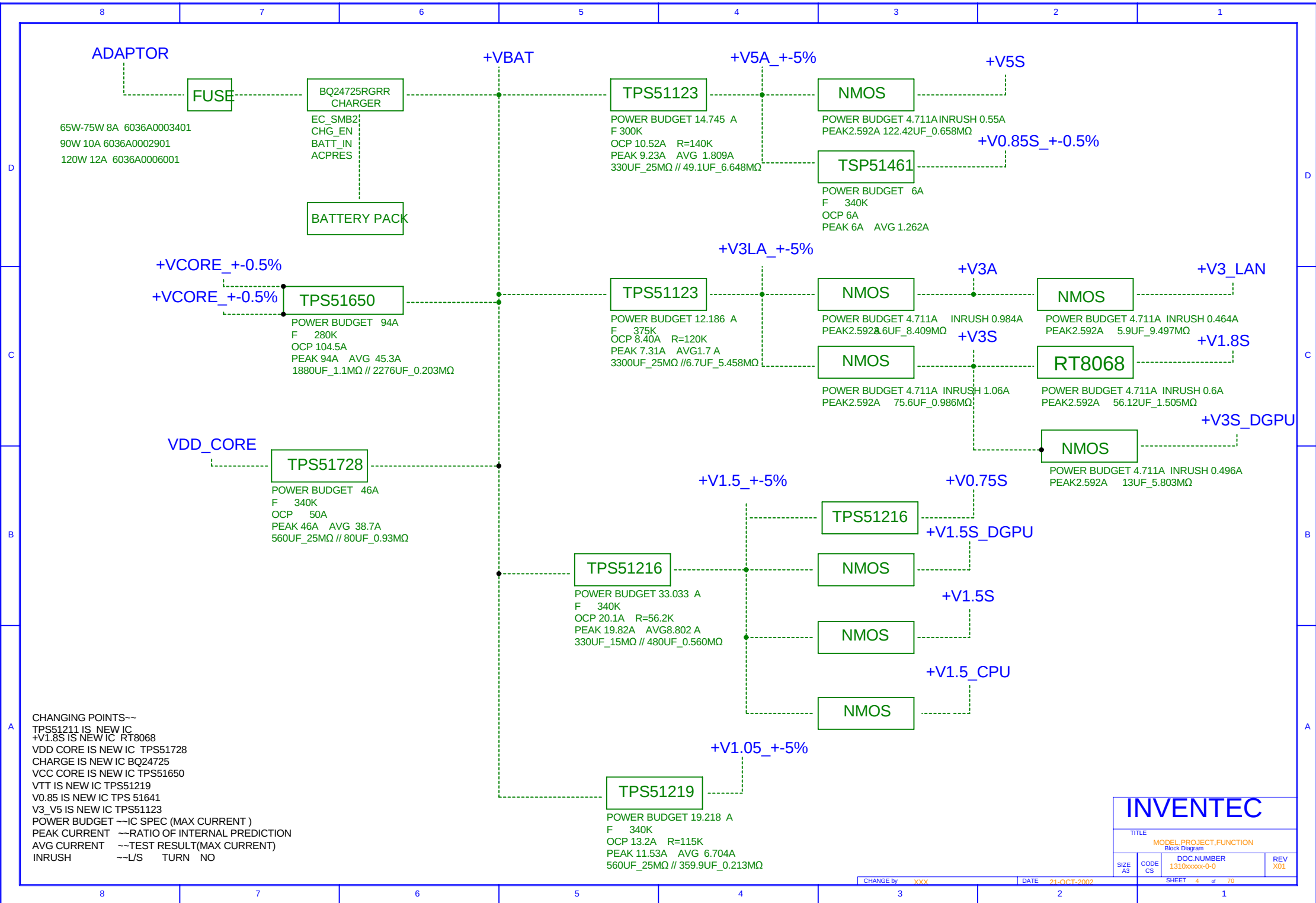
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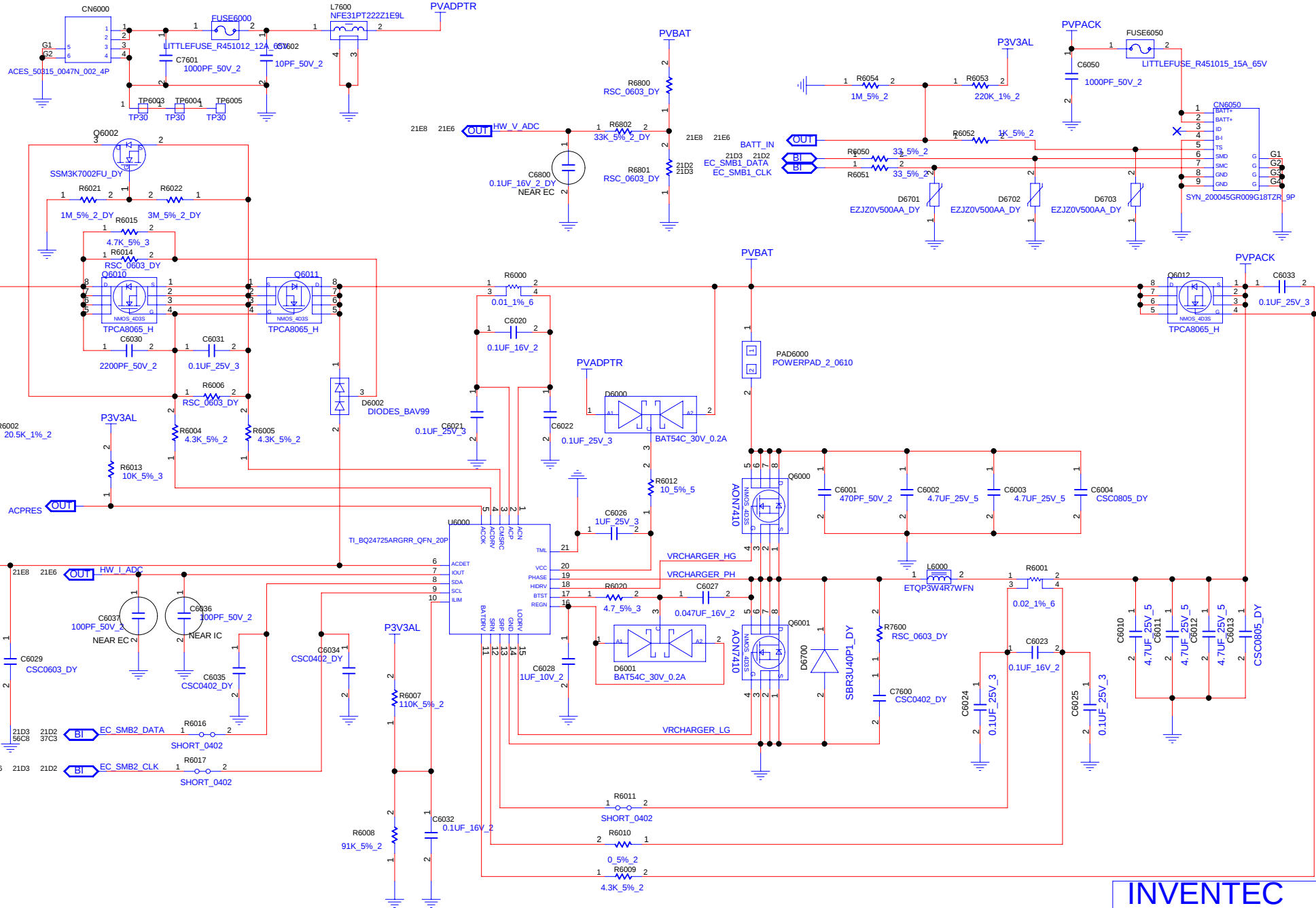


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FUSE6000
65W-75W 8A(6036A0003401)
90W 10A(6036A0002901)
120W 12A(6036A0006001)



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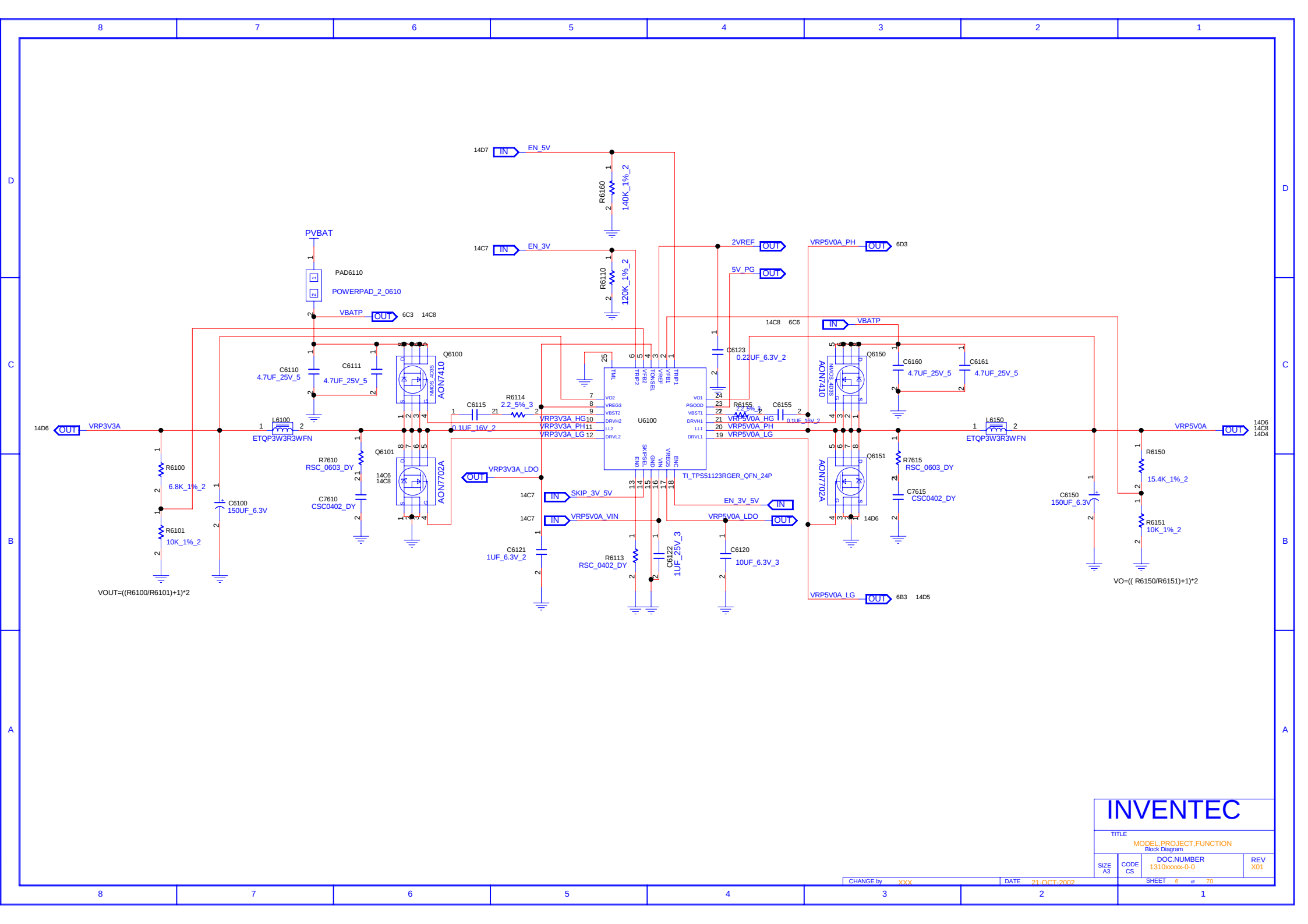
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TITLE

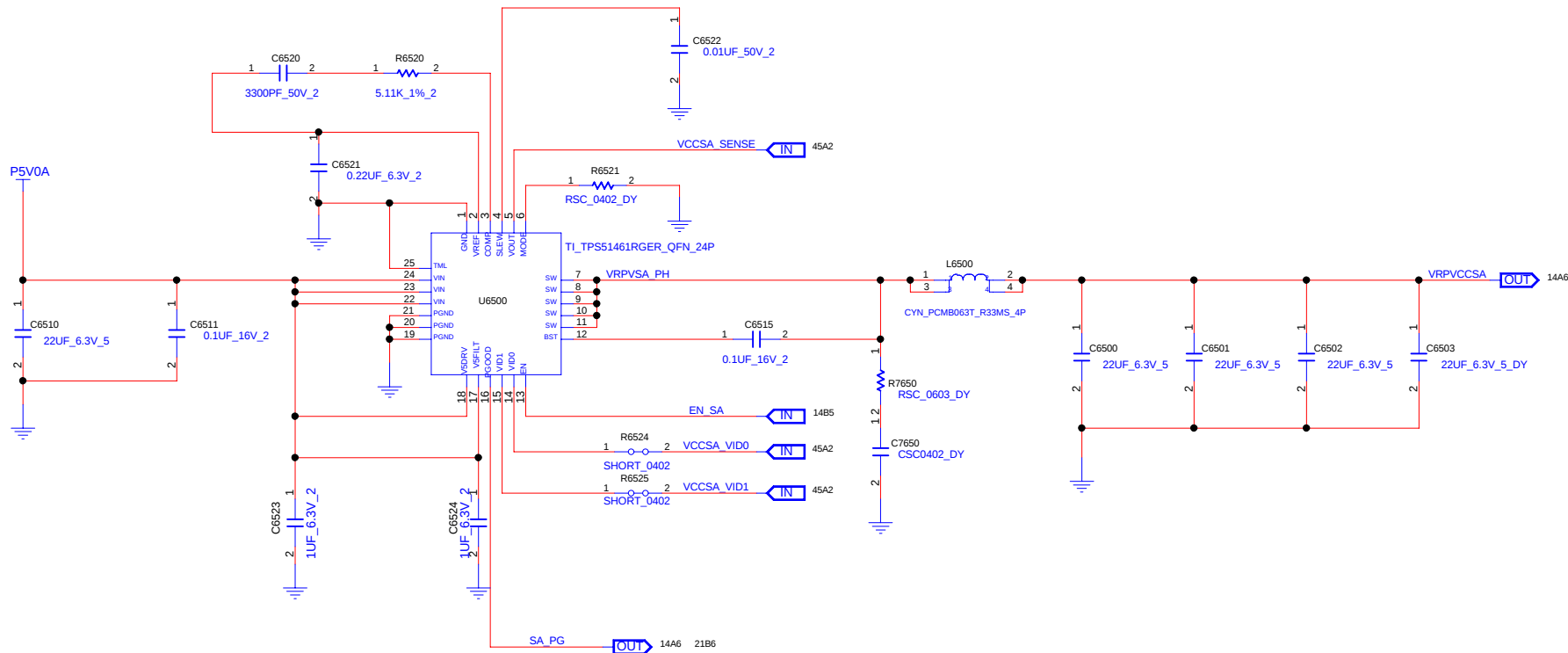
MODEL PROJECT FUNCTION

Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

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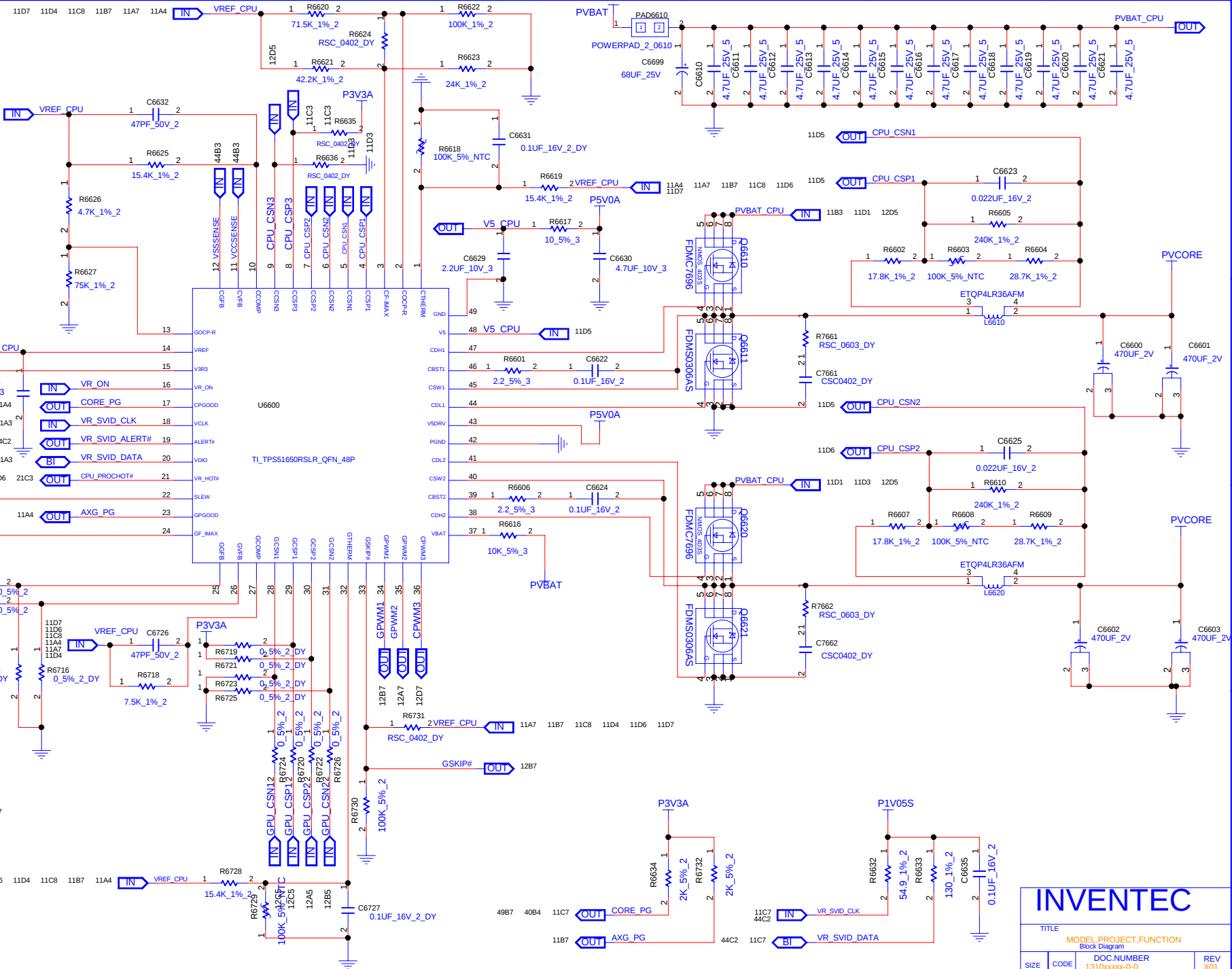
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UMA/PX DIS.	3+2	3+0	2+1	2+0
R6620	71.5K	71.5K	44.2K	44.2K
R6621	42.2K	42.2K	90.9K	90.9K
R6622	100K	100K	39K	39K
R6626	4.7K	DNP	3.3K	DNP
R6627	75K	DNP	56K	DNP
R6711	100K	DNP	200K	DNP
R6712	24K	DNP	30K	DNP
R6719	DNP	0	DNP	0
R6723	DNP	0	DNP	0
R6635	DNP	DNP	0	0
R6636	DNP	DNP	0	0
R6714	DNP	0	DNP	0
R6716	DNP	0	DNP	0
R6721	DNP	0	0	0
R6725	DNP	0	0	0
R6726	0	DNP	DNP	DNP
R6724	0	DNP	0	DNP
R6722	0	DNP	DNP	DNP
R6720	0	DNP	0	DNP
R6713	0	DNP	0	DNP
R6715	0	DNP	0	DNP



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X01

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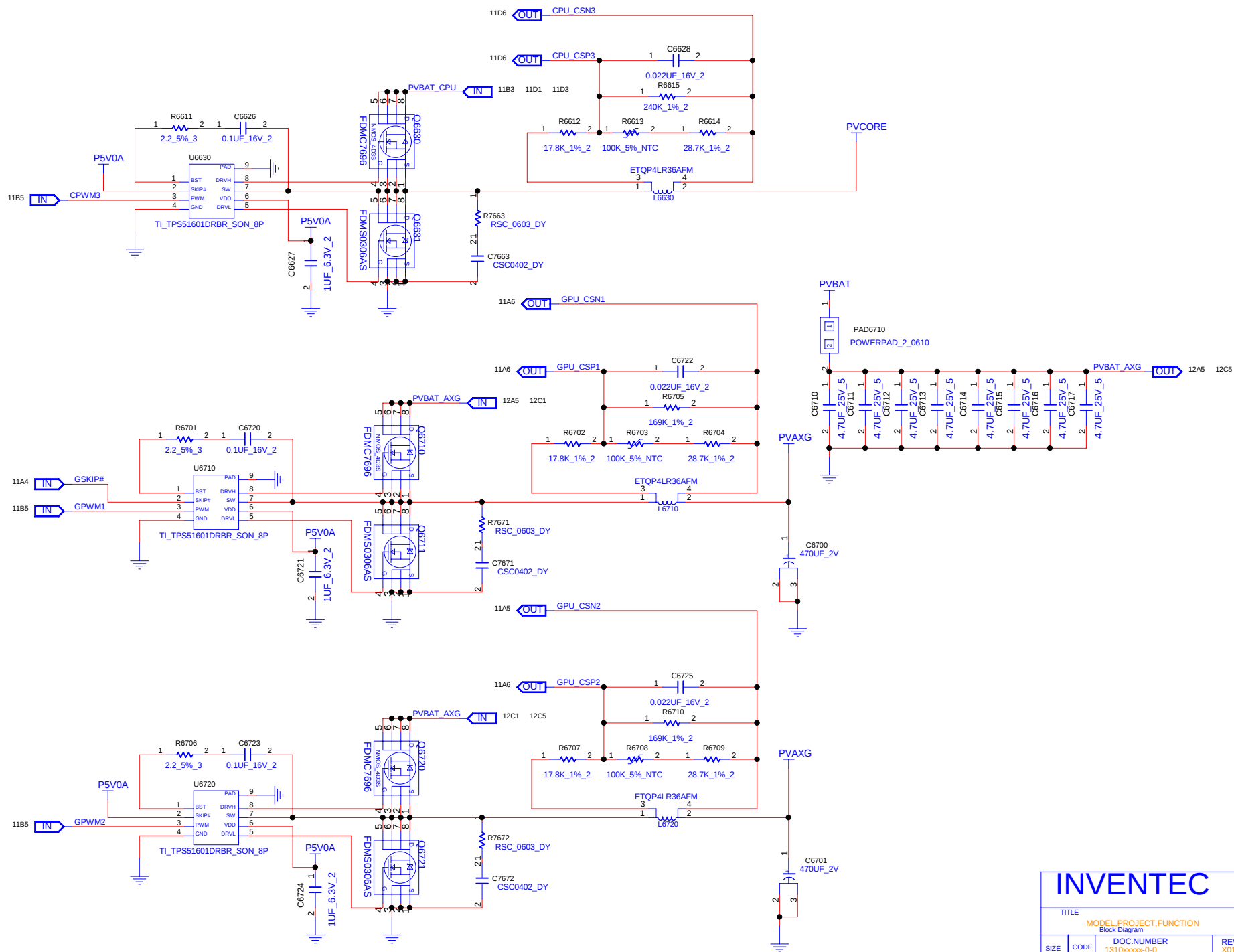
21-OCT-2002

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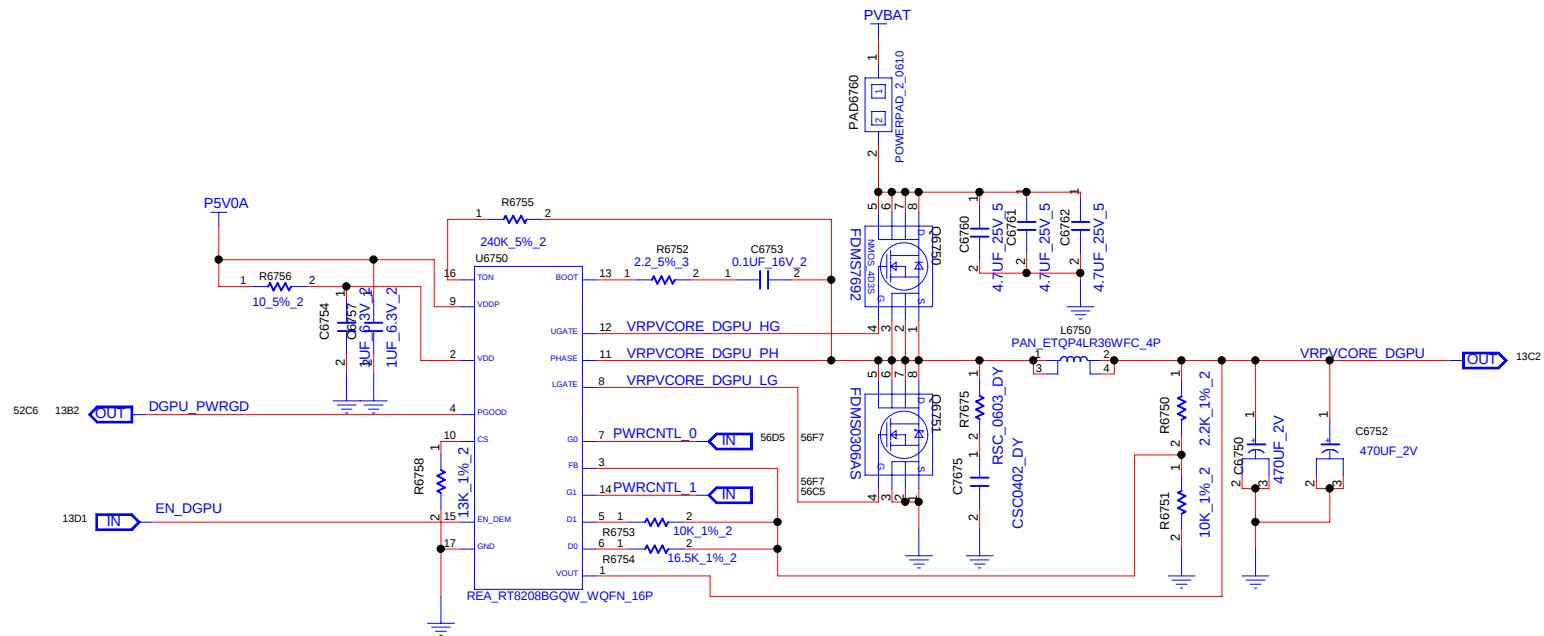
11

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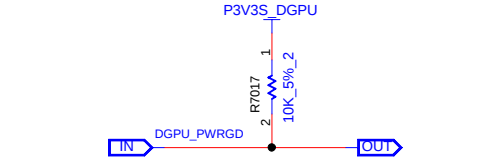
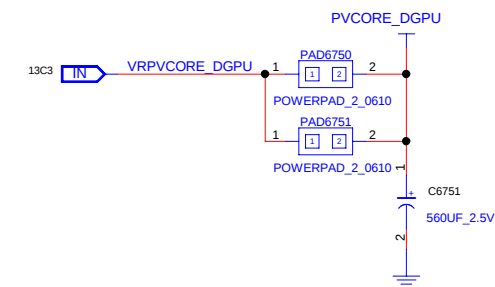
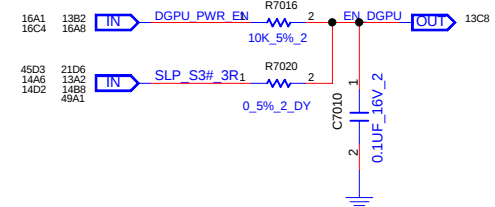
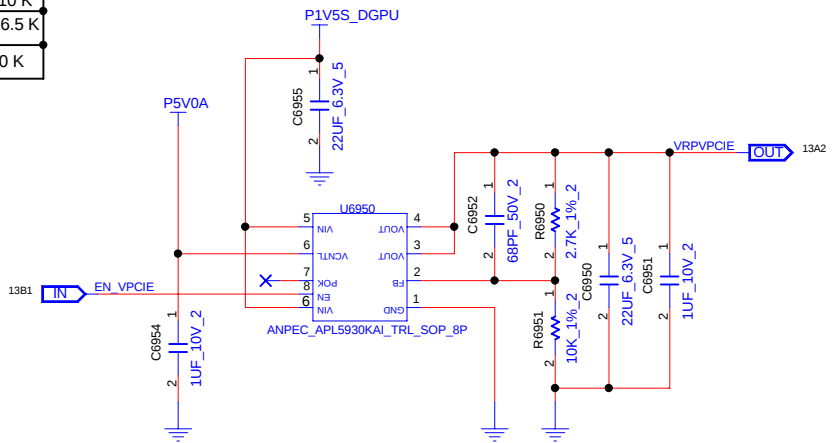
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VGA TYPE :	SEYMOUR GTX	THAMES XT	THAMES LE	
G0	G1	VOUT	VOUT	VOUT
0	0	1.15	N/A	N/A
0	1	1.05	N/A	N/A
1	0	1	1	1
1	1	0.9	0.9	0.9

P.S. R6750(R1)R6751(R2)R6753(R3)R6754(R4)

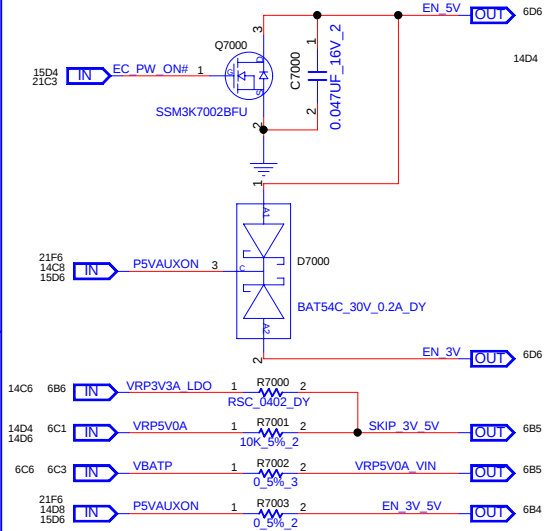
RT8208A	RT8208B	Output Voltage Equation	
G0	G1	G0	G1
0	0	1	1
1	0	0	1
0	1	1	0
1	1	0	0



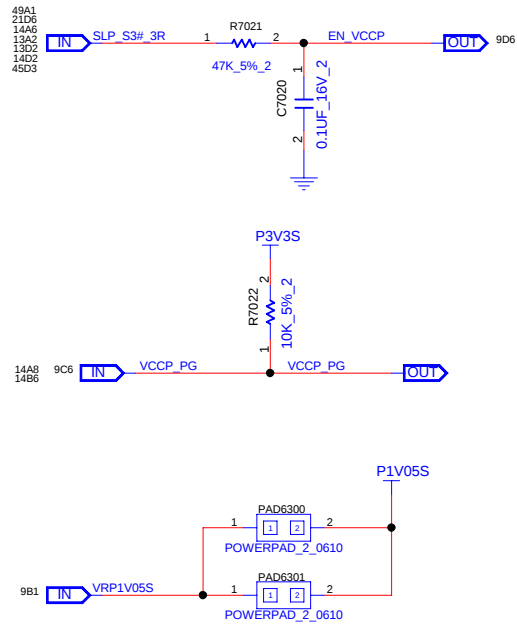
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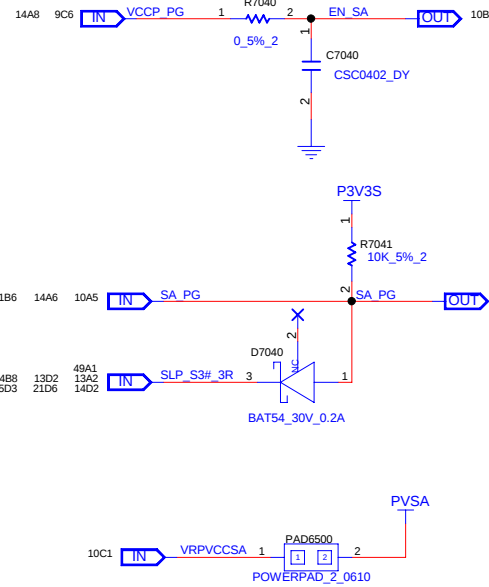
3V & 5V



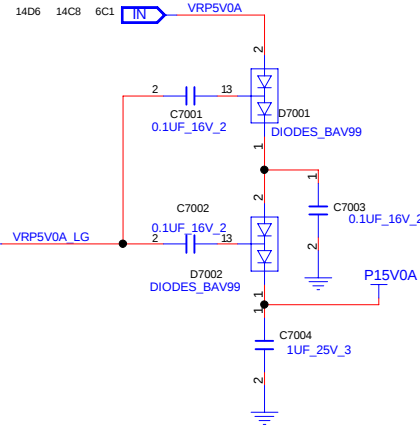
VCCIO



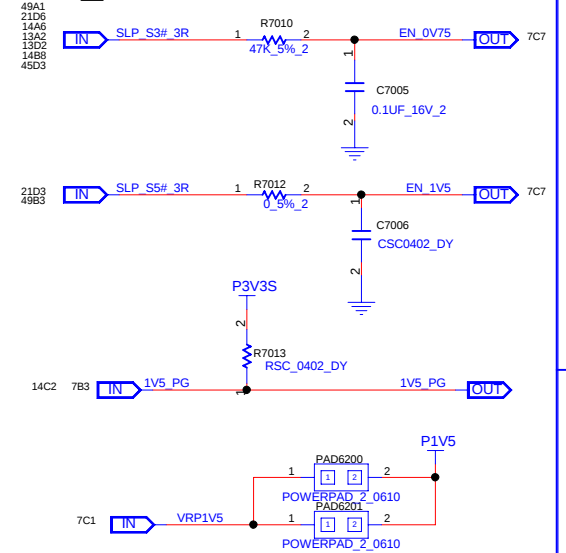
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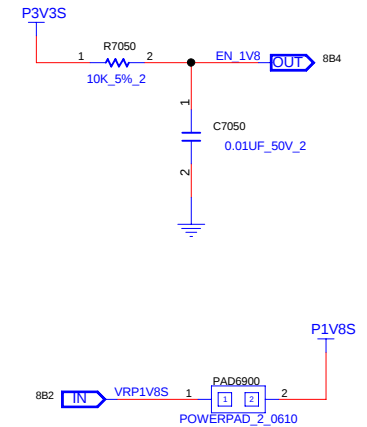
DGPU



DDR_P1V5

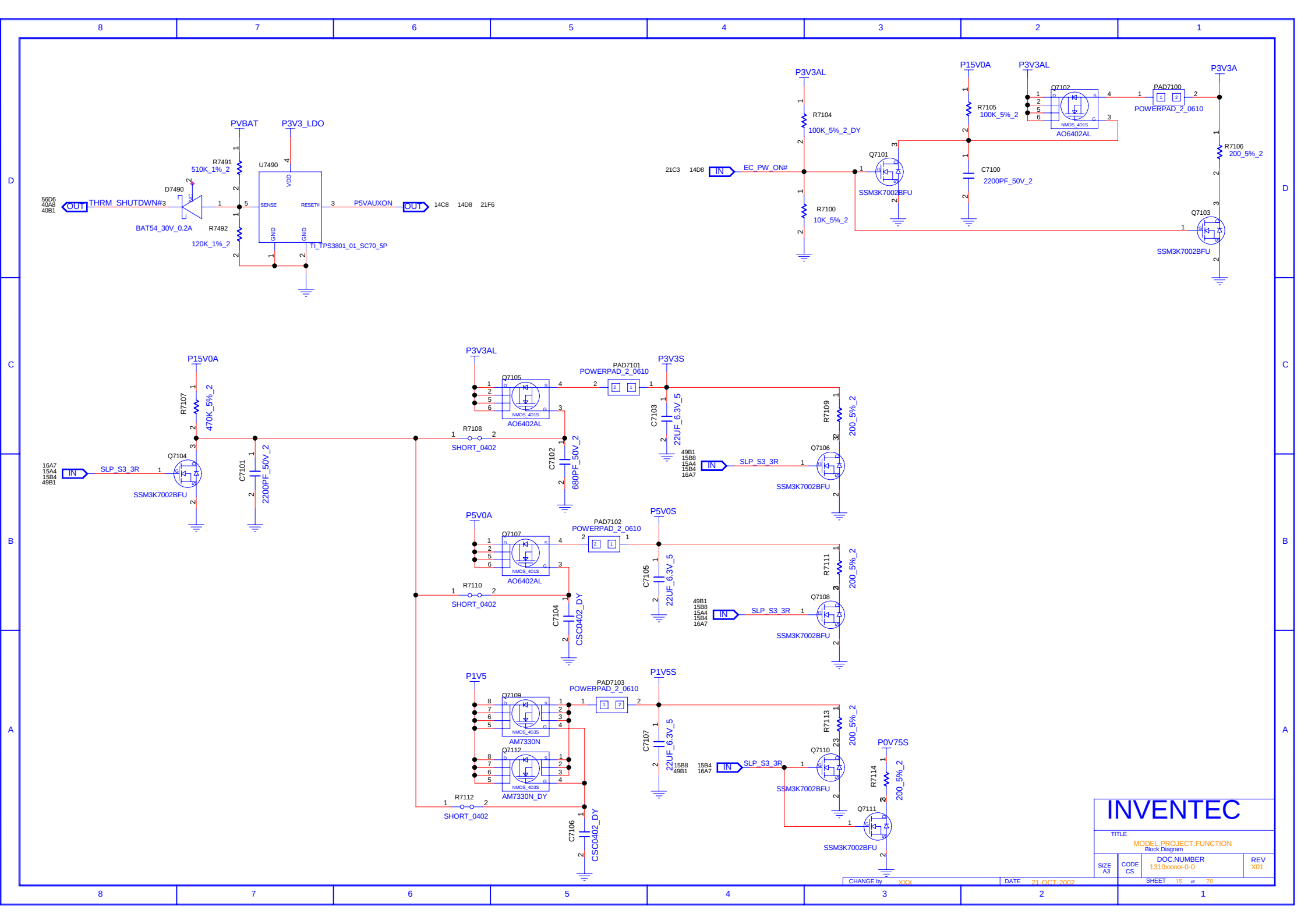


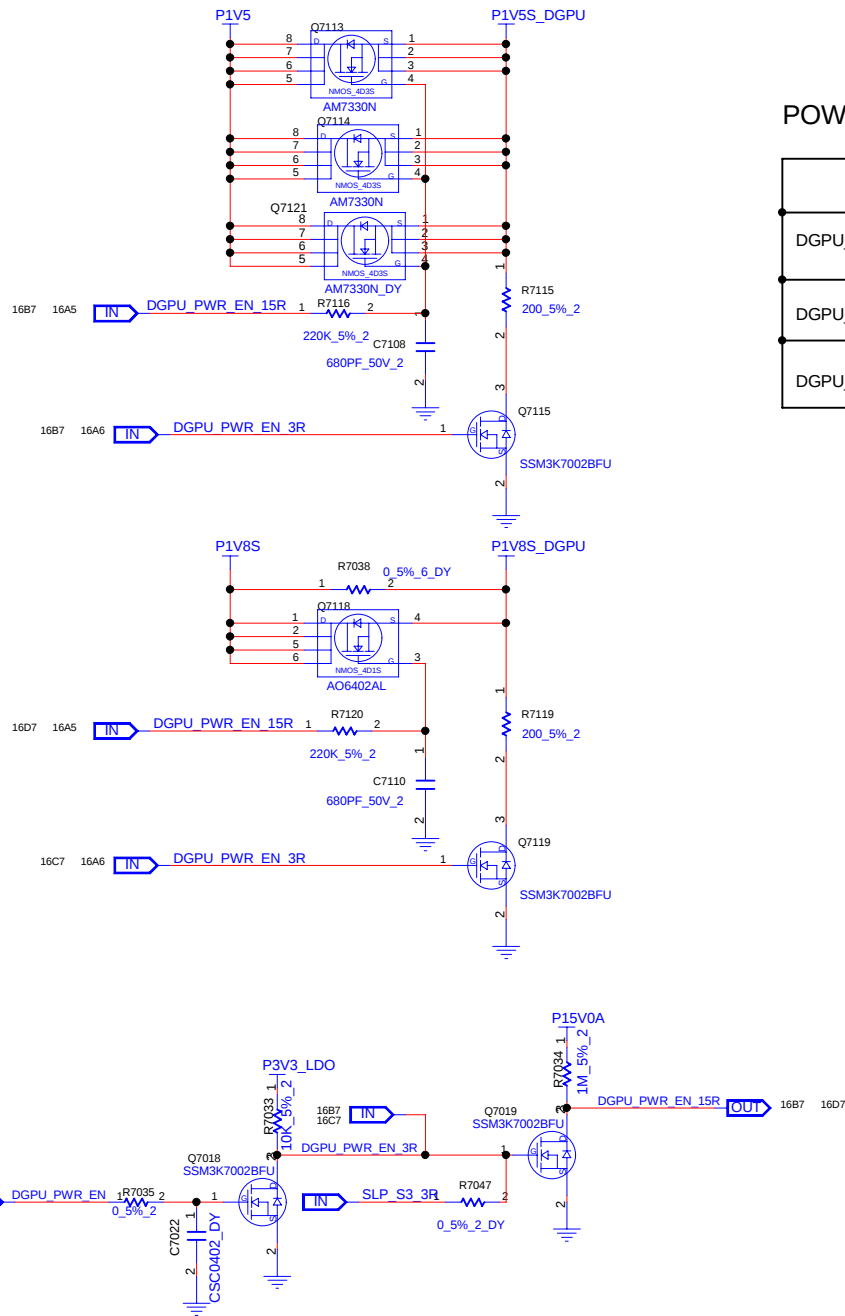
P1V8S



INVENTEC

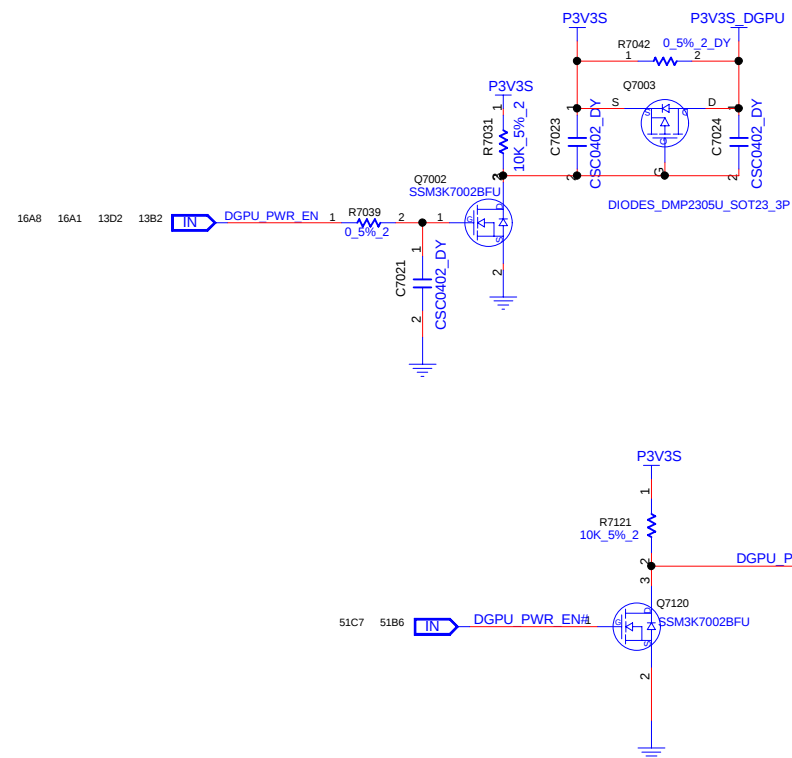
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POWER EXPRESS

	DURING RESET	AFTER RESET	
DGPU_PWR_EN#	HIGH	HIGH	0 : DGPU POWER SWITCH TURNED ON 1 : POWER SWITCH TURNED OFF
DGPU_PWRGD			0 : DGPU POWER IS NOT STABLE 1 : DGPU POWER IS STABLE
DGPU_HOLD_RST#	LOW	LOW	0 : KEEP DGPU IN RESET 1 : RESET IS RELEASED



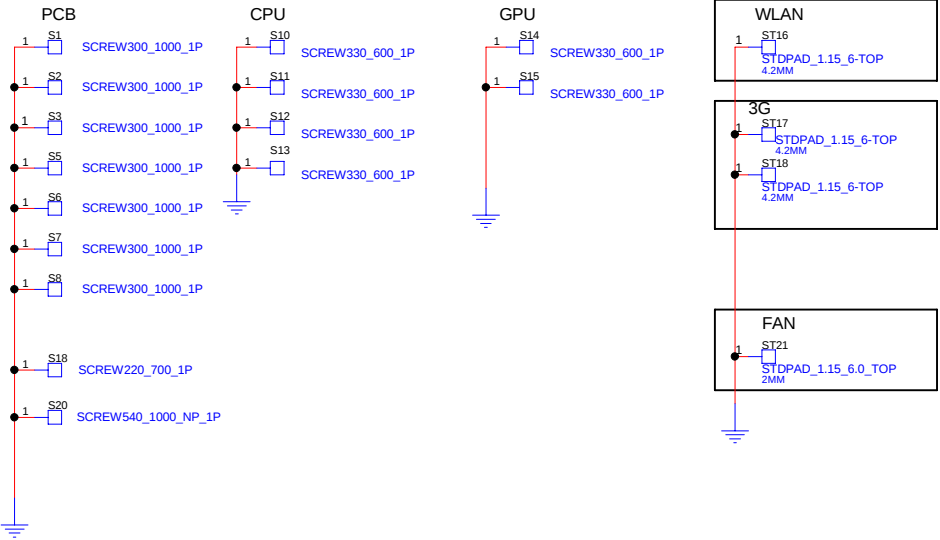
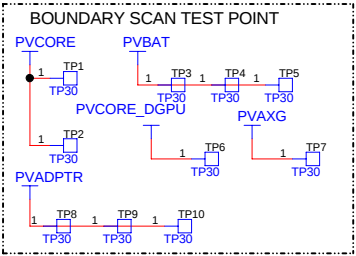
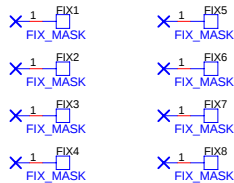
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Block Diagram			
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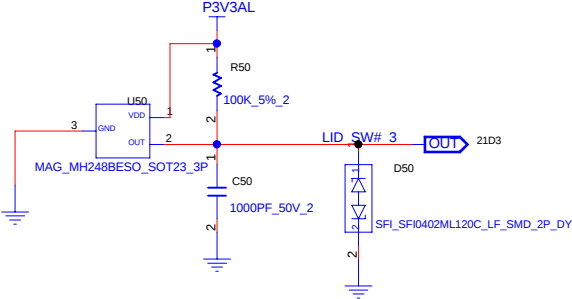
REFERENCE 0~49(PCB SCREW)



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MODEL,PROJECT,FUNCTION Block Diagram			
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REFERENCE 50-99(HALL SENSOR)



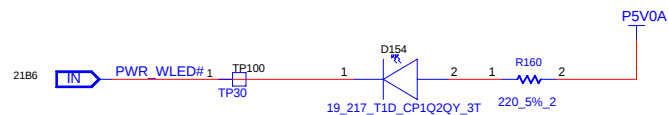
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MODEL,PROJECT,FUNCTION			
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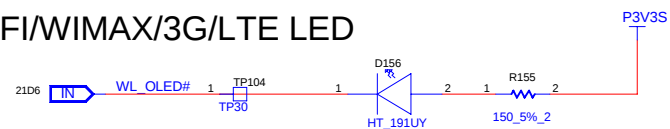
REFERENCE 100~199(LED)

POWER ON LED

D154 BRIGHT WHEN SYSTEM IS POWER ON
D154 BLINK WHEN SYSTEM GO TO SLEEP

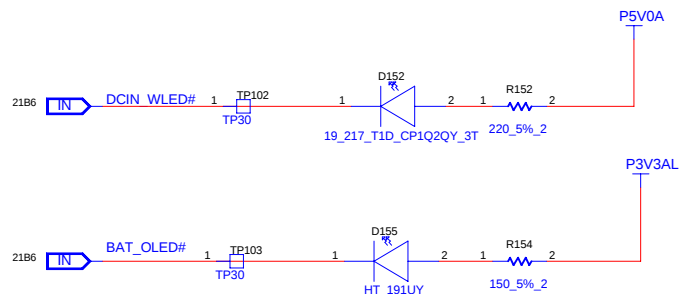


WIFI/WIMAX/3G/LTE LED



DC IN / BATTERY CHARGE LED

D152 BRIGHT: BOTH AC-ADAPTER IS PLUGGED IN AND BATTERY IS FULL CHARGED
D155 BRIGHT: WHILE CHARGING BATTERY FROM AC-ADAPTER
BLINK: LOW BATTERY



INVENTEC

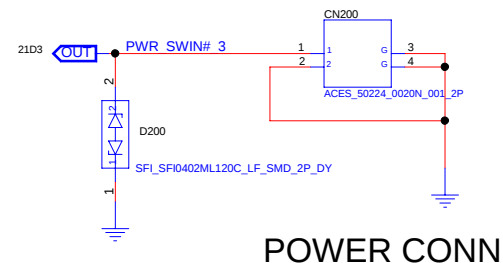
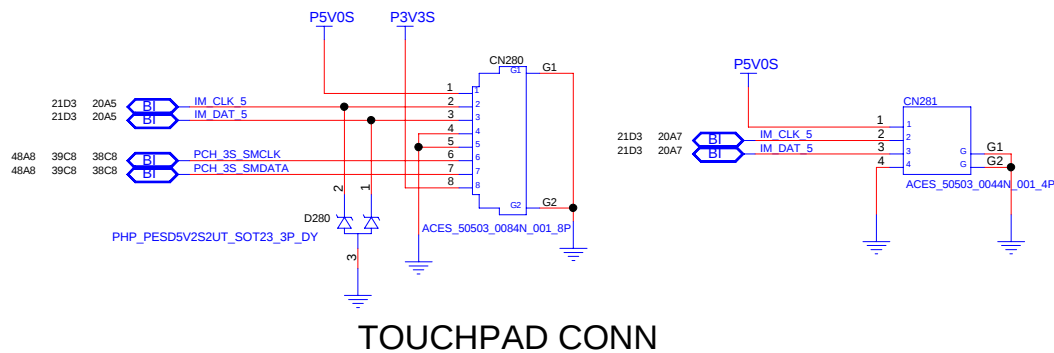
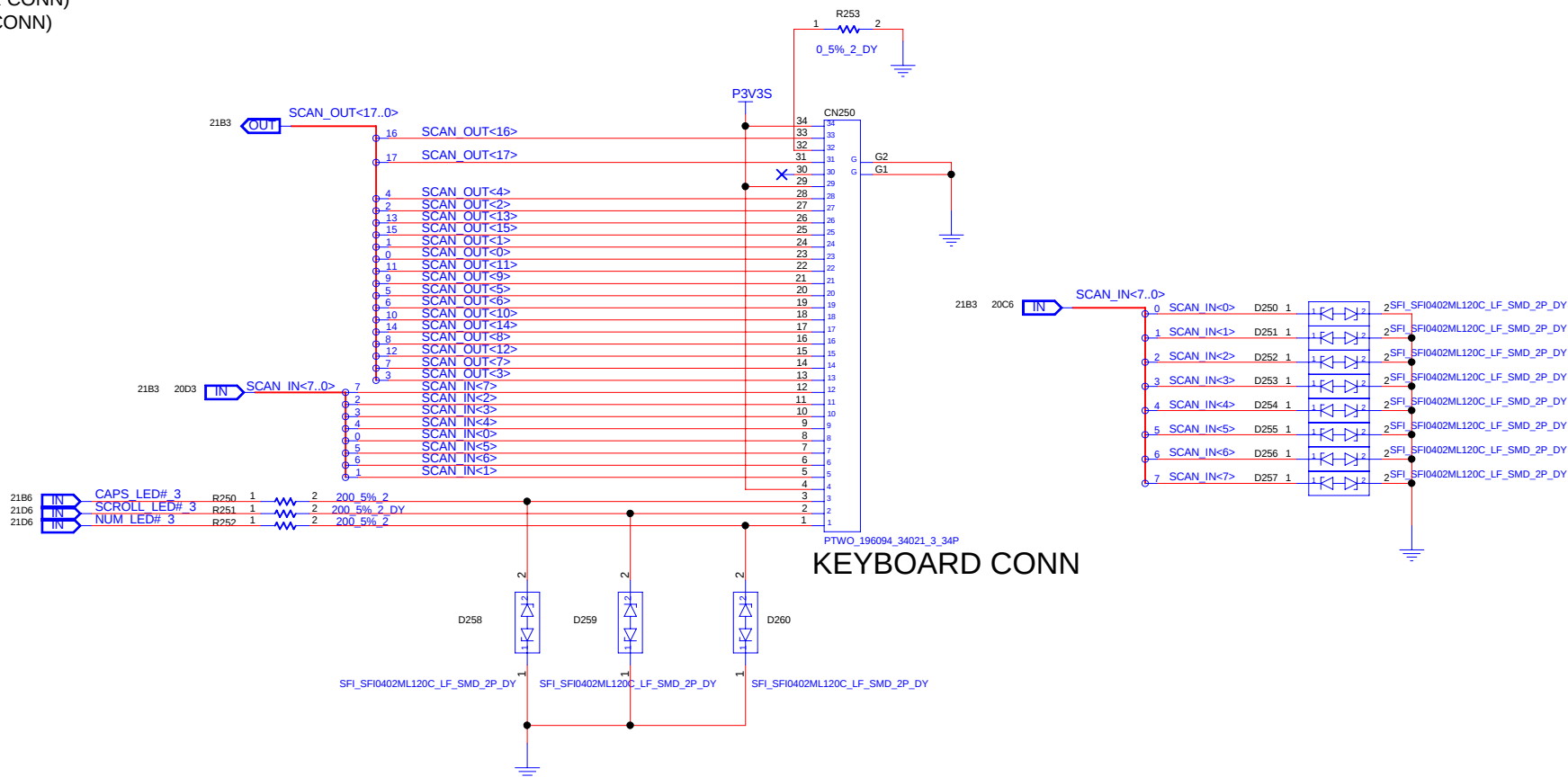
TITLE
MODEL PROJECT FUNCTION
Block Diagram

SIZE A3	CODE CS	DOC NUMBER 1310xxxx-0-0	REV X01
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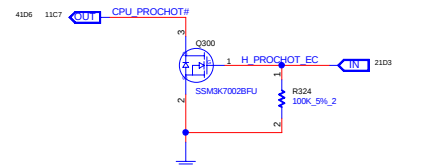
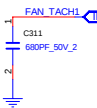
REFERENCE 200~249(POWER CONN)
REFERENCE 250~299(KB/TP CONN)

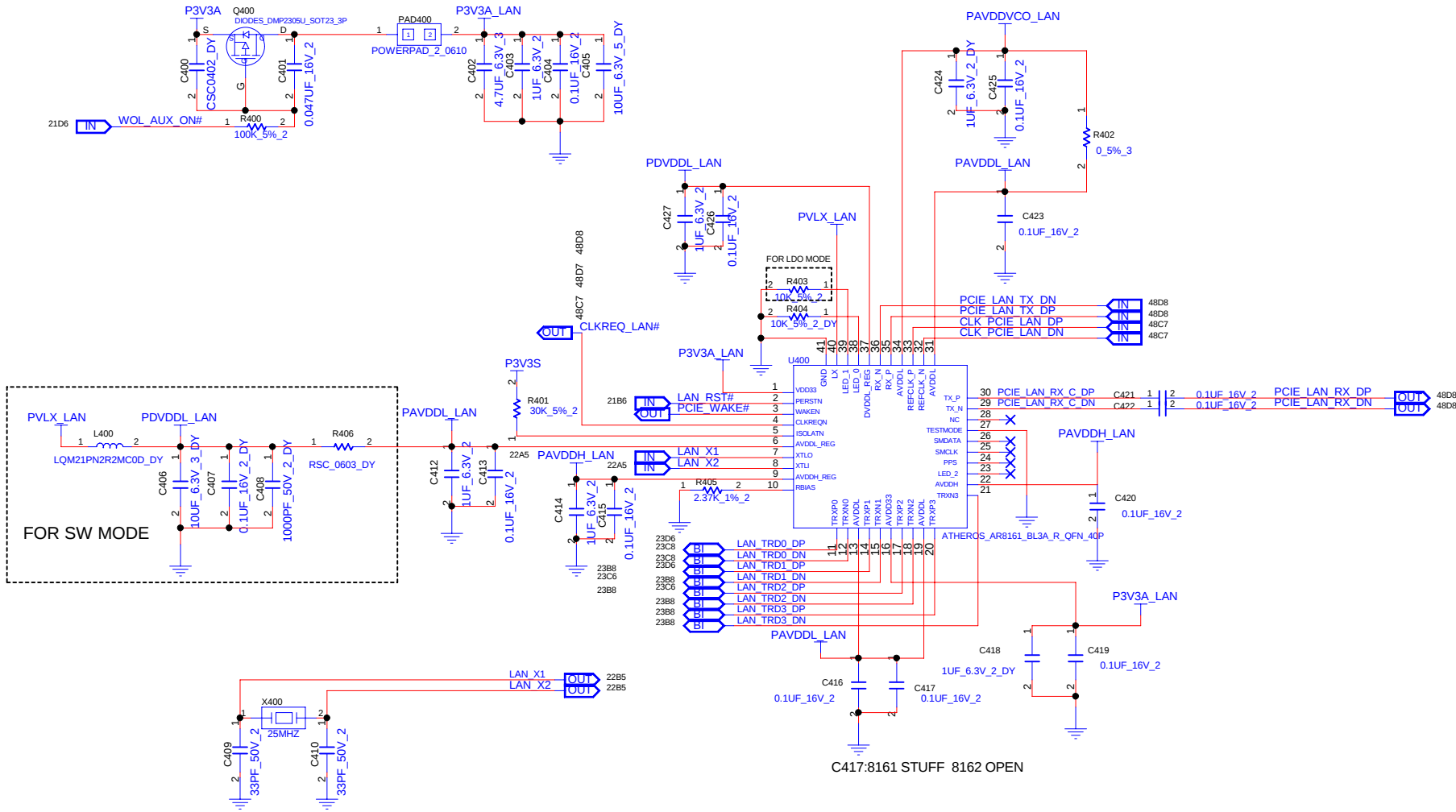


INVENTEC

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MODEL.PROJECT.FUNCTION			
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A	B	C	D	E	F
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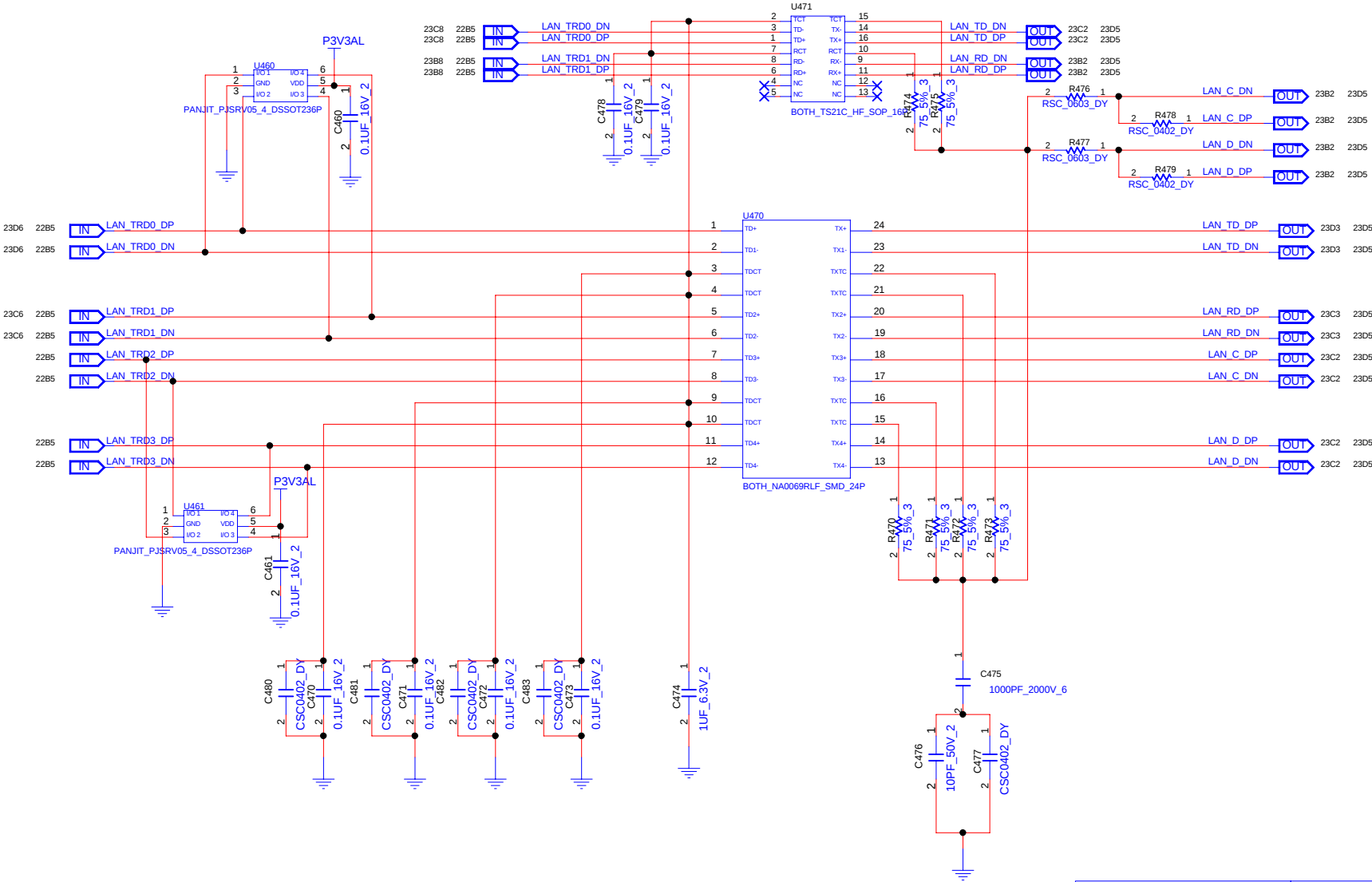
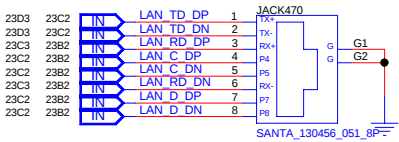




INVENTEC

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REFERENCE 400~499(LAN)



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION
Block Diagram

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A3

CODE
CS

DOC NUMBER
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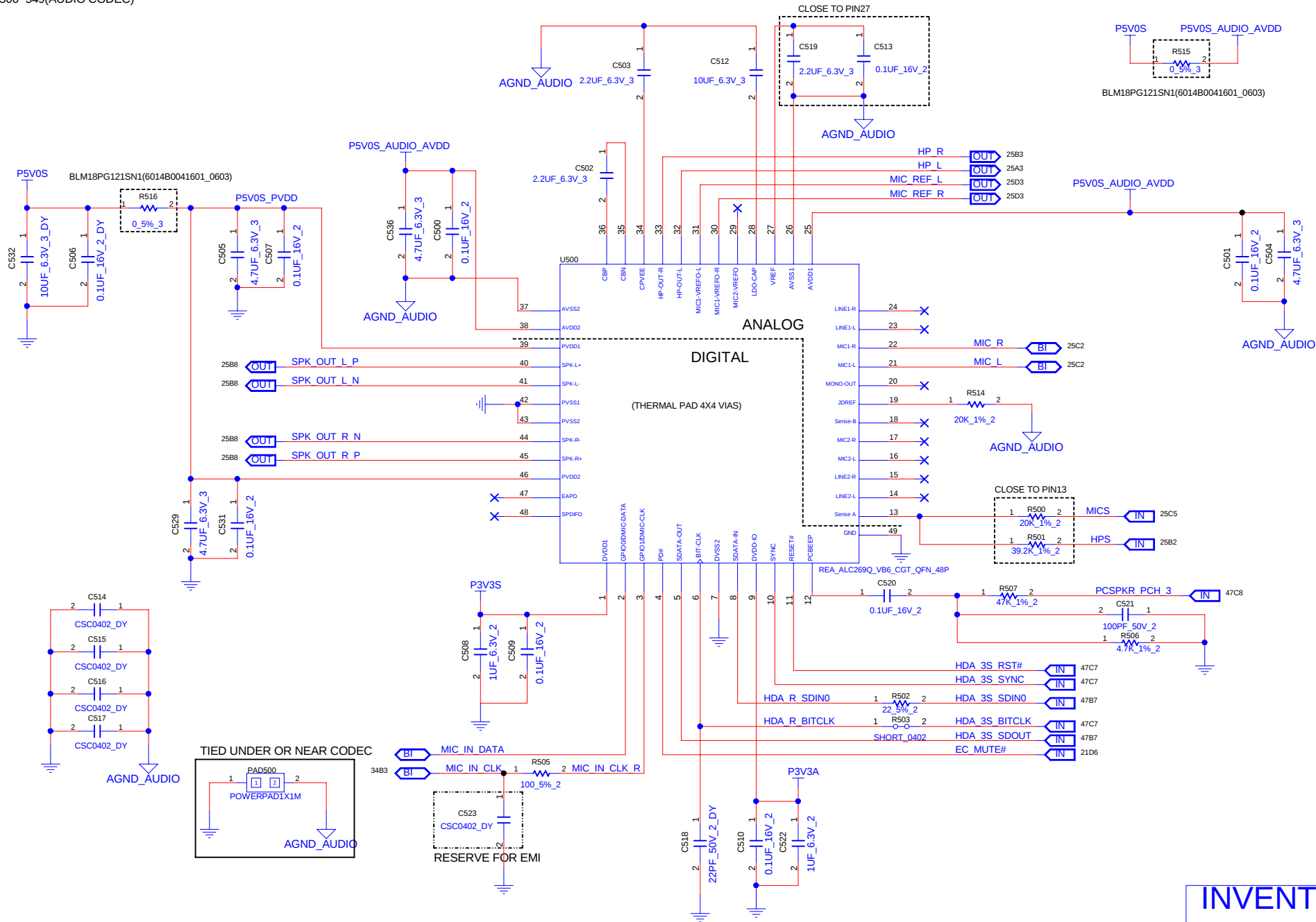
REV
X01

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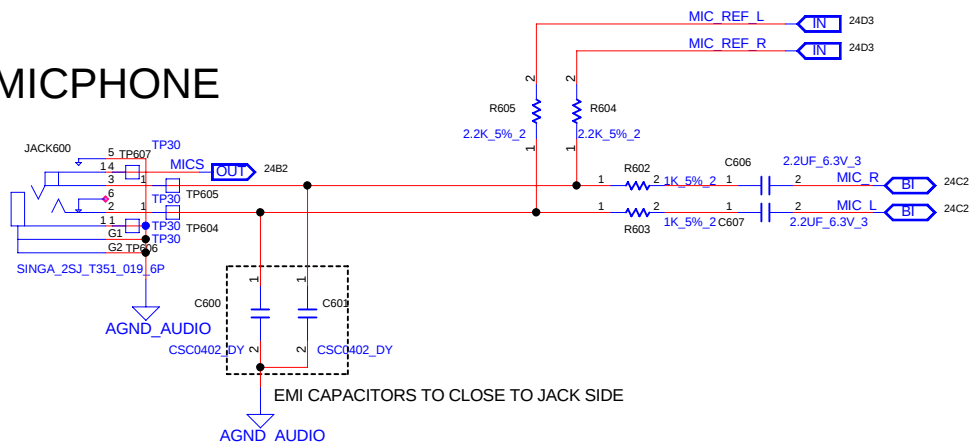
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REFERENCE 500-549(AUDIO CODEC)

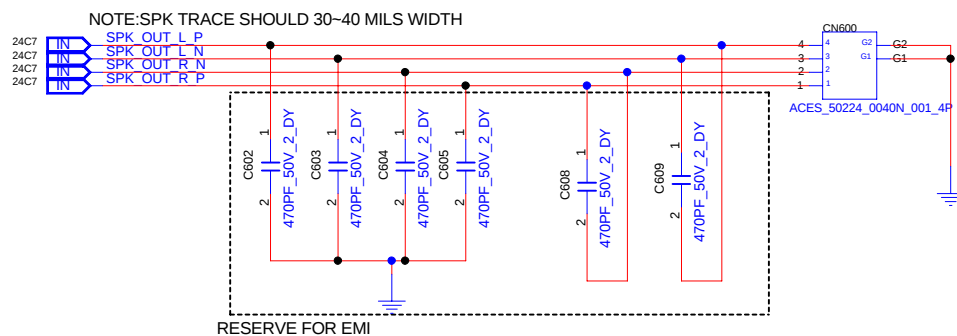


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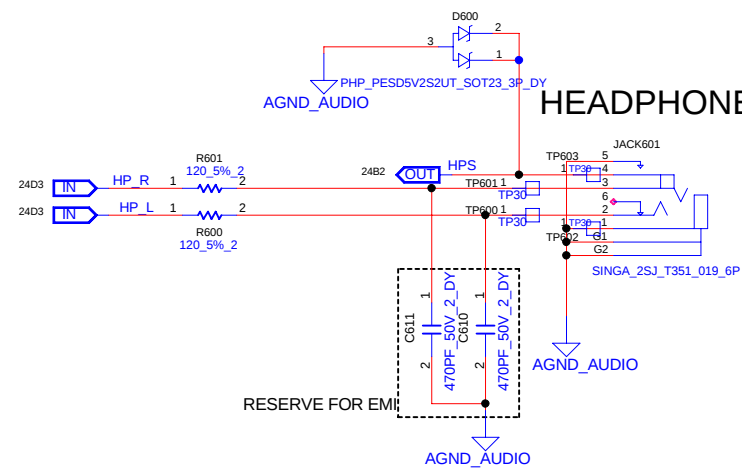
MICPHONE



INTERNAL SPEAKERS



P_DY HEADPHONE



INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risk Management	7. Project Risk Management
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

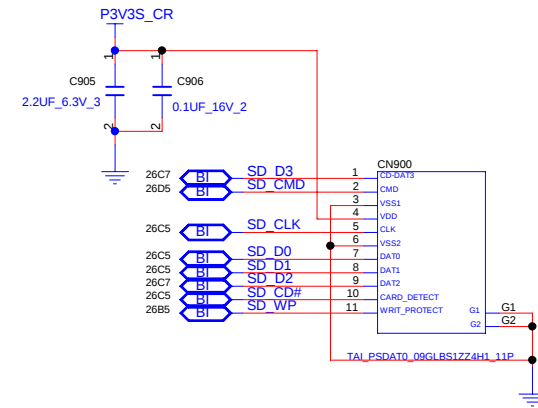
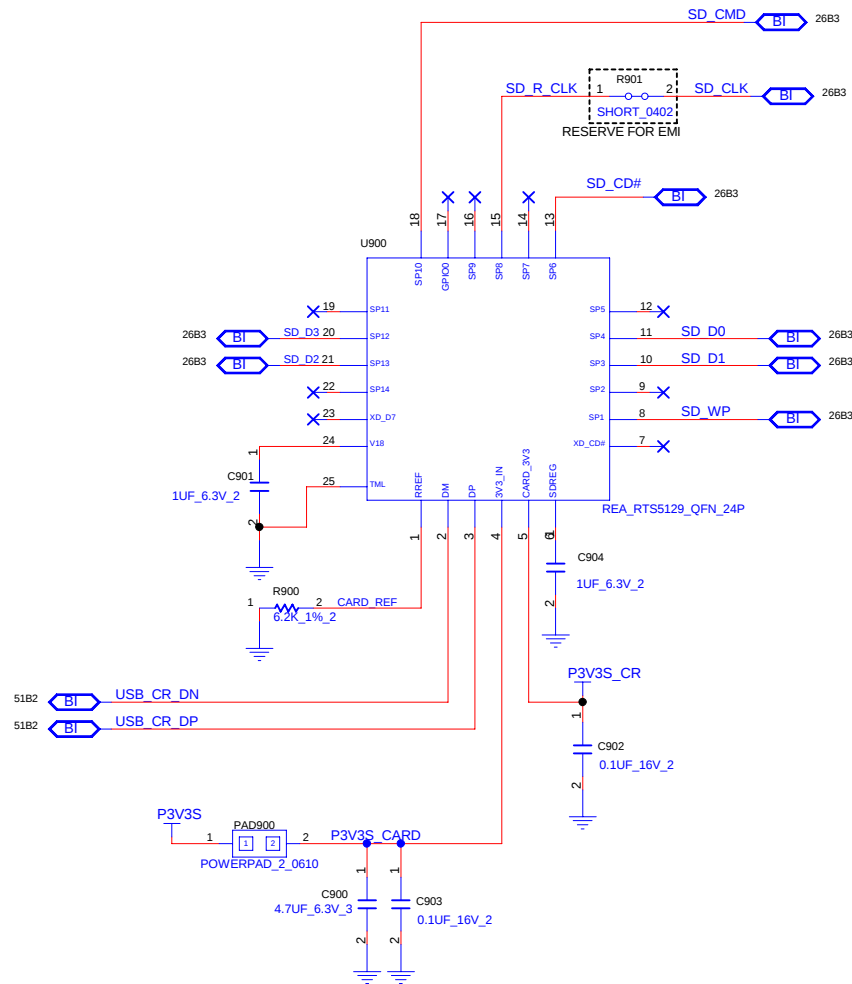
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REFERENCE 900~999(CARDREADER)



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

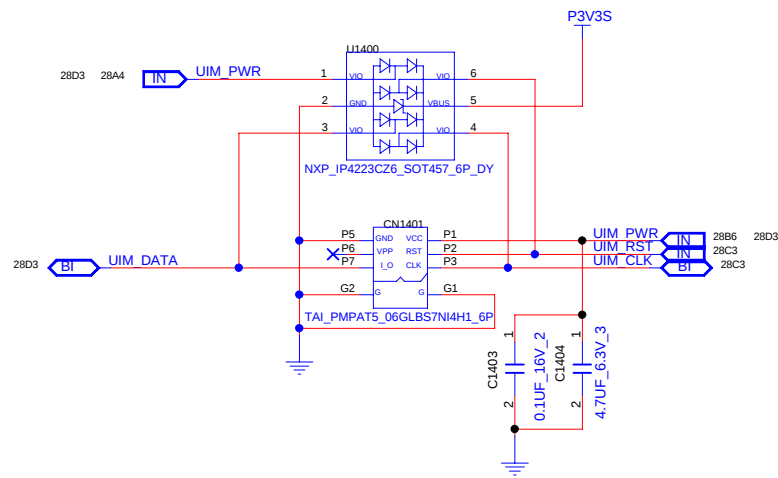
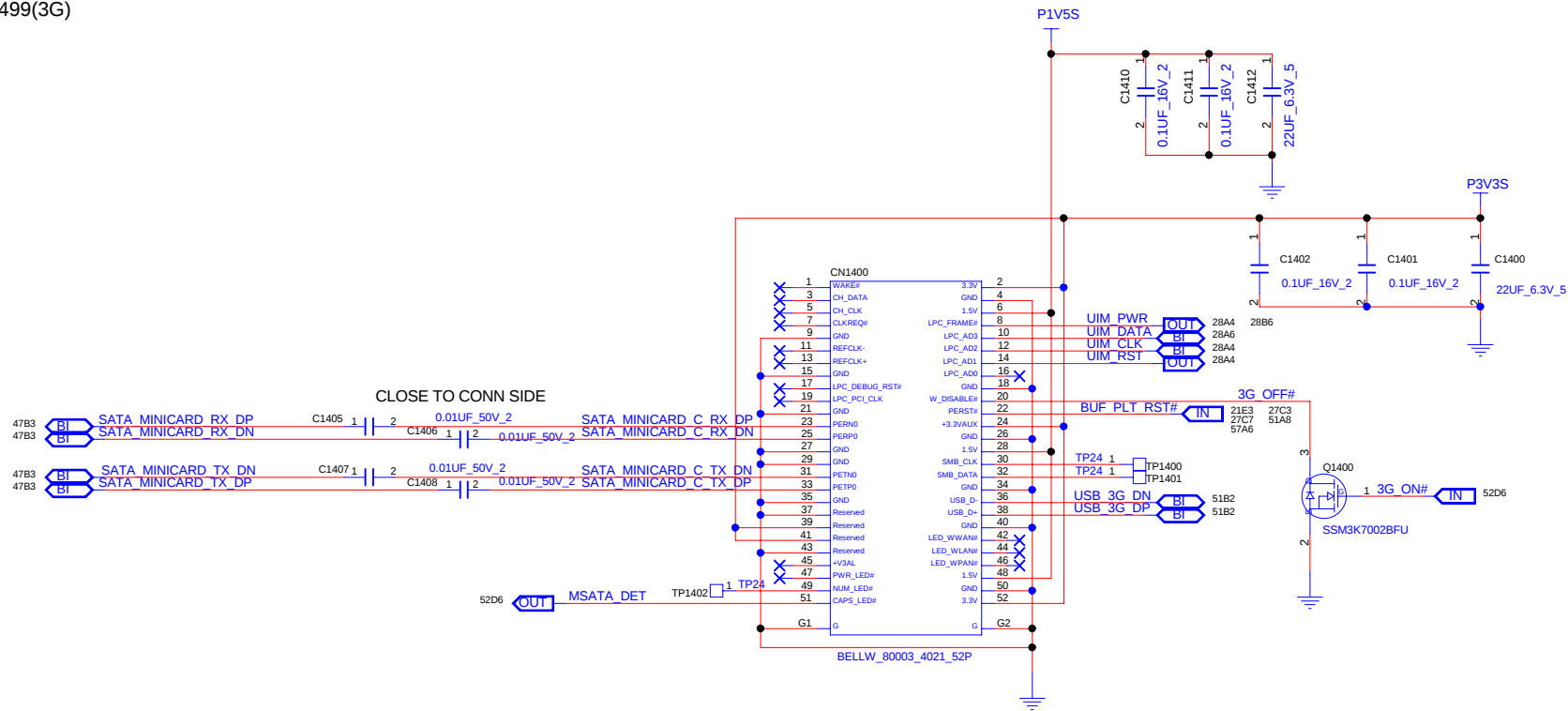
Block Diagram

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REFERENCE 1400~1499(3G)



INVENTEC

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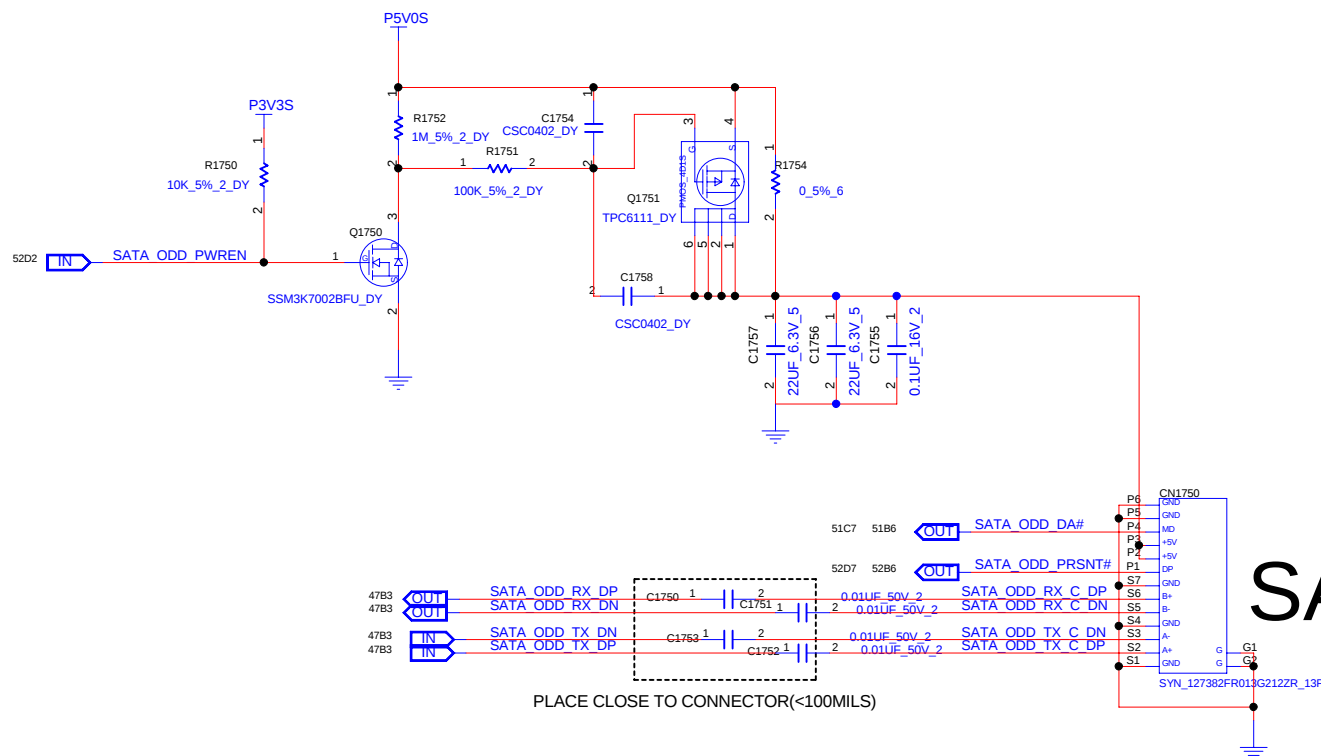
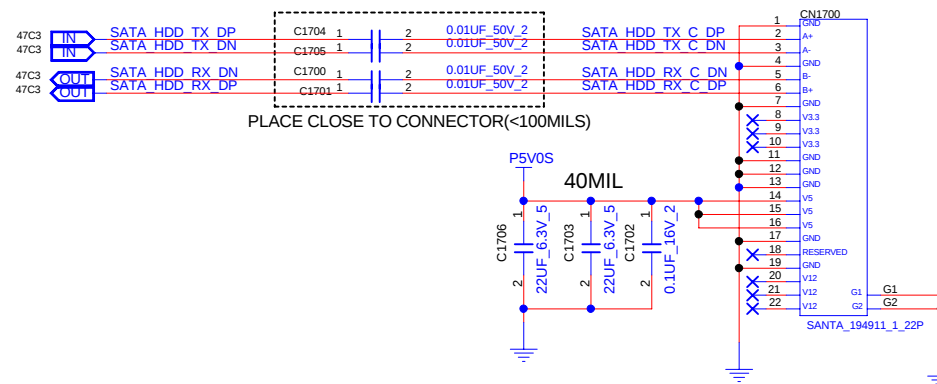
X01

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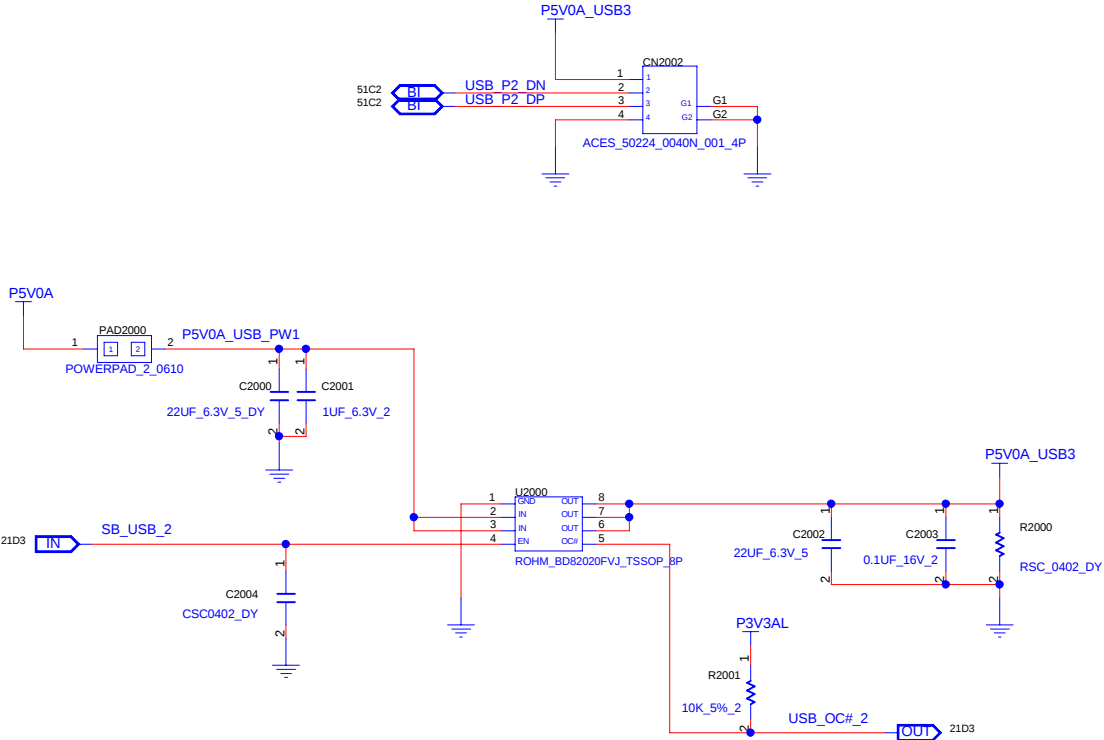
SATA HDD



SATA ODD

INVENTEC

TITLE			
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INVENTEC

TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risks	7. Project Risks
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

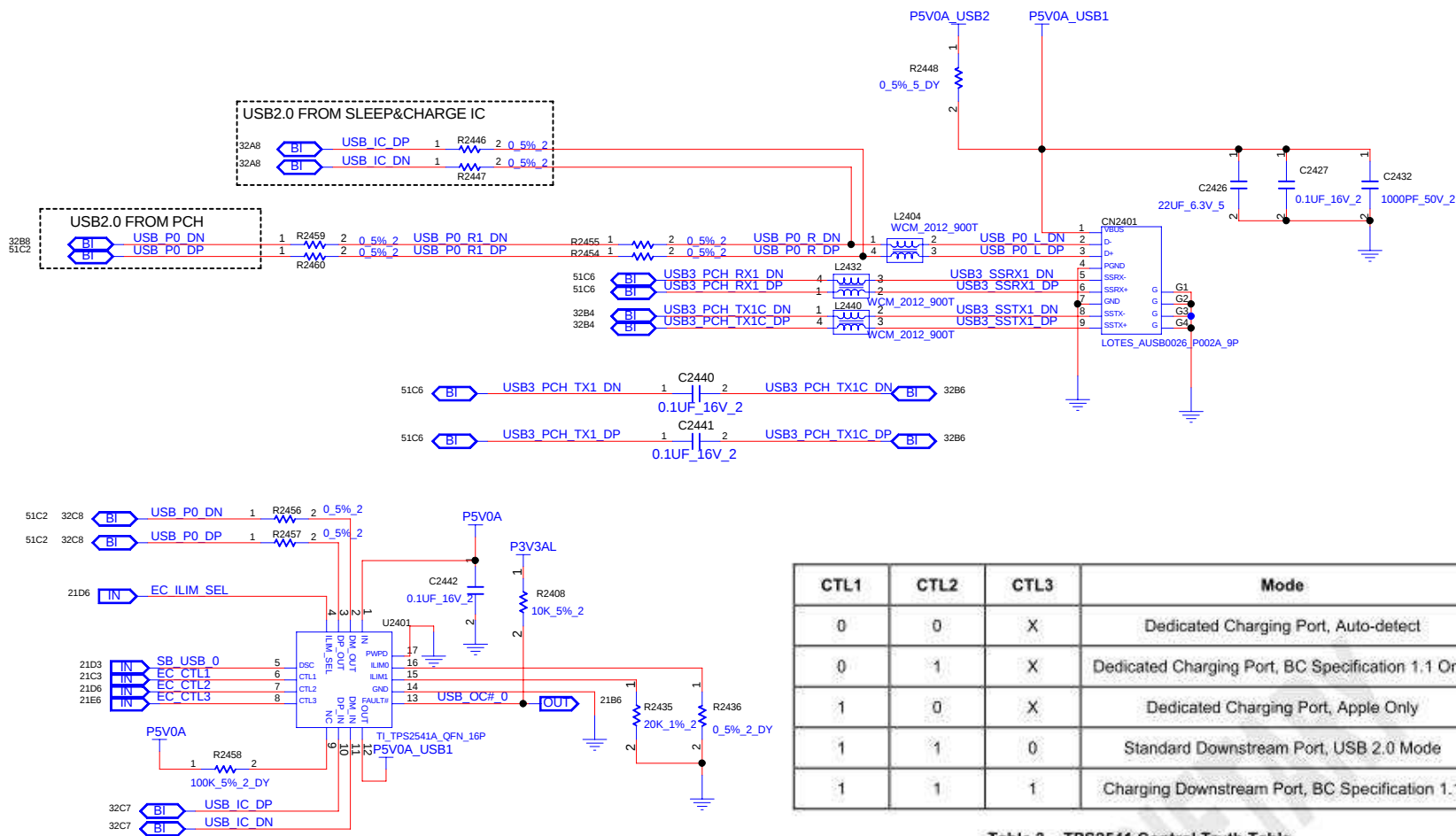
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REFERENCE 2400~2499

REMOVE USB3.0 CONTROLLER IC

INVENTEC			
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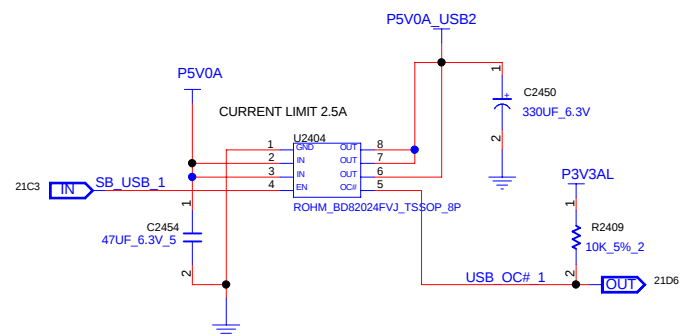


CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1.1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

Table 3 – TPS2541 Control Truth Table

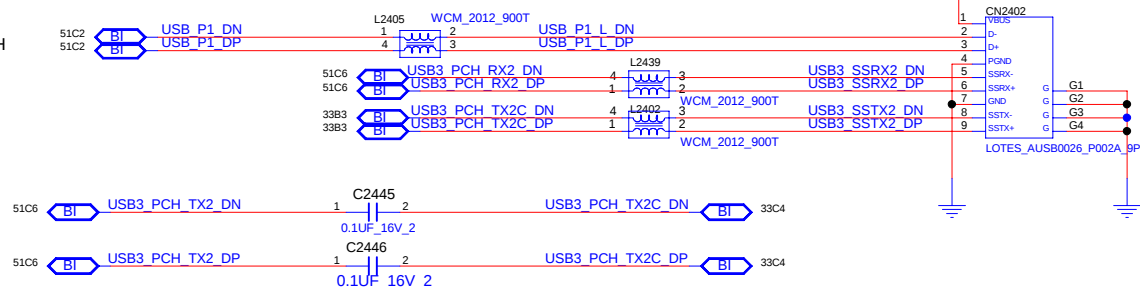
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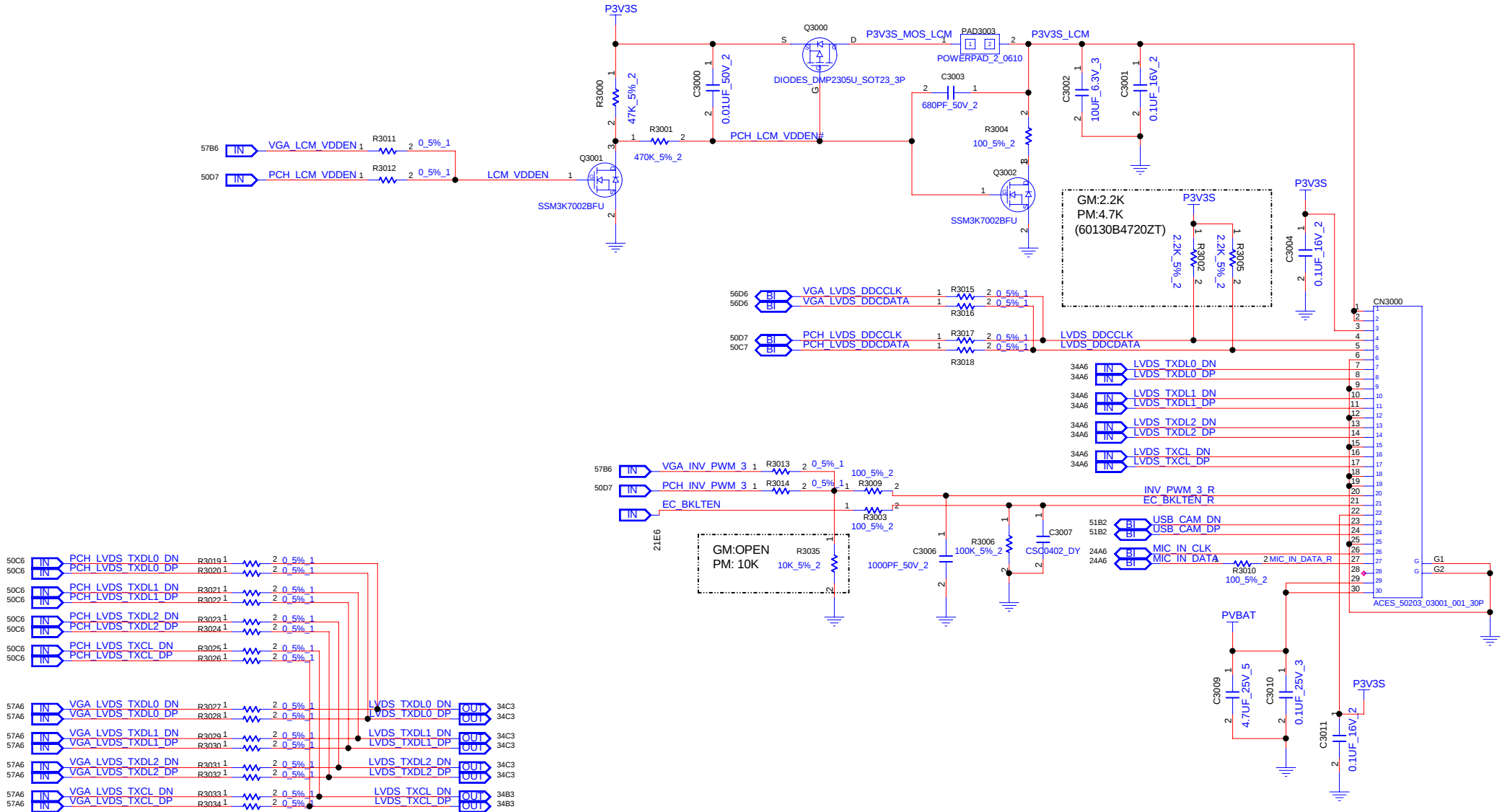
USB 3.0 CONNECTOR

USB2.0 FROM PCH

**INVENTEC**

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 3000~3049(LCM)



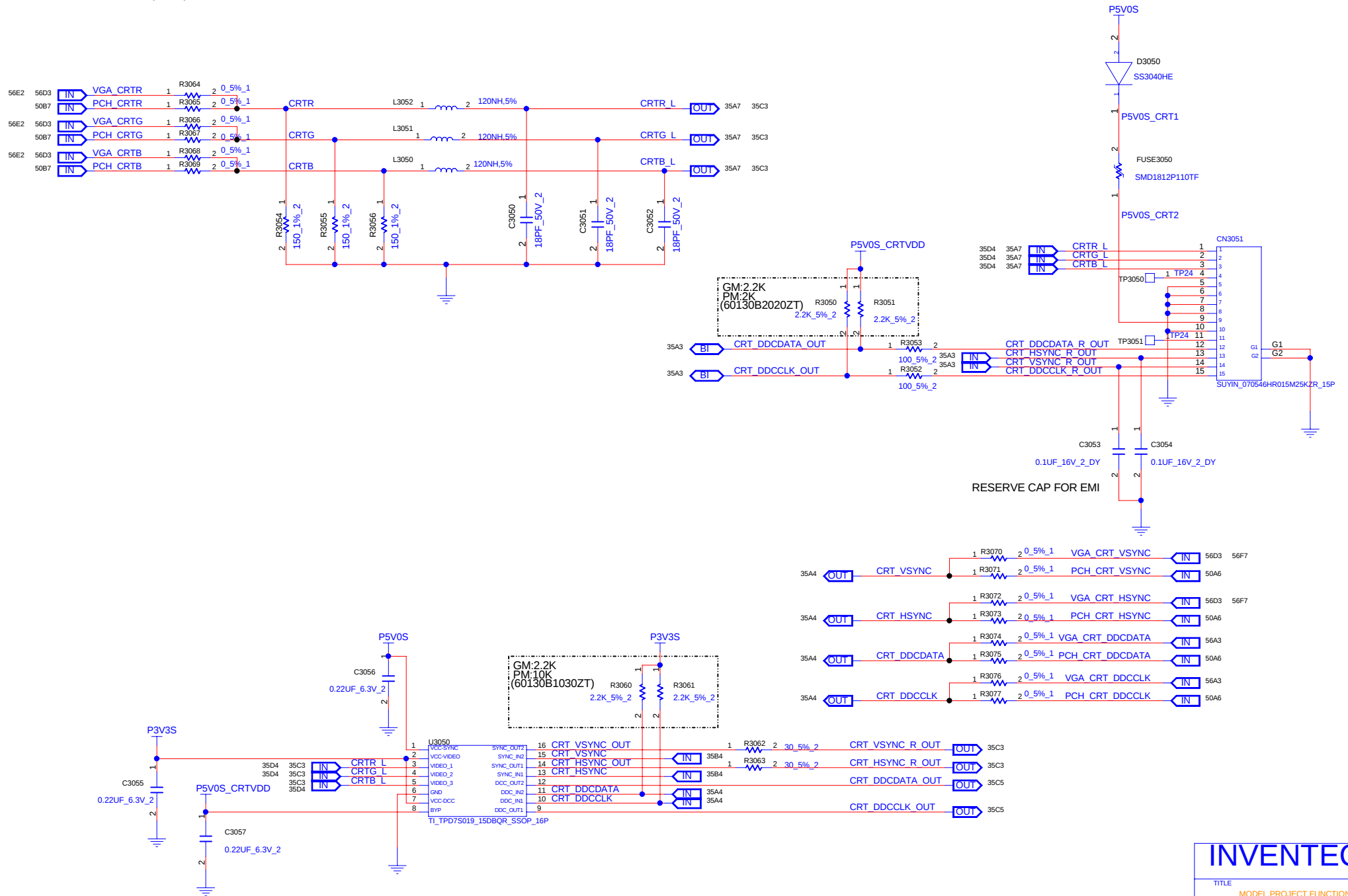
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
DOC NUMBER			
1310xxxx-0-0			
REV			
X01			

CHANGE by XXX DATE 21-OCT-2002

SHEET 34 of 70

REFERENCE 3050~3099(CRT)



INVENTEC

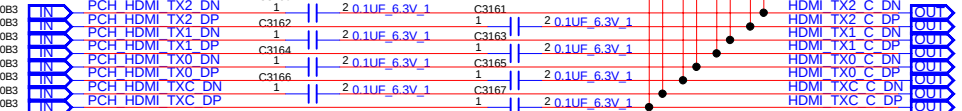
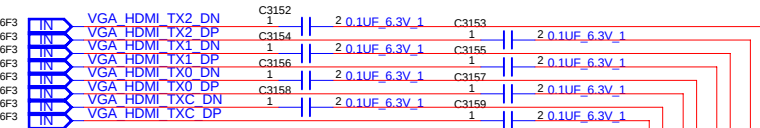
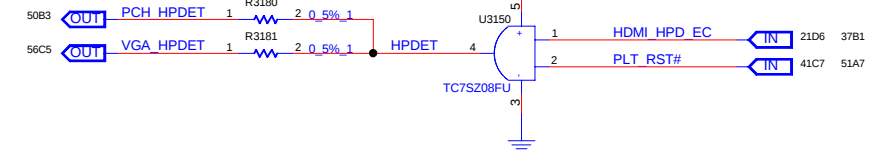
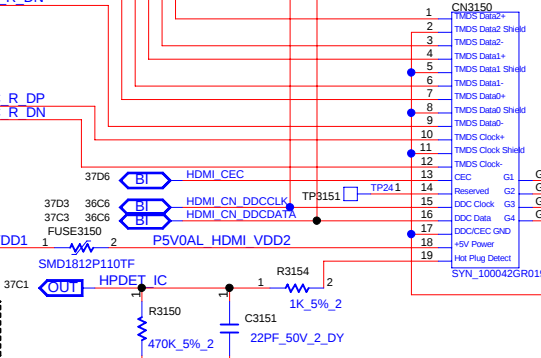
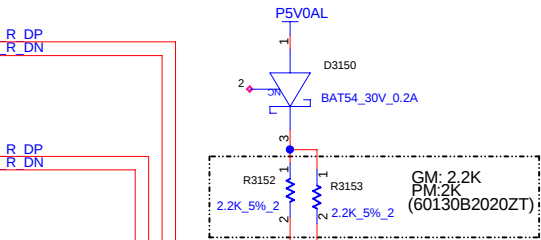
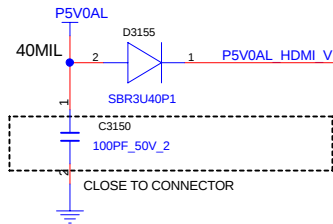
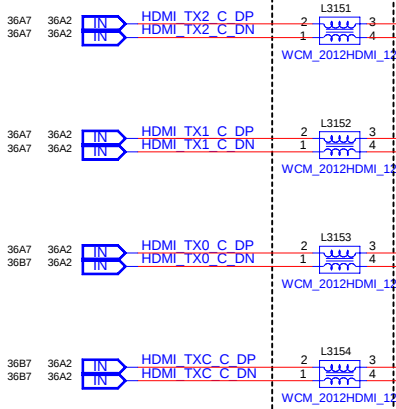
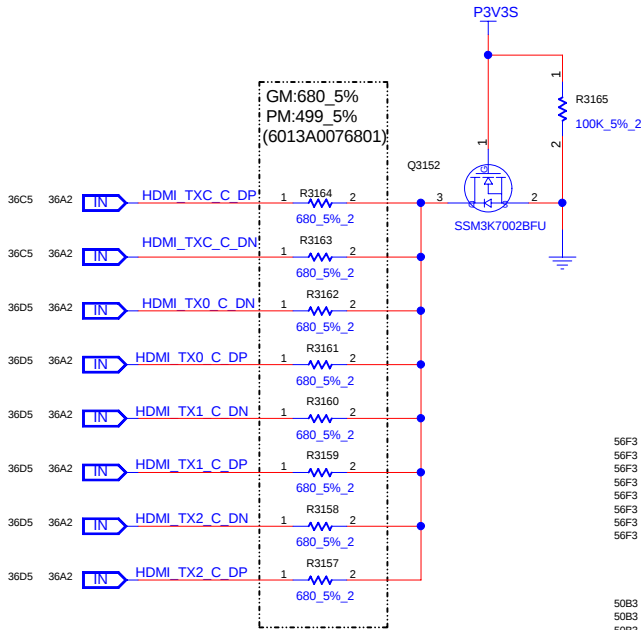
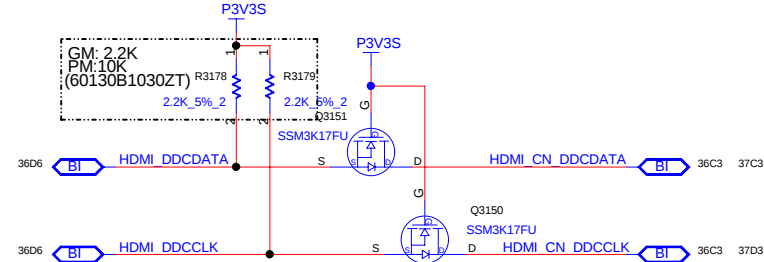
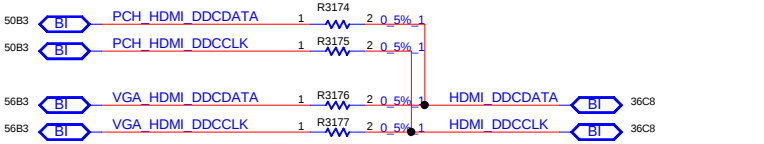
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1.2	1.2.1
1.3	1.3.1
1.4	1.4.1
1.5	1.5.1
1.6	1.6.1
1.7	1.7.1
1.8	1.8.1
1.9	1.9.1
1.10	1.10.1
1.11	1.11.1
1.12	1.12.1
1.13	1.13.1
1.14	1.14.1
1.15	1.15.1
1.16	1.16.1
1.17	1.17.1
1.18	1.18.1
1.19	1.19.1
1.20	1.20.1
1.21	1.21.1
1.22	1.22.1
1.23	1.23.1
1.24	1.24.1
1.25	1.25.1
1.26	1.26.1
1.27	1.27.1
1.28	1.28.1
1.29	1.29.1
1.30	1.30.1
1.31	1.31.1
1.32	1.32.1
1.33	1.33.1
1.34	1.34.1
1.35	1.35.1
1.36	1.36.1
1.37	1.37.1
1.38	1.38.1
1.39	1.39.1
1.40	1.40.1
1.41	1.41.1
1.42	1.42.1
1.43	1.43.1
1.44	1.44.1
1.45	1.45.1
1.46	1.46.1
1.47	1.47.1
1.48	1.48.1
1.49	1.49.1
1.50	1.50.1
1.51	1.51.1
1.52	1.52.1
1.53	1.53.1
1.54	1.54.1
1.55	1.55.1
1.56	1.56.1
1.57	1.57.1
1.58	1.58.1
1.59	1.59.1
1.60	1.60.1
1.61	1.61.1
1.62	1.62.1
1.63	1.63.1
1.64	1.64.1
1.65	1.65.1
1.66	1.66.1
1.67	1.67.1
1.68	1.68.1
1.69	1.69.1
1.70	1.70.1
1.71	1.71.1
1.72	1.72.1
1.73	1.73.1
1.74	1.74.1
1.75	1.75.1
1.76	1.76.1
1.77	1.77.1
1.78	1.78.1
1.79	1.79.1
1.80	1.80.1
1.81	1.81.1
1.82	1.82.1
1.83	1.83.1
1.84	1.84.1
1.85	1.85.1
1.86	1.86.1
1.87	1.87.1
1.88	1.88.1
1.89	1.89.1
1.90	1.90.1
1.91	1.91.1
1.92	1.92.1
1.93	1.93.1
1.94	1.94.1
1.95	1.95.1
1.96	1.96.1
1.97	1.97.1
1.98	1.98.1
1.99	1.99.1
2.00	2.00.1
2.01	2.01.1
2.02	2.02.1
2.03	2.03.1
2.04	2.04.1
2.05	2.05.1
2.06	2.06.1
2.07	2.07.1
2.08	2.08.1
2.09	2.09.1
2.10	2.10.1
2.11	2.11.1
2.12	2.12.1
2.13	2.13.1
2.14	2.14.1
2.15	2.15.1
2.16	2.16.1
2.17	2.17.1
2.18	2.18.1
2.19	2.19.1
2.20	2.20.1
2.21	2.21.1
2.22	2.22.1
2.23	2.23.1
2.24	2.24.1
2.25	2.25.1
2.26	2.26.1
2.27	2.27.1
2.28	2.28.1
2.29	2.29.1
2.30	2.30.1
2.31	2.31.1
2.32	2.32.1
2.33	2.33.1
2.34	2.34.1
2.35	2.35.1
2.36	2.36.1
2.37	2.37.1
2.38	2.38.1
2.39	2.39.1
2.40	2.40.1
2.41	

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	R X
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SHEET 35 of 70

REFERENCE 3150~3199(HDMI)

PLACE CLOSE TO CONNECTOR



INVENTEC

36A7 36D5

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

xxx

DATE

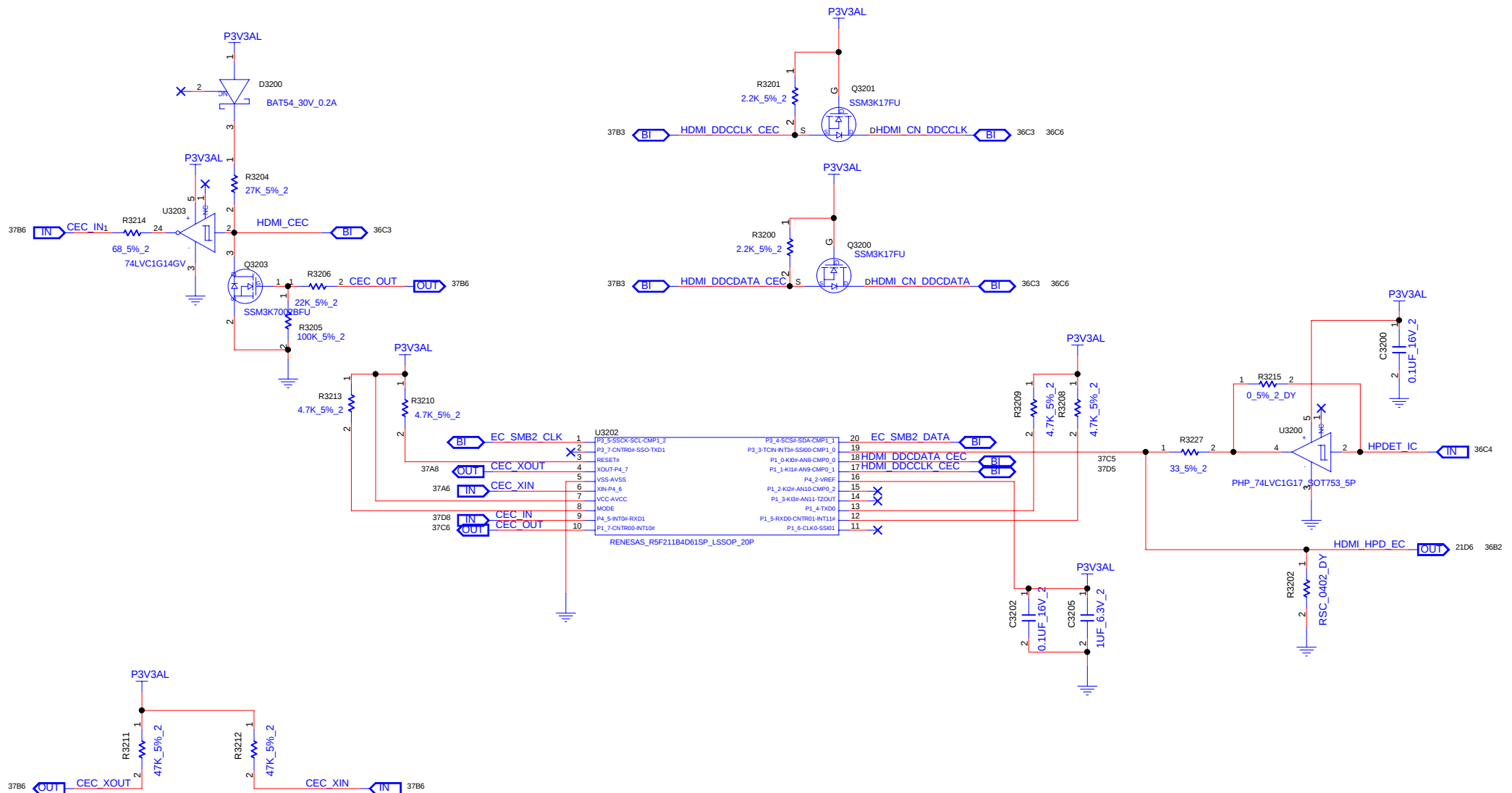
21-OCT-2002

SHEET

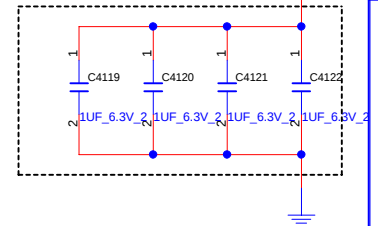
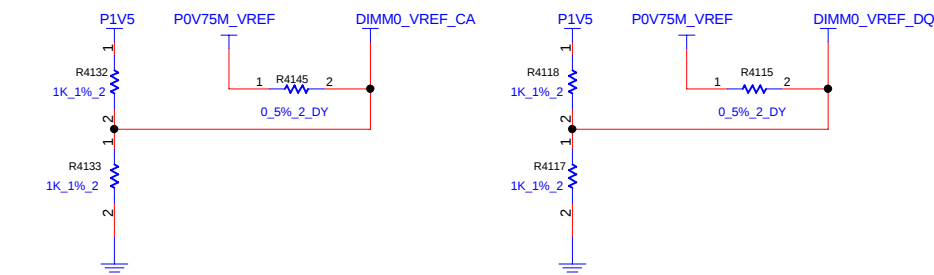
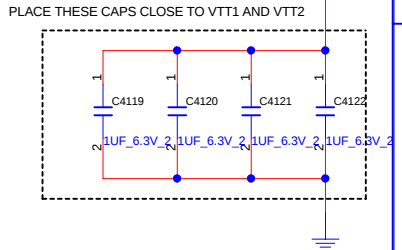
36

of

70

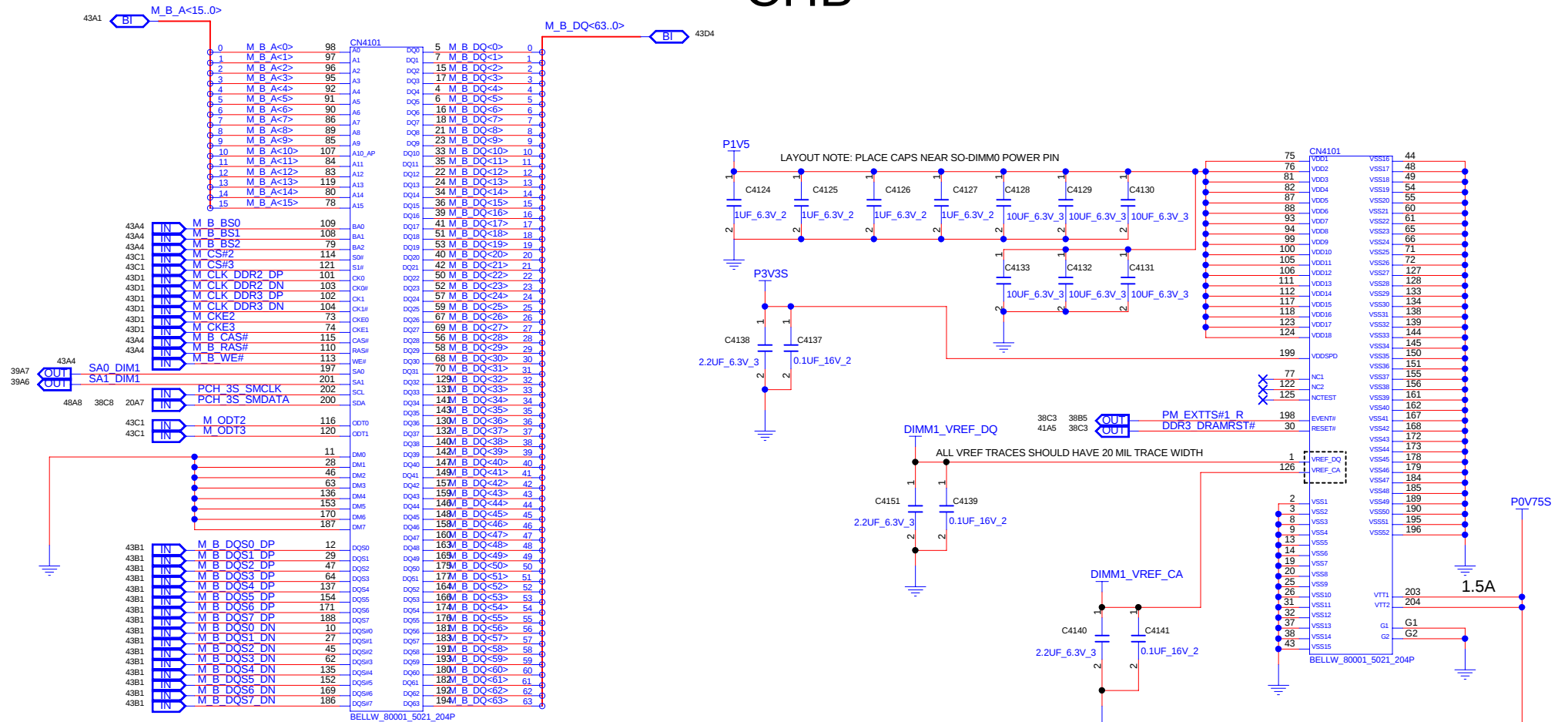


INVENTEC			
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE A3		DOC NUMBER	REV
CODE CS		1310xxxx-0-0	X01
SHEET 37 of 70			

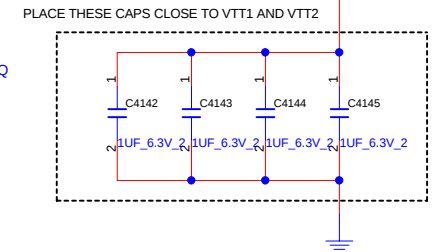
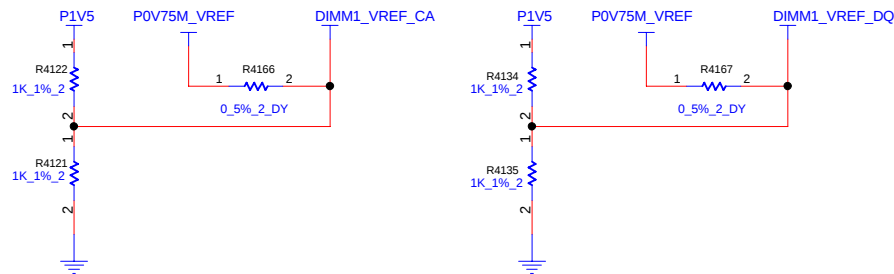
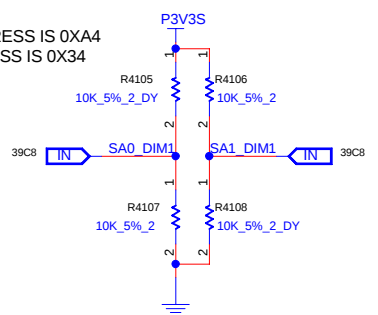


SHEET 38 of 70

CHB



NOTE:
SO-DIMMB SPD ADDRESS IS 0XA4
SO-DIMMB TS ADDRESS IS 0X34



INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

REFERENCE 4300~4349(FAN)
REFERENCE 4411~4449(THERMAL)

D

D

C

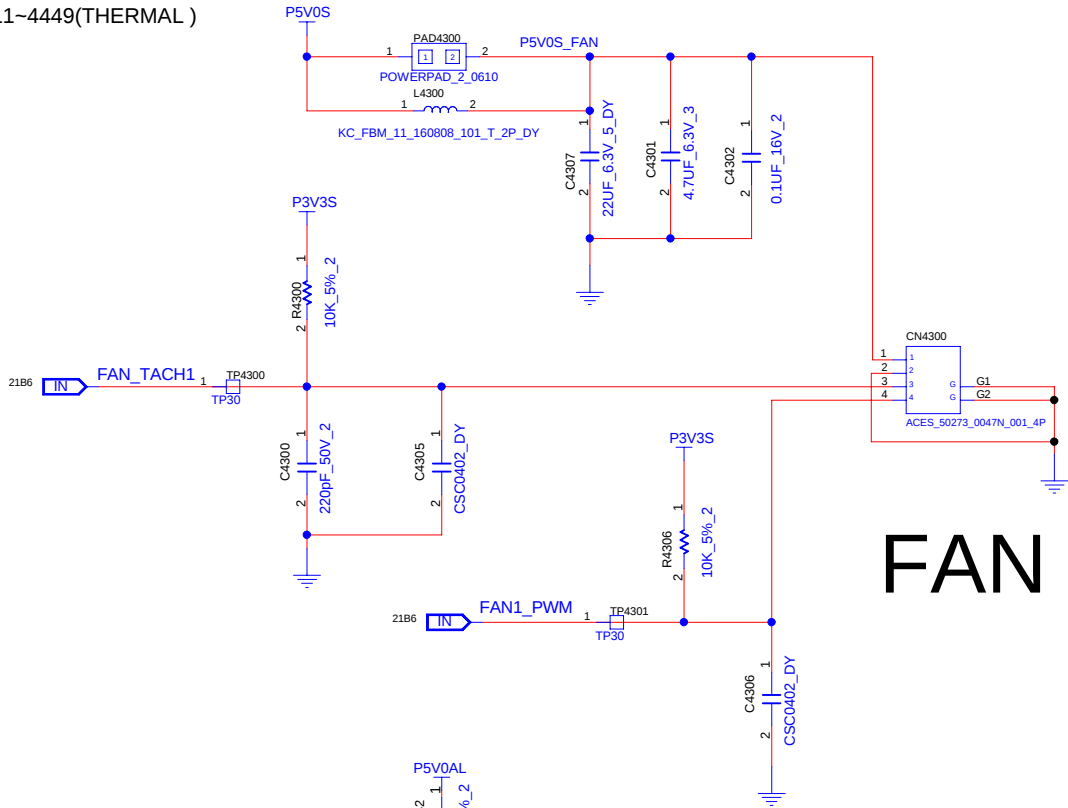
C

B

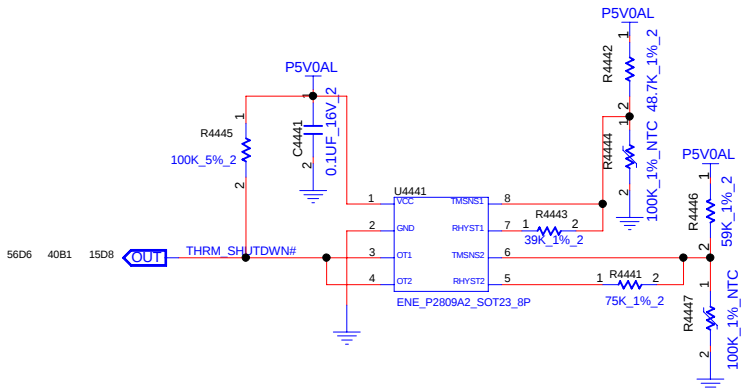
B

A

A



FAN CN

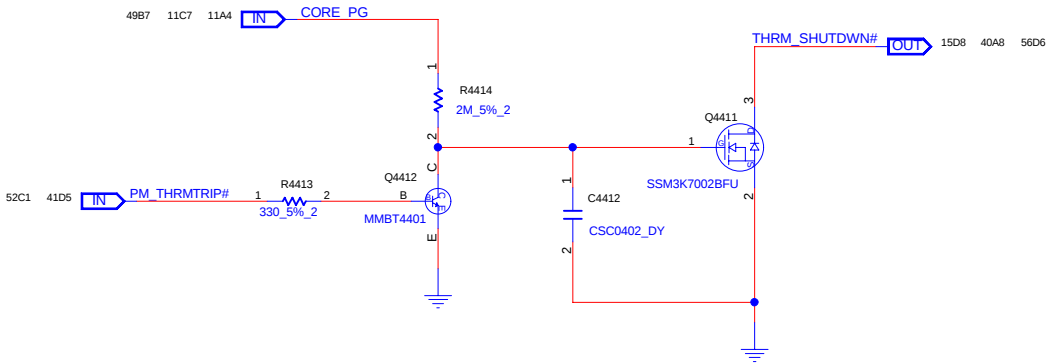


THERMAL SHUTDOWN

GM THERMAL SHUTDOWN CPU SIDE AT 68.6 C
GM THERMAL SHUTDOWN VGA SIDE AT 63.4 C

PM THERMAL SHUTDOWN CPU SIDE AT 71.9 C
PM THERMAL SHUTDOWN VGA SIDE AT 80.2 C

	10F	10FG
R4442	48.7K	43.2K
R4443	39K	30.1K
R4446	59K	32.4K
R4441	75K	18.2K



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

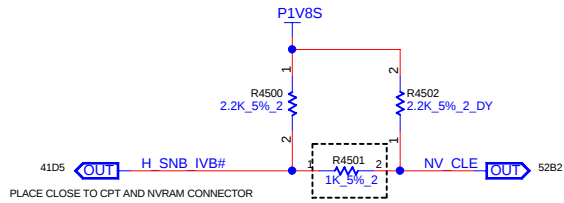
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

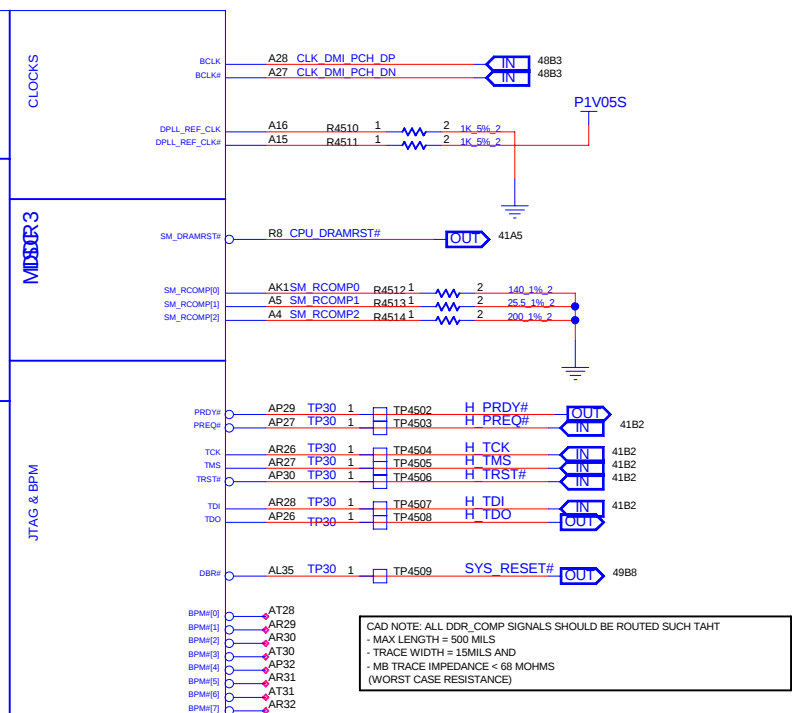
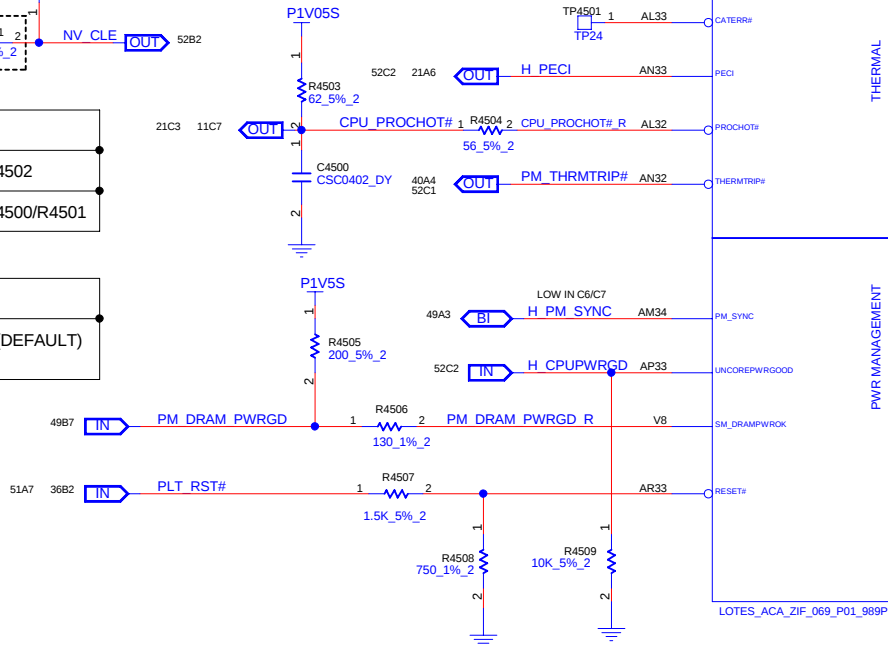
SHEET 40 of 70

REFERENCE 4500~4699(CPU)

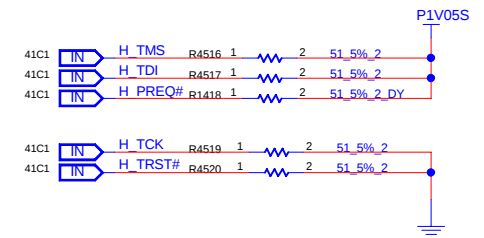


PROCESS STRAP SETTING	
SANDY BRIDGE ONLY	STUFF R4502
SANDY BRIDGE/IVY BRIDGE	STUFF R4500/R4501

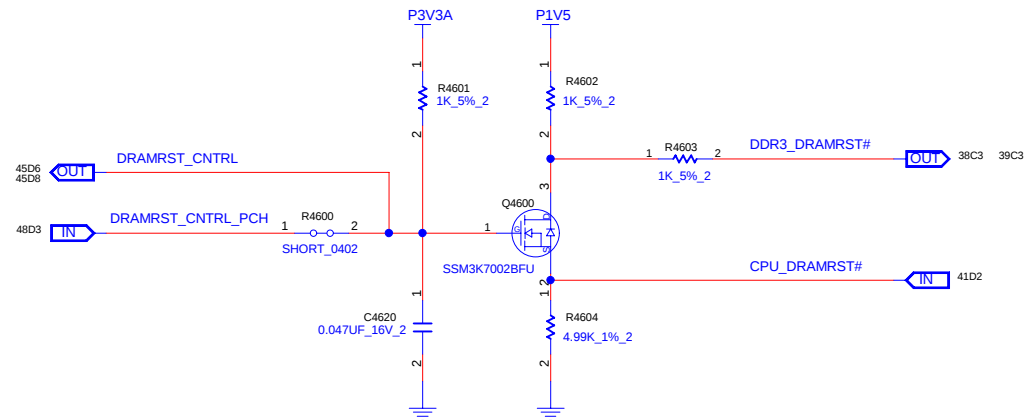
DMI&FDI TERMINATIONVOLTAGE	
NV_CLE	SET TOVSS WHEN LOW (DEFAULT) SET TOVCC WHEN HIGH



CAD NOTE: ALL DDR_COMP SIGNALS SHOULD BE ROUTED SUCH THAT
- MAX LENGTH = 500 MILS
- TRACE WIDTH = 15MILS AND
- MB TRACE IMPEDANCE < 68 MOHMS
(WORST CASE RESISTANCE)



S3 CIRCUIT: DRAM_RST# TO MEMORY SHOULD BE HIGH DURING S3



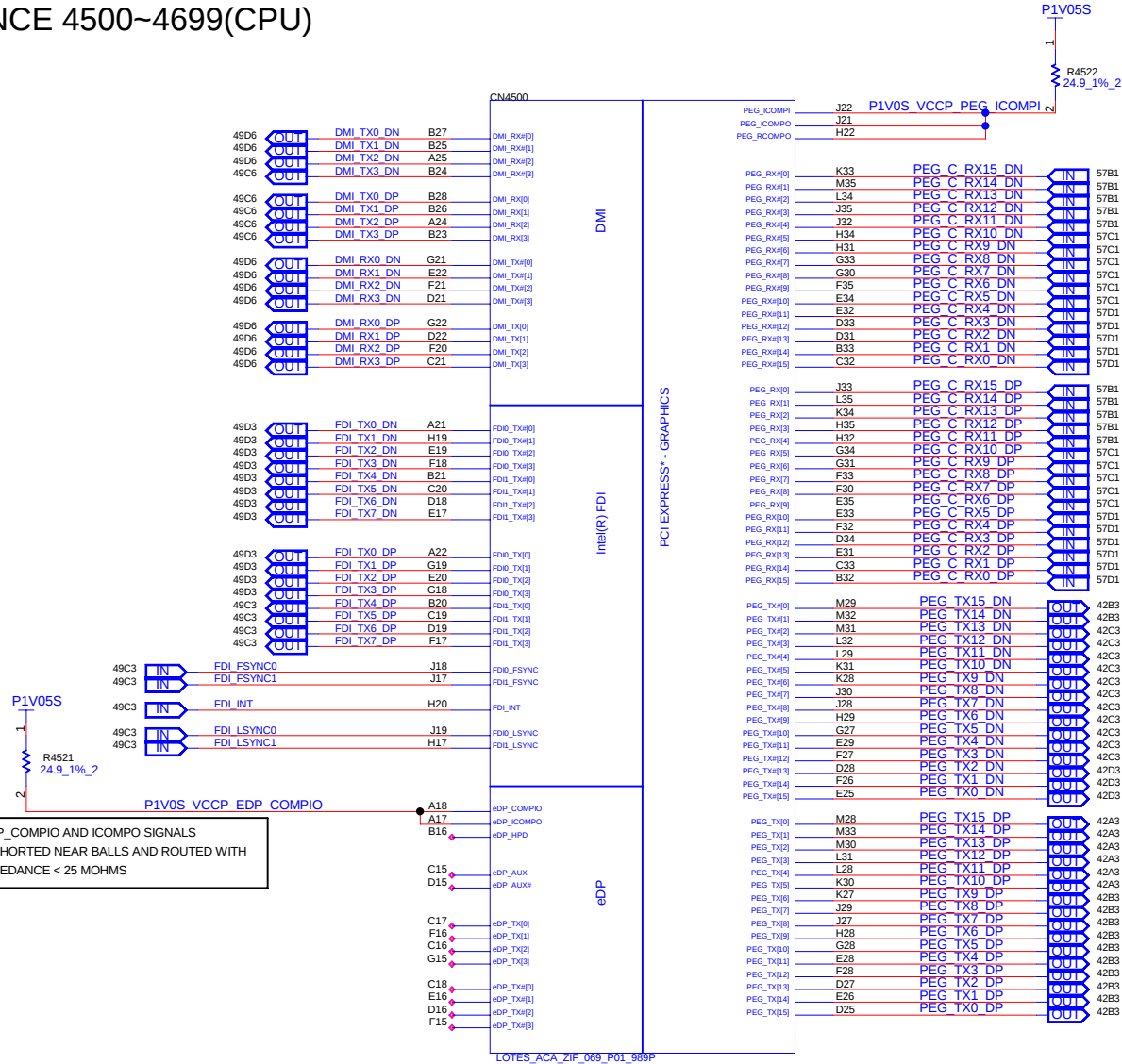
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	

CHANGE by	XXX	DATE	21-OCT-2002
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SHEET 41 of 70

REFERENCE 4500~4699(CPU)



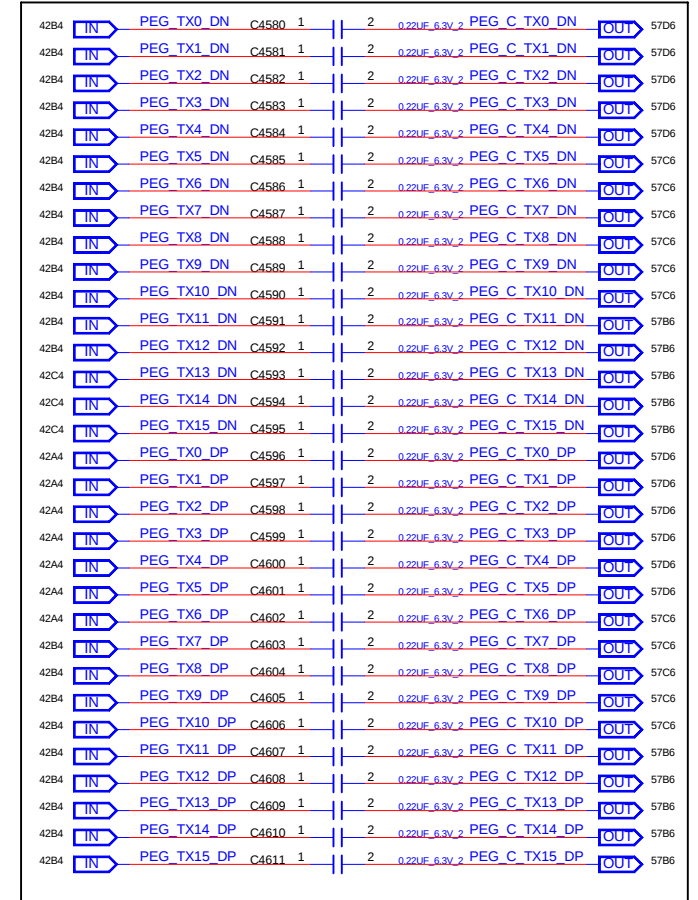
CAD NOTE: PEG_ICOMPI AND RCOMPO SIGNALS SHOULD BE SHORTED AND ROUTED WITH

- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 43 MOHMS

PEG_ICOMPO SIGNALS SHOULD BE ROUTED WITH

- MAX LENGTH = 500 MILS
- TYPICAL IMPEDANCE = 14.5 MOHMS

CLOSE TO CPU



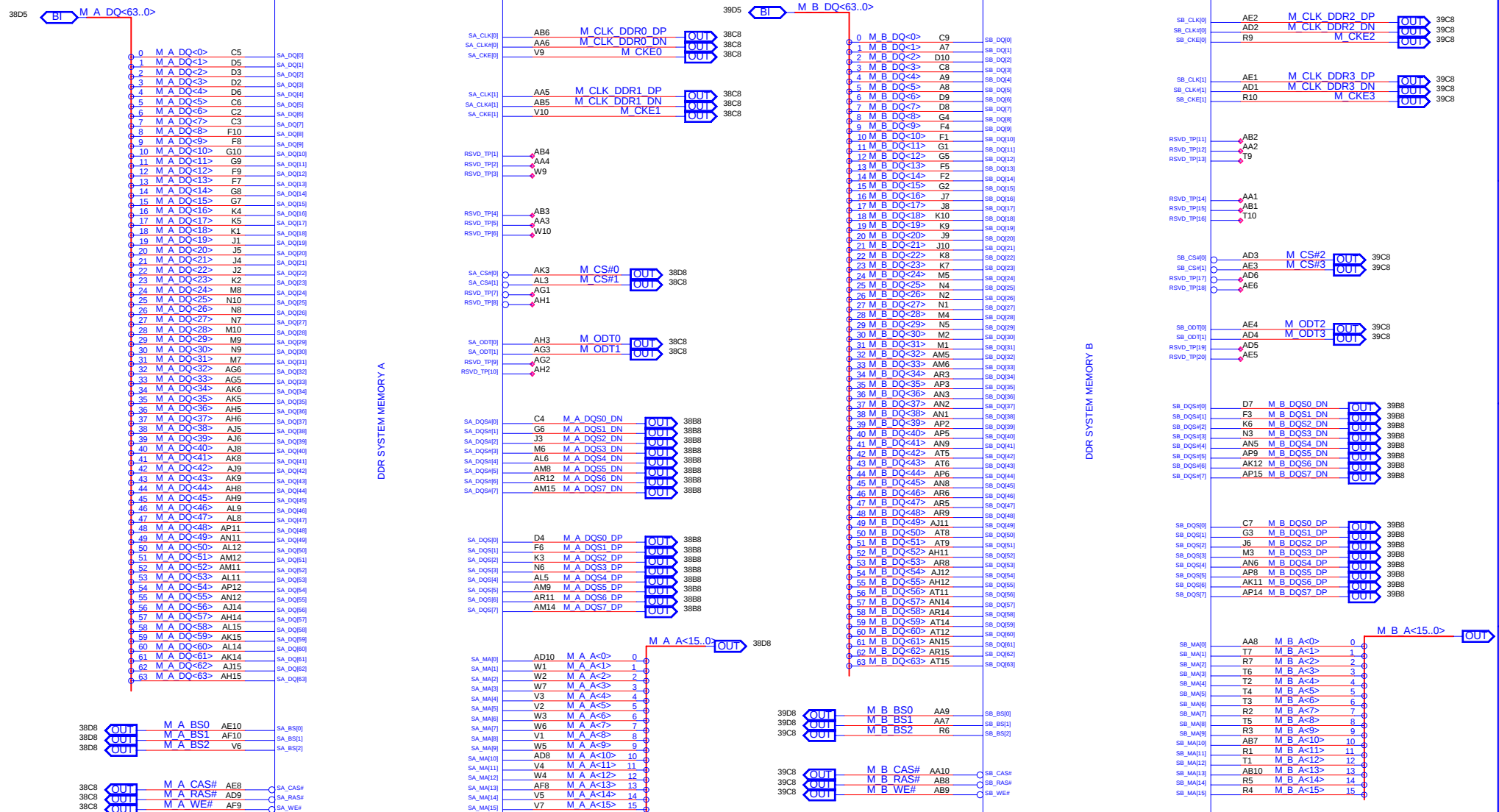
INVENTEC

TITLE
MODEL PROJECT FUNCTION
Block Diagram
SIZE A3 CODE CS DOC NUMBER 1310xxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 42 of 70

CN4500

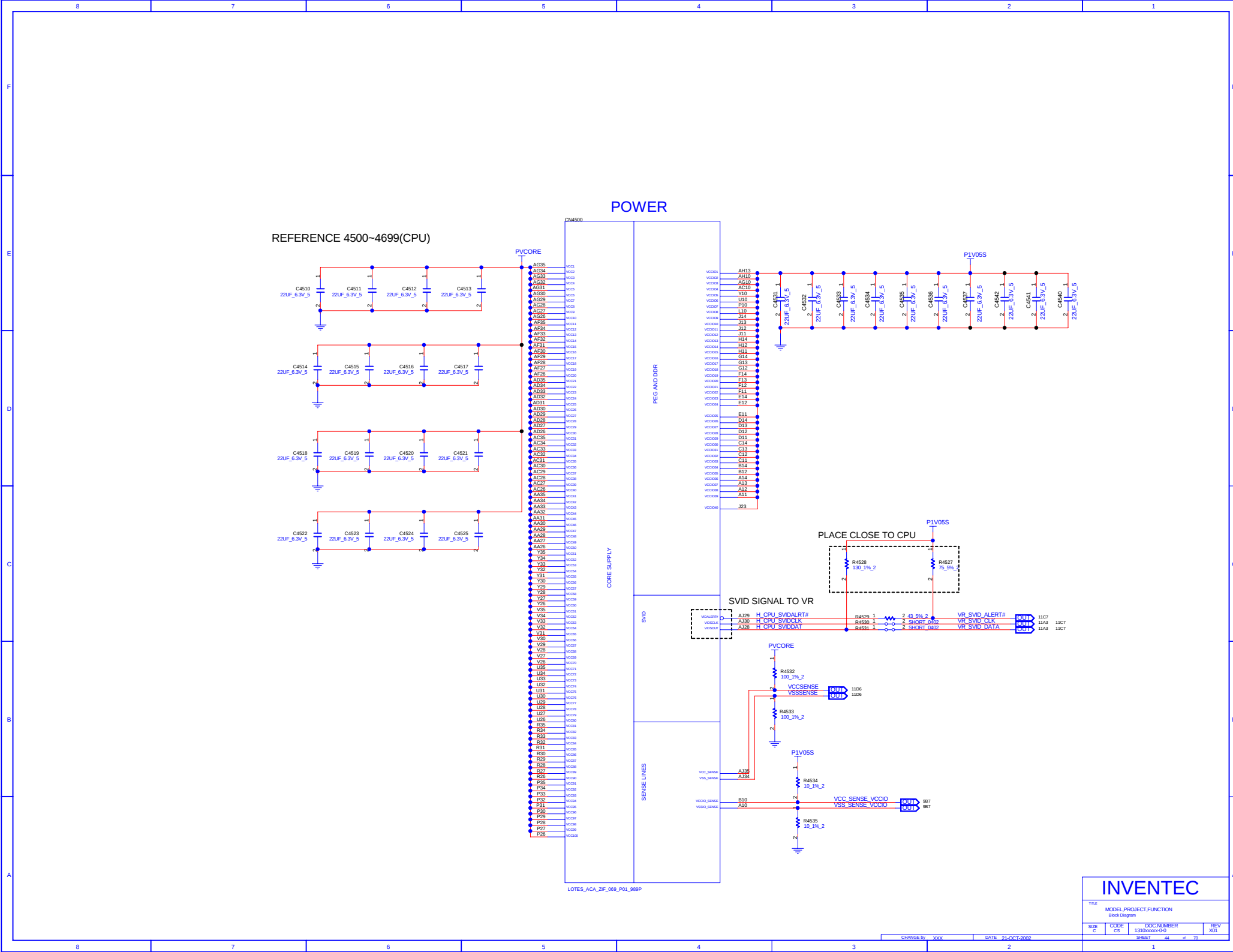


OTES_ACA_ZIF_069_P01_989P

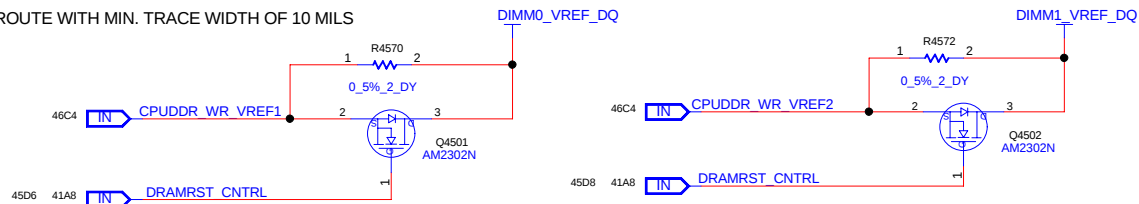
TITLE	MODEL PROJECT FUNCTION
1. Project Overview	1. Project Overview
2. Project Objectives	2. Project Objectives
3. Project Scope	3. Project Scope
4. Project Organization	4. Project Organization
5. Project Schedule	5. Project Schedule
6. Project Budget	6. Project Budget
7. Project Risk Management	7. Project Risk Management
8. Project Communication	8. Project Communication
9. Project Monitoring and Control	9. Project Monitoring and Control
10. Project Closure	10. Project Closure

SIZE A3	CODE CS	1310xxxxx-0-0	
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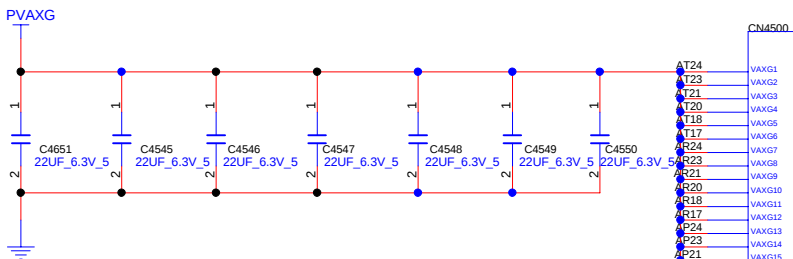
SHEET 43 of 70



PROCESSOR DRIVEN VREF PATH WAS STUFFED BY DEFAULT:
ROUTE WITH MIN. TRACE WIDTH OF 10 MILS



POWER



CN4500

AT24 VAXG1
AT23 VAXG2
AT21 VAXG3
AT20 VAXG4
AT18 VAXG5
AT17 VAXG6
R24 VAXG7
R23 VAXG8
R21 VAXG9
R20 VAXG10
R19 VAXG11
R17 VAXG12
R24 VAXG13
R23 VAXG14
R21 VAXG15
R20 VAXG16
R18 VAXG17
R17 VAXG18
R16 VAXG19
R15 VAXG20
R24 VAXG21
R23 VAXG22
R21 VAXG23
R20 VAXG24
R19 VAXG25
R18 VAXG26
R17 VAXG27
R16 VAXG28
R15 VAXG29
R14 VAXG30
R13 VAXG31
R12 VAXG32
R11 VAXG33
R10 VAXG34
R9 VAXG35
R8 VAXG36
R7 VAXG37
R6 VAXG38
R5 VAXG39
R4 VAXG40
R3 VAXG41
R2 VAXG42
R1 VAXG43
R0 VAXG44
R21 VAXG45
R20 VAXG46
R19 VAXG47
R18 VAXG48
R17 VAXG49
R16 VAXG50
R15 VAXG51
R14 VAXG52
R13 VAXG53
R12 VAXG54

GRAPHICS

SENSE LINES

VREF

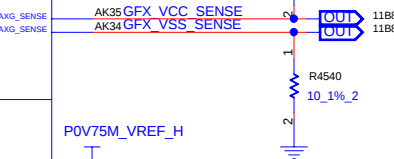
DDR3 -1.5V RAILS

SERIAL

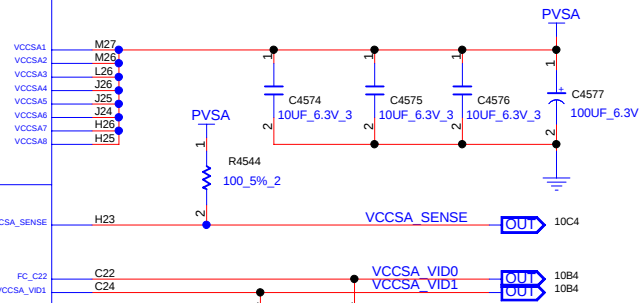
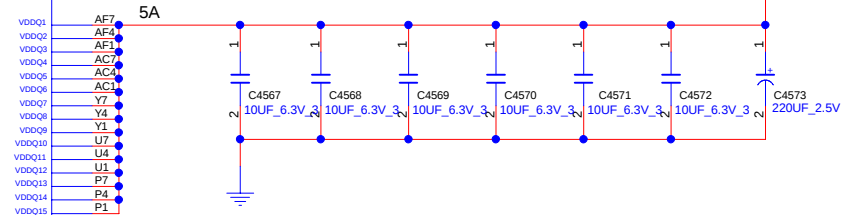
MISC

1.8V RAIL

LOTES_ACA_ZIF_069_P01_989P

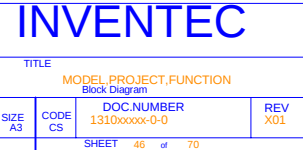


NOTE : DDR_WR_VREF SHOULD HAVE 20/20 MIL WHEREVER POSSIBLE

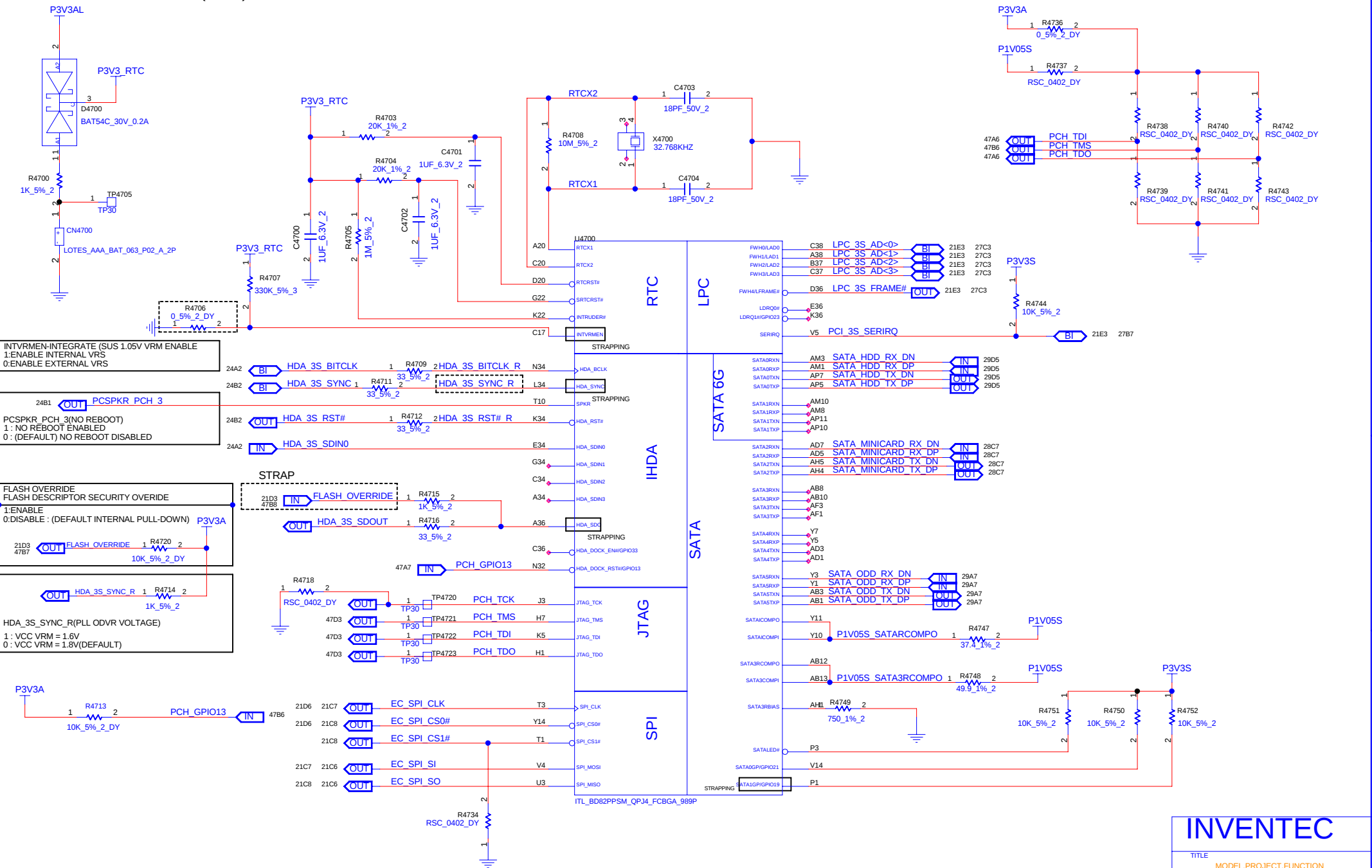


INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



REFERENCE 4700~4949(PCH)



INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

XXX

DATE

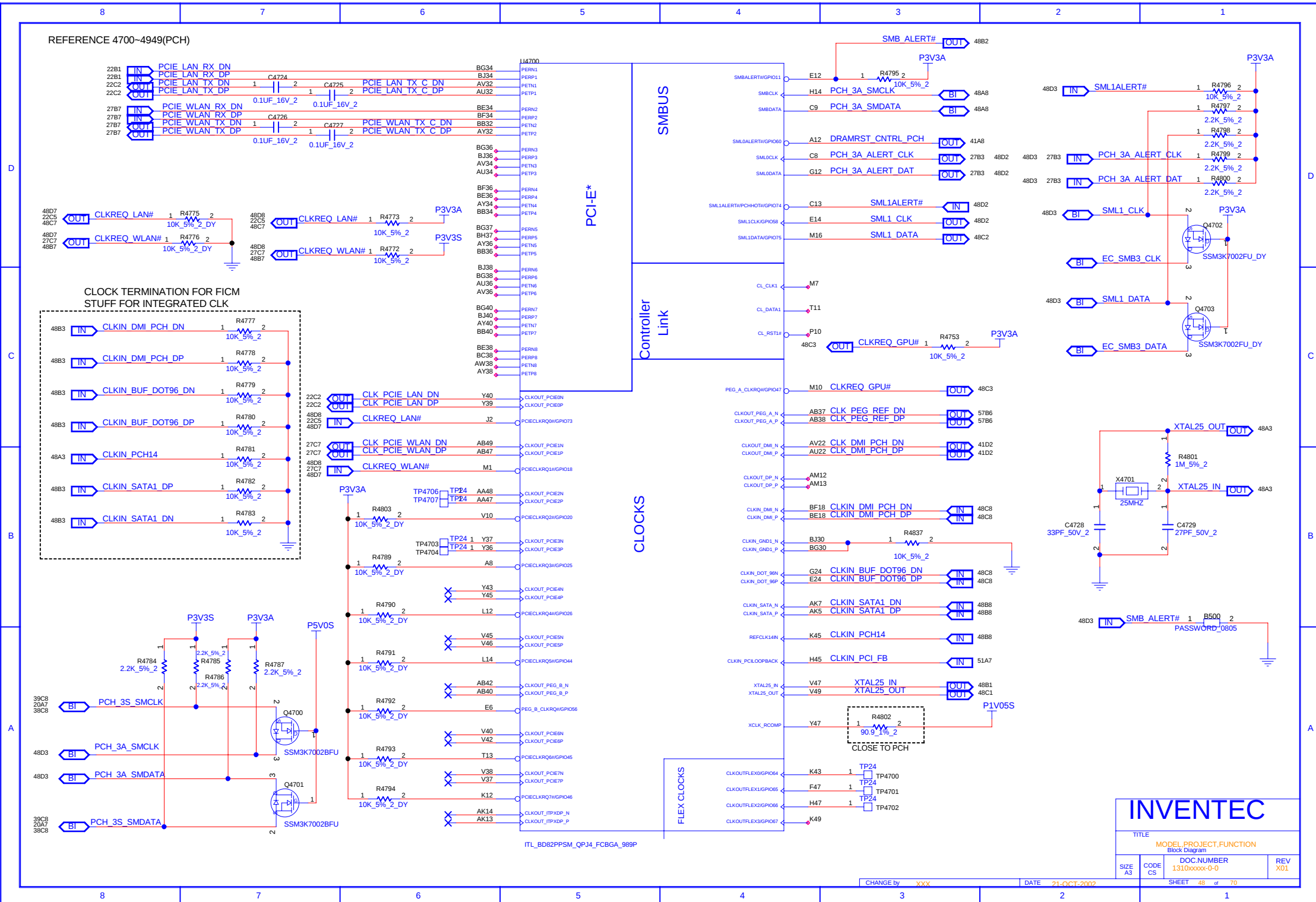
21-OCT-2002

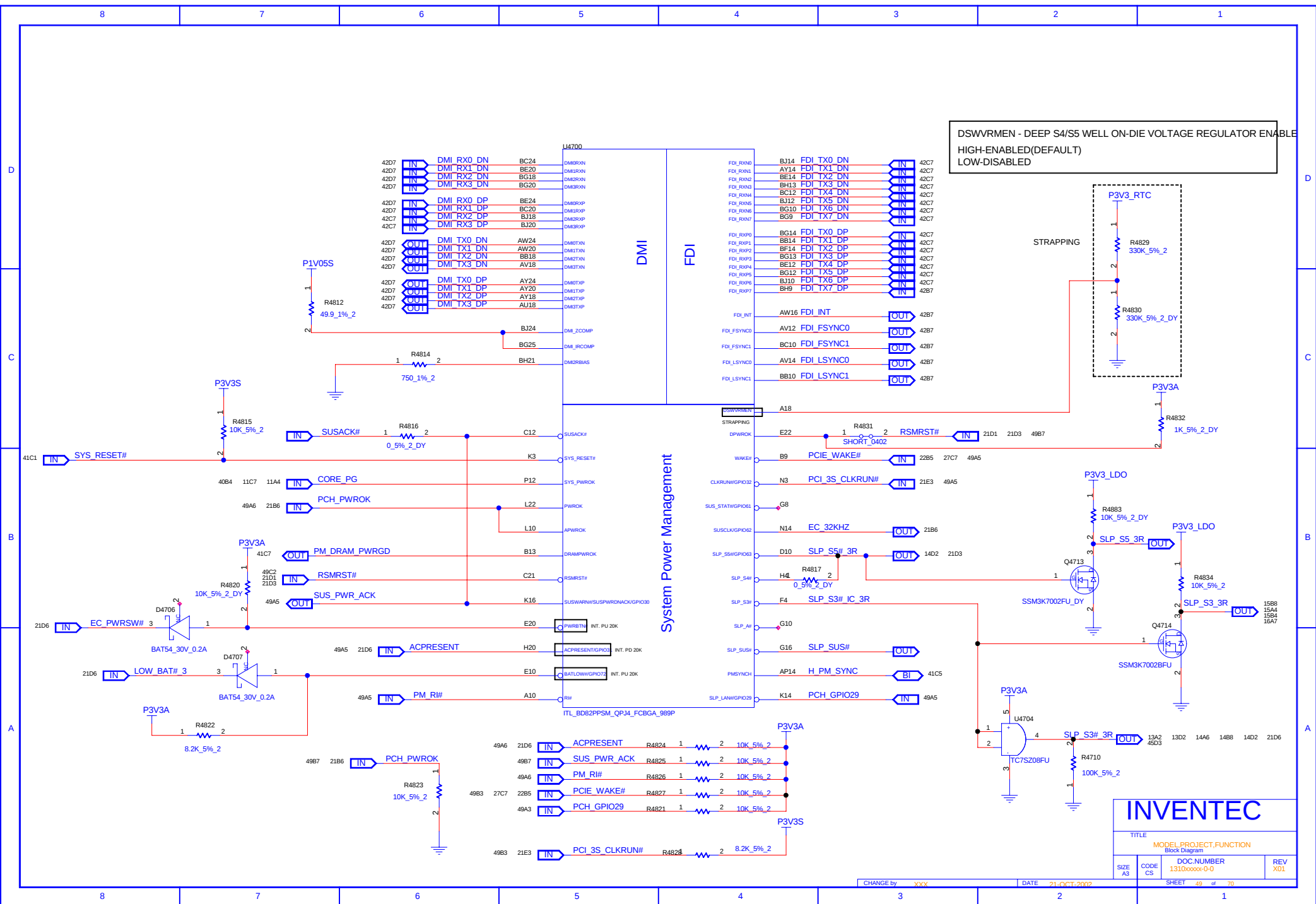
SHEET

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of

70



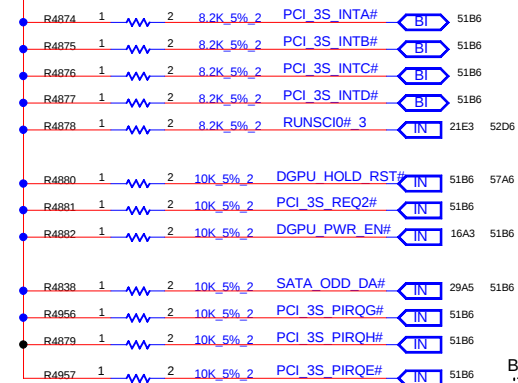




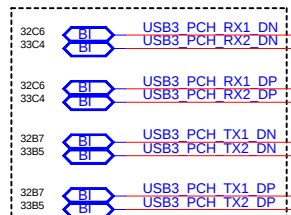
REFERENCE 4700-4949(PCH)

GPIOS1 BBS_BIT1	GPIOS19 BBS_BIT0	BOOT BIOS DESTINATION
0	1	RESERVED(NAND)
1	0	-----
1	1	SPI (DEFAULT)
0	0	LPC

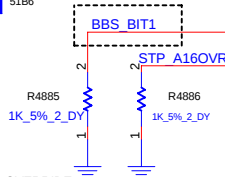
P3V3S



ROUTE WITH 90 OHMS IMPEDANCE
TOTAL LENGTH NO LONGER THAN 11 INCHES



BBS STRAPPING



LOW=A16 SWAP OVERRIDE

STP_A160VR
TOP-BLOCK SWAP OVERRIDE
HIGH=DEFAULT

P3V3A

10K_5%_2_DY

P3V3A PME#

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

P3V3A

10K_5%_2_DY

MACHINE ID 0: USB ID0
MACHINE ID 1: POWER EXPRESS (YES:1 NO:0)
MACHINE ID 2: UMA:1 / DIS:0
MACHINE ID 3: MAINSTREAM:1 / ENTRY:0
MACHINE ID 4: HDMI:1 / NO HDMI:0
MACHINE ID 5: SLEEP&CHARGE&DOLBY:(YES1 / NO 0)
MACHINE ID 6: USB ID1
MACHINE ID 1_DB : 35W CPU ONLY :1/ 35W&45W CPU:0

USB ID0	USB ID1	USB ID2	USB ID3	USB ID4	USB ID5	USB ID6	USB ID7	USB ID8	USB ID9
1	1	0	0	0	0	0	0	0	0

NOTE:10K_5%(60130B1030ZT)

U4700
BG26 TP1
BJ26 TP2
BH25 TP3
BJ16 TP4
BG16 TP5
AH38 TP6
AH37 TP7
AK43 TP8
AK45 TP9
C18 TP10
N30 TP11
H3 TP12
AH12 TP13
AM4 TP14
AM5 TP15
Y13 TP16
K24 TP17
L24 TP18
AB46 TP19
AB45 TP20

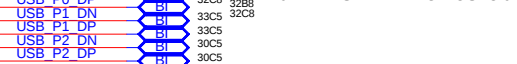
RSVD

NVRAM

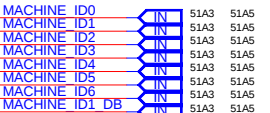
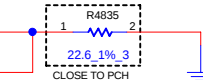
RSVD1 AY7
RSVD2 AV7
RSVD3 AU3
RSVD4 BG4
RSVD5 AT10
RSVD6 BC8
RSVD7 AU2
RSVD8 AT4
RSVD9 AT3
RSVD10 AT1
RSVD11 AY3
RSVD12 AT5
RSVD13 AV3
RSVD14 AV1
RSVD15 BB1
RSVD16 BA3
RSVD17 BB5
RSVD18 BB3
RSVD19 BB7
RSVD20 BE8
RSVD21 BD4
RSVD22 BF6
RSVD23 AV5
RSVD24 AV10
RSVD25 AT8
RSVD26 AY5
RSVD27 BA2
RSVD28 AT12
RSVD29 BF3

NOTE:
USB2.0/3.0 COMBO-USB2.0 PORT 0,1 MAPPED USB3.0 PORT 1,2

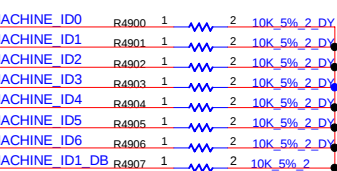
P0.P1 RESERVER FOR USB3.0



CARD READER
WLAN
WEBCAM
3G



TO BE USED AS GPIO



NOTE:10K_5%(60130B1030ZT)

INVENTEC

TITLE
MODEL PROJECT.FUNCTION
Block Diagram

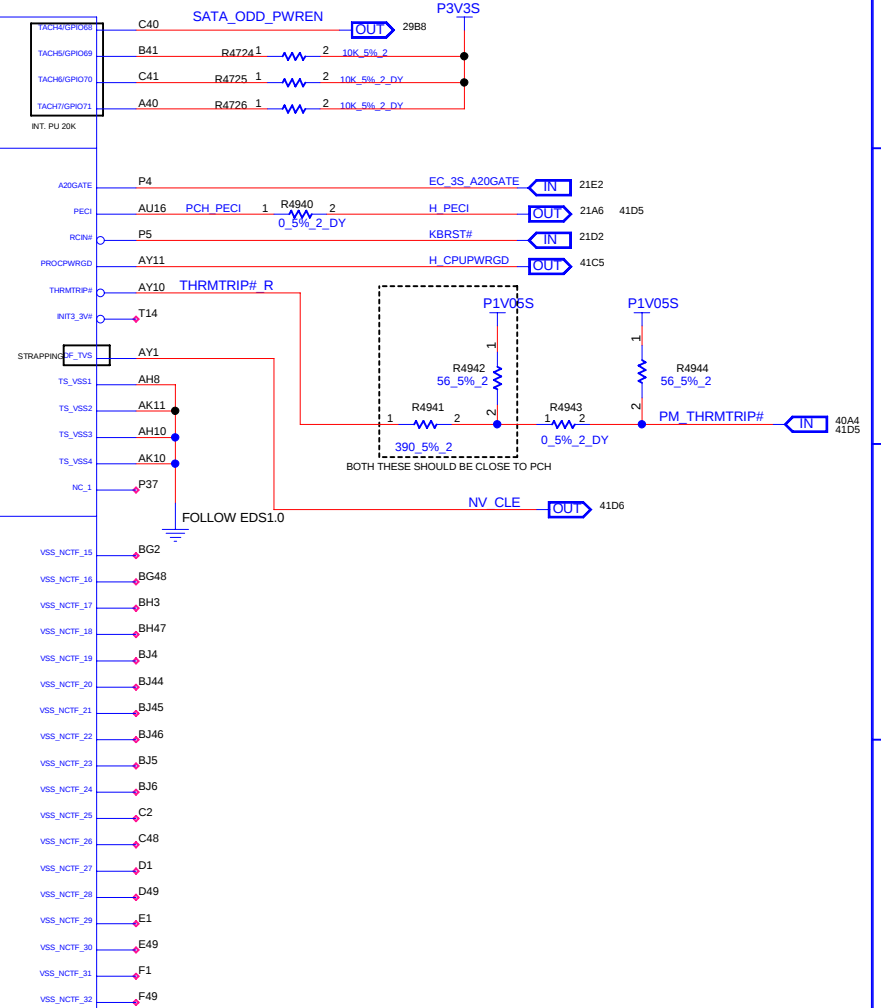
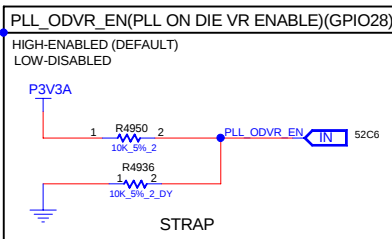
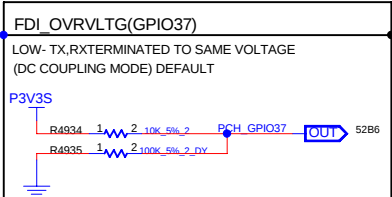
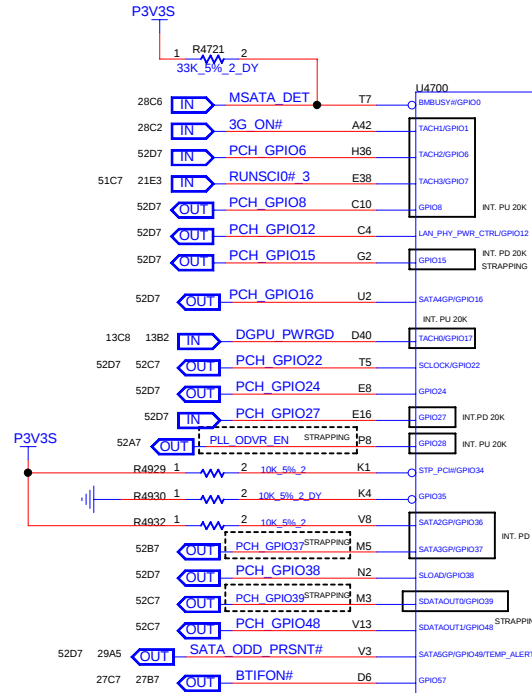
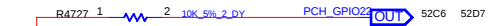
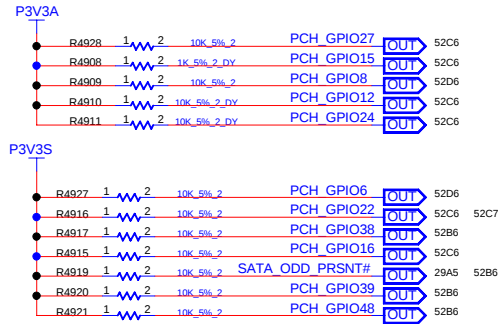
DOC NUMBER
1310xxxx-0-0

REV
X01

SHEET 51 of 70

CHANGE by XXX

DATE 21-OCT-2002



INVENTEC

TITLE			
MODEL PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET 52 of 70			

REFERENCE 4700-4949(PCH)

TITLE

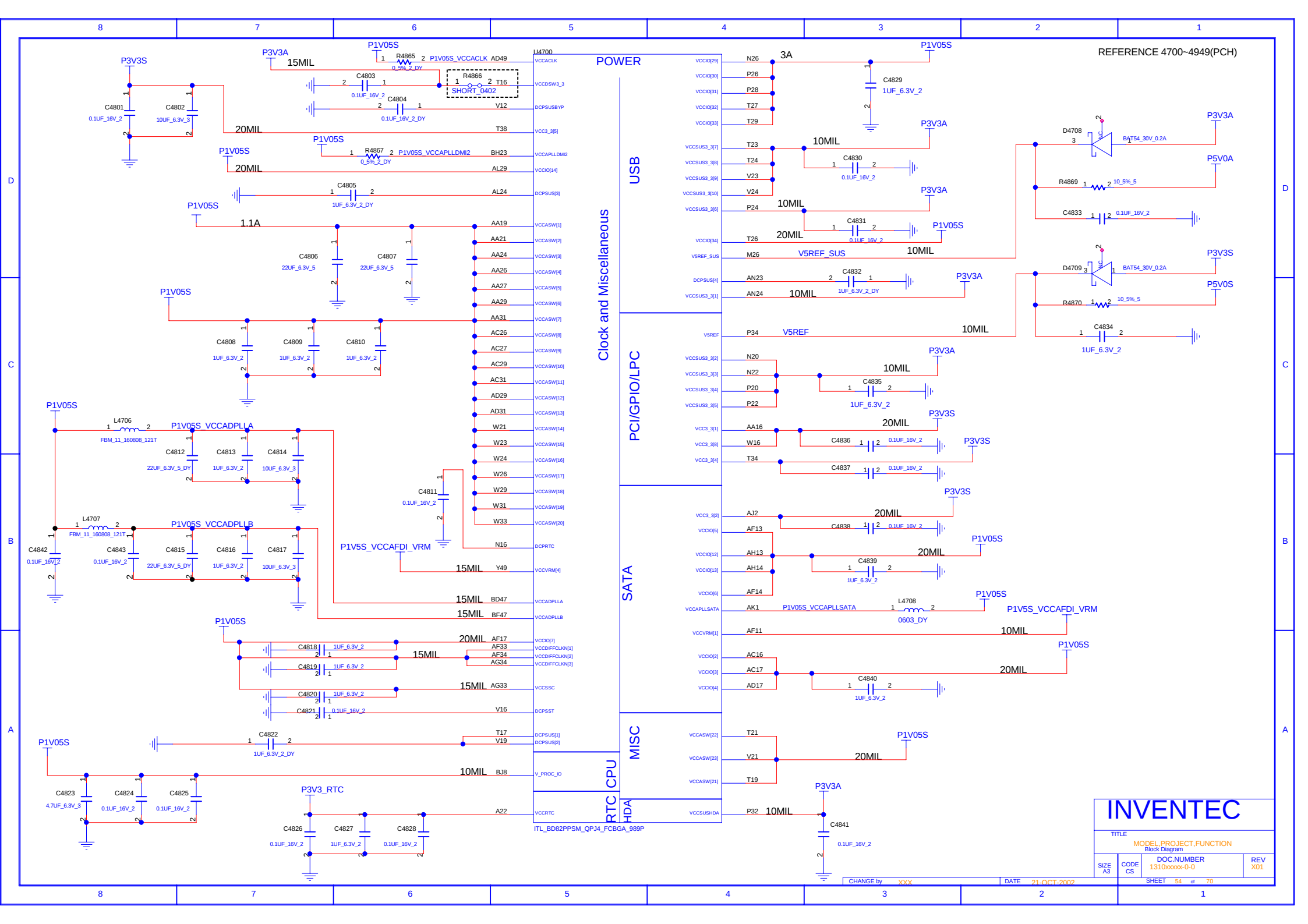
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE	CODE	DOC.NUMBER	
		1010-0000-0-0	

SIZE A3	CODE CS	1310xxxx-U-U	
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SHEET 53 of 70

CHANGE by	XXX	DATE	21-OCT-2002
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8

7

6

5

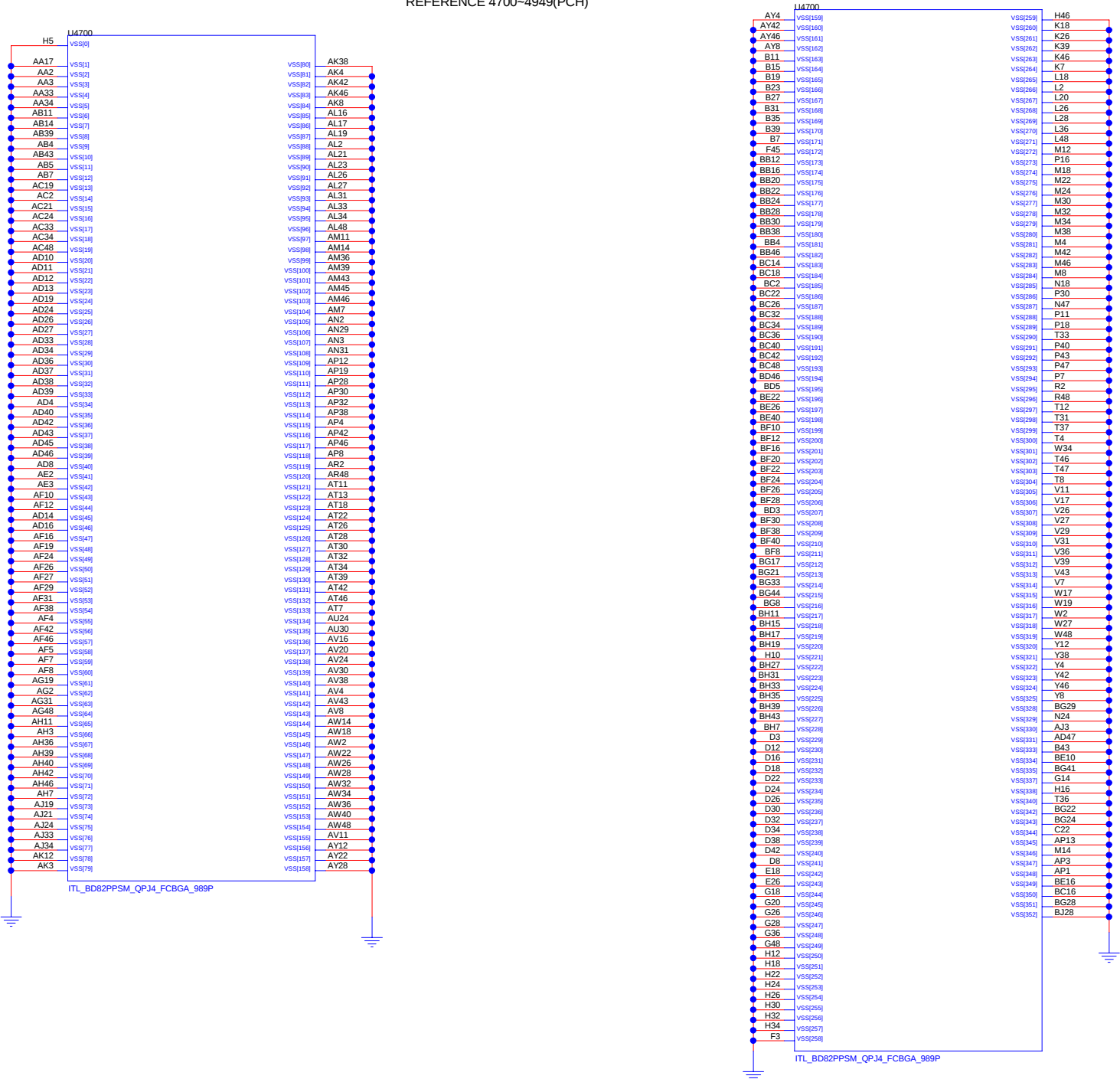
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3

2

1

REFERENCE 4700~4949(PCH)



INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

SIZE

A3

CODE

CS

DOC NUMBER

1310xxxx-0-0

REV

X01

DATE 21-OCT-2002

CHANGE by XXX

SHEET 55 of 70

8

7

6

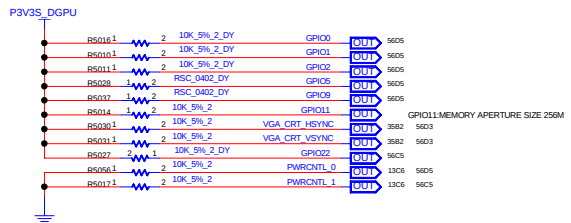
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4

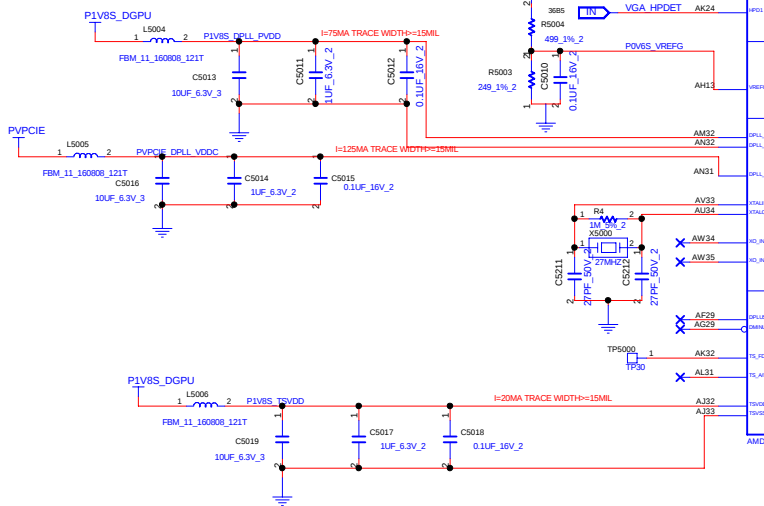
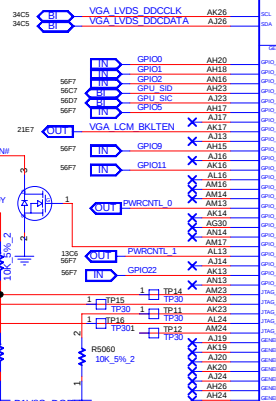
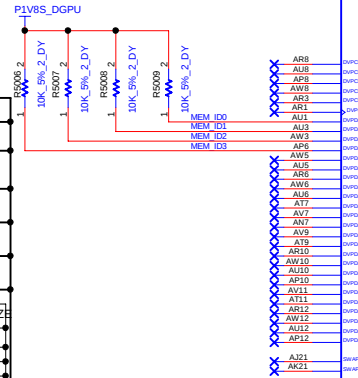
3

2

1

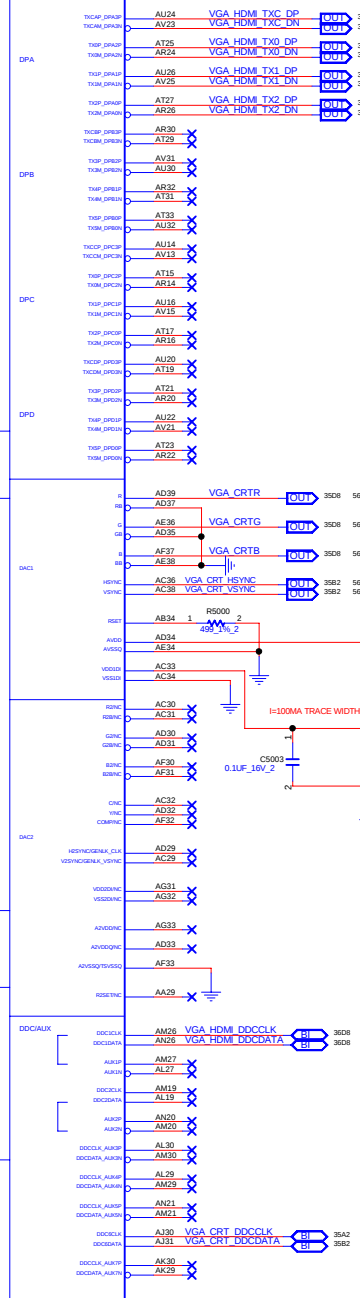
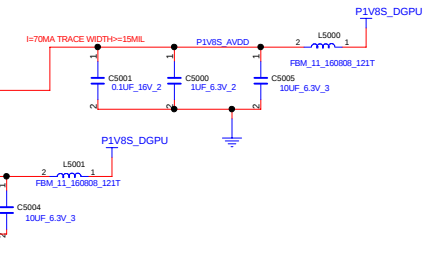
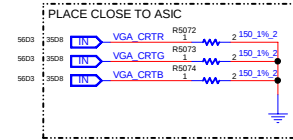


PIN BASE STRAPS					
GPIO_0	TRANSMITTER POWER SAVING ENABLE	0: 50% TX OUTPUT SWING (DEFAULT) 1: FULL TX OUTPUT SWING			
GPIO_1	PCIe TRANSMITTER DE-EMPHASIS	0: DE-EMPHASIS DISABLED (DEFAULT) 1: DE-EMPHASIS ENABLED			
GPIO_2	GEN1/GEN2 ENABLE	0: GEN1 (DEFAULT) 1: GEN2			
GPIO_8	MUST BE LOW DURING RESET	LEFT UNCONNECTED			
GPIO_9	VGA DISABLE	0: ENABLE (DEFAULT) 1: DISABLE			
GPIO_[11:13]	MEMORY APERTURE SIZE	GPIO_13	GPIO_12	GPIO_11	MEMORY APERTURE SIZE
		0	0	0	128M
		0	0	1	256M
		0	1	0	64M
		0	1	1	32M
GPIO_22	ENABLE EXTERNAL BIOS ROM DEVICE	0: DISABLE (DEFAULT) 1: ENABLE			
HSYNC[1] VSYNC[0]	AUDIO[1:0]	00: NO AUDIO FUNCTION 01: AUDIO FOR DP ONLY 10: AUDIO FOR DP AND HDMI IF DONGLE IS DETECTED 11: AUDIO FOR BOTH DP AND HDMI			



THAMES (6019B0917601)

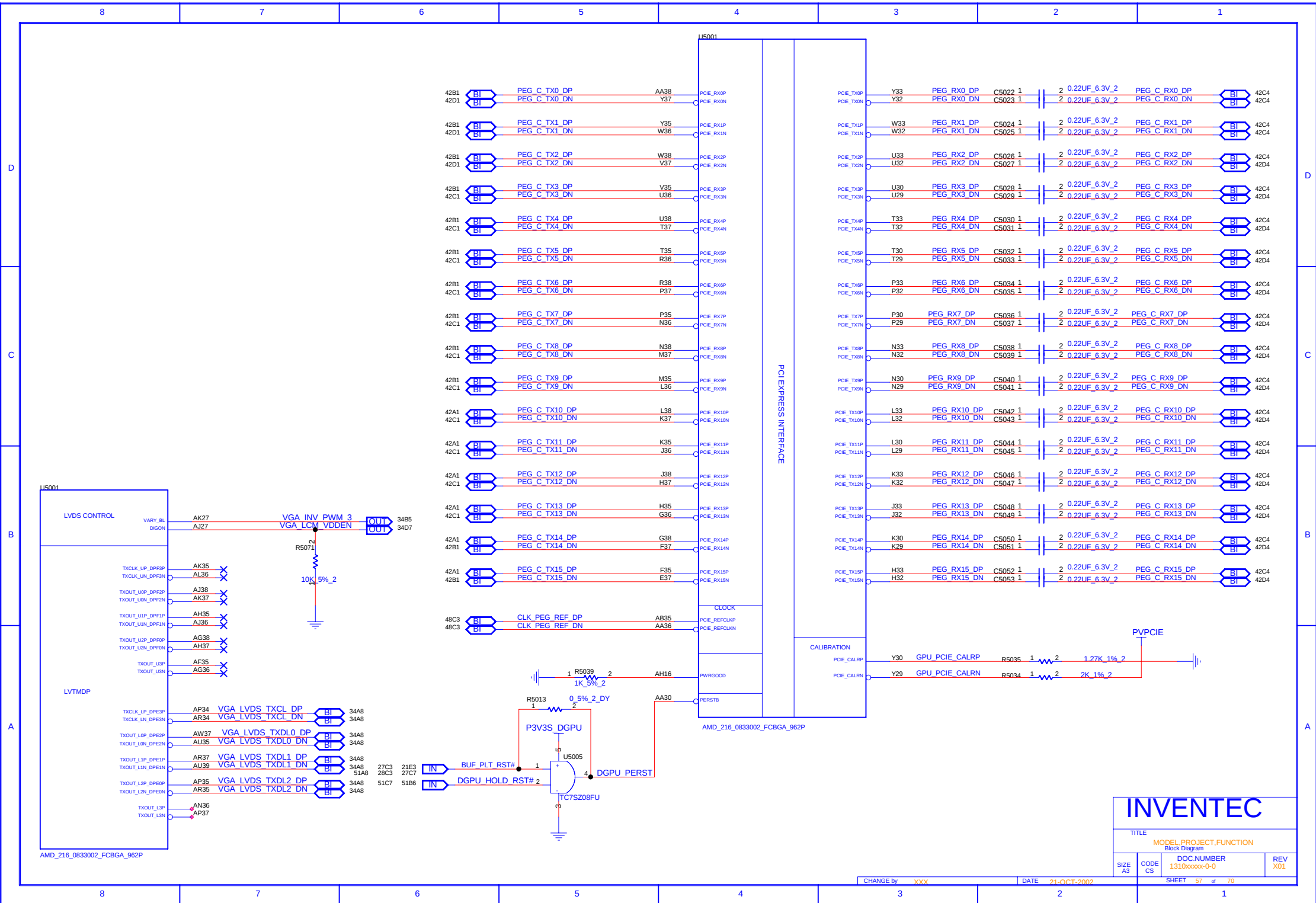
MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	
0	0	0	0	HYNX1GDRF4 512MB
0	0	0	1	HYNX2GBRFP 41GB
0	0	1	0	HYNX2GBRFP 41GB
0	1	0	0	RESERVE
0	1	0	1	HYNX1GDRF4 512MB
0	1	1	0	HYNX2GBRFP 41GB
0	1	1	1	RESERVE
1	0	0	0	AMDC114 512MB
1	0	0	1	AMDC114 1GB
1	0	1	0	AMDC114 1GB
1	0	1	1	RESERVE
1	1	0	0	AMDC116 1GB
1	1	0	1	AMDC116 2GB
1	1	1	0	AMDC116 2GB
1	1	1	1	RESERVE



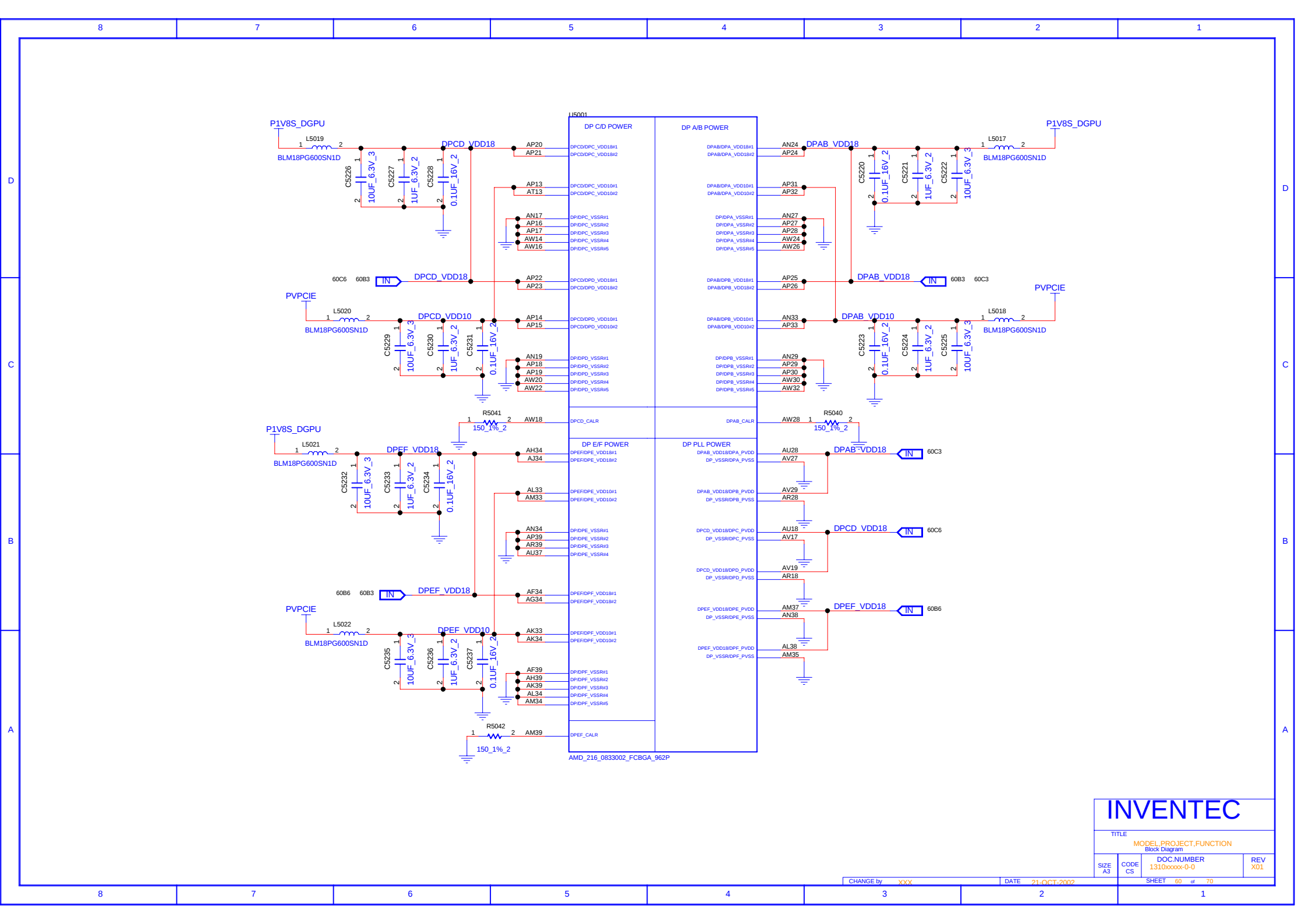
INVENTEC

TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310000000-03	201
SHEET 34 of 34			

CHANGE BY: VCK DATE: 21-07-2000





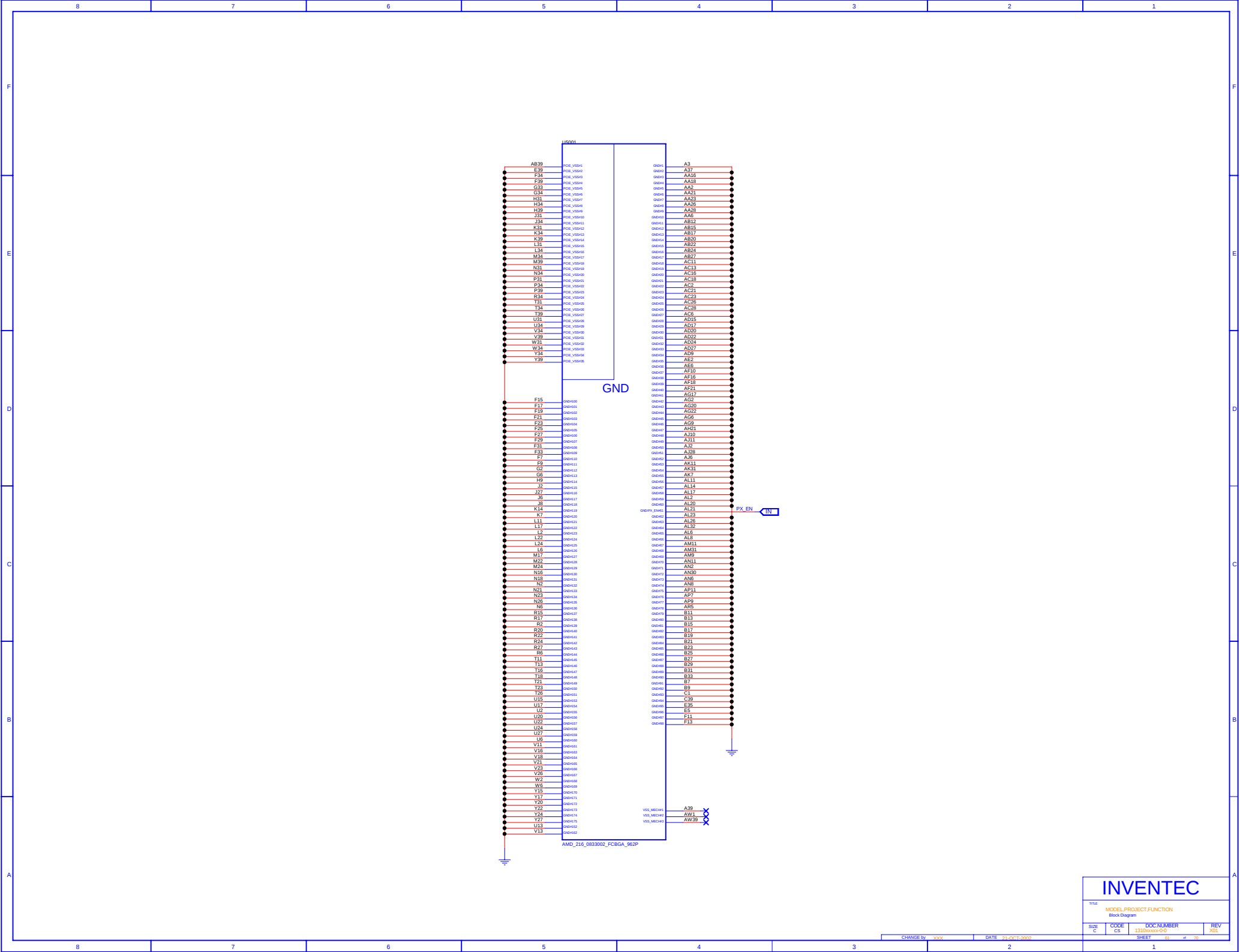


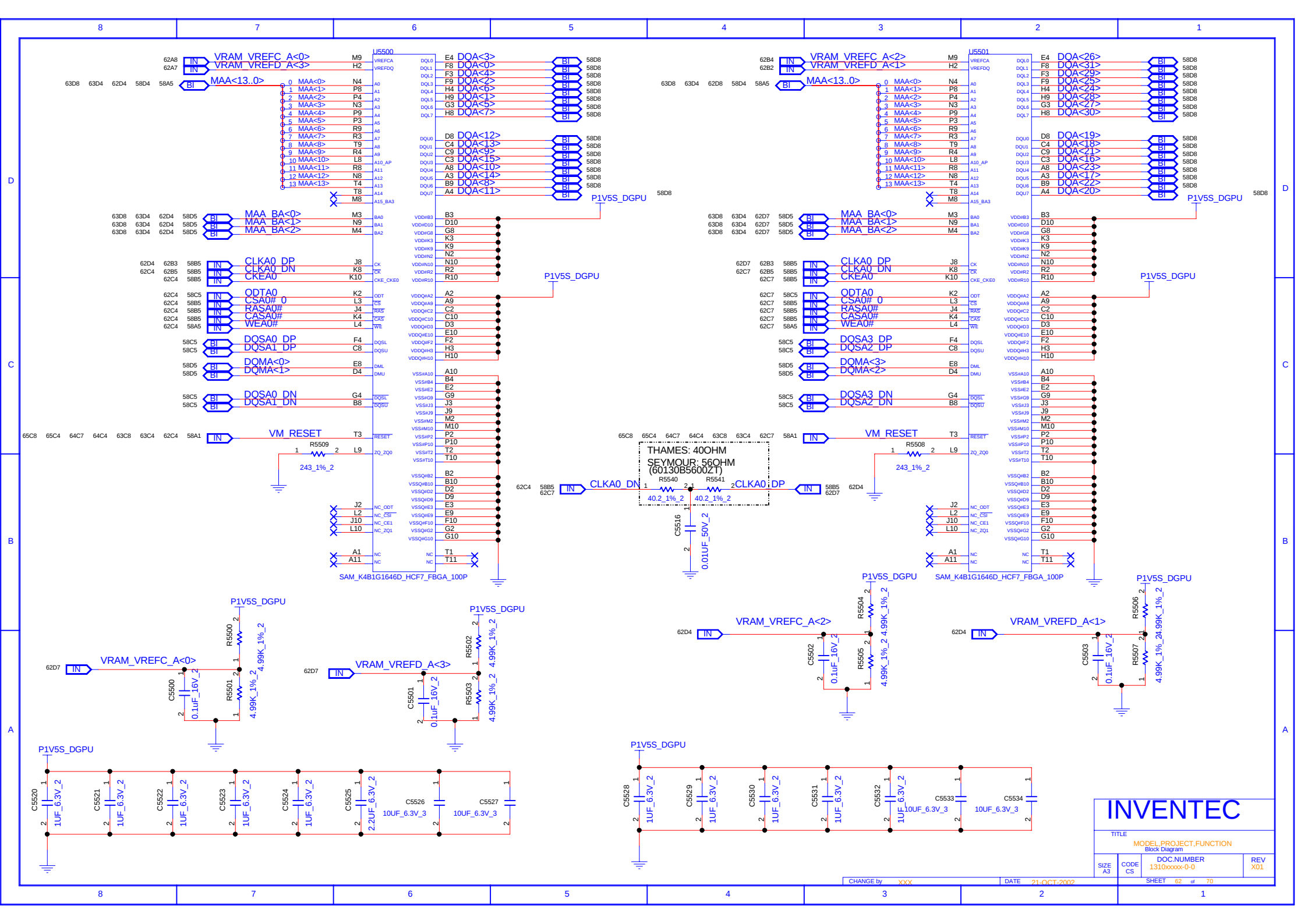
INVENTEC

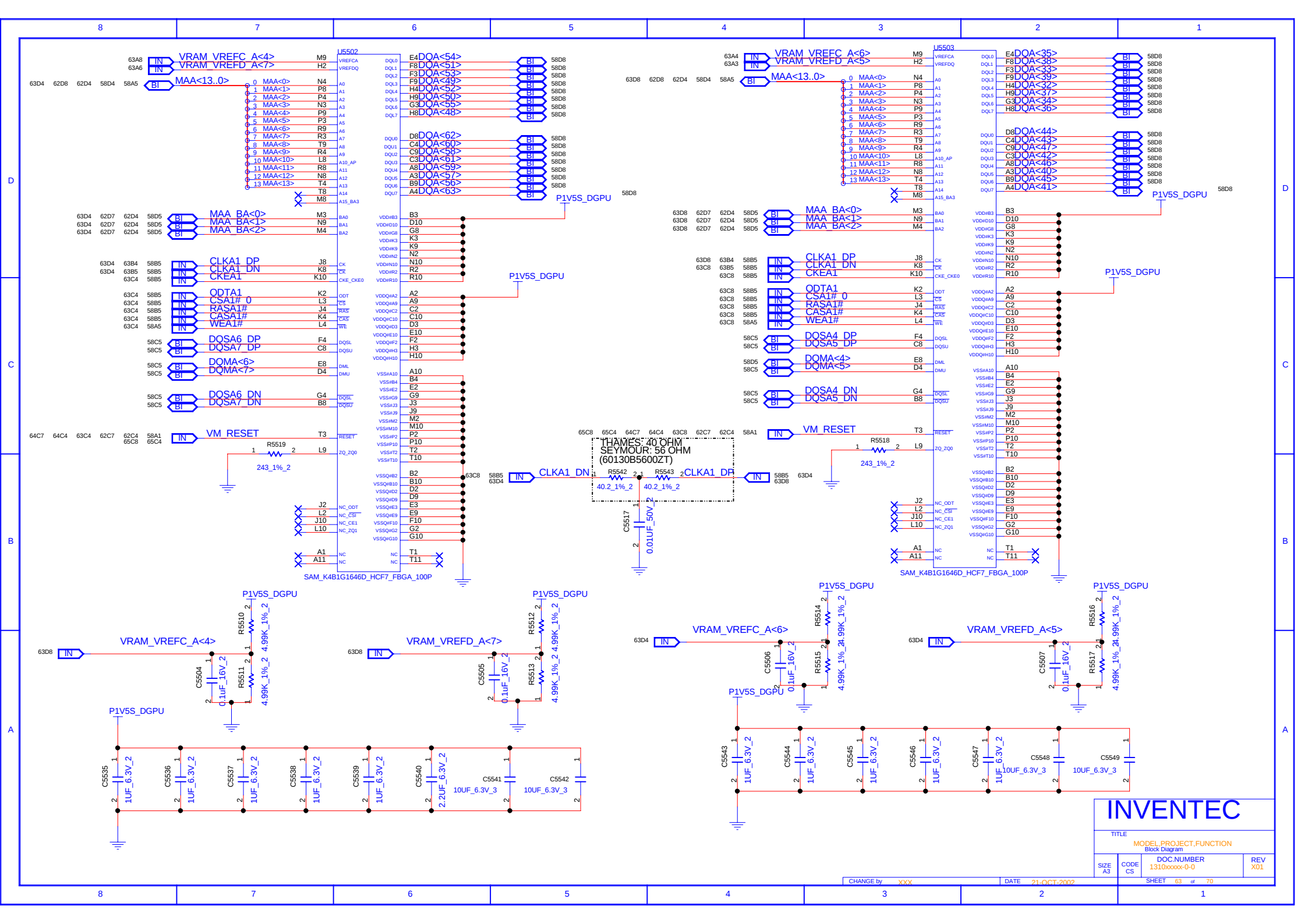
TITLE			
MODEL PROJECT FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

CHANGE by XXX DATE 21-OCT-2002

SHEET 60 of 70







INVENTEC

TITLE

MODEL PROJECT FUNCTION

Block Diagram

DOC NUMBER

1310xxxx-0-0

REV

X01

SIZE

A3

CODE

CS

CHANGE by

XXX

DATE

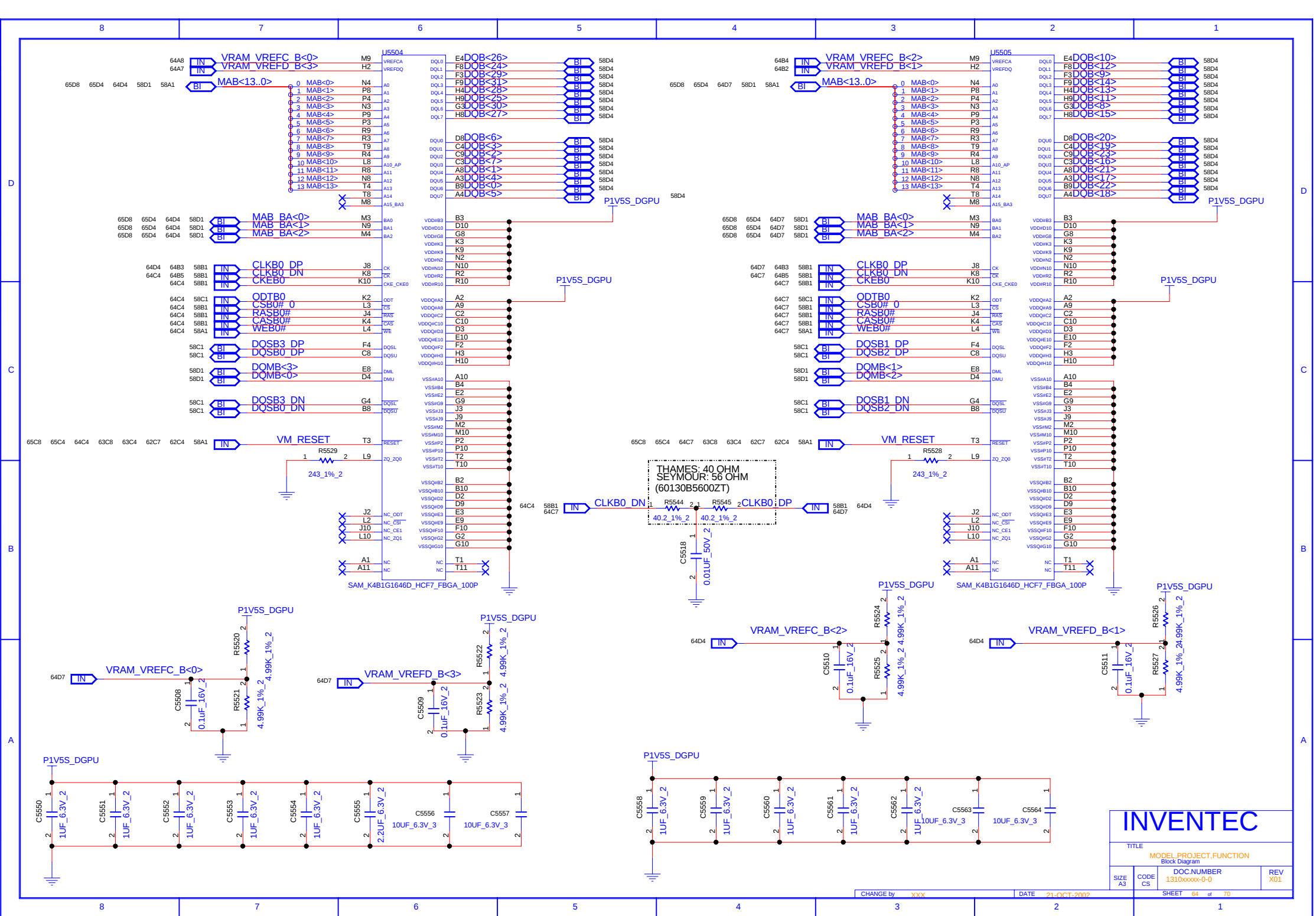
21-OCT-2002

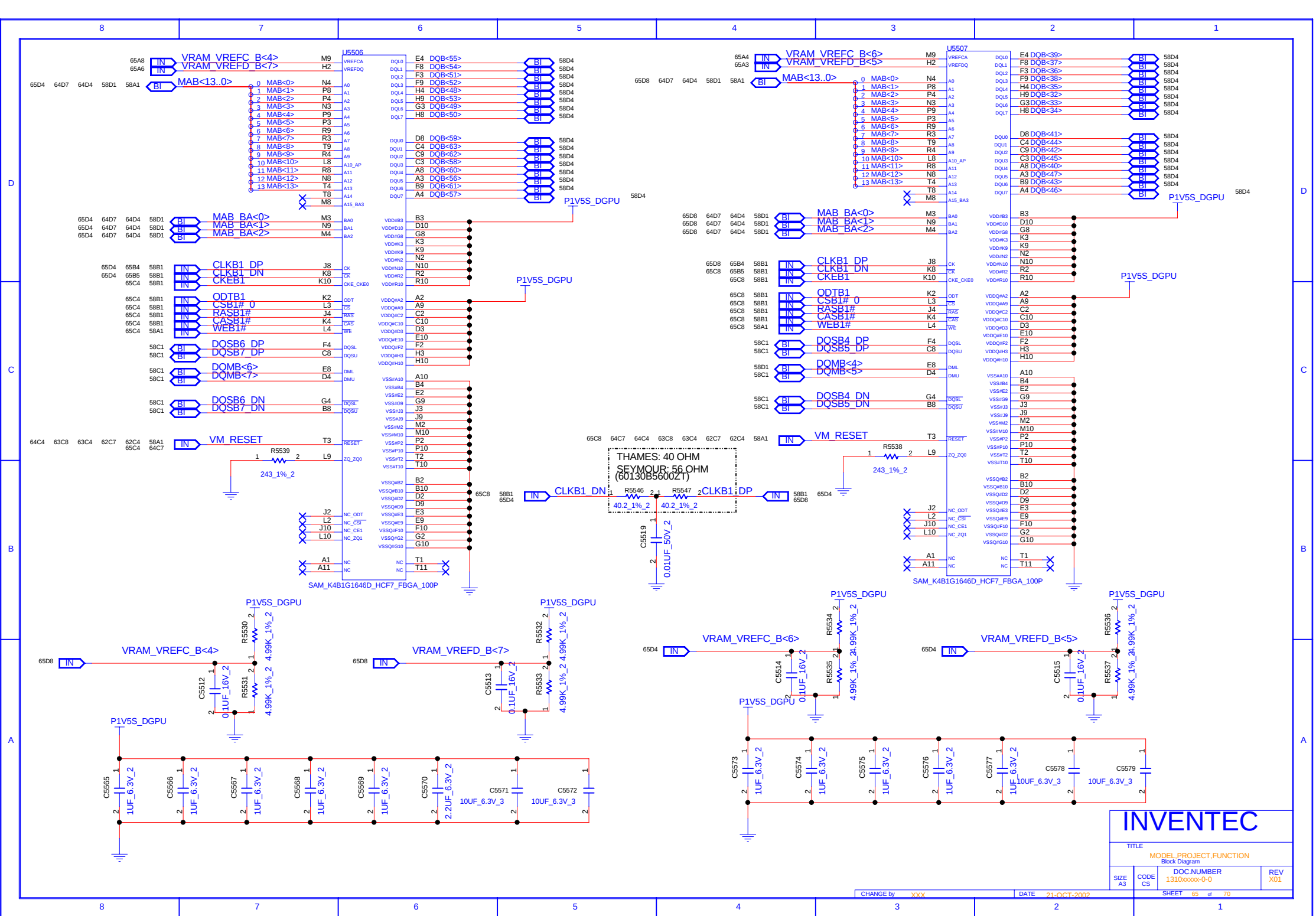
SHEET

63

of

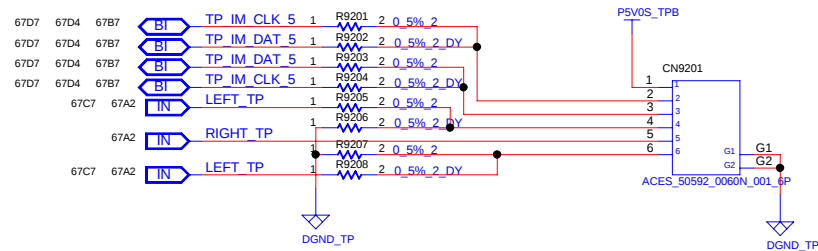
70



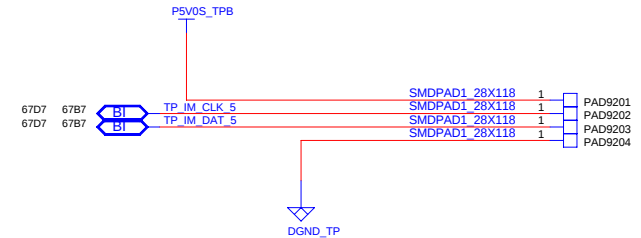


TOUCH PAD BOARD 4 PIN

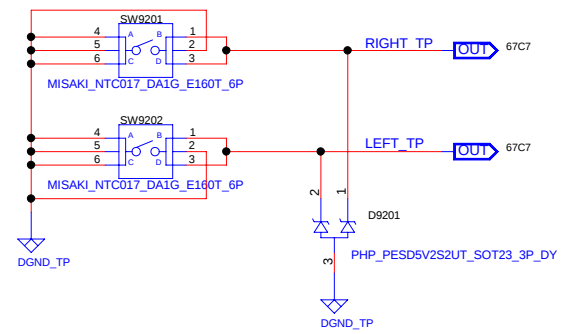
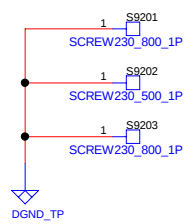
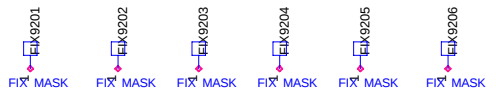
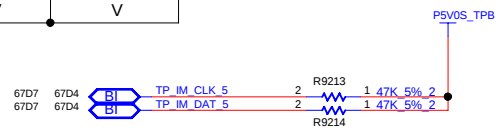
TOUCHPAD TO MODULE CONN



TOUCHPAD TO MB CONN



4PIN	IMR/METAL	TEXTURE
R9201		V
R9202	V	
R9203		V
R9204	V	
R9205		V
R9206	V	
R9207		V
R9208	V	
R9213	V	V
R9214	V	V



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

Block Diagram

SIZE A3

CODE CS

DOC NUMBER

1310xxxx-0-0

REV

X01

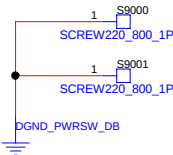
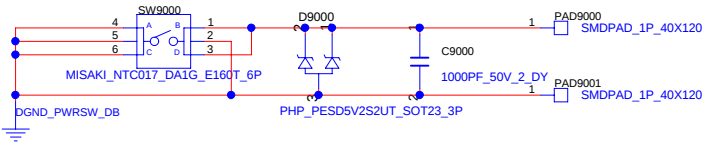
CHANGE by XXX

DATE 21-OCT-2002

SHEET 67 of 70

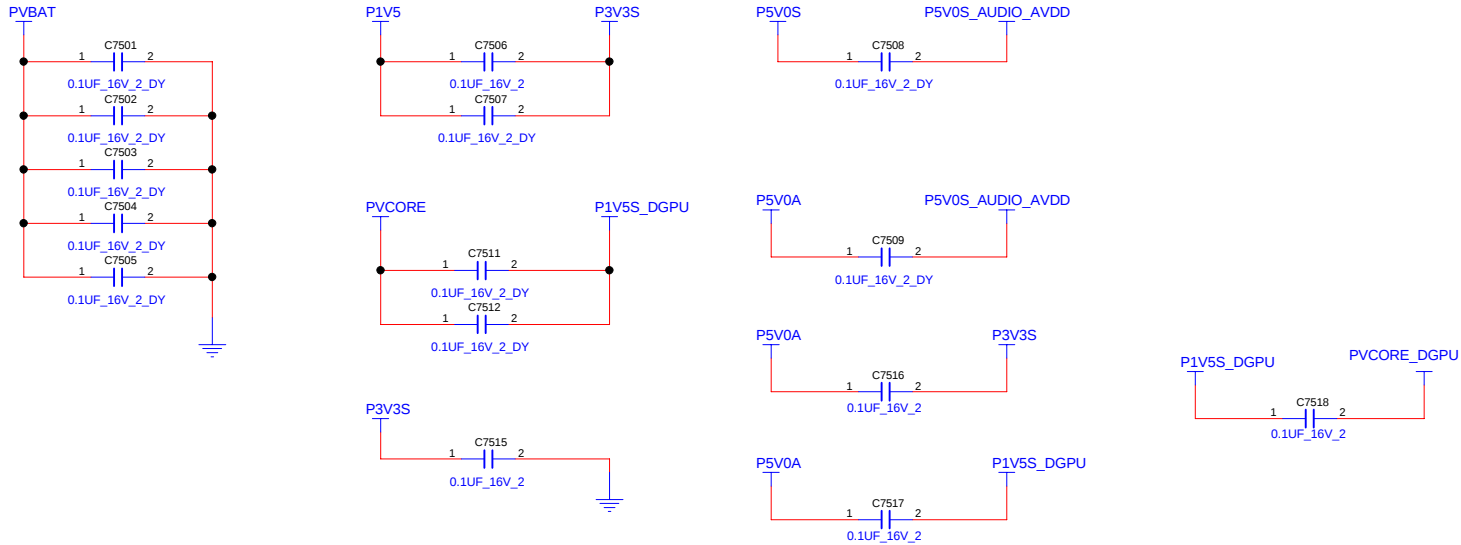
REFERENCE 9000~9999(SMALL BOARD)

POWER BUTTON



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01



INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxx-0-0	X01

D

C



A

