

DIAMOND

MV B- BUILD FINAL
2008/08/21

Engineer <i>David Du</i>	INVENTEC		
Drawn by <i>David Du</i>			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK	TITLE DIAMOND		Size A3
QA CHK	VER A02	Model Number PC8803	Sheet 1 of 67

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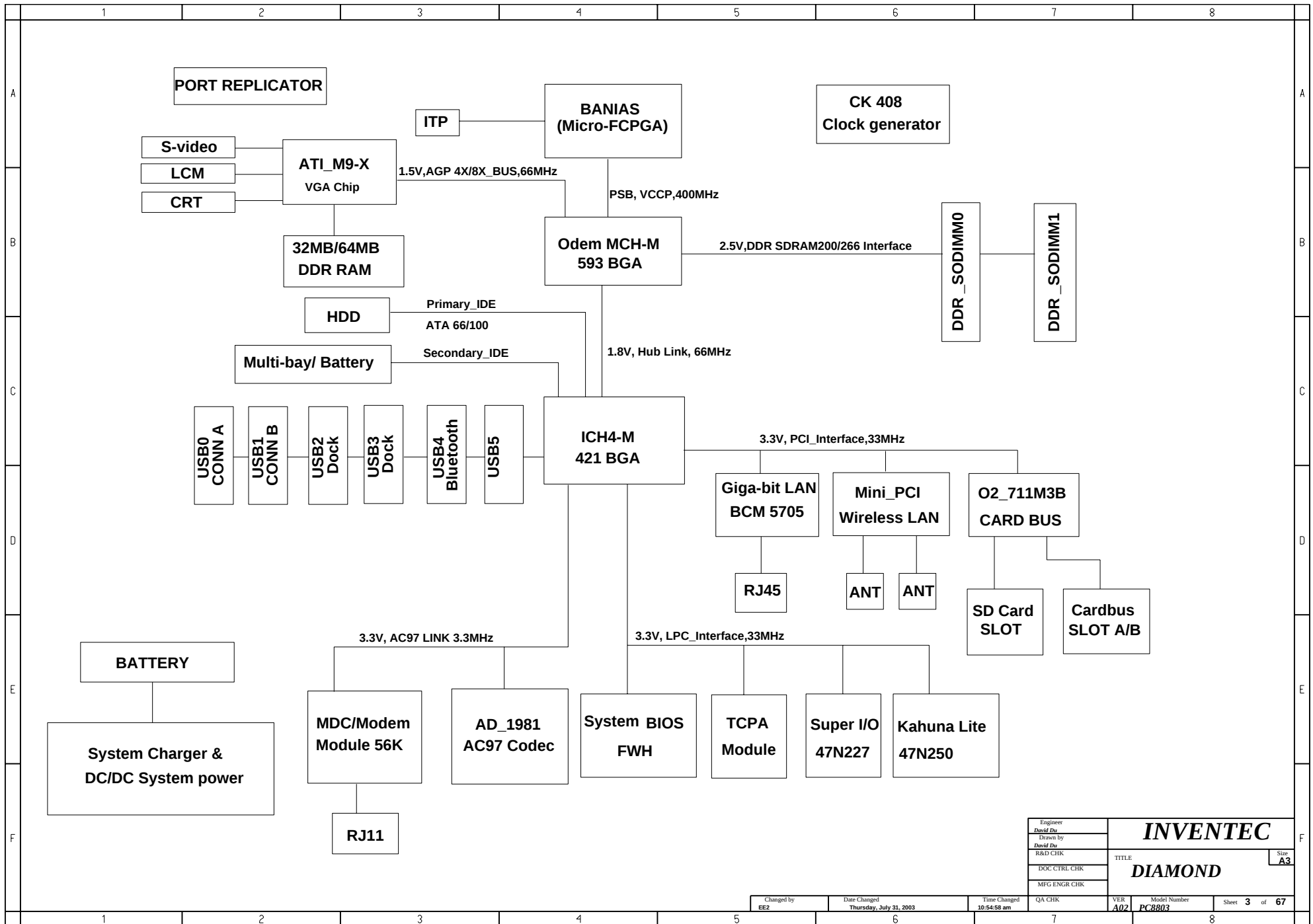
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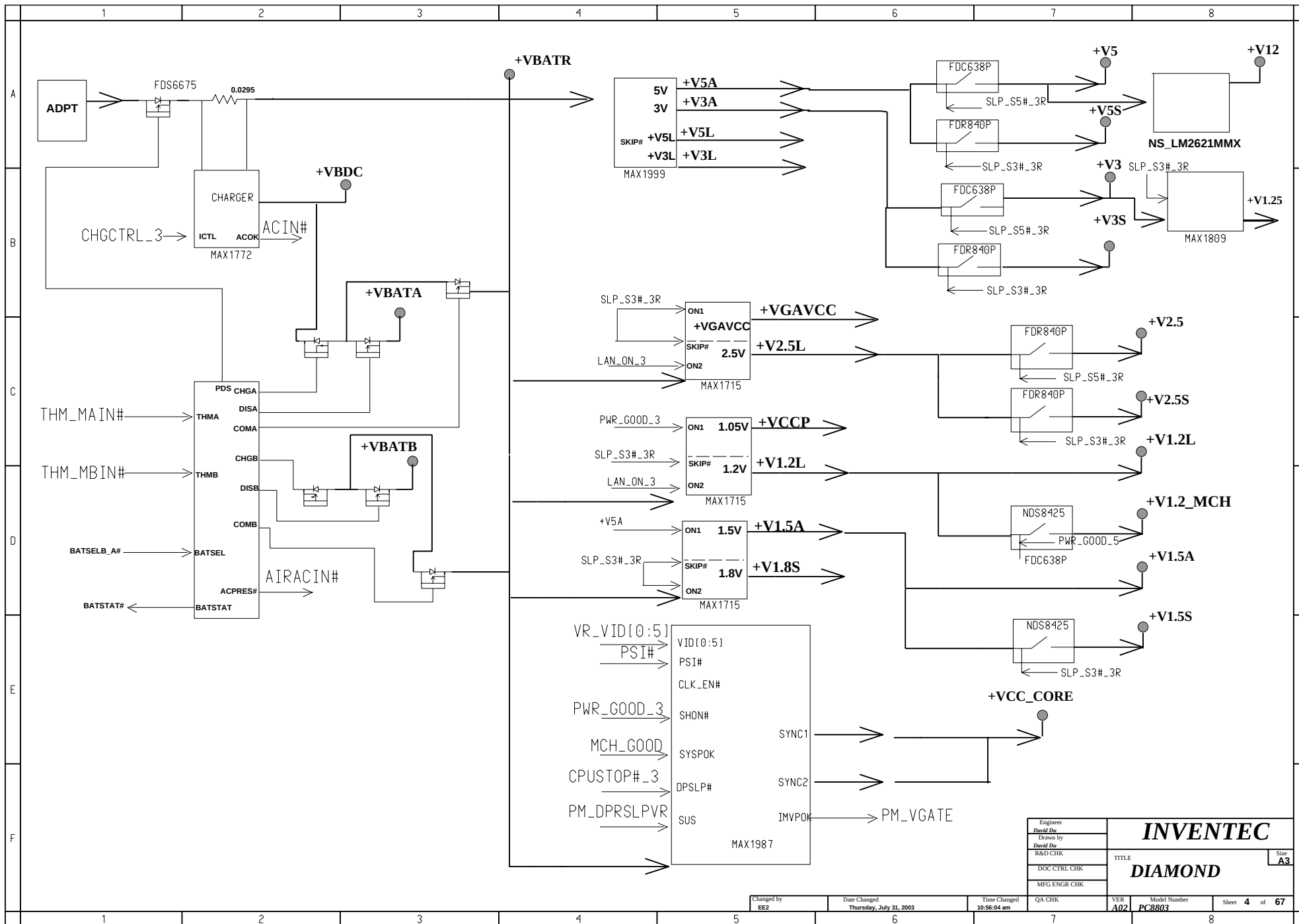
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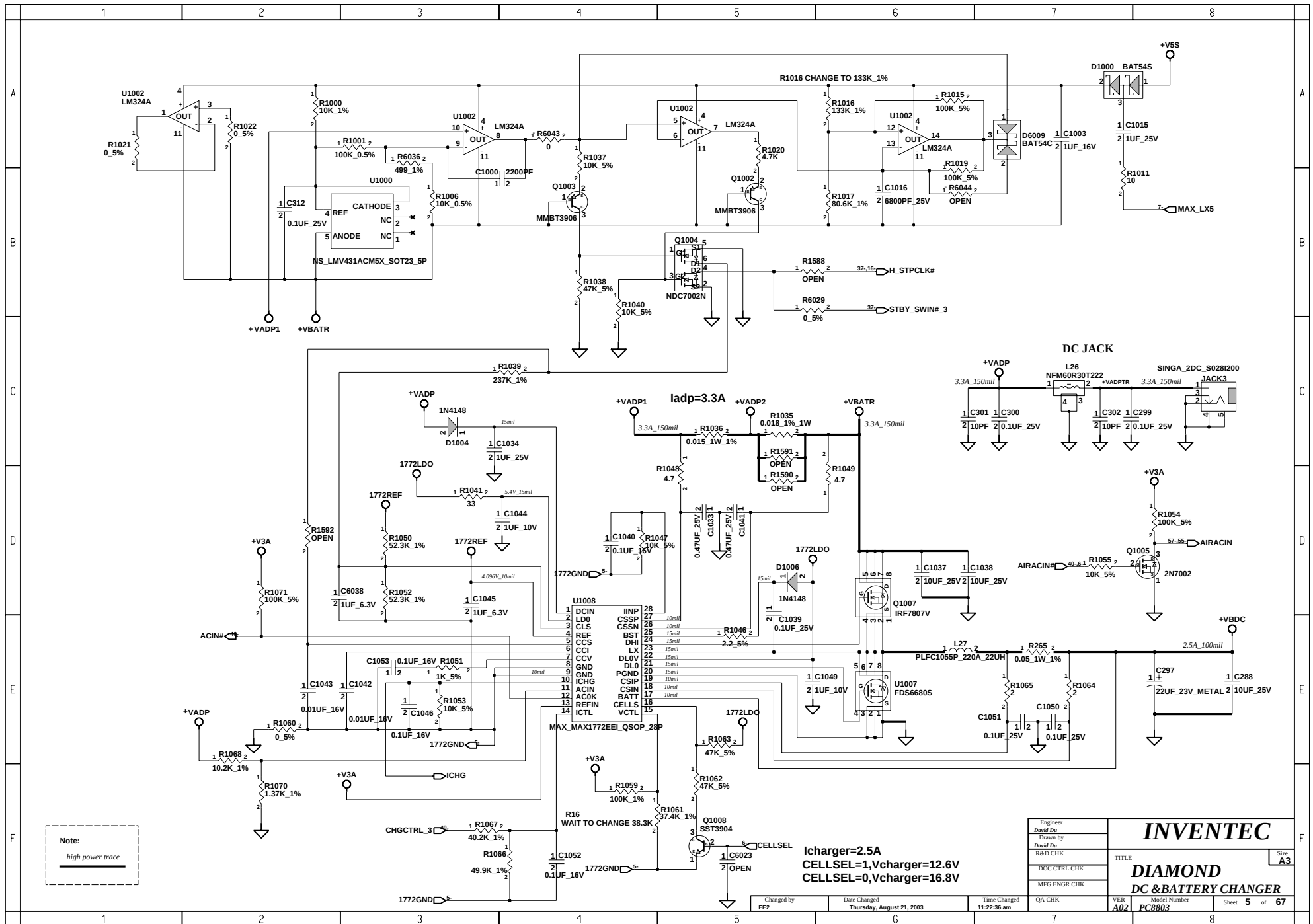
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Drawn by <i>David Du</i>						
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Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:53:56 am	QA CHK	VER A02	Model Number PC8803	Sheet 2 of 67

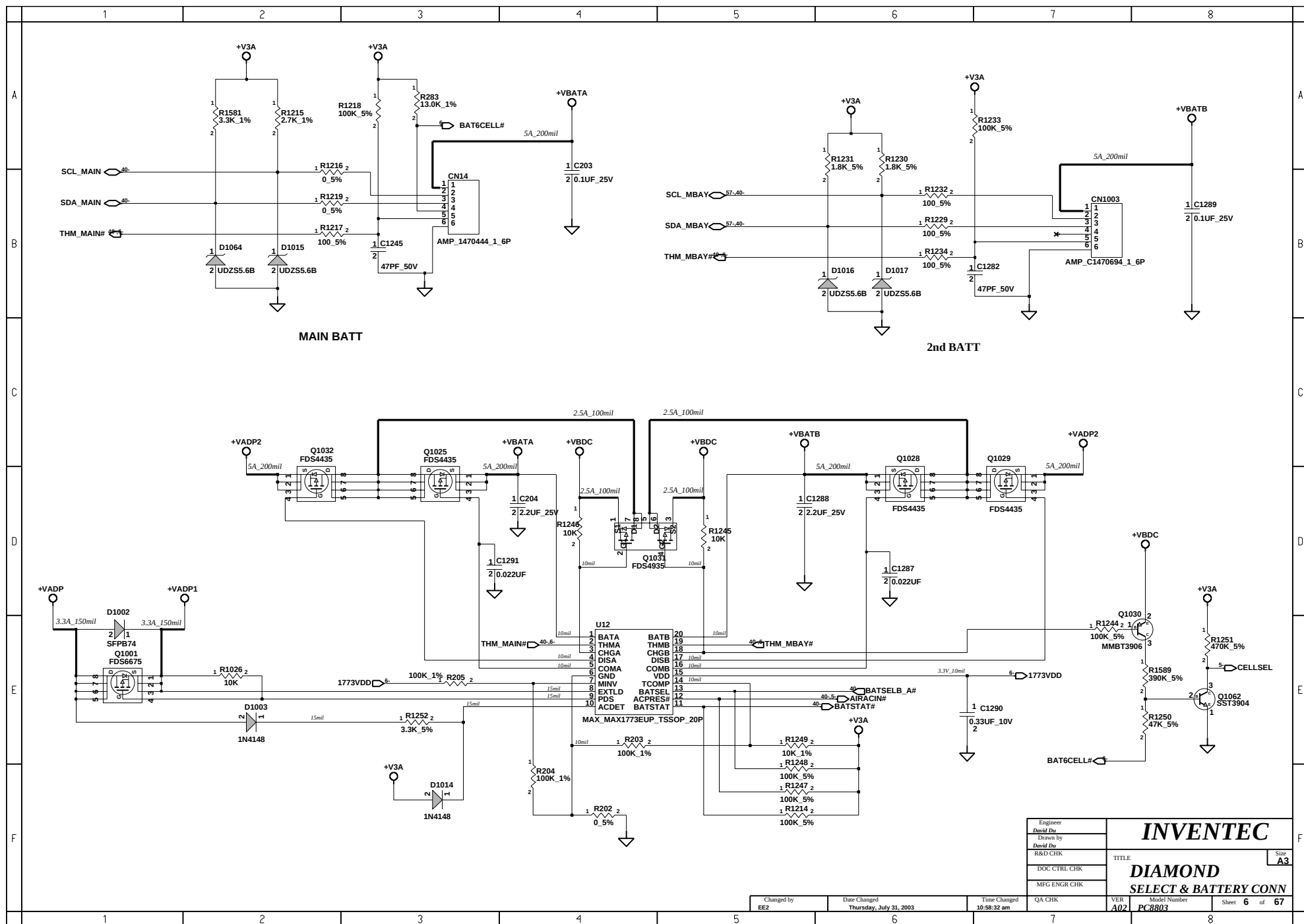


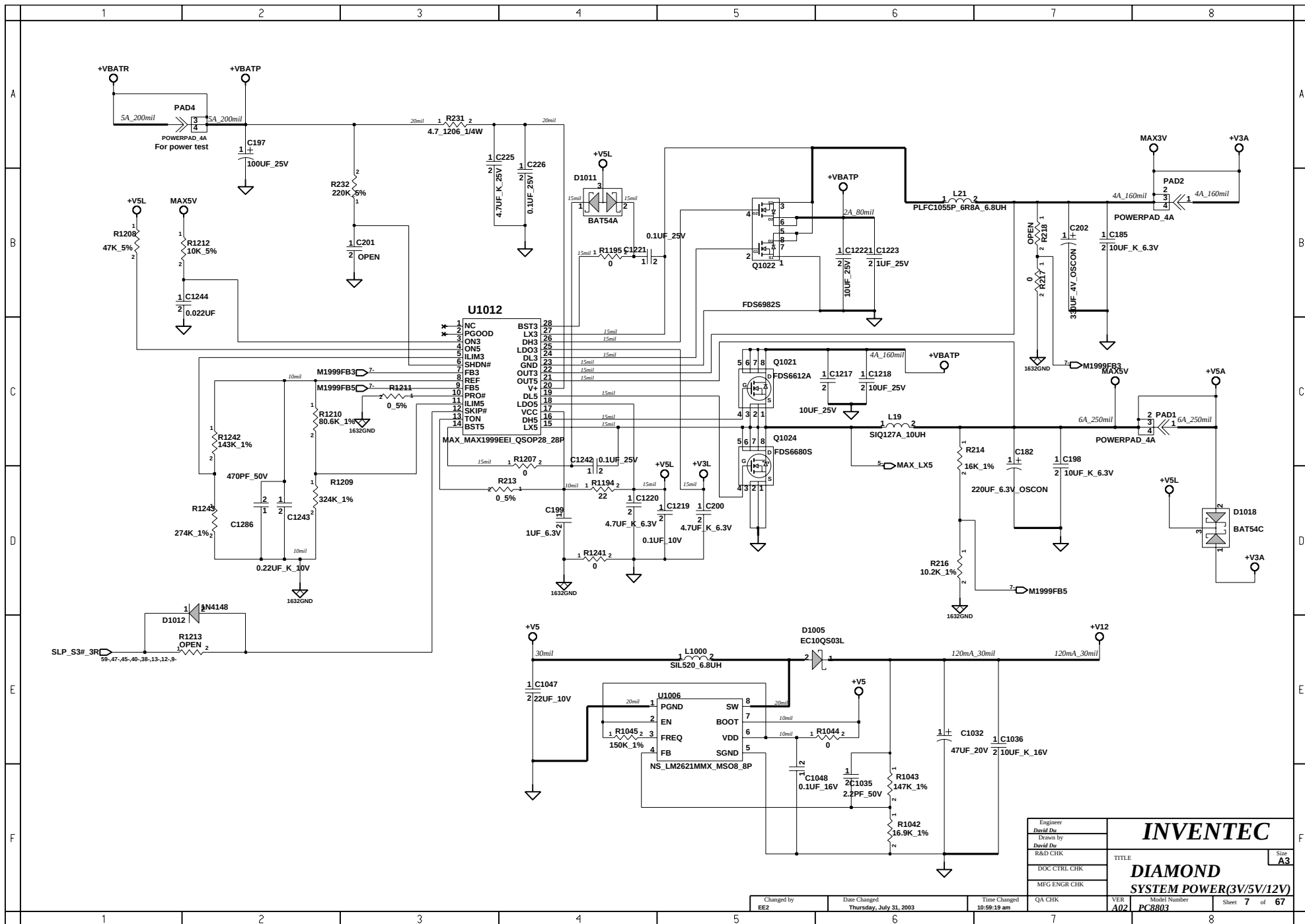
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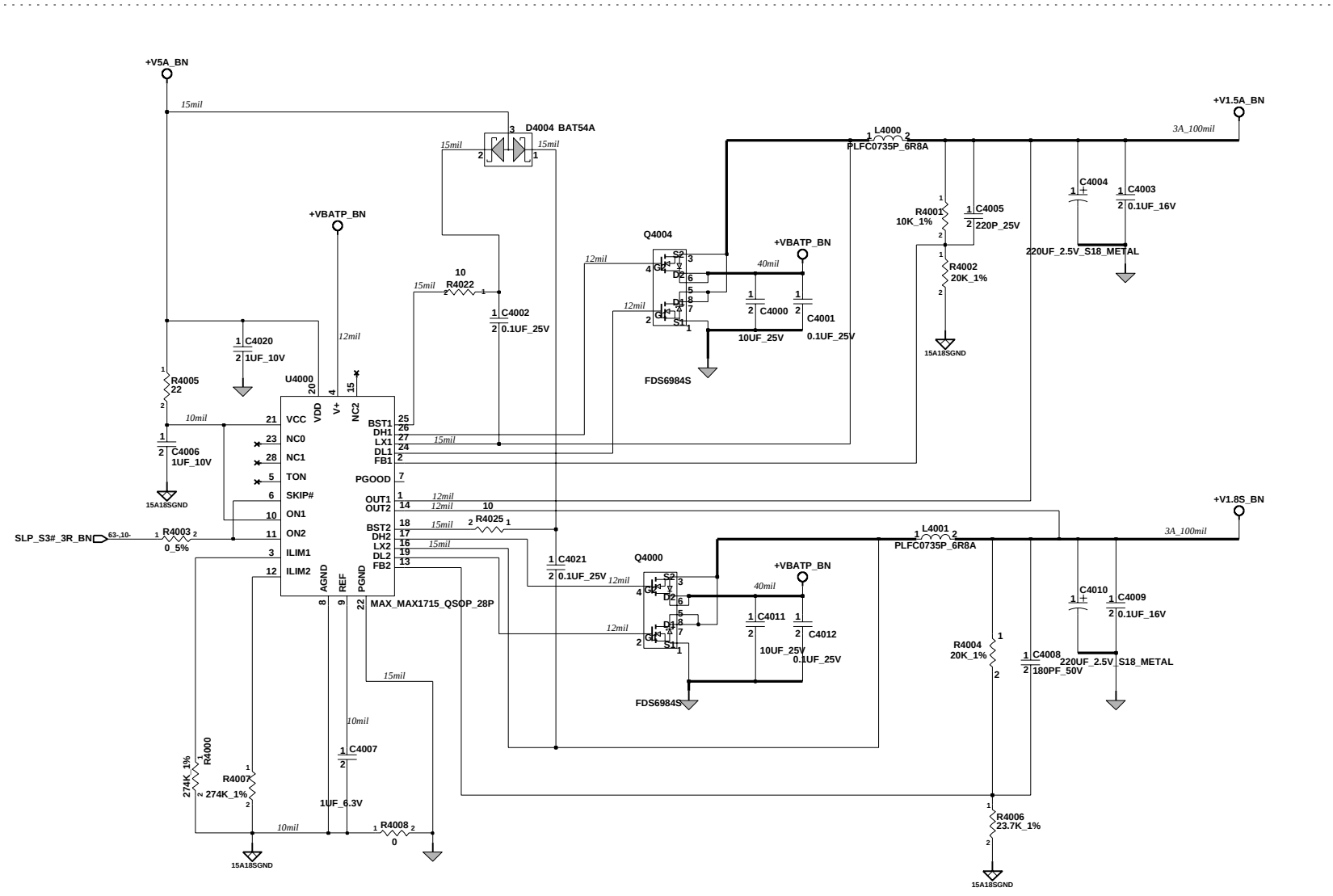


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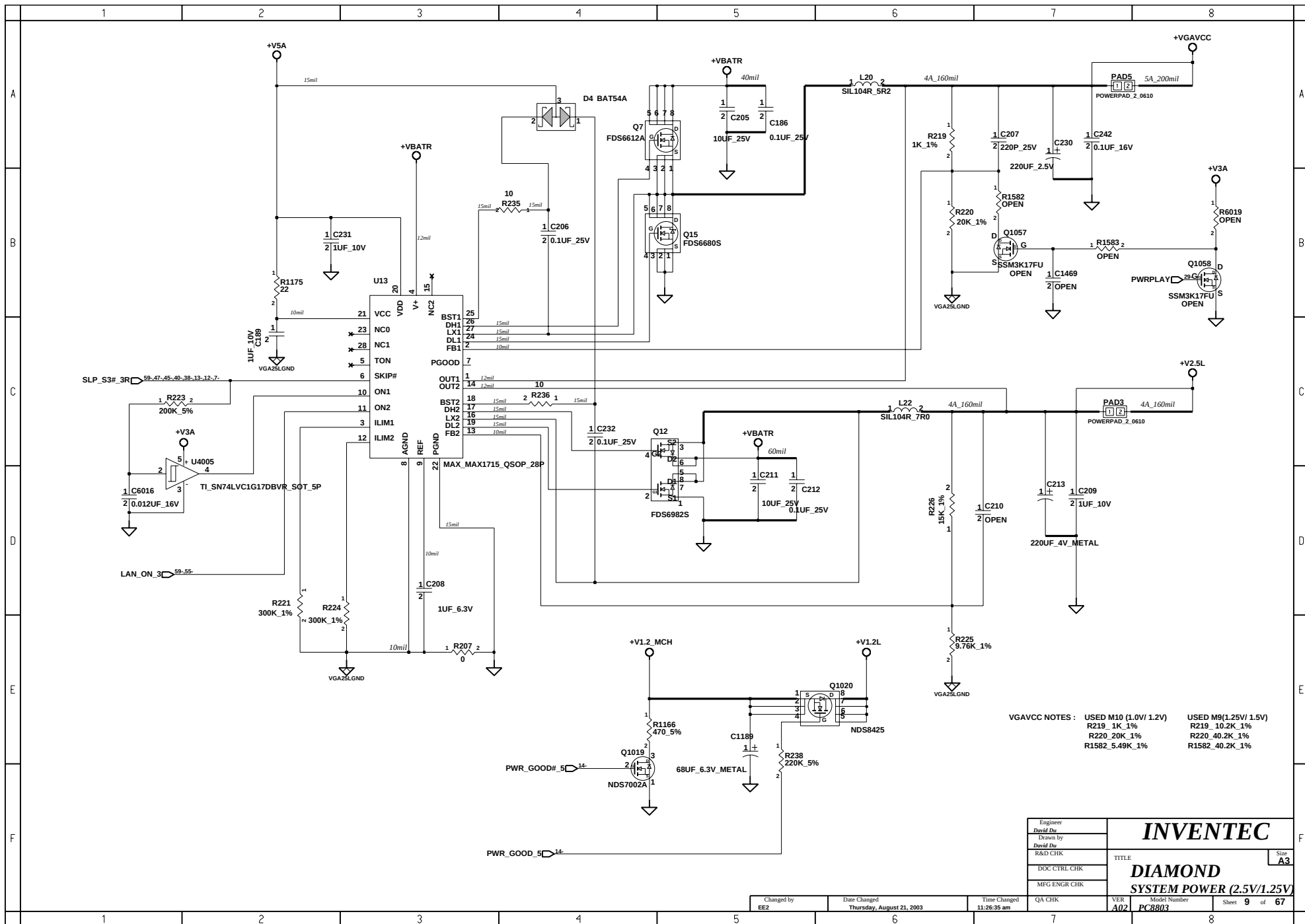


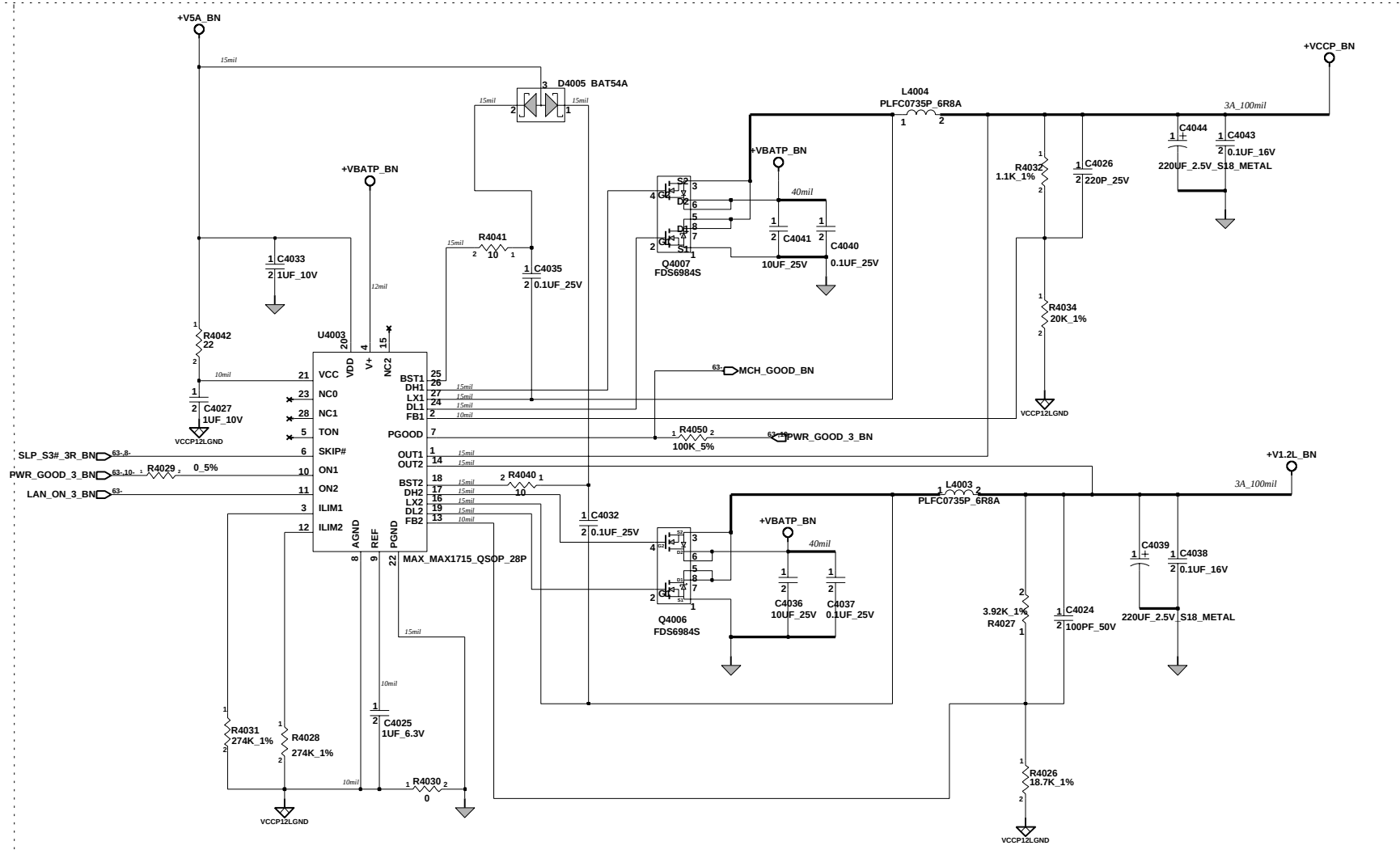


SYSTEM POWER (1.5&1.8) ON BUTTON DAUGHTER BOARD

Engineer David Du	INVENTEC TITLE: DIAMOND SYSTEM POWER(1.5V/1.8V)	Size A3
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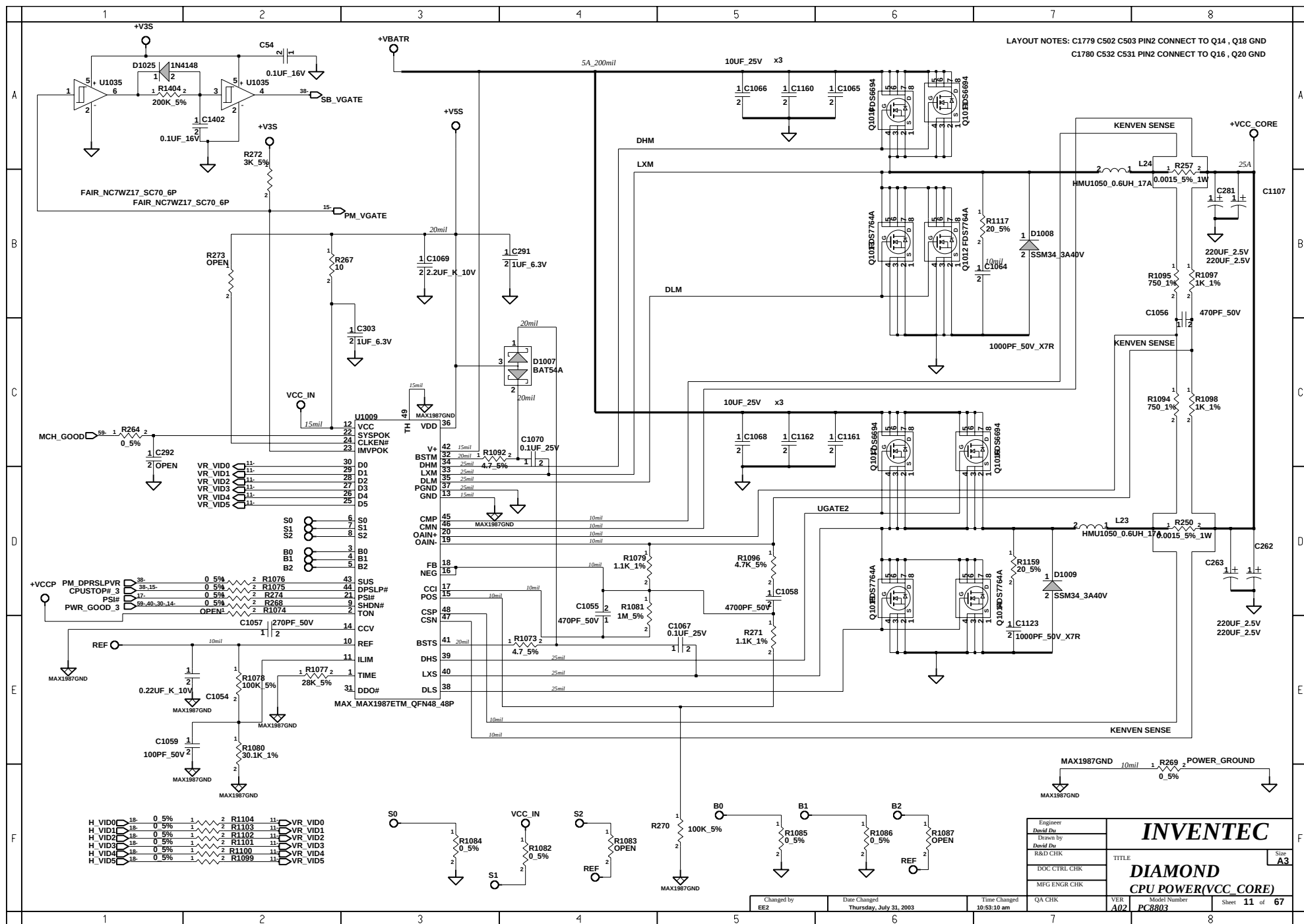


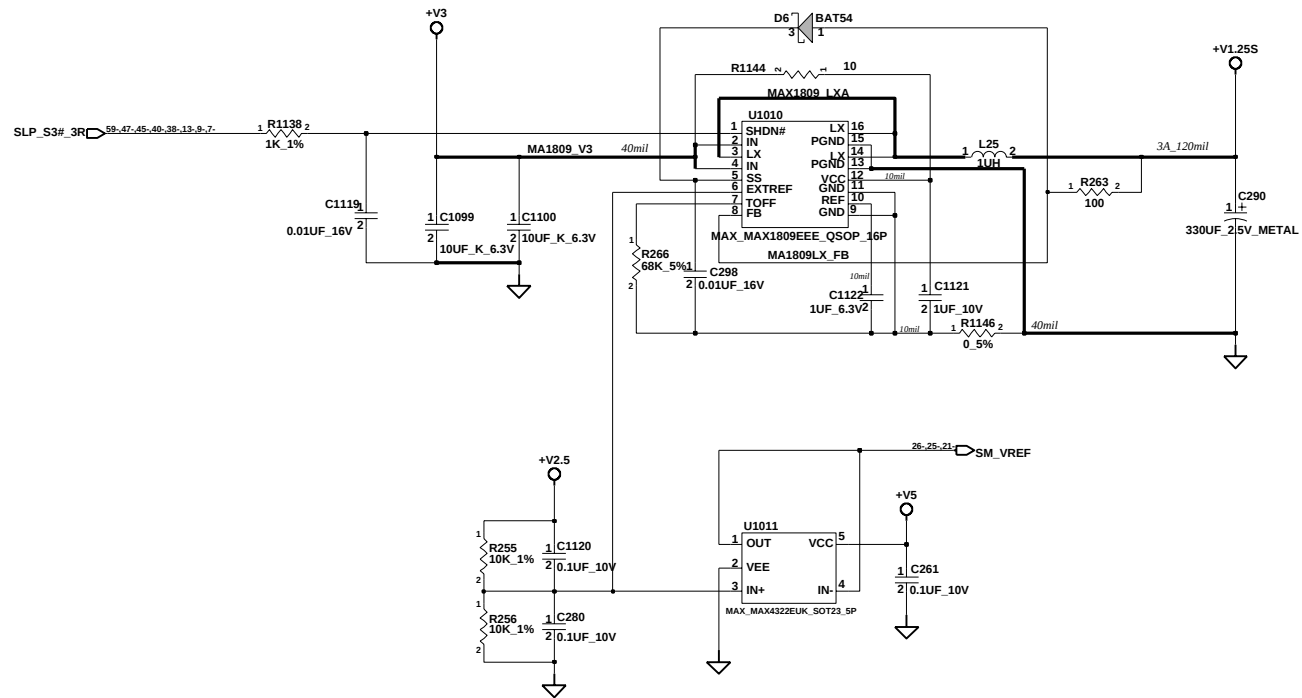


SYSTEM POWER (VCCP&1.2) ON BUTTON DAUGHTER BOARD

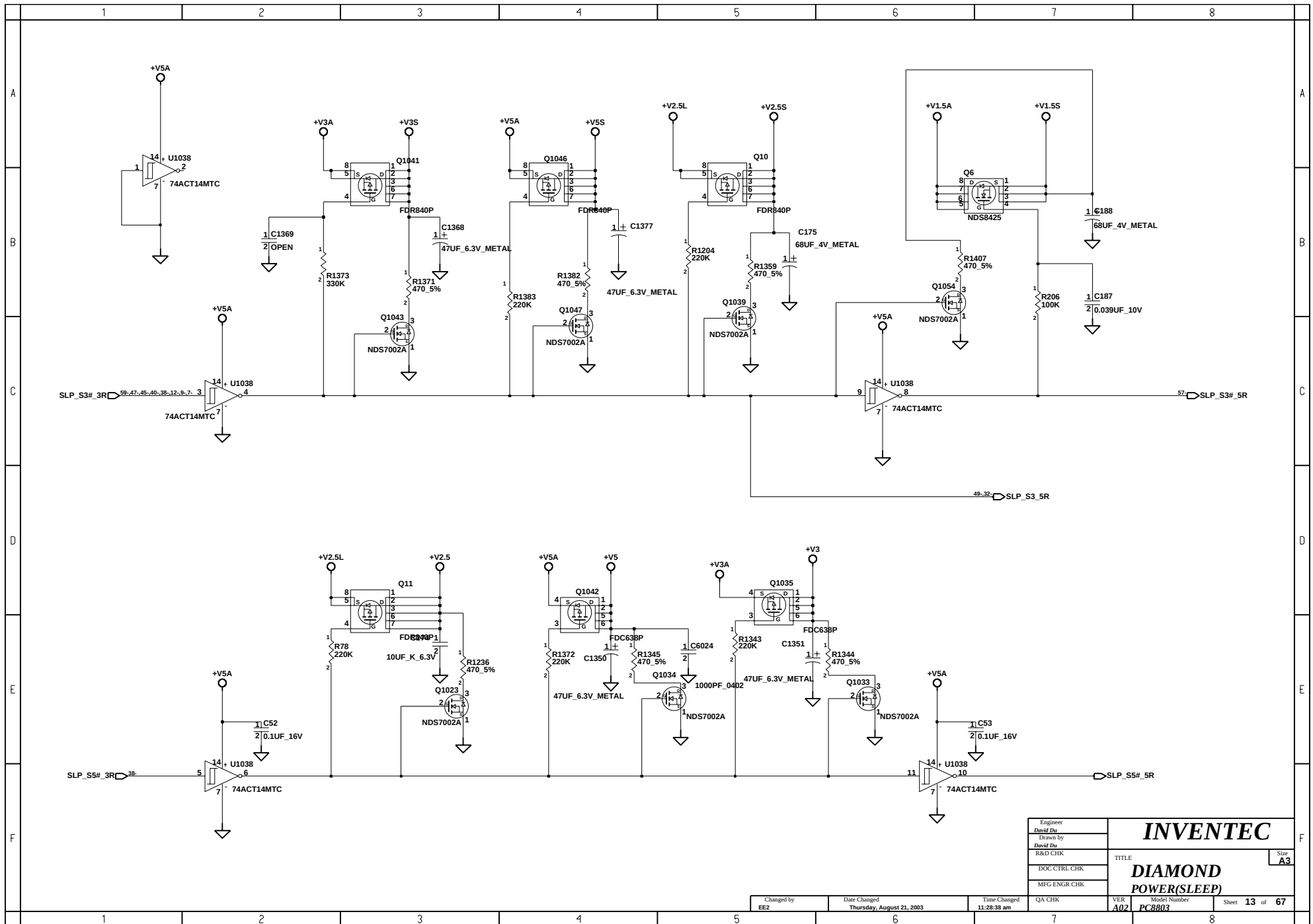
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QA CHK	VER A02	Model Number PC8803	Sheet 10 of 67

Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 6:06:02 pm
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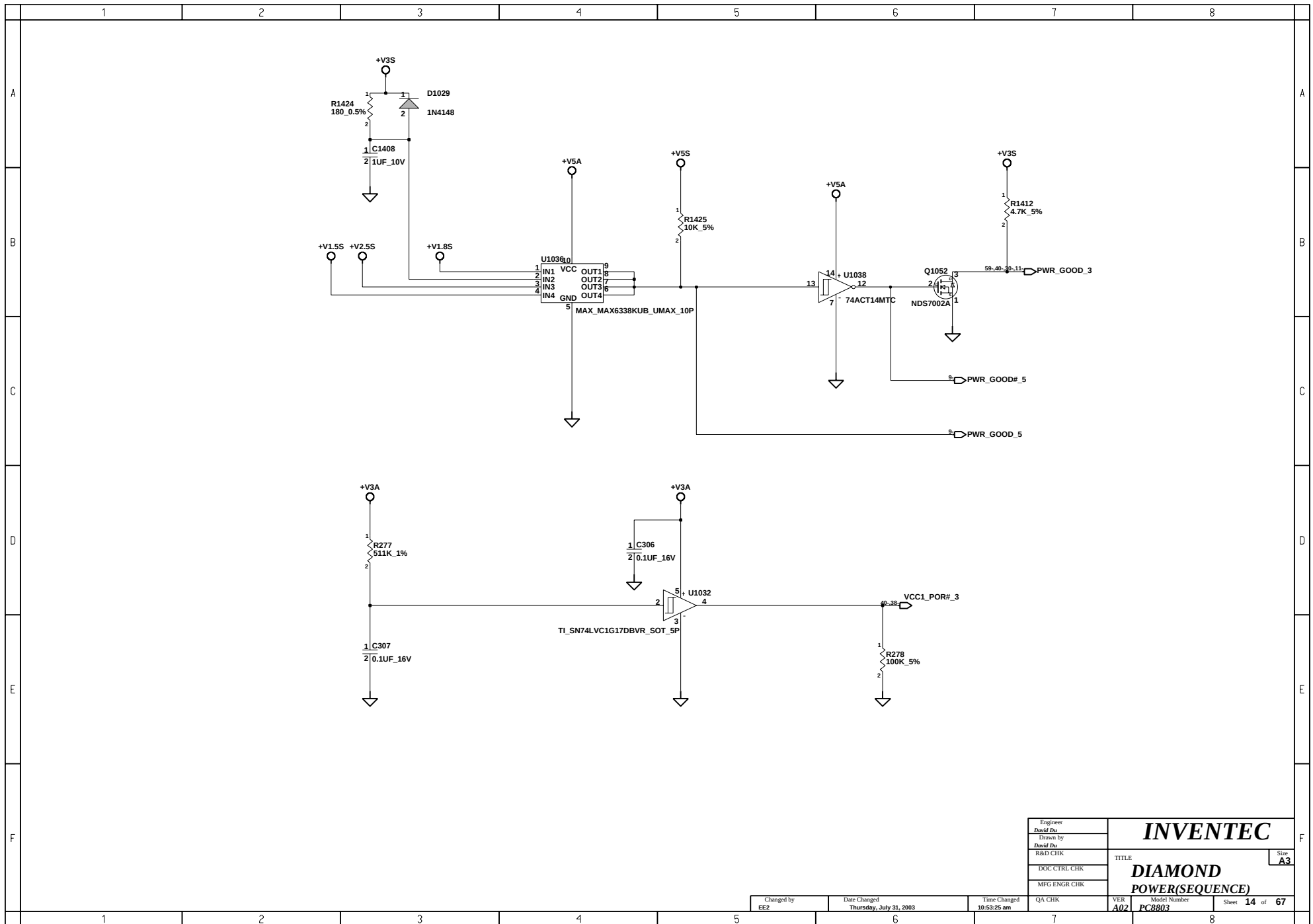




Engineer <i>David Du</i>	INVENTEC TITLE DIAMOND VGA POWER(VDD_CORE)				Size A3
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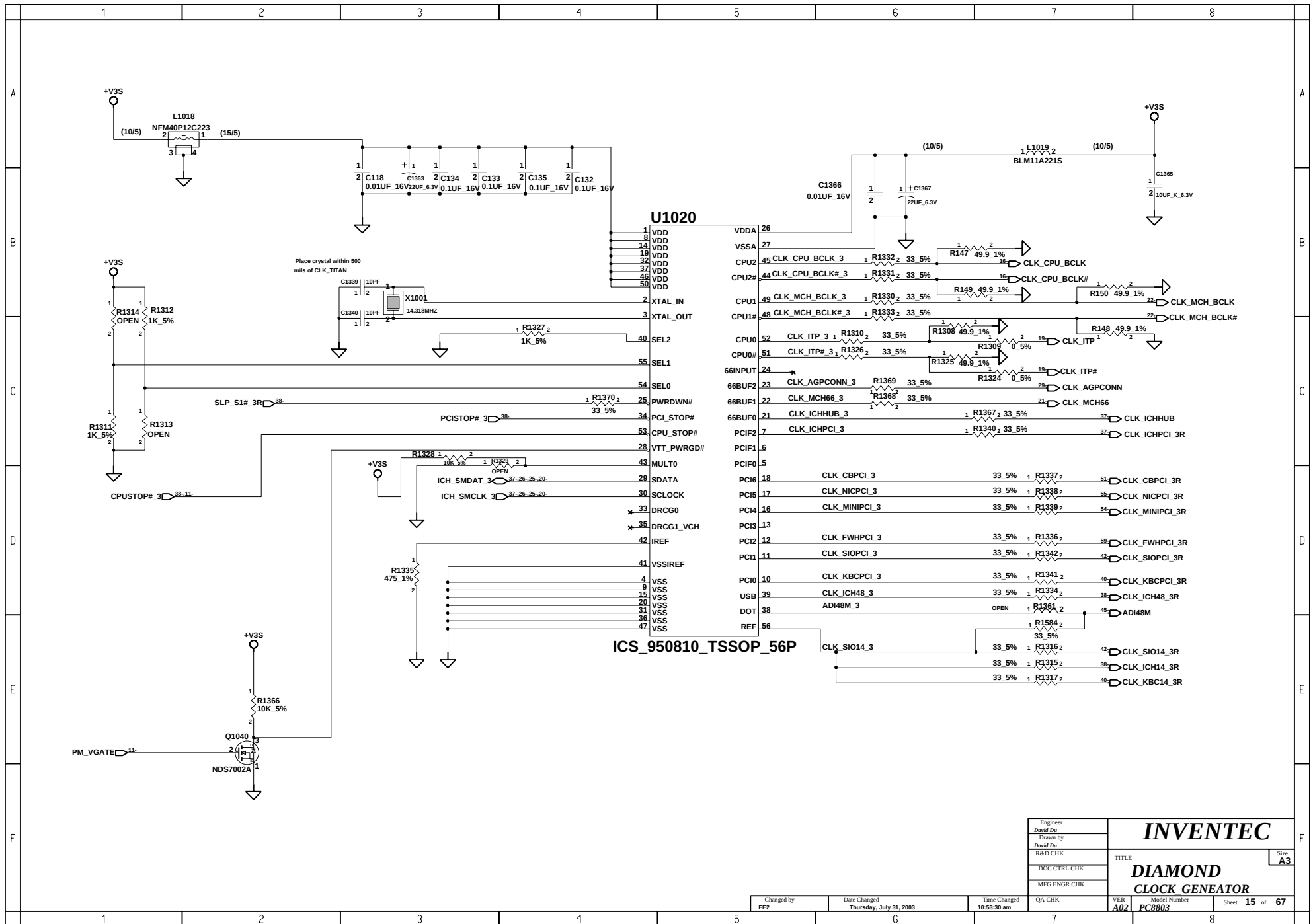


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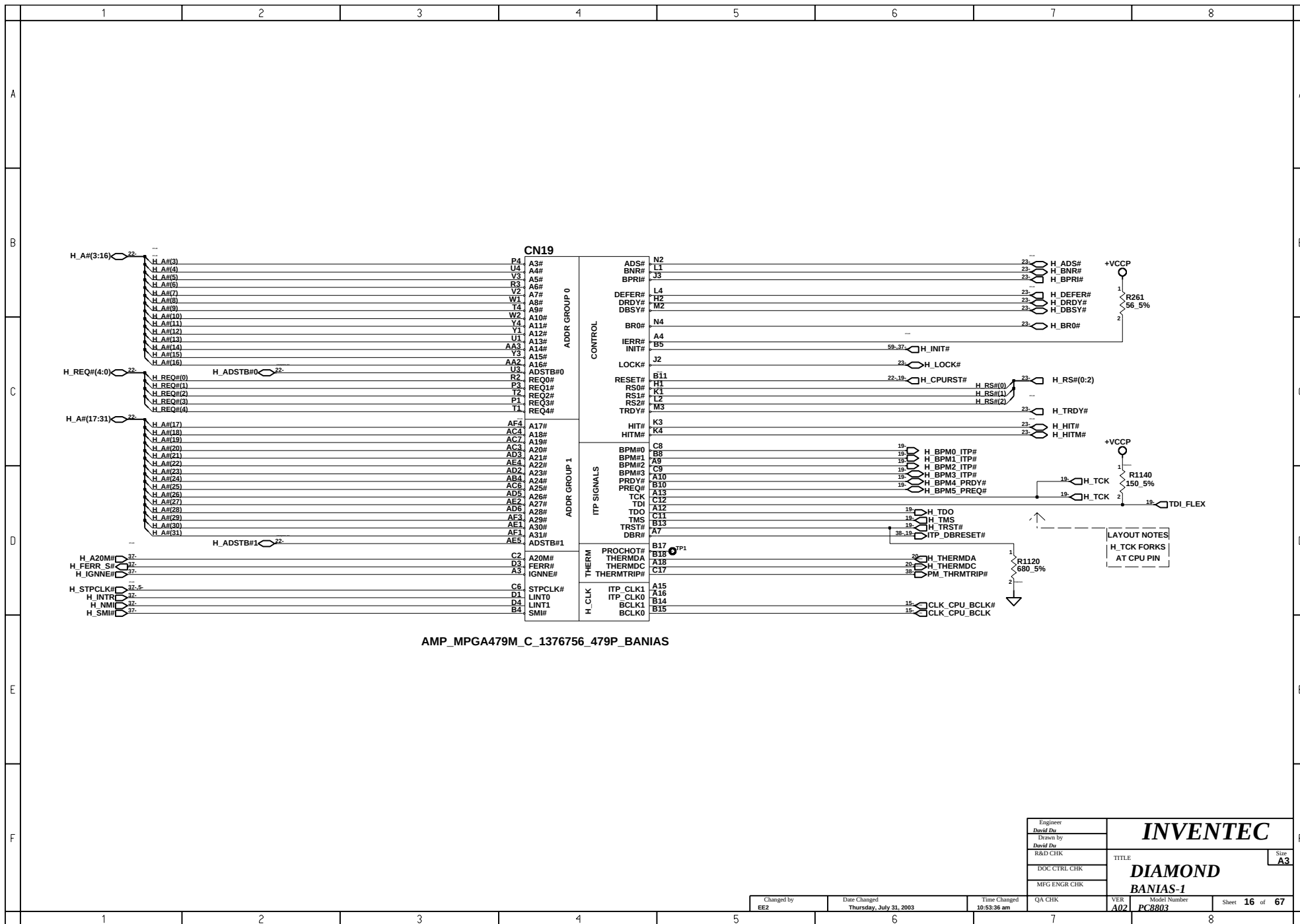
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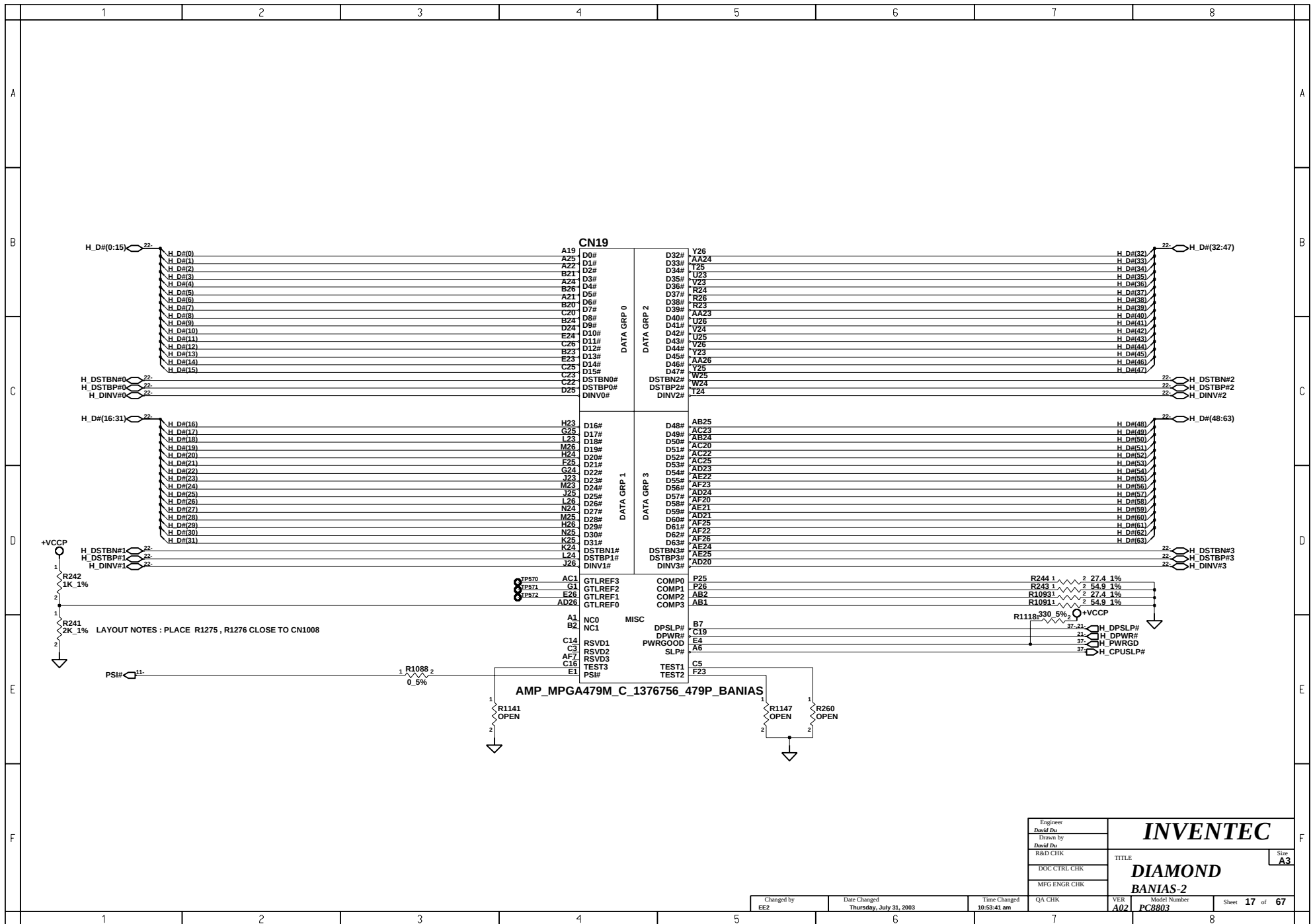


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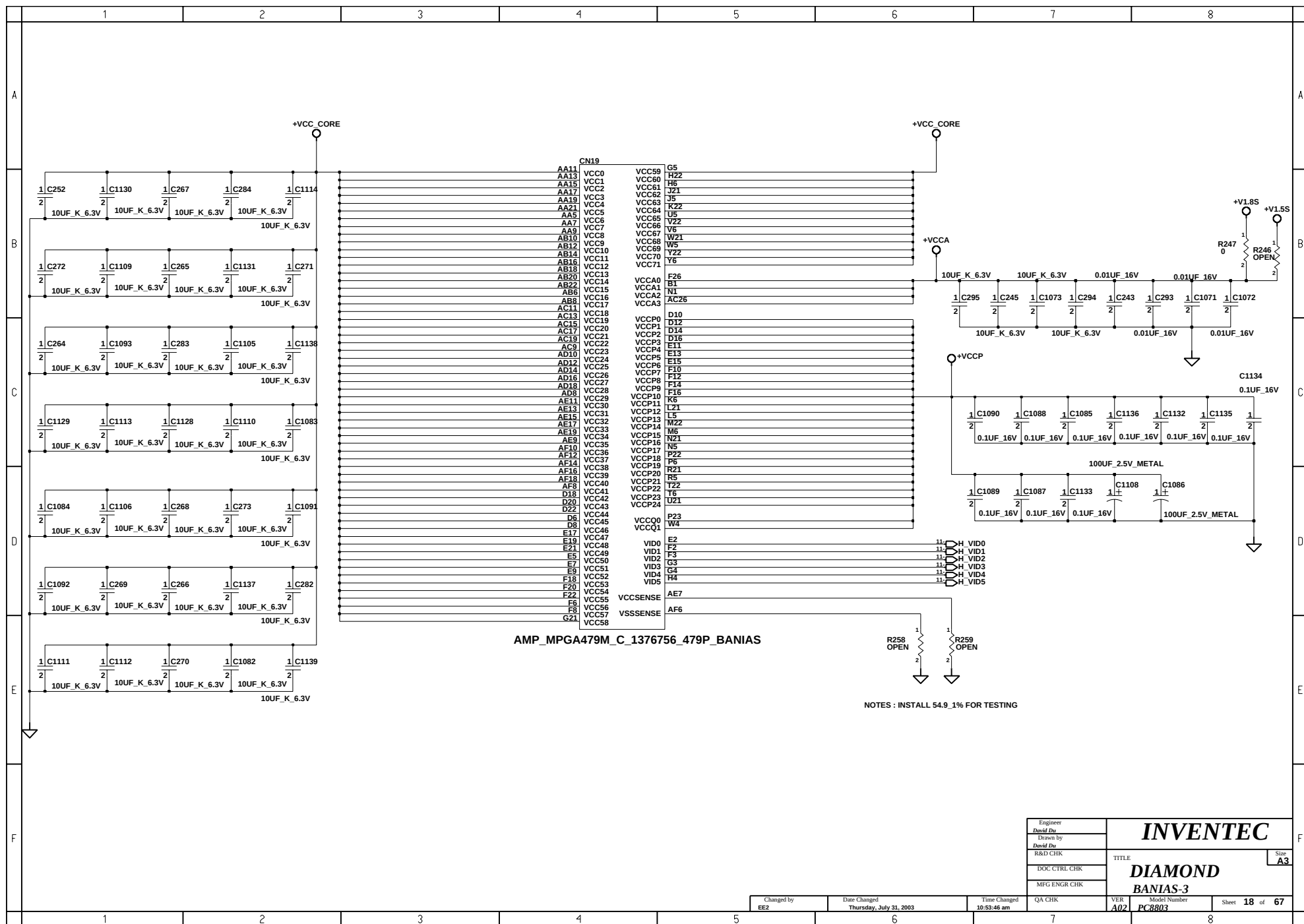
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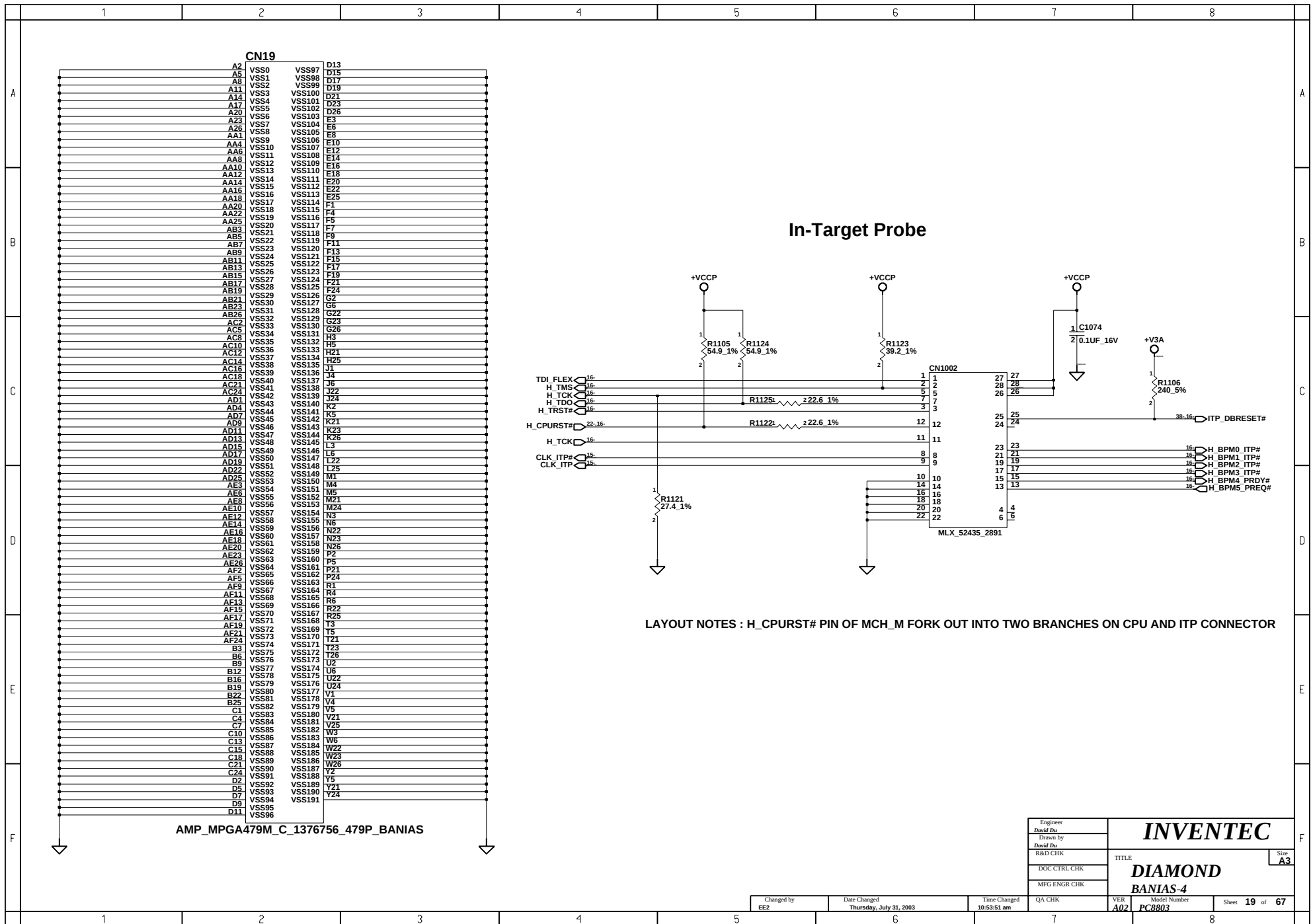


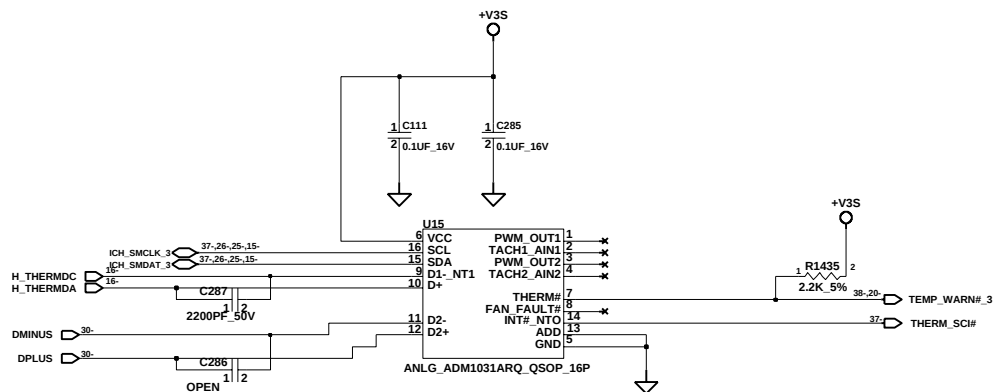
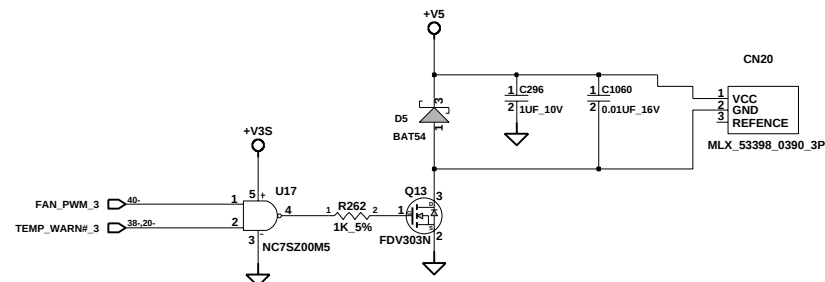
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Drawn by David Du		
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Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:53:36 am
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Engineer David Du	INVENTEC TITLE DIAMOND BANIAS-2	
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Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:53:41 am
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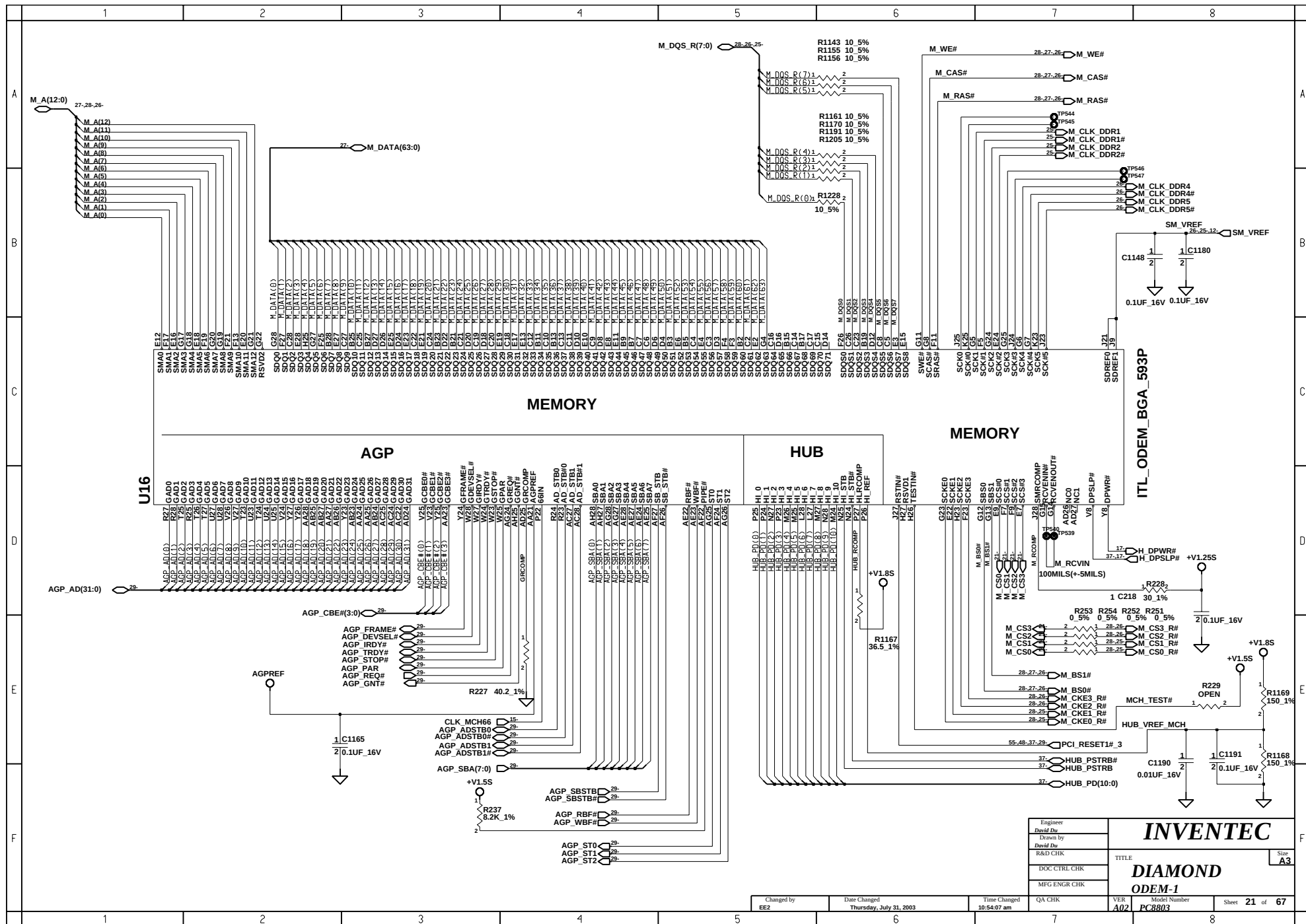


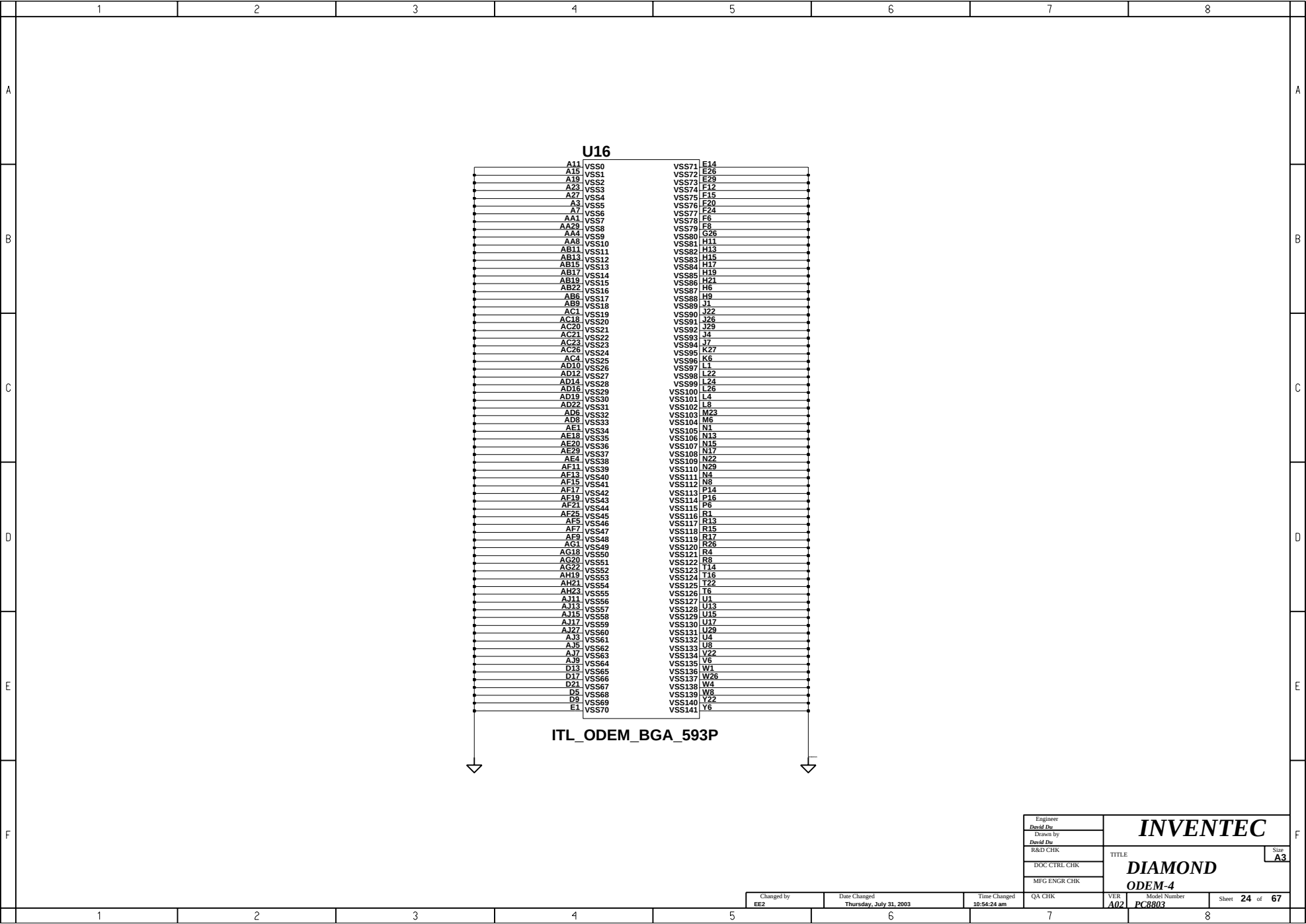




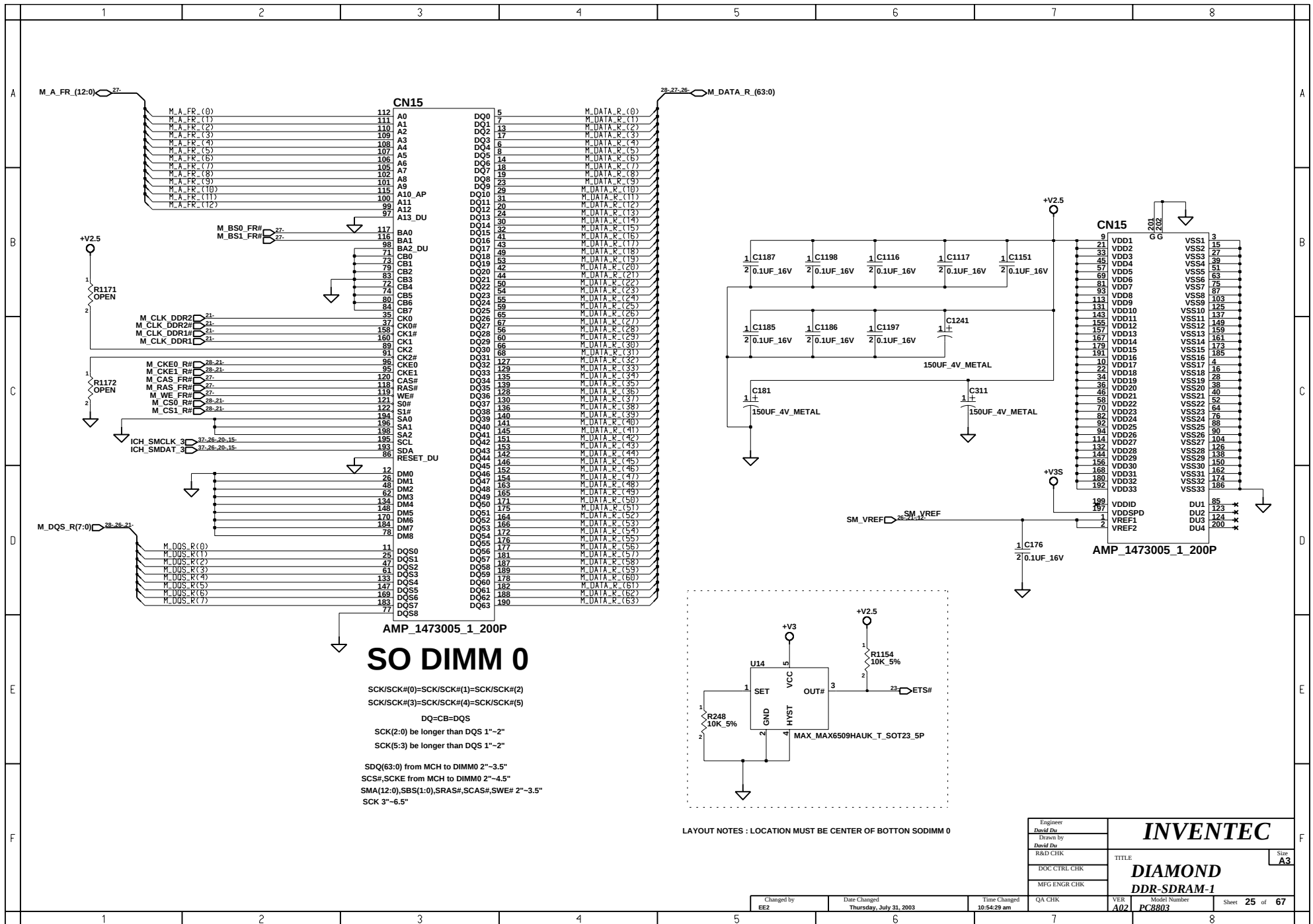
LAYOUT NOTES: PUT THE THERMAL SENSOR CLOSE TO CPU

Engineer <i>David Du</i>		INVENTEC TITLE Size A3 DIAMOND THERMAL&FAN CONTROLLER	
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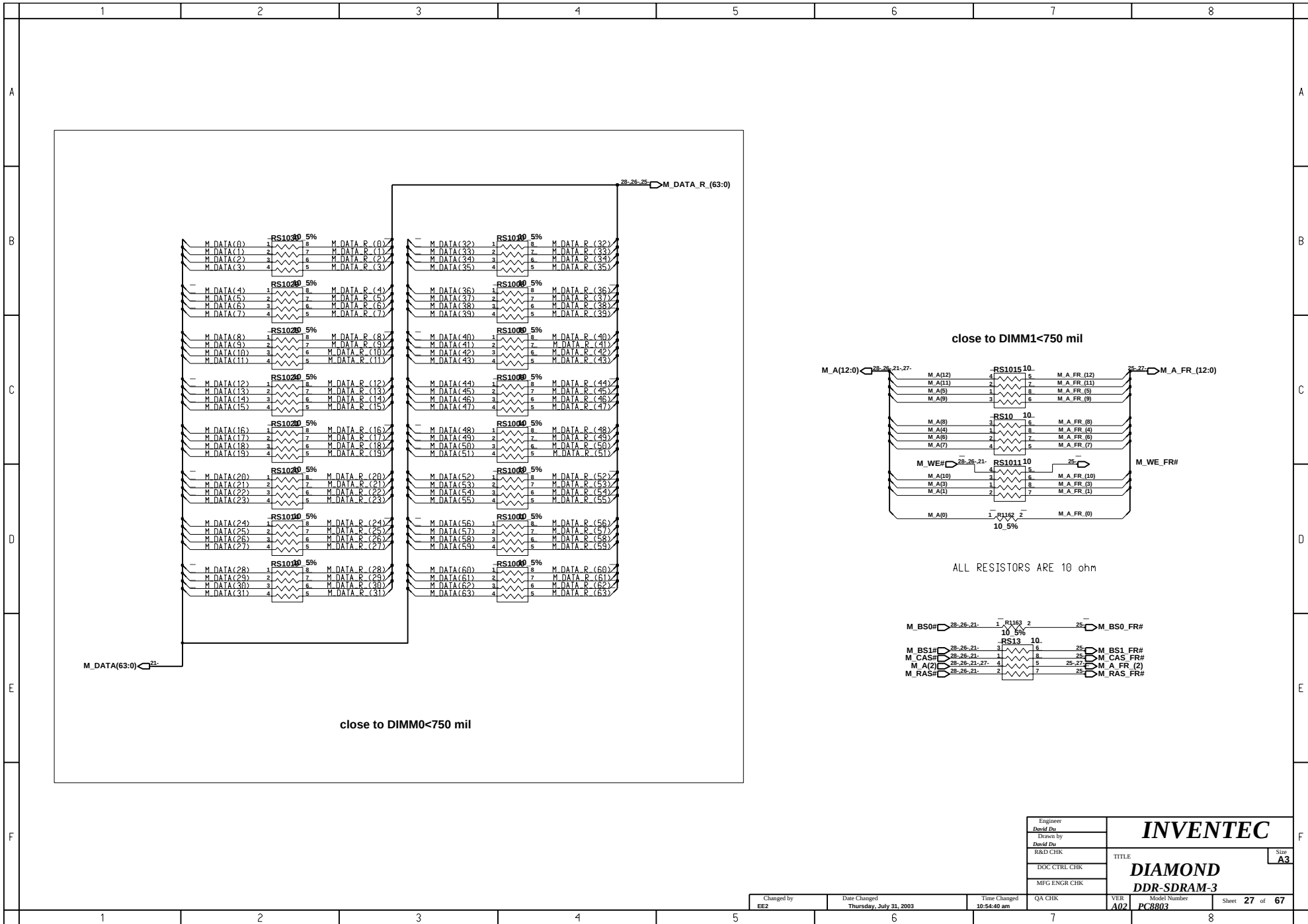


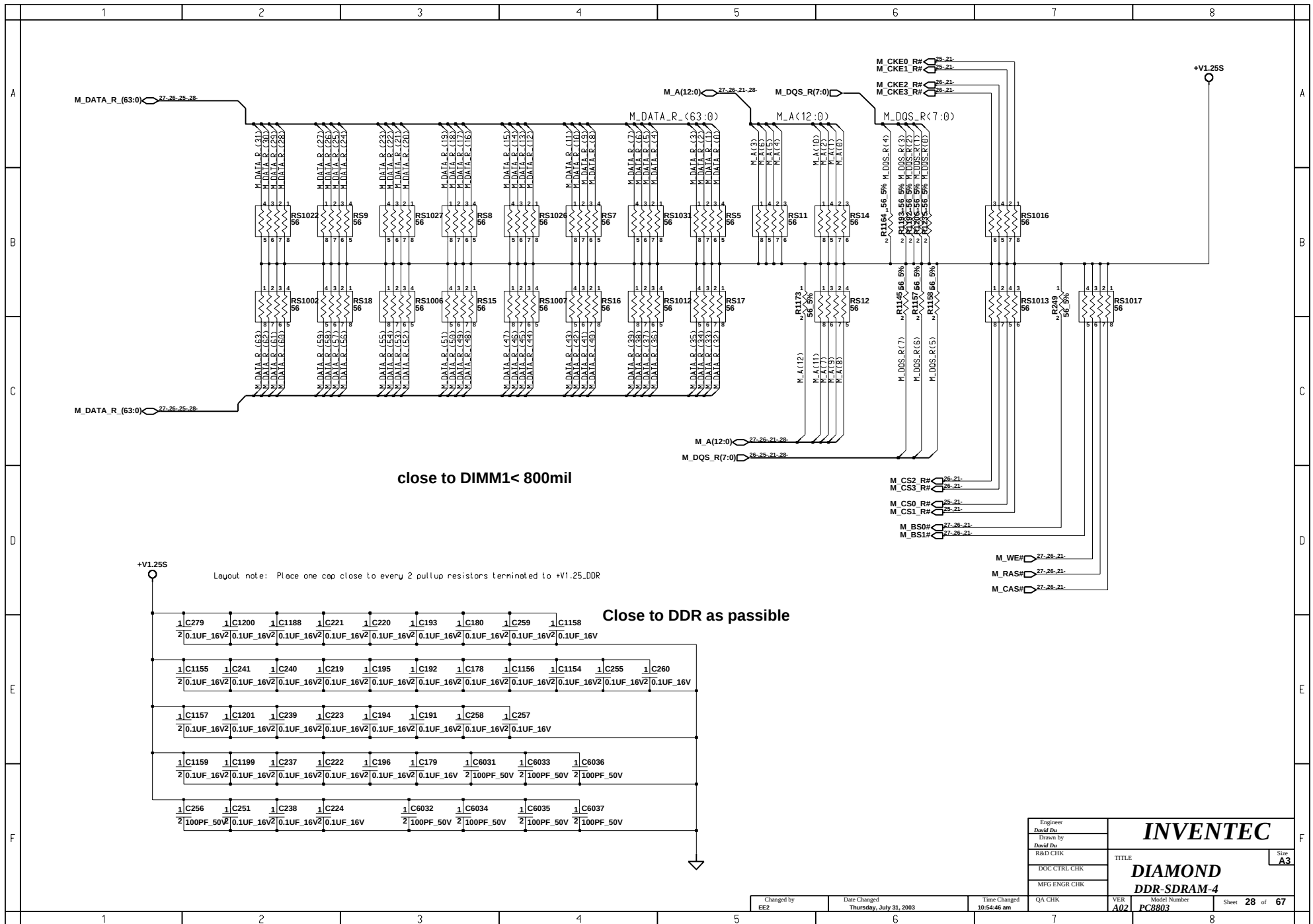
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QA CHK	VER A02	Model Number PC8803
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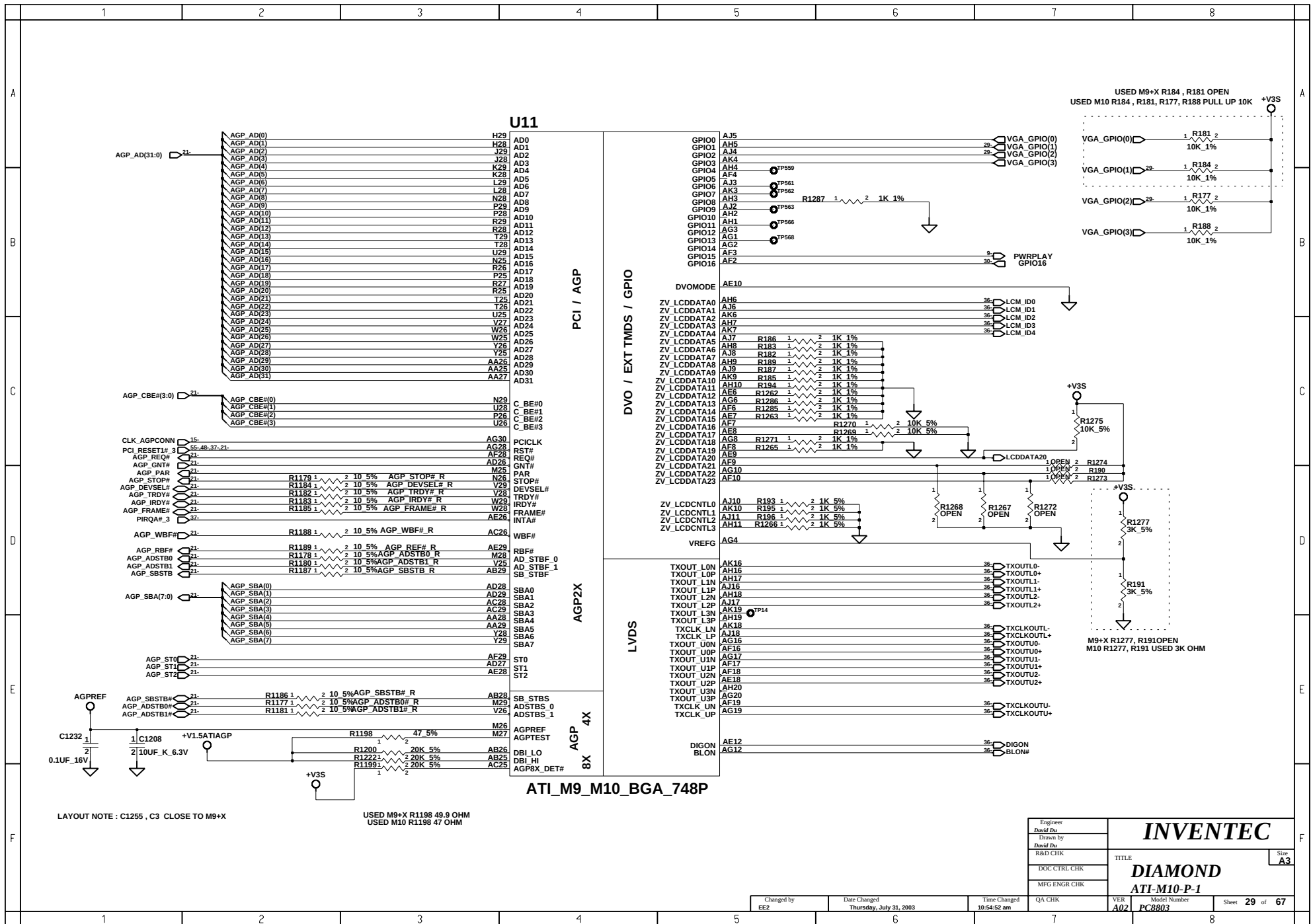


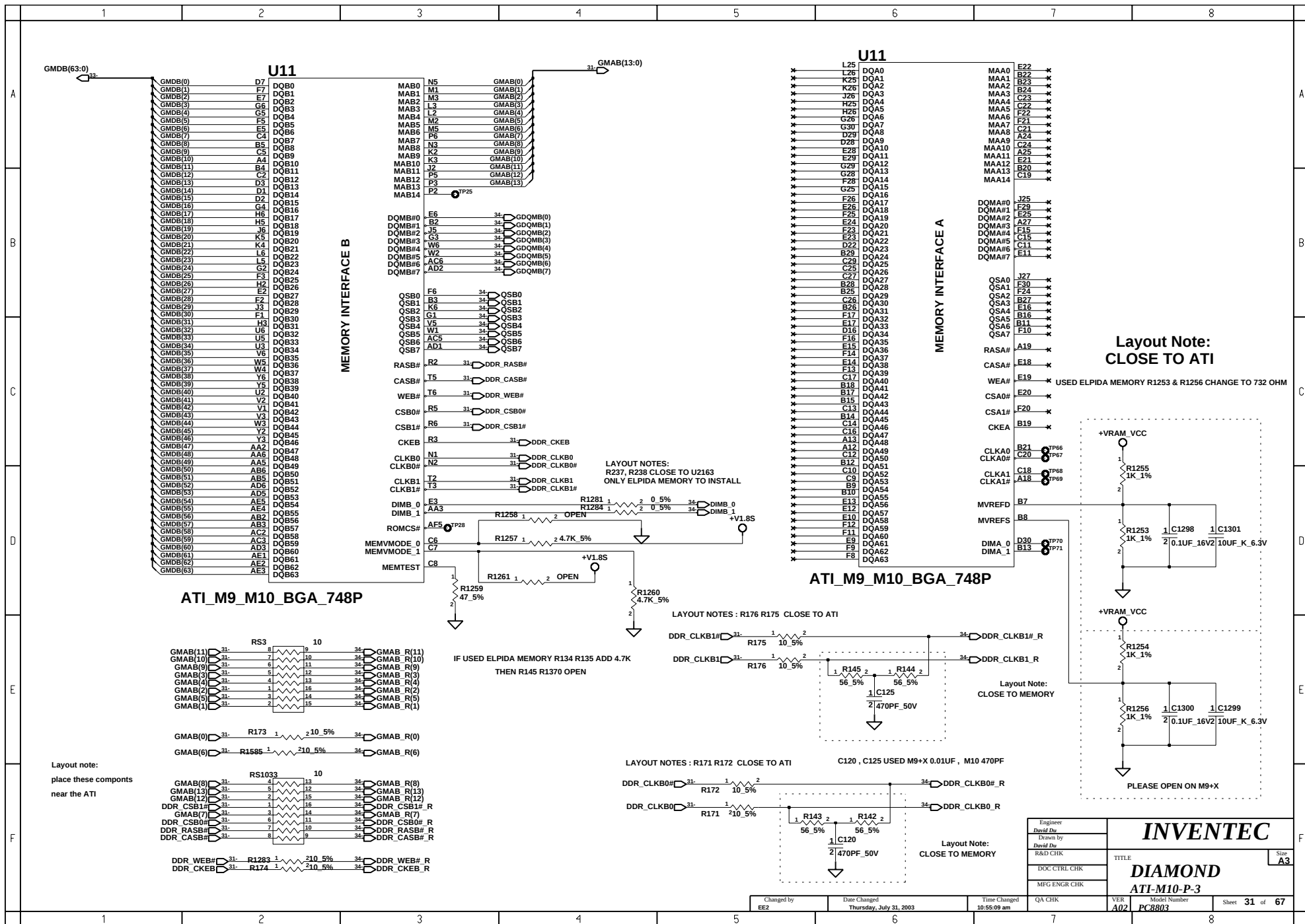
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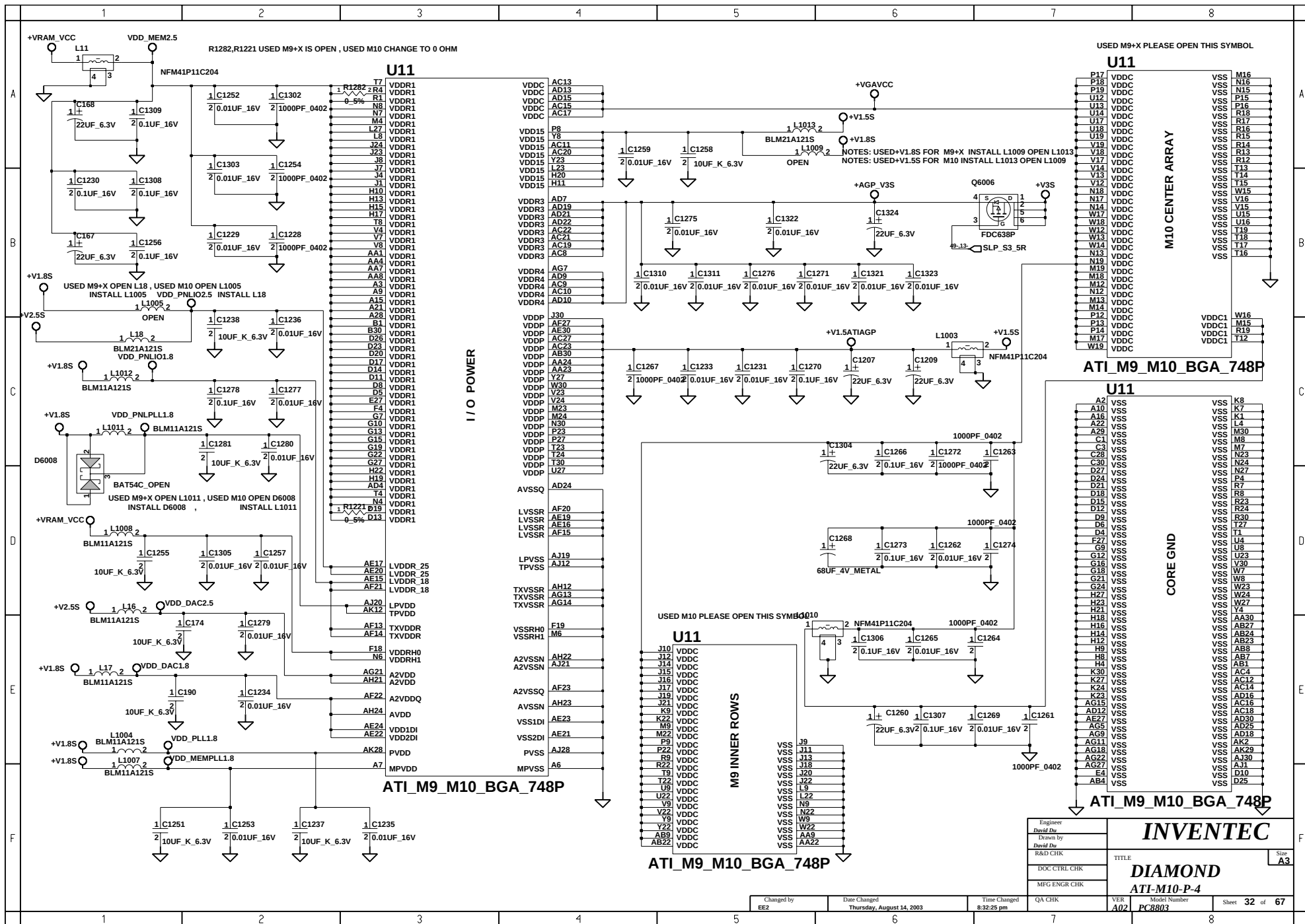
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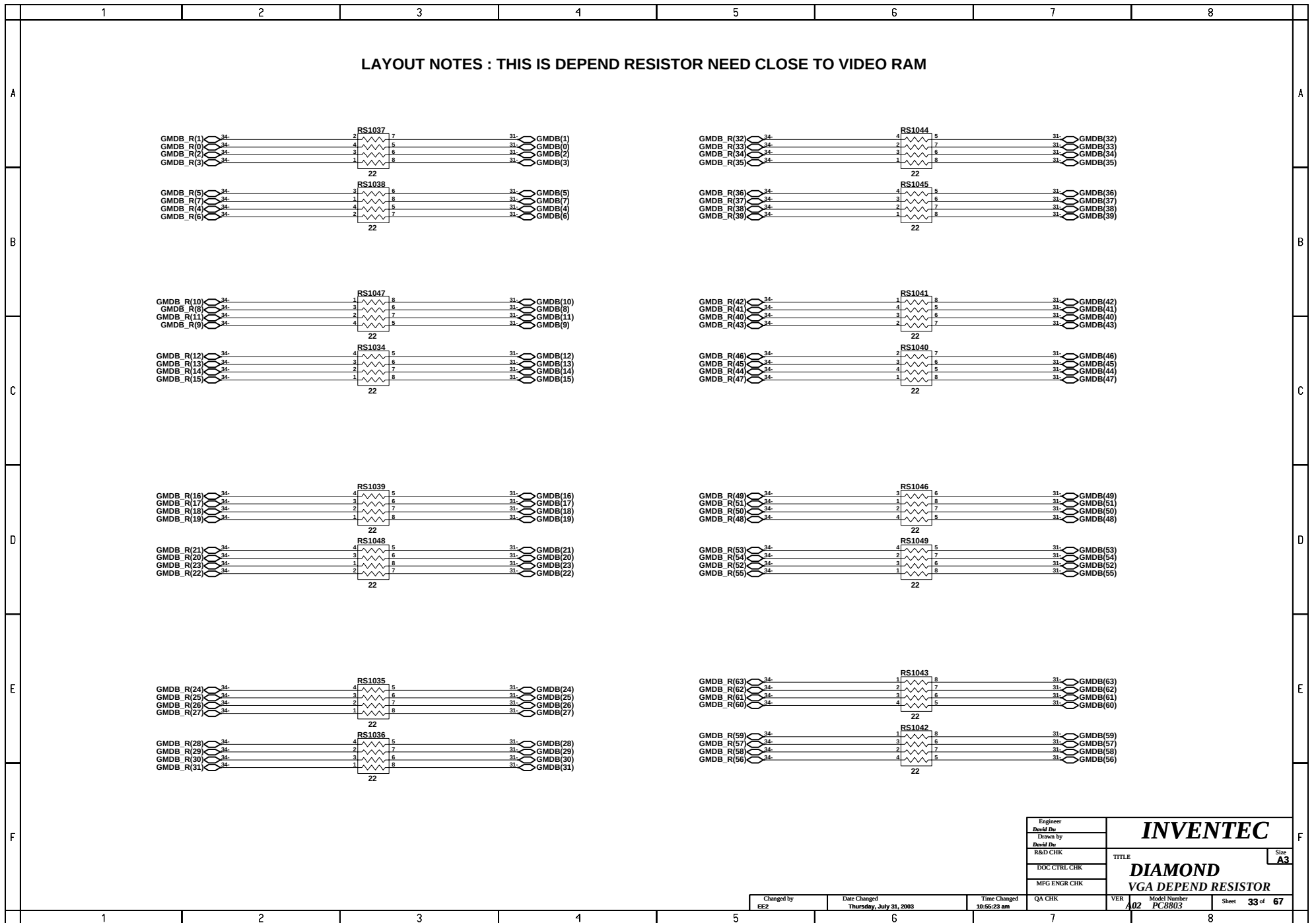






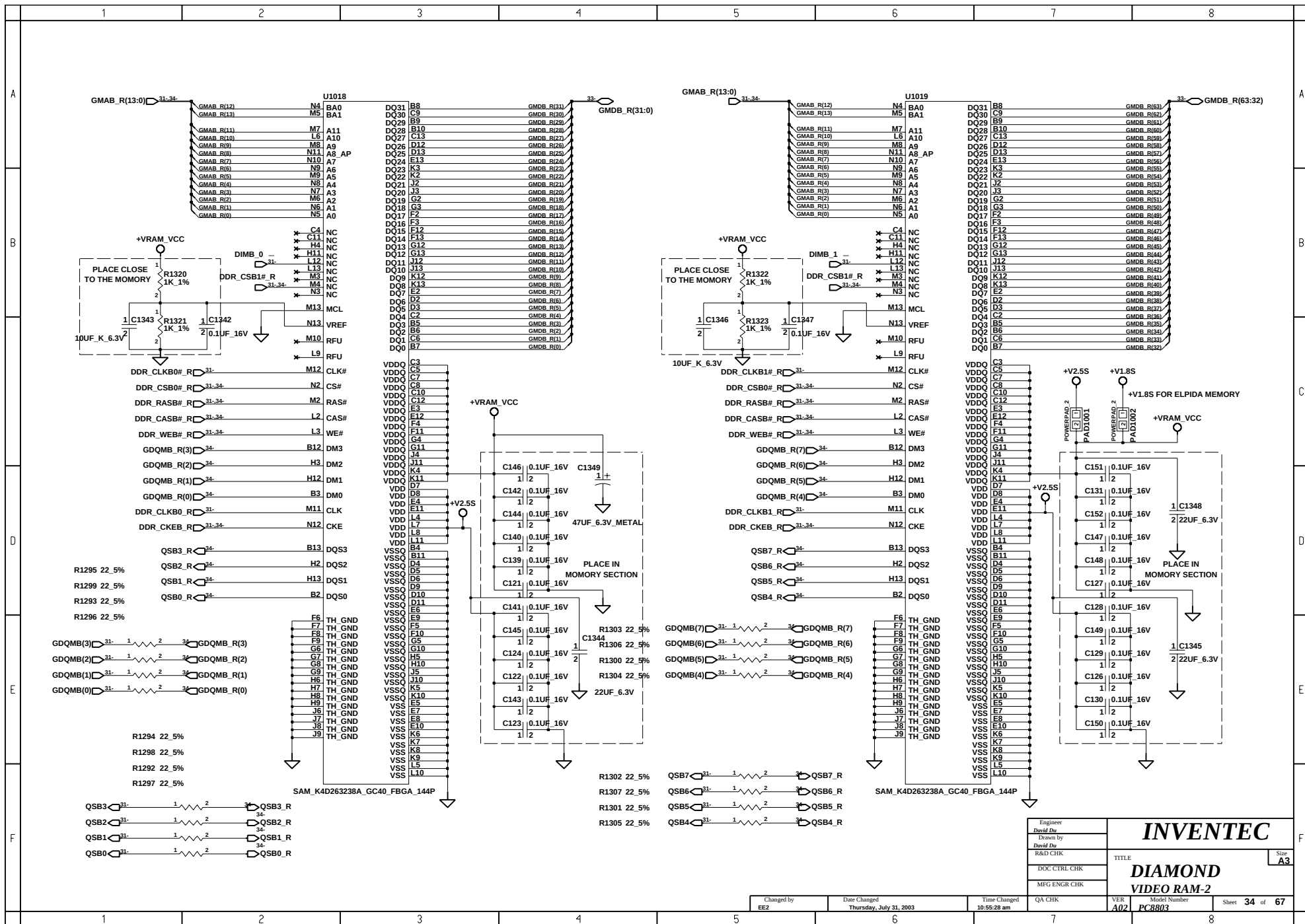


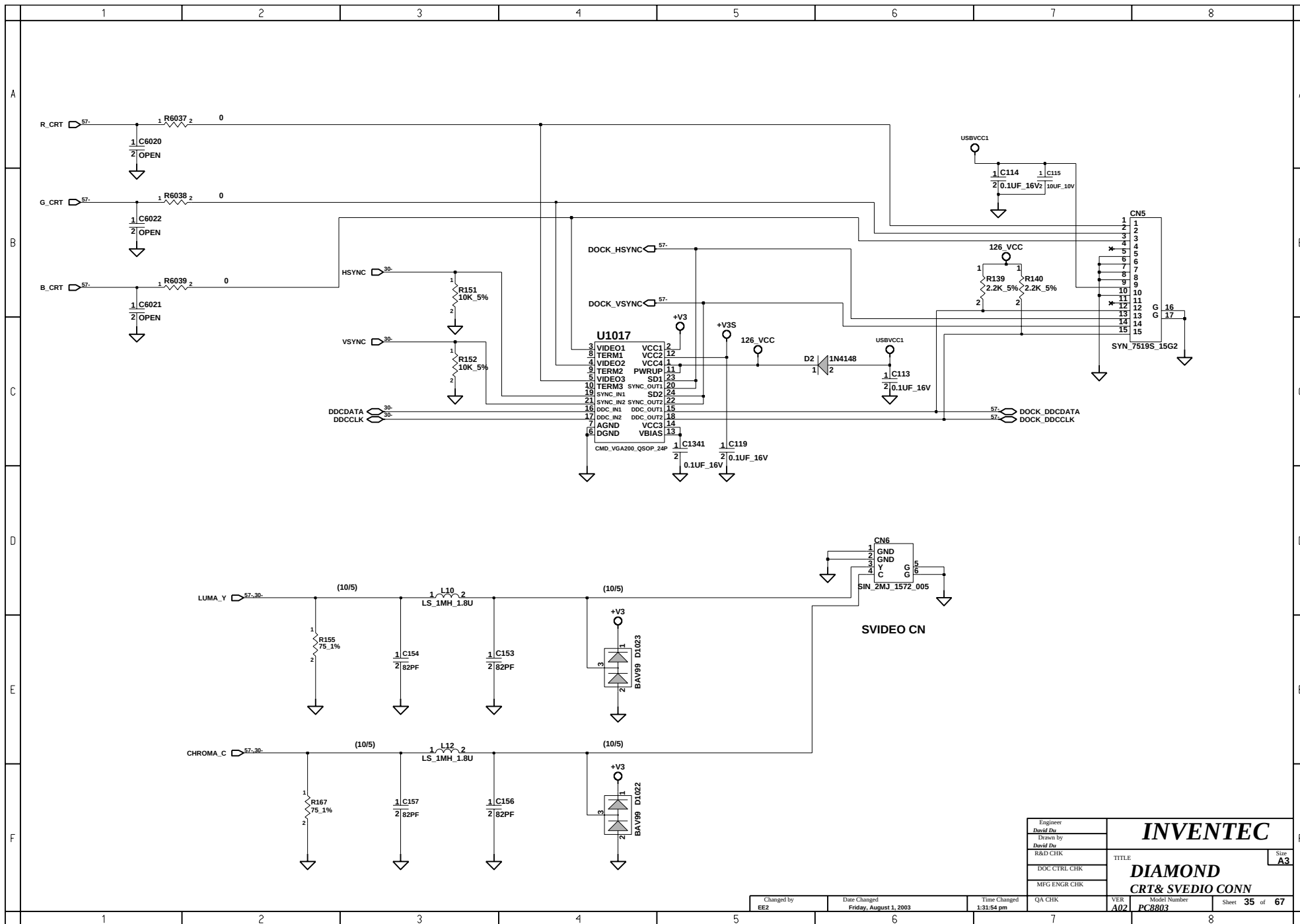




Engineer <i>David Du</i>	INVENTEC TITLE DIAMOND VGA DEPEND RESISTOR	Size A3
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MFG ENGR CHK	VER A02	Model Number PC8803
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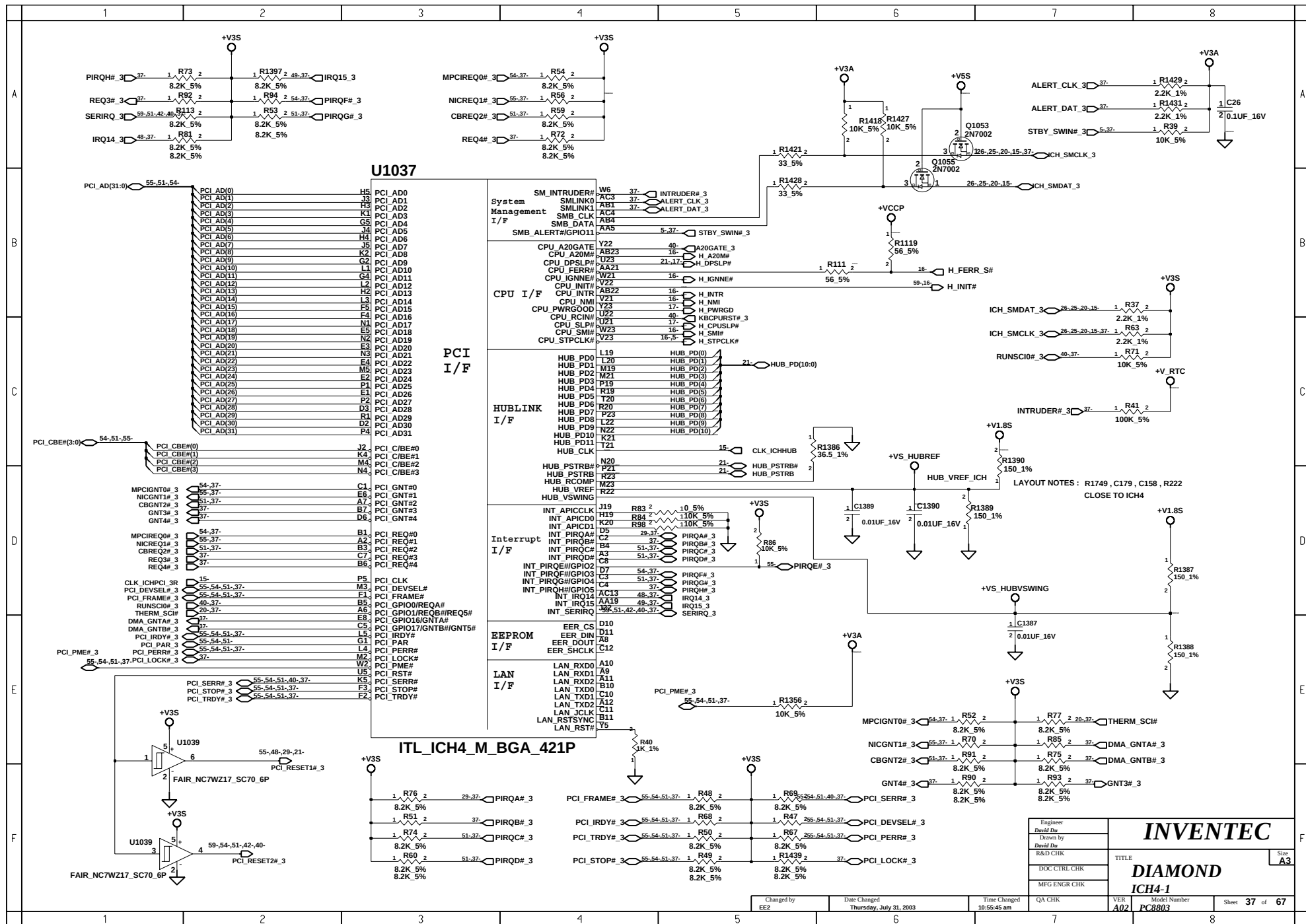


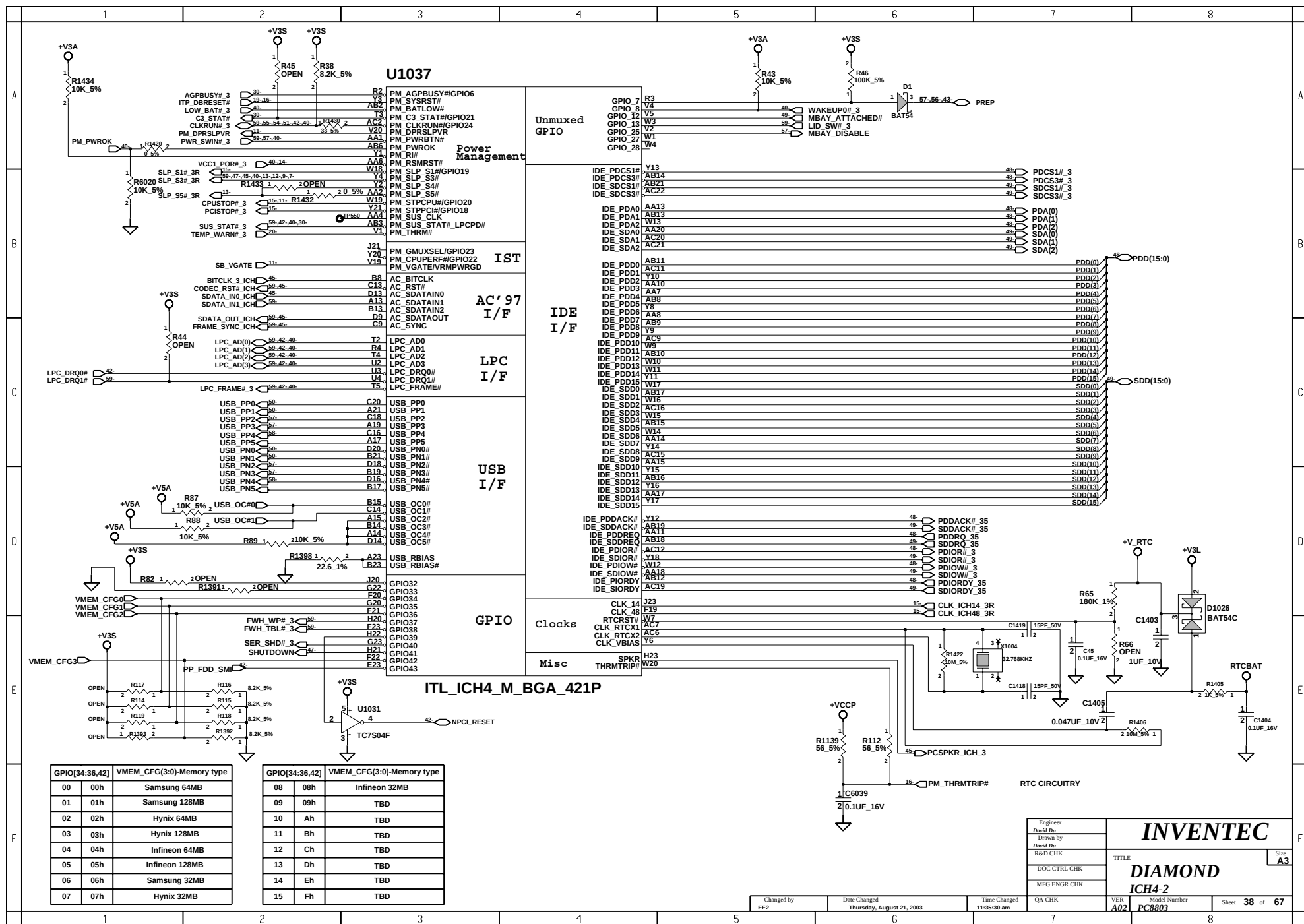
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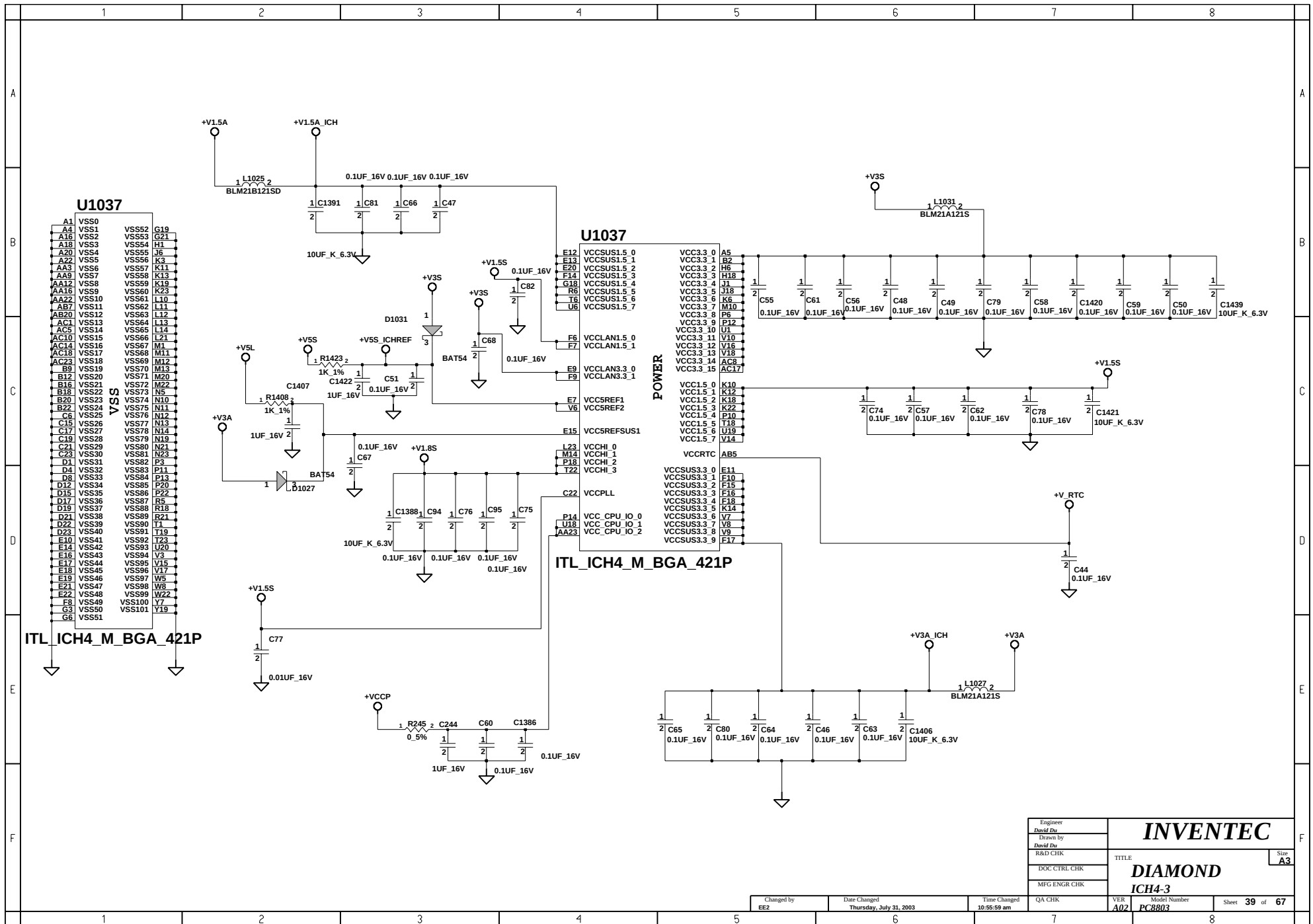
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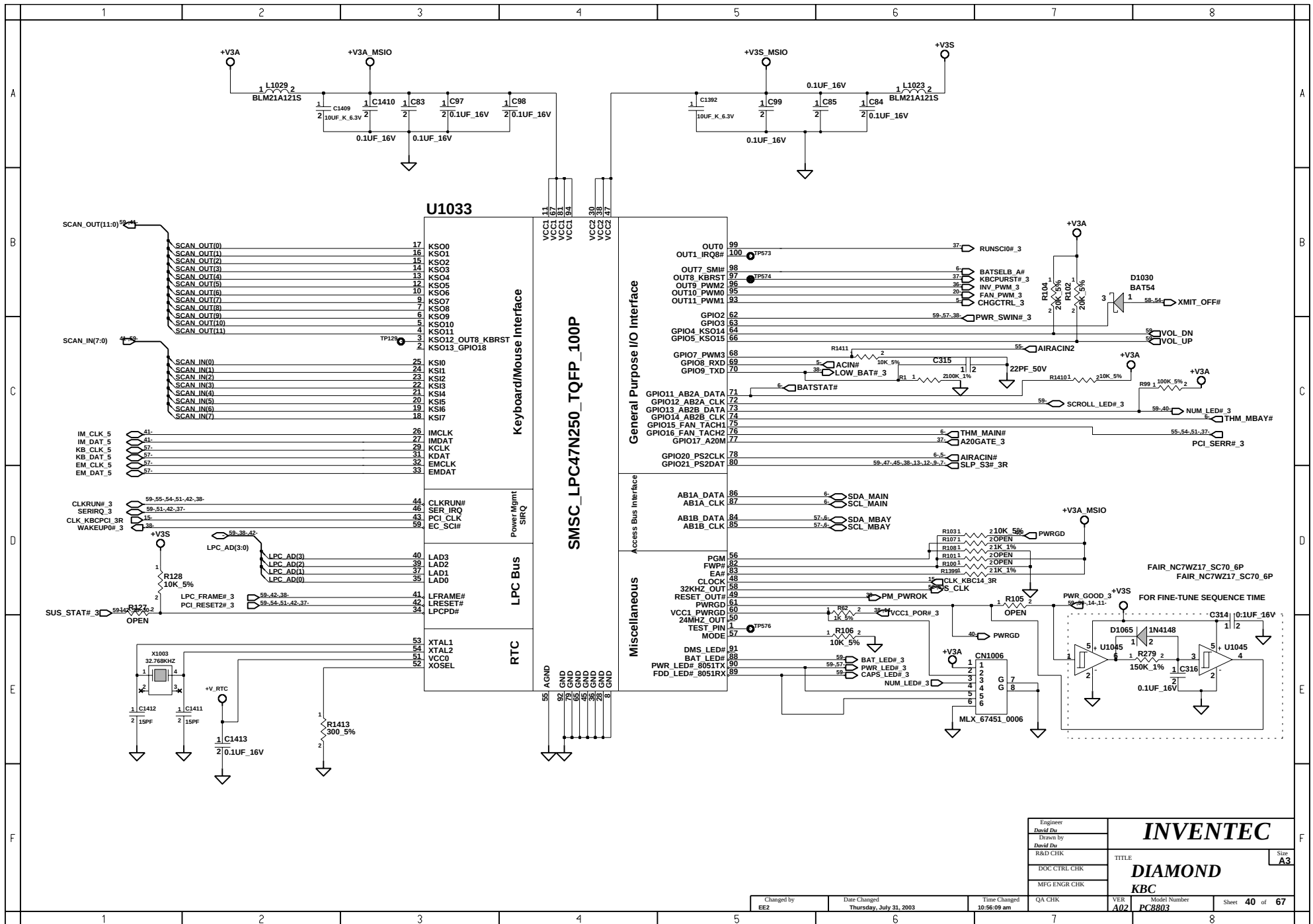


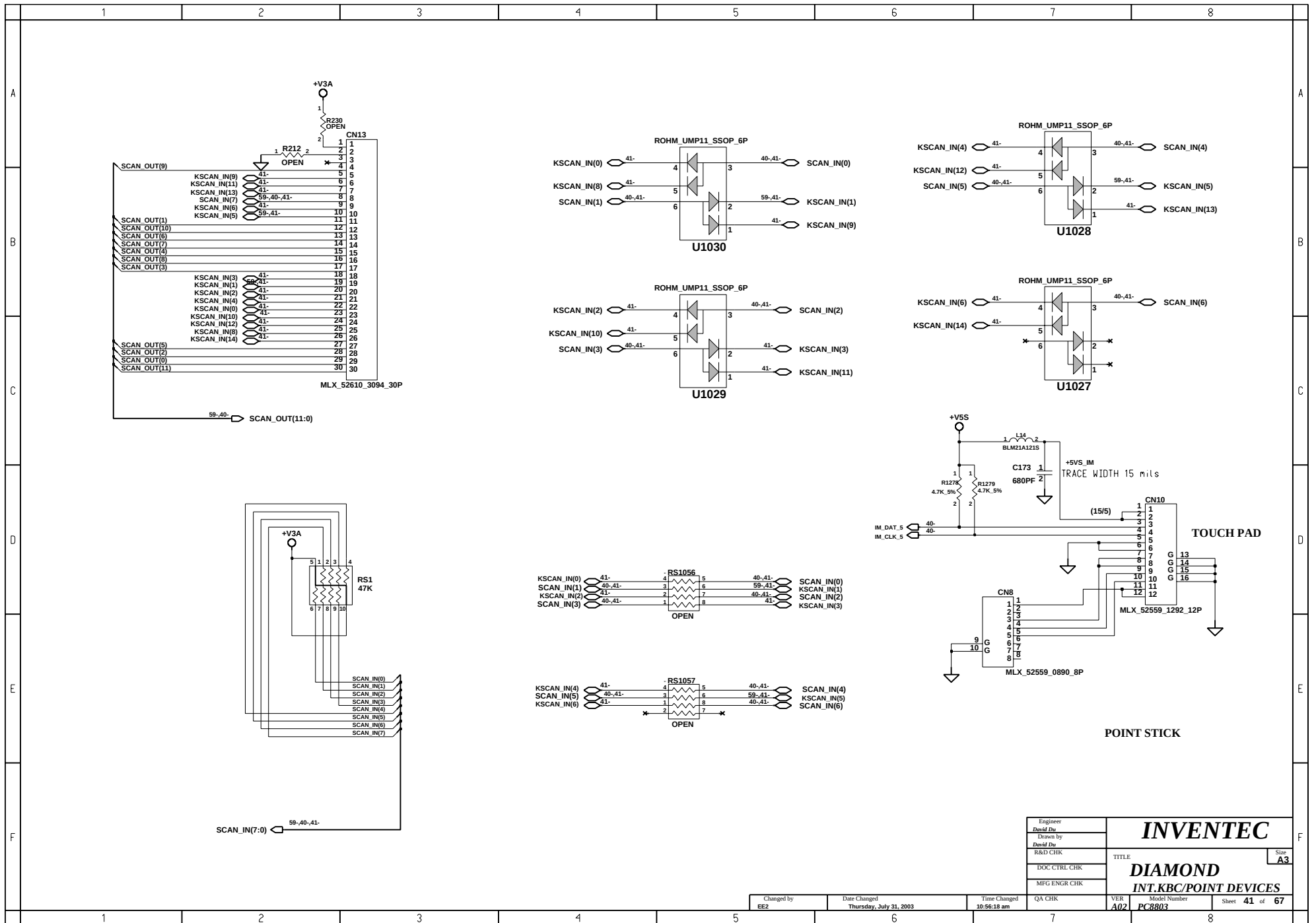
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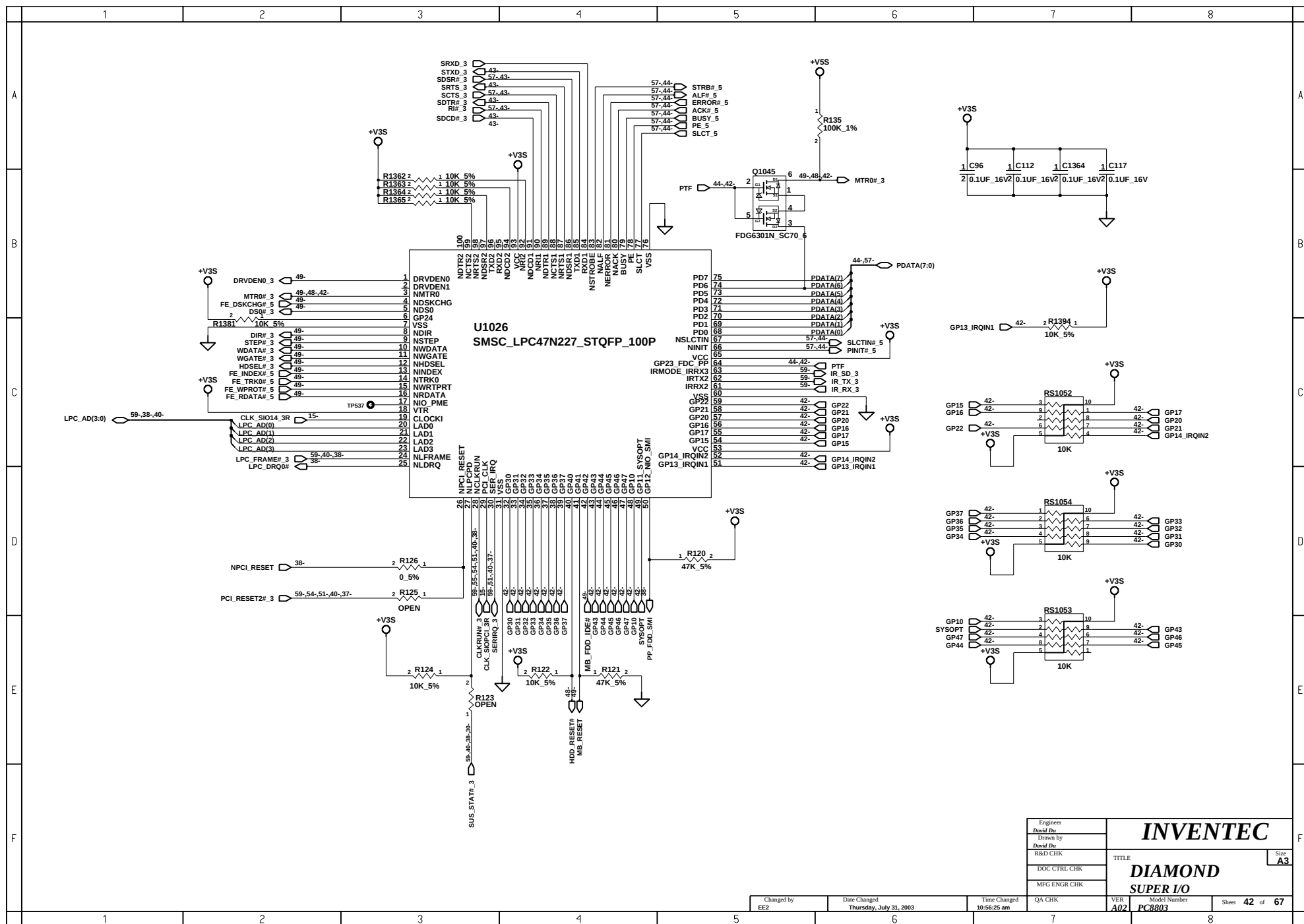


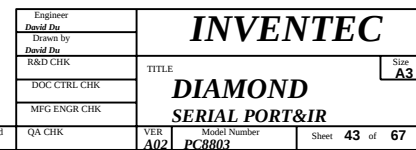


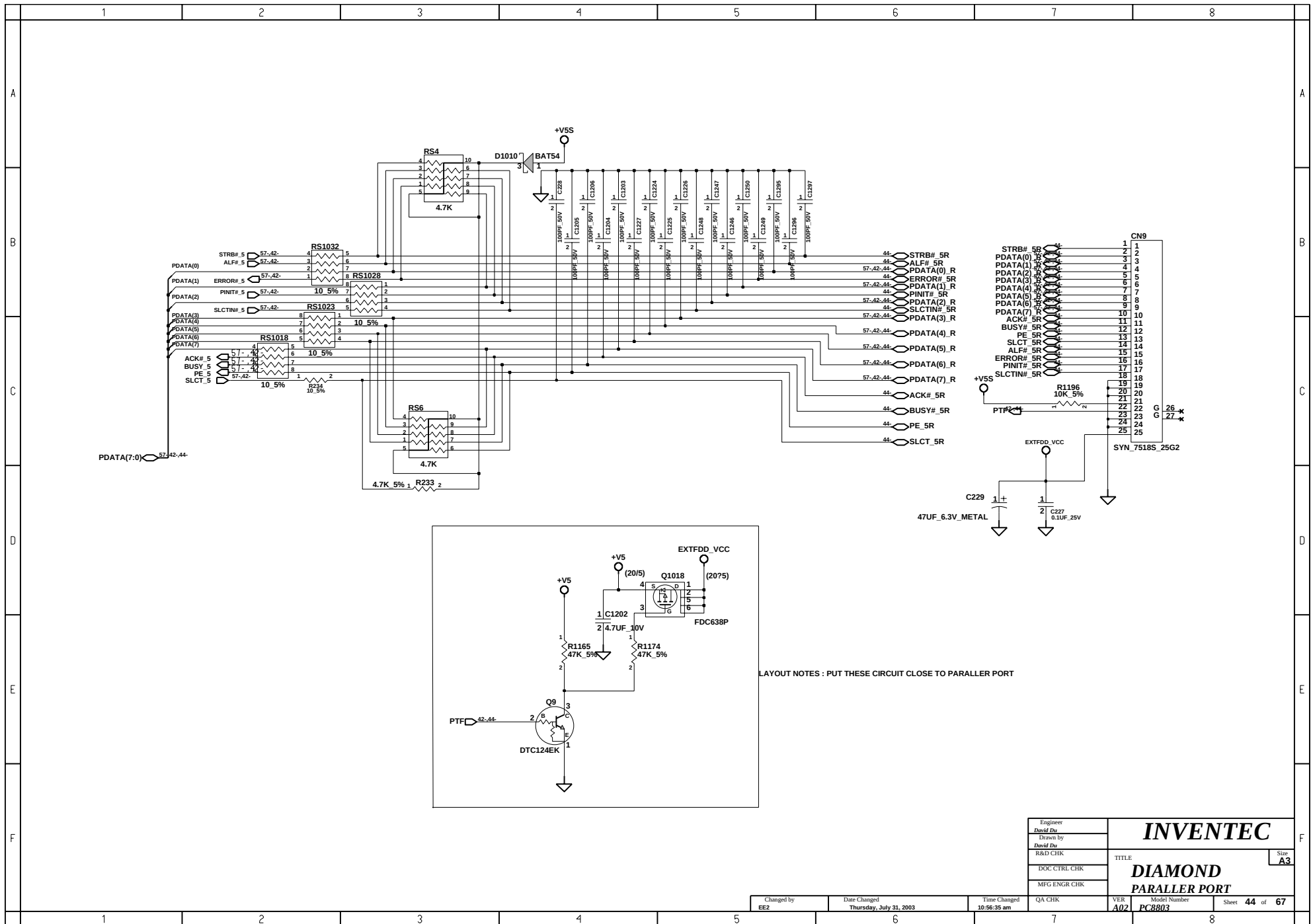
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MFG ENGR CHK	Model Number PC8803	Sheet 41 of 67

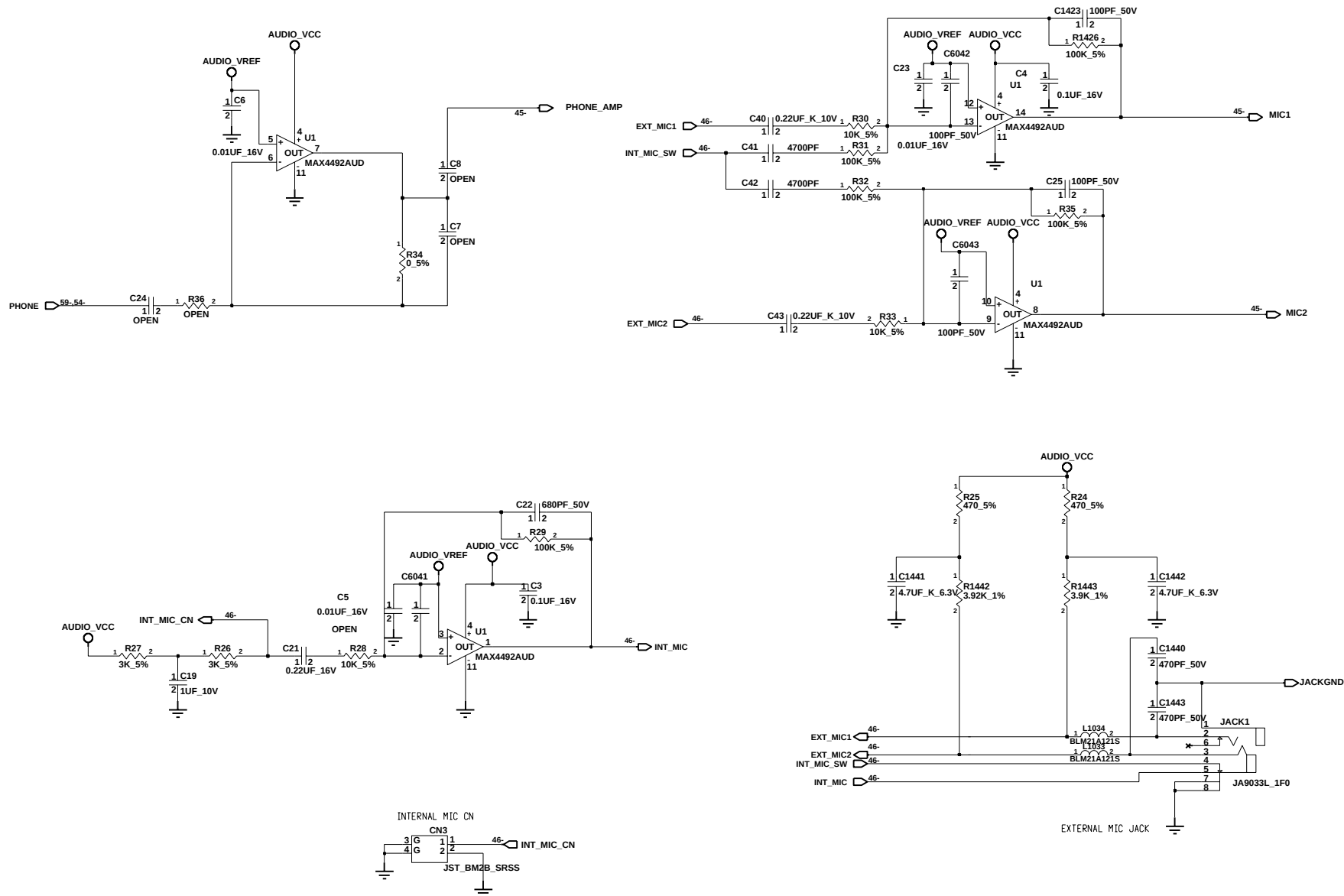
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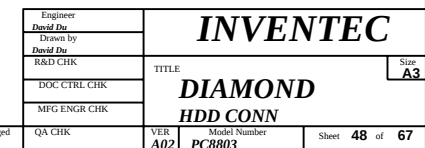


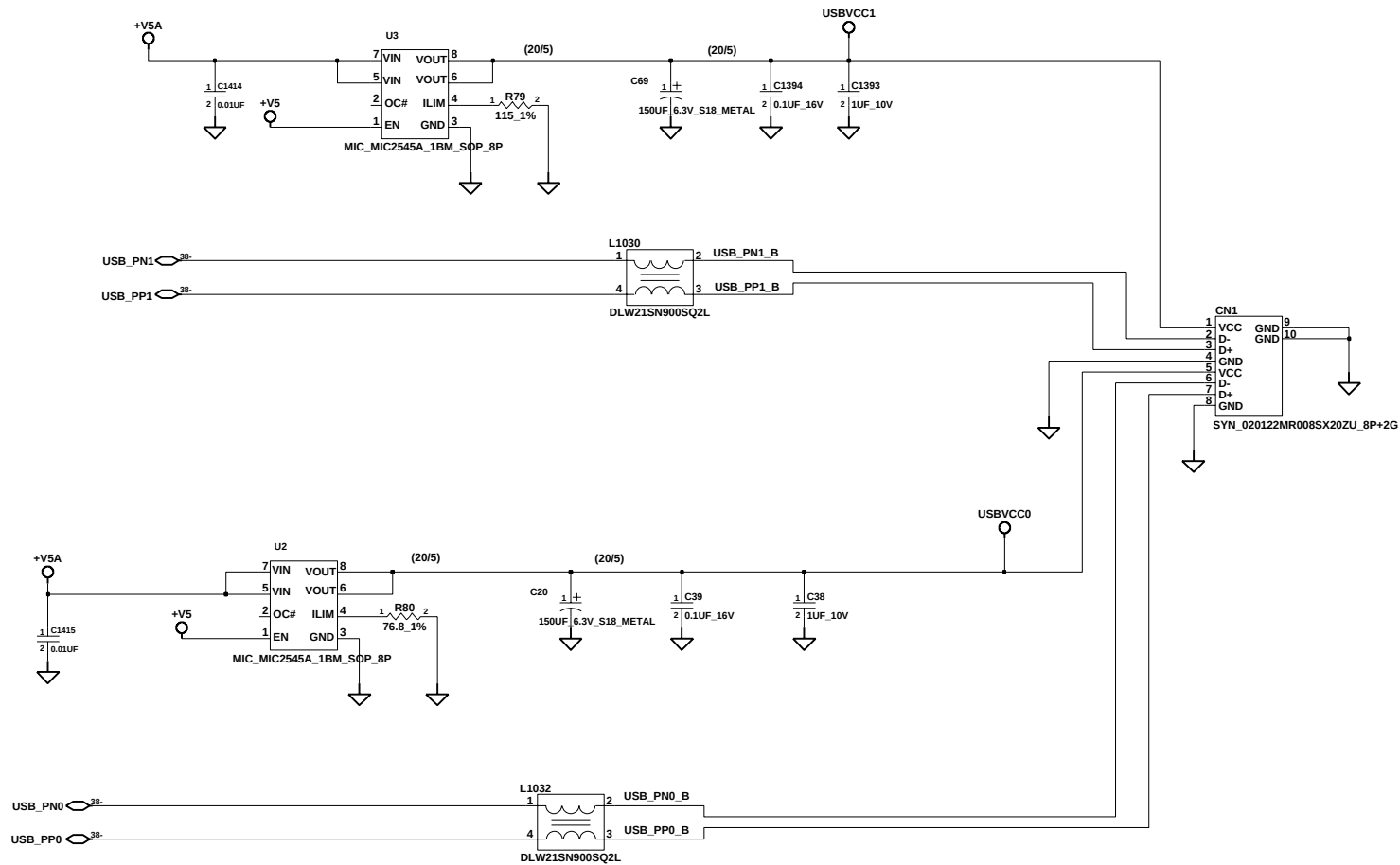






Engineer David Du	INVENTEC TITLE: DIAMOND EQ&MIC JACK Model Number: PC8803	
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Changed by EE2	Date Changed Friday, August 15, 2003	Time Changed 1:50:14 pm
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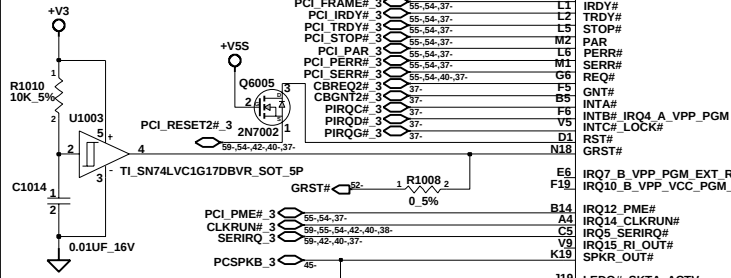
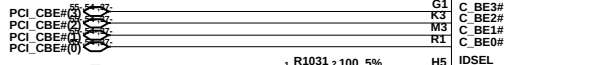
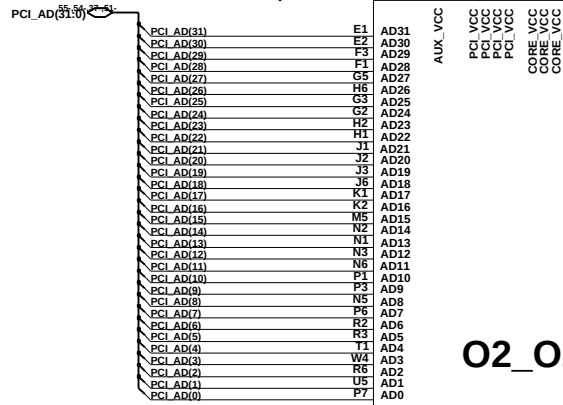
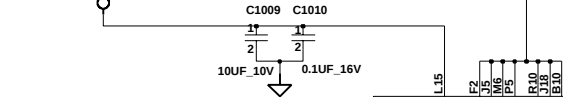
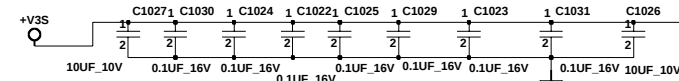




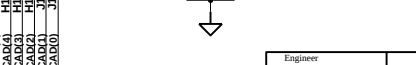
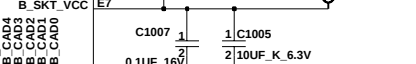
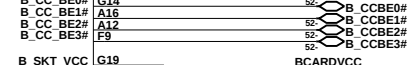
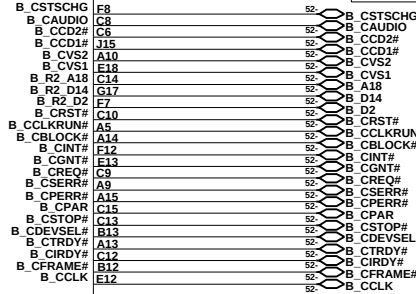
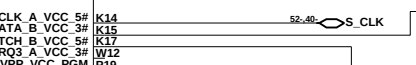
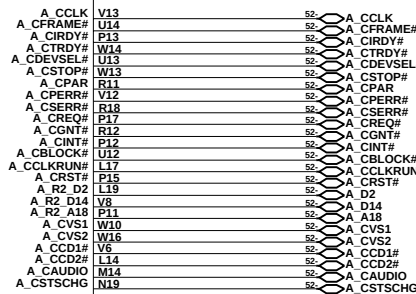
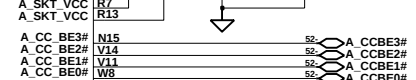
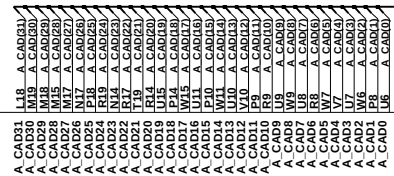
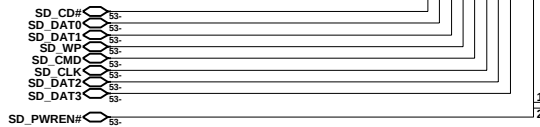
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TITLE					
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U2054

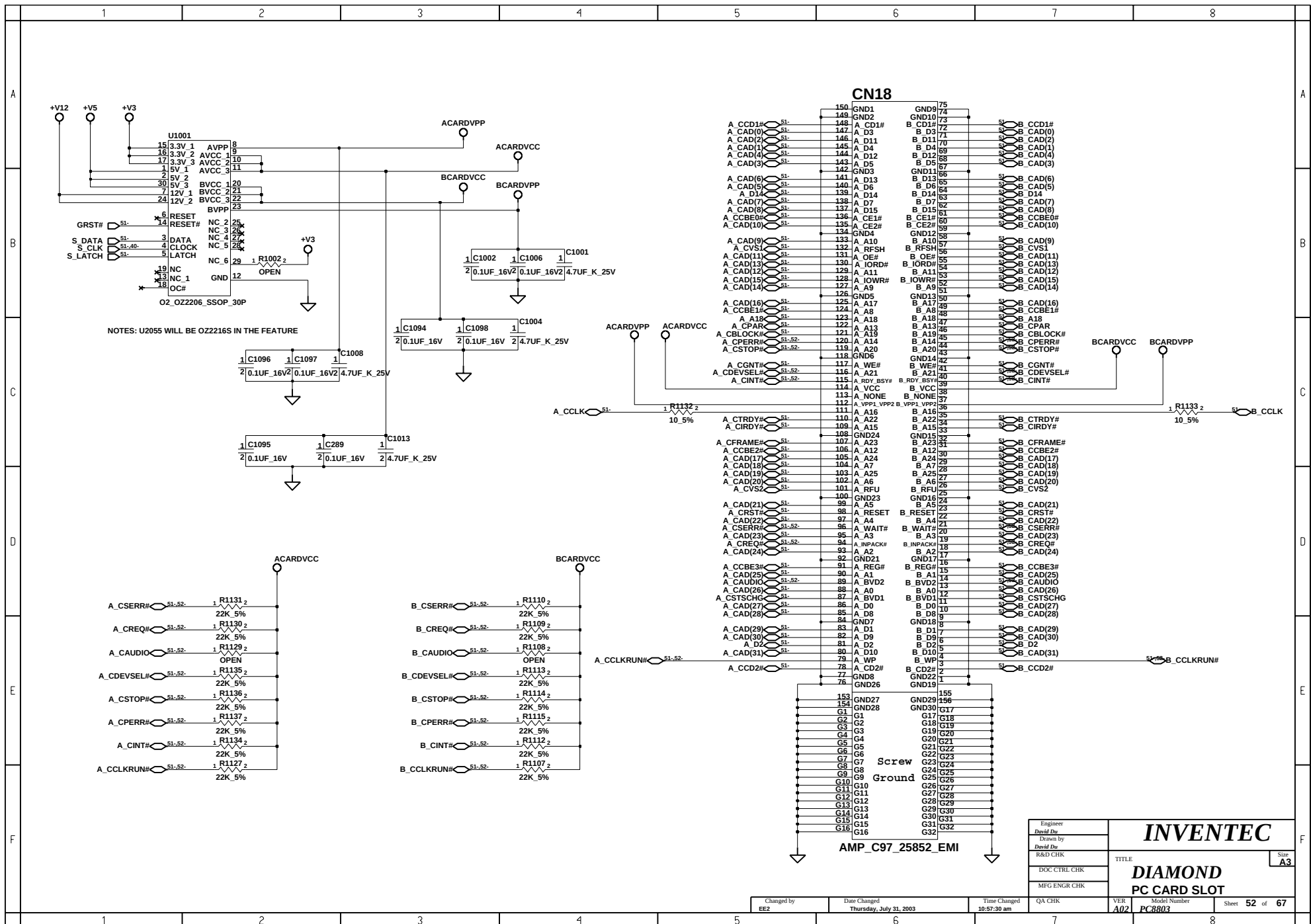
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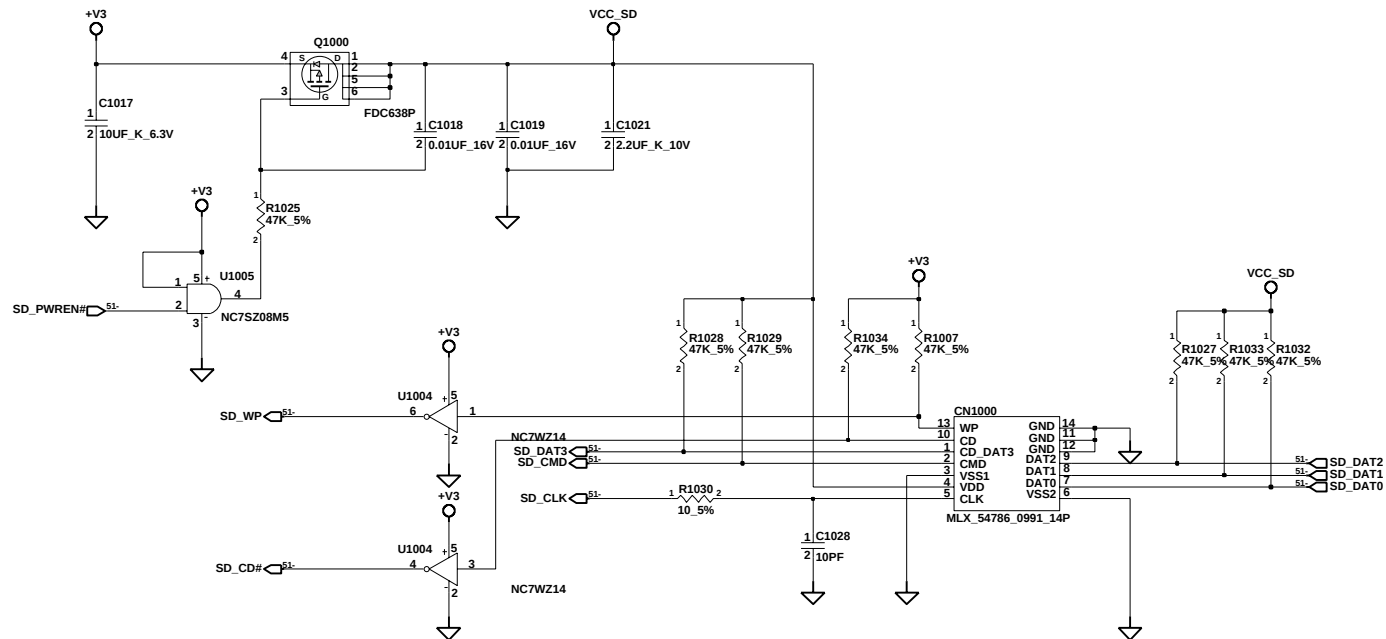


NOTS : U2054 WILL BE OZH31B IN THE FEATURE

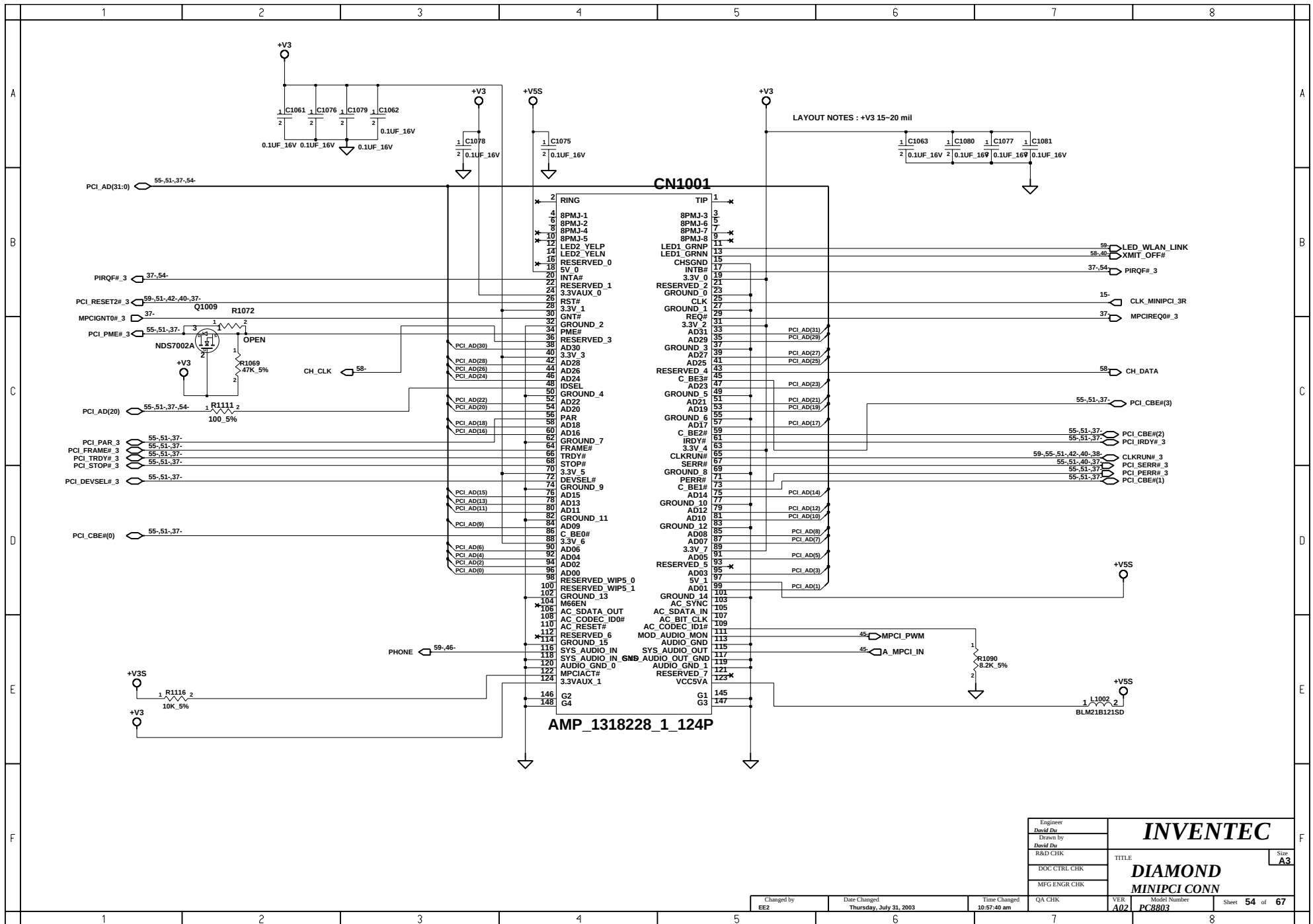


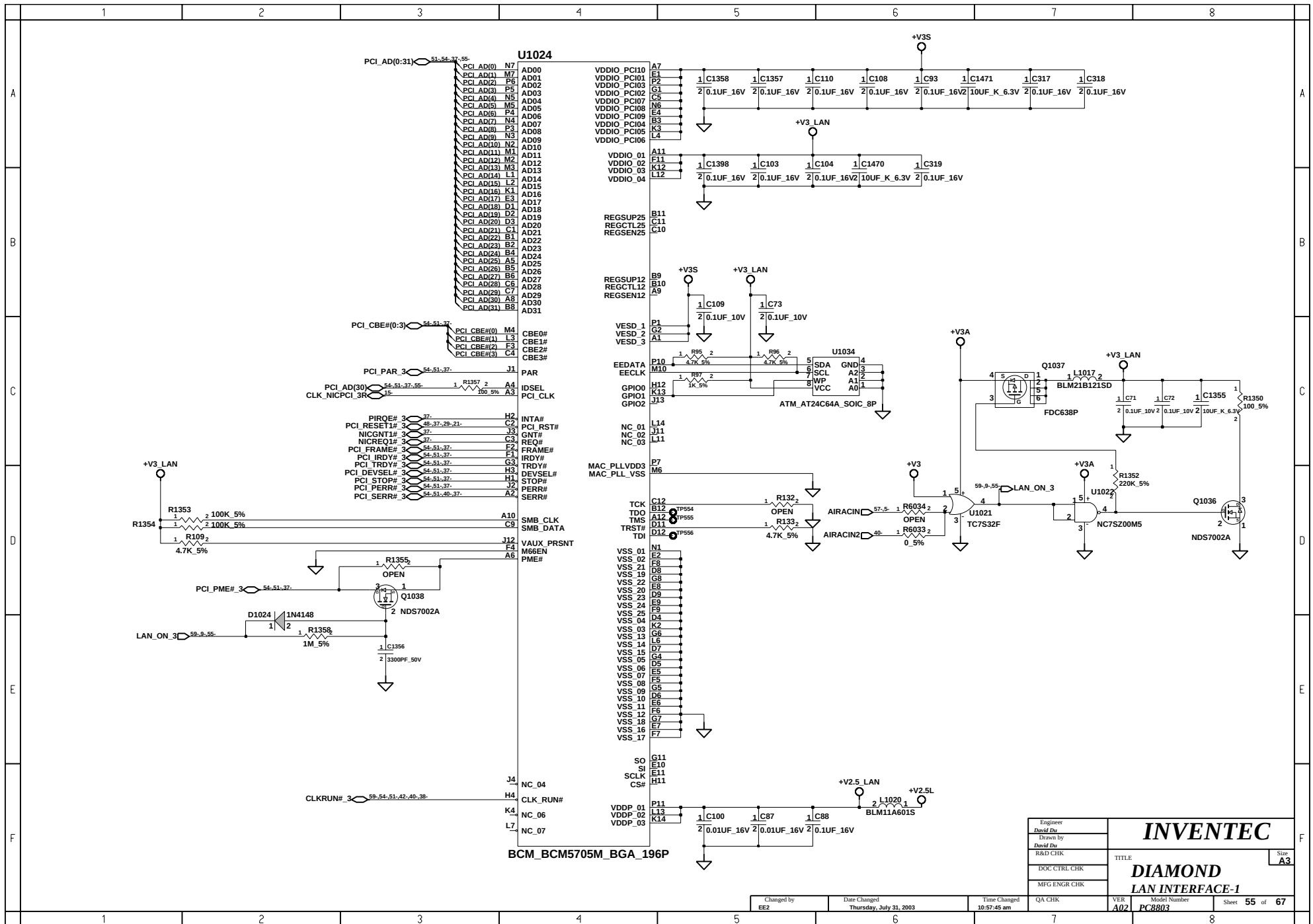
Engineer David Du	INVENTEC TITLE DIAMOND CARDBUS CONTROLLER Model Number PC8803 Sheet 51 of 67			Size A3
Drawn by David Du				
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				
QA CHK	VER A02	Model Number PC8803	Sheet 51 of 67	

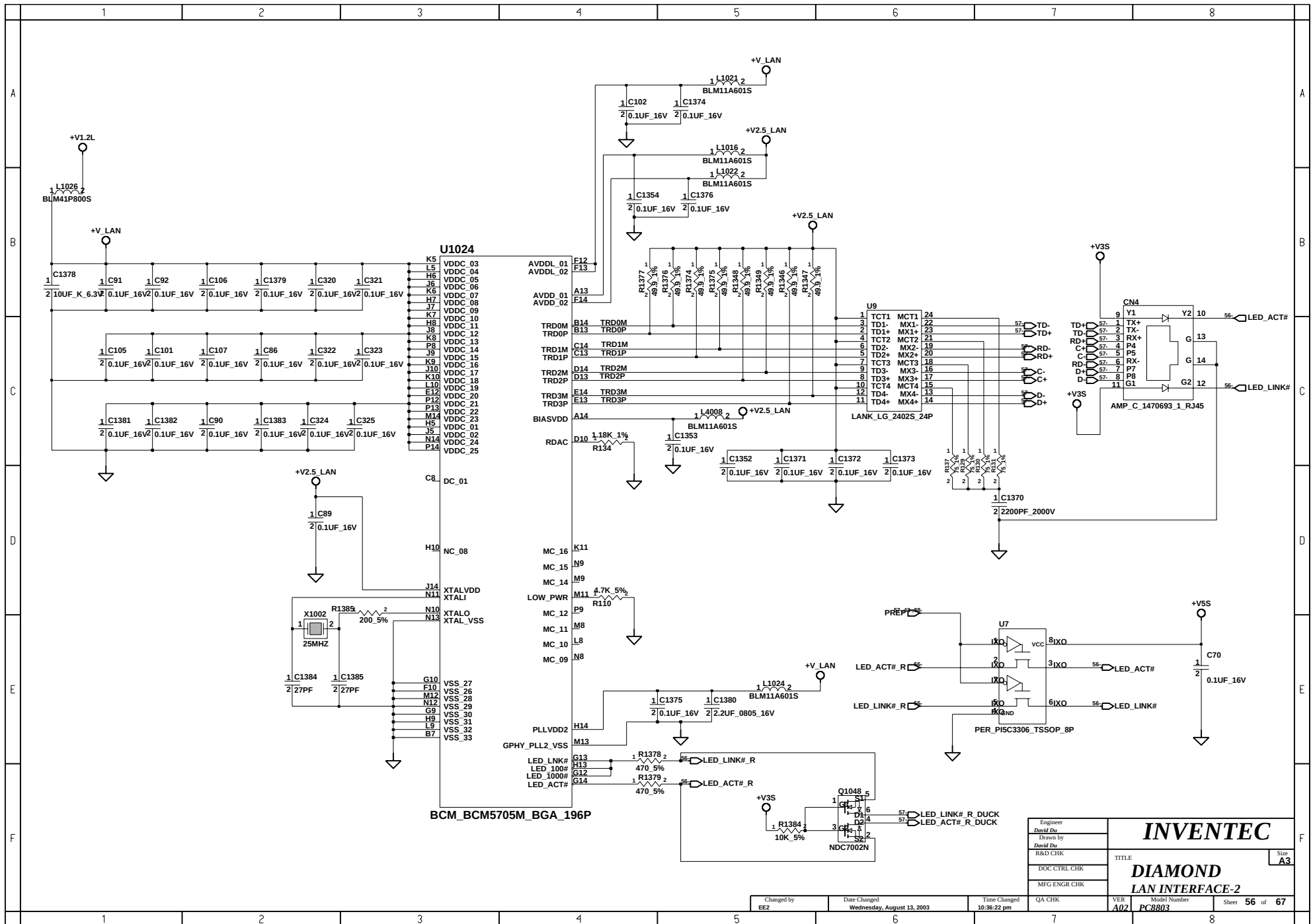


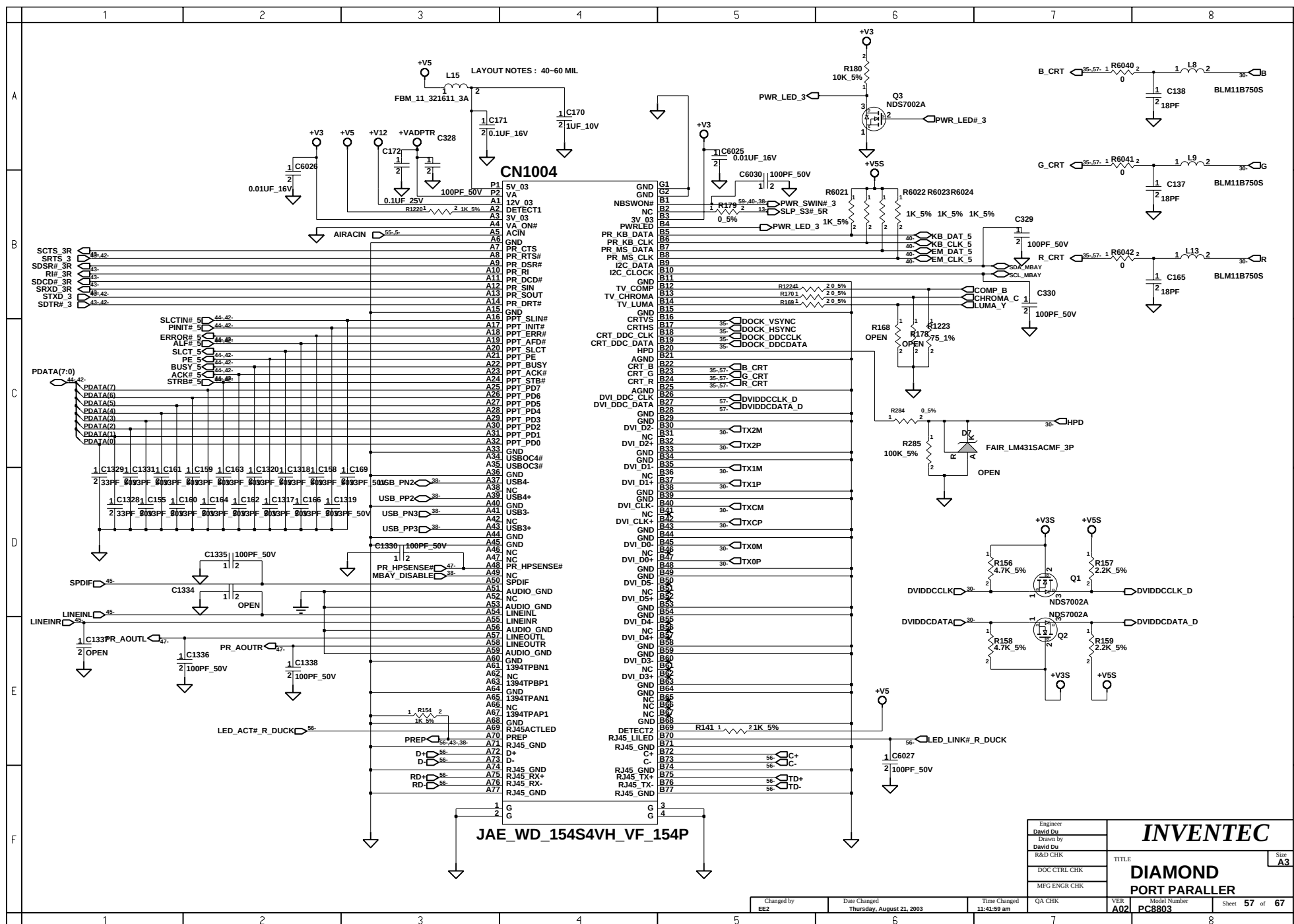


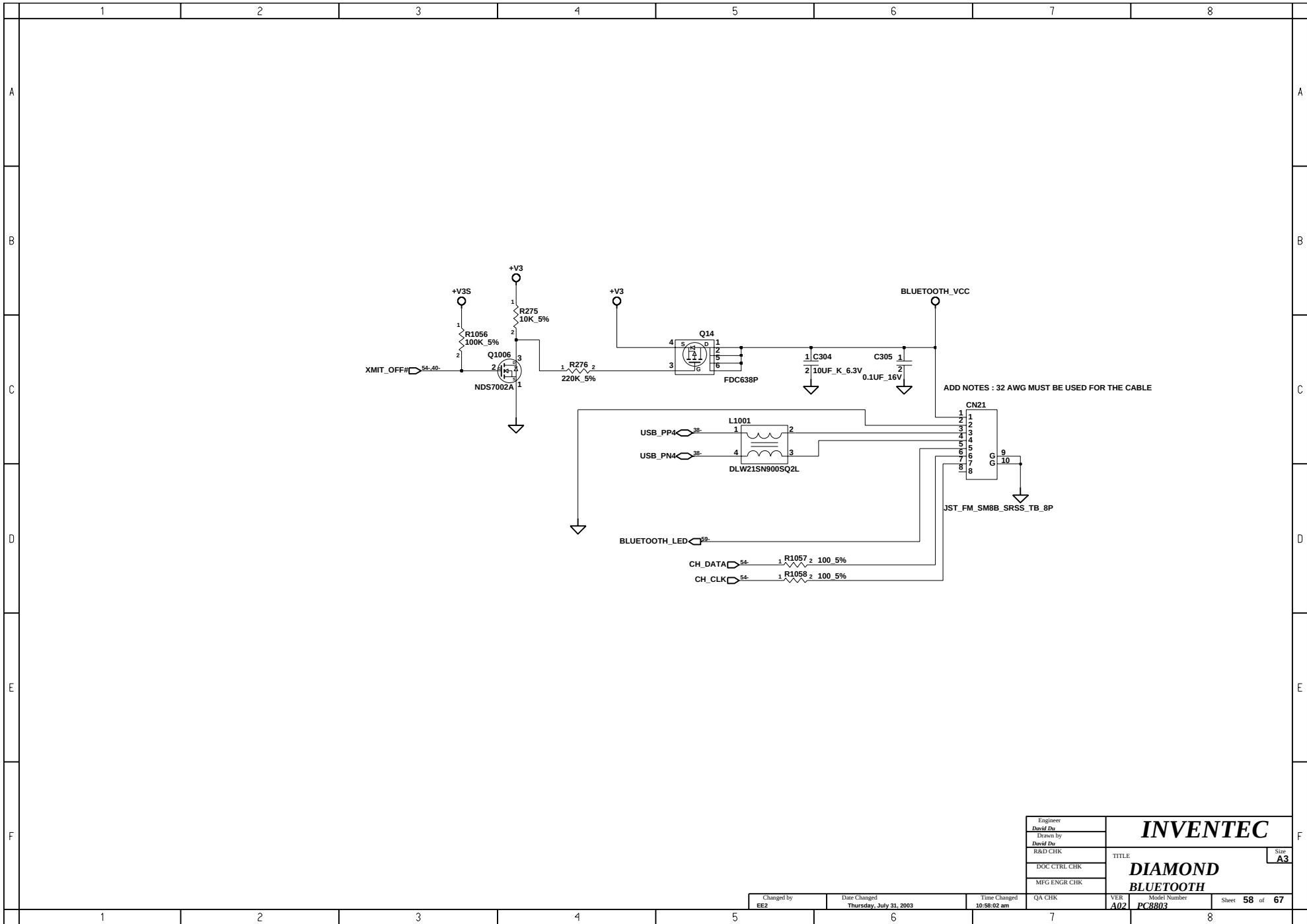
Engineer <i>David Du</i>		INVENTEC TITLE DIAMOND SD CARD CONN		Size A3
Drawn by <i>David Du</i>				
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				
QA CHK	VER A02	Model Number PC8803	Sheet	53 of 67

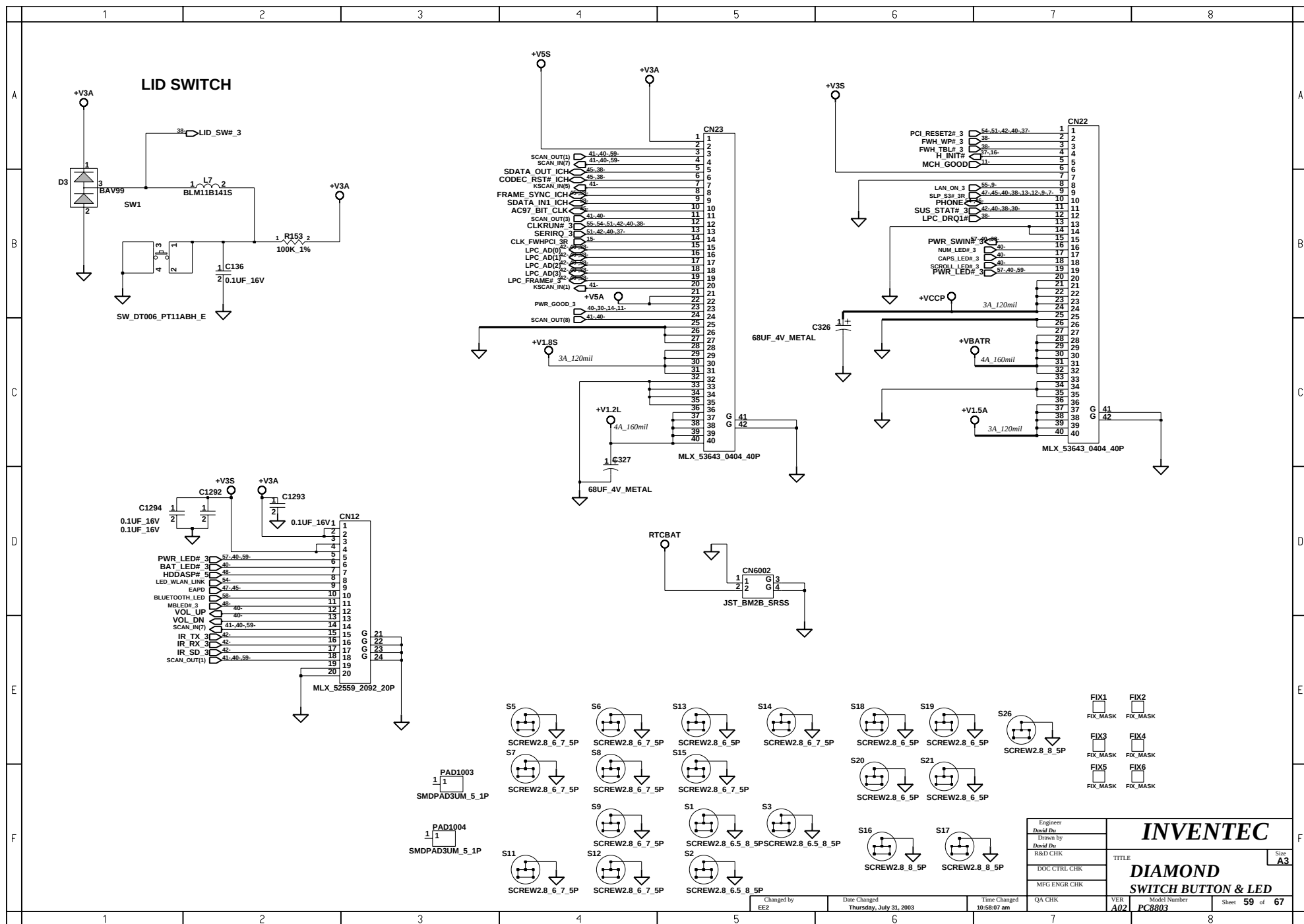


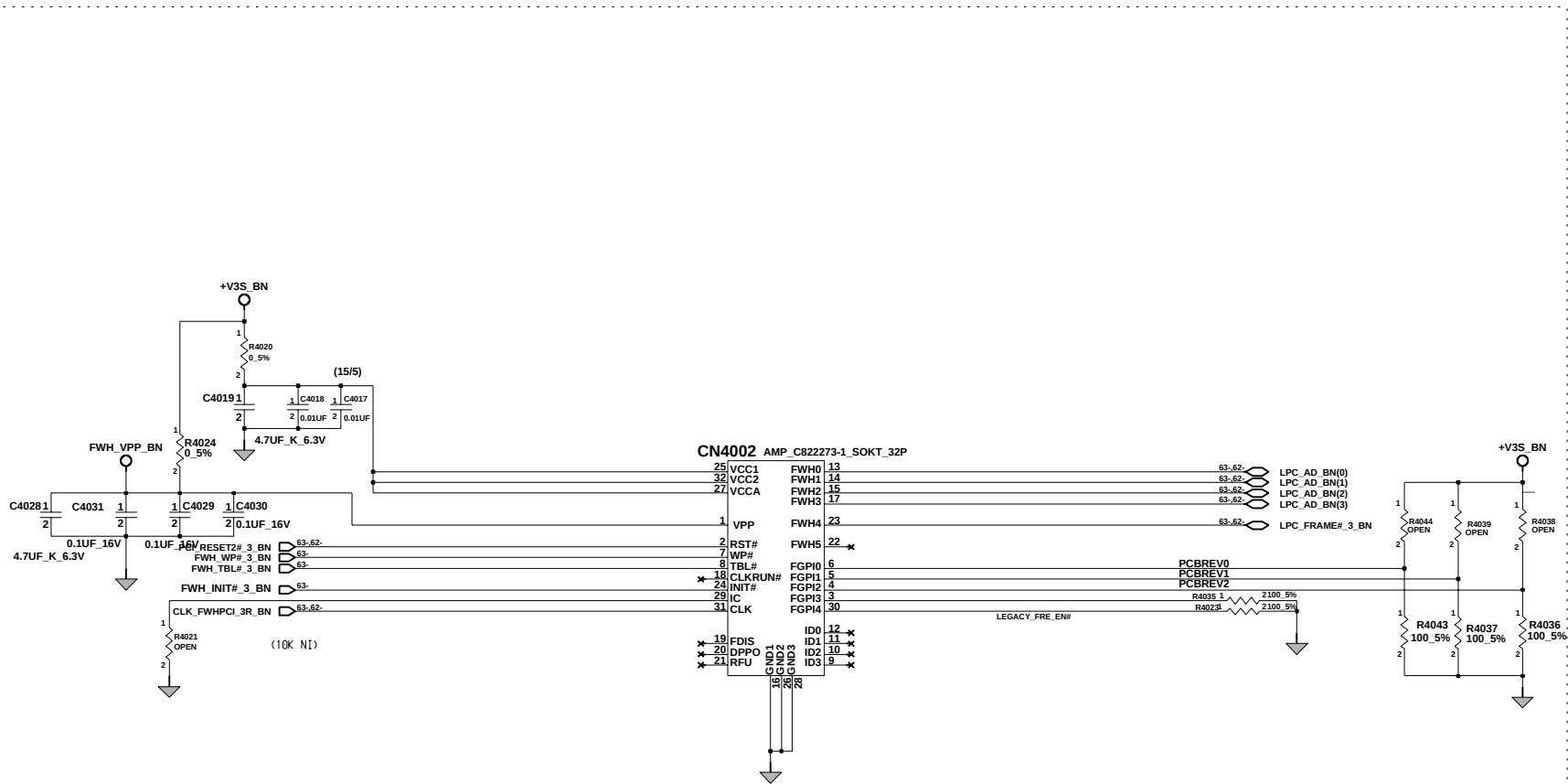








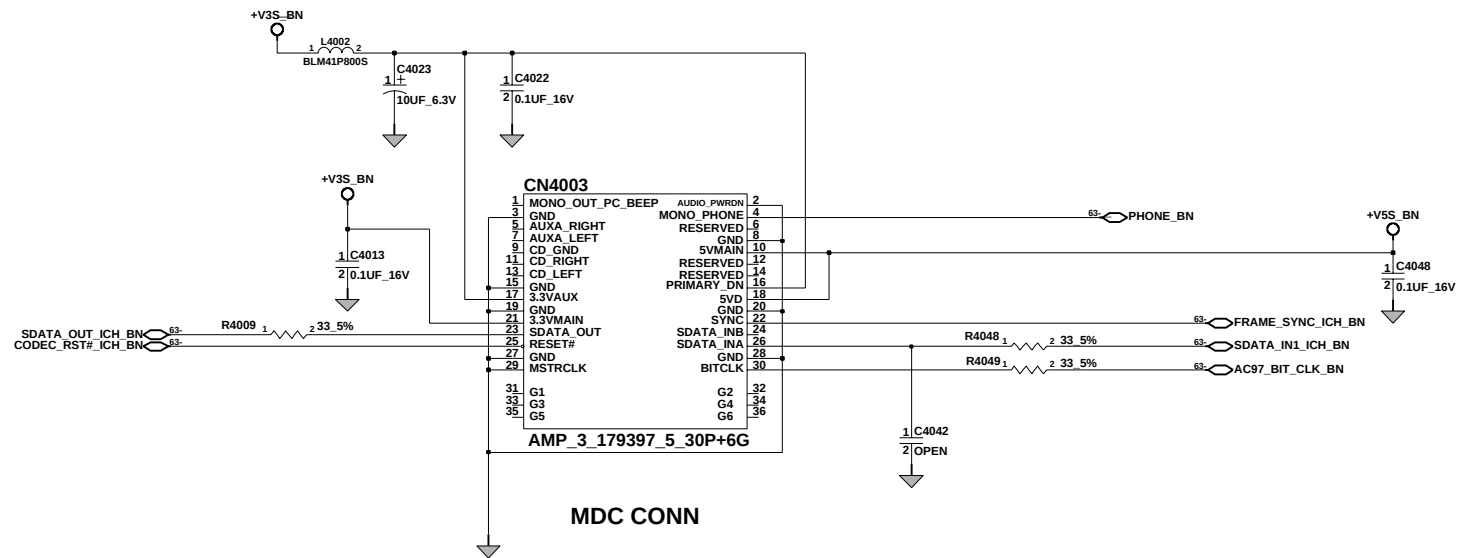




FHW ON BUTTON DAUGHTER BOARD

Engineer <i>David Du</i>	INVENTEC TITLE: DIAMOND FHW Model Number: PC8803		Size: A3 Sheet 60 of 67
Drawn by <i>David Du</i>			
R&D CHK			
DOC CTRL CHK			
MFG ENGR CHK			
QA CHK	VER: A02	Model Number: PC8803	Sheet 60 of 67

Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:58:37 am
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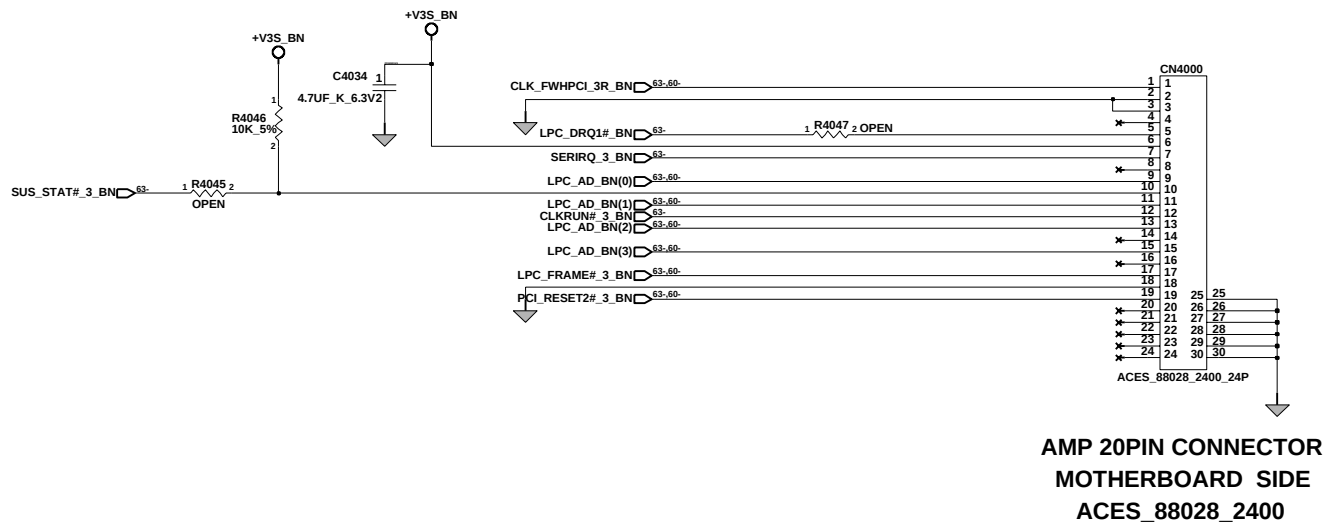


MDC CONN ON BUTTON DAUGHTER BOARD

Engineer <i>David Du</i>	INVENTEC	
Drawn by <i>David Du</i>	TITLE: DIAMOND	
R&D CHK	MDC CONN	
DOC CTRL CHK	Model Number PC8803	
MFG ENGR CHK	Sheet 61 of 67	

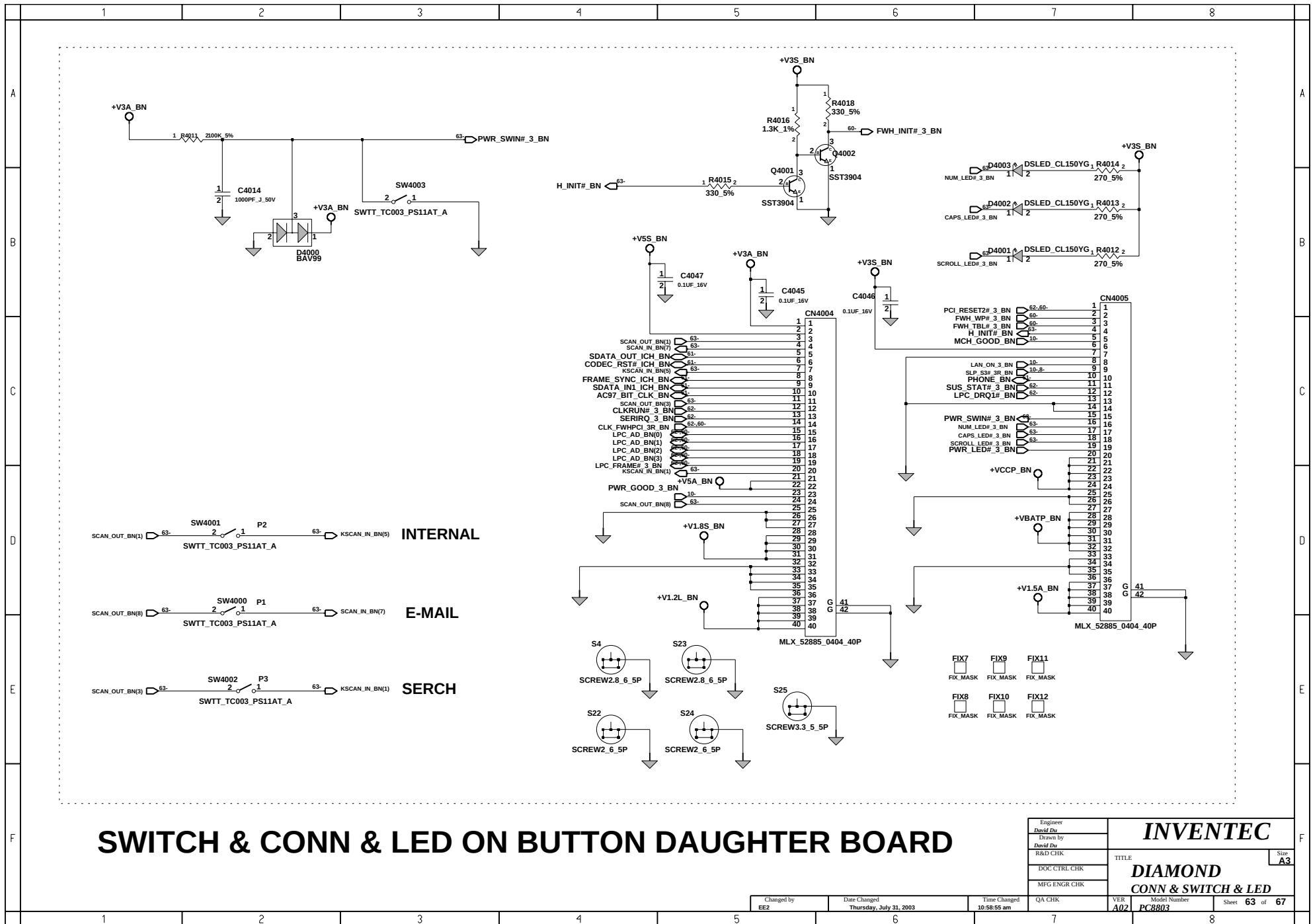
Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:58:42 am	QA CHK
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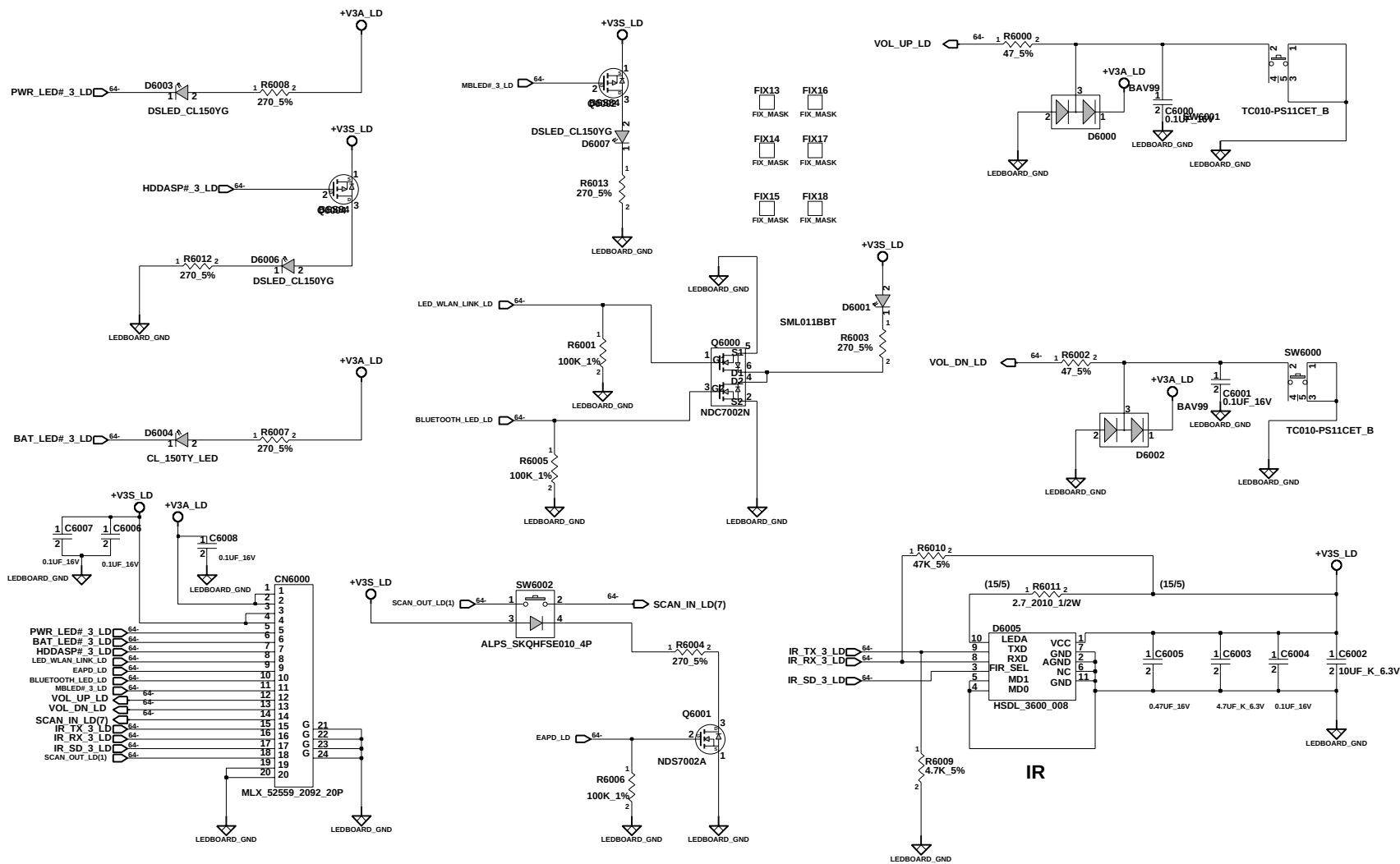
VER A02	Model Number PC8803	Sheet 61 of 67
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T CPA CONN ON BUTTON DAUGHTER BOARD

Engineer David Du	INVENTEC	
Drawn by David Du		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK	TITLE DIAMOND	
	T CPA	
Changed by EE2	VER A02	Model Number PC8803
Date Changed Thursday, July 31, 2003	QA CHK	Sheet 62 of 67
Time Changed 10:58:47 am		





LED & VOL BUTTON ON LED DAUGHTER BOARD

Engineer David Du	INVENTEC TITLE DIAMOND LED & VOL BUTTON	Size A3
Drawn by David Du		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
Changed by EE2	Date Changed Thursday, July 31, 2003	Time Changed 10:59:01 am
QA CHK	VER A02	Model Number PC8803
Sheet		64 of 67

	1	2	3	4	5	6	7	8																									
A									A																								
B	SHEET 10 1.R1096 change from 10K to 200K and add CAP (0.01UF) to GND NOTE: For ATI power sequence		SHEET 50 1. Change CN2068 for mechanical		SHEET 31 1. CHANGE C1982 C1983 TO 10 PF THESE TWO CAPACITORS VALUSE CAN BE TUNED 2. ADD SERIES RESISTORS R237 R238 ON EACHOF THE TWO SIGNAL (DIMB_0 DIMB_1) MAKE A NOTE TO ONLY INSTALL THESE RESISTORS FOR ELPIDA MEMORY																												
	SHEET 11 1. Add PM_VGATE DELAY CIRCUIT: U18, D23, R156, C90, C107 2. Add CAP (0.1UF) in R2024 pin1 to GND NOTE: For PCI_RESET, PCI_CLOCK , PM_VGATE TIMING		SHEET 56 1. Change CN2062 point name NOTE: Modify LAN signal to right		SHEET 32 1. ADD L21 TO POWER PIN(P8, Y8, AC11,AC20,Y23, L23,H20,H11) MAKE A NOTES: THIS RAIL HAVE JUMPER TO SWITCH FOR +V1.5S FOR M10 , +V1.8S FOR M9+X																												
	SHEET 12 1. Del PAD2 , PAD515 and to short power		SHEET 63 1.Change SW4 for mechanical		SHEET 33 1. CHANGE THE TITLE , THESE IS A TYPO" VEDIO". IT SHOULD BE " VIDEO"																												
C	SHEET 13 1. R1122 change from 220K to 100K and add CAP(0.1UF) in R1122 pin1 to GND 2. Add CAP (OPEN) in Q25 pin3 to GND NOTE: For ATI power sequence		CHANGE LIST FEBRUARY 12 SHEET 11 1.Change C65 TO CONNECTED TO PIN2 OF R2024(REF.RUBY) 2.CHANGE R1061 TO OPEN(REF.RUBY)		CHANGE LIST FEBRUARY 17 SHEET 32 1. OPEN L12 AND L21 PUT BLM21A121S TO USED M9+X +V1.8S																												
	SHEET 29 1. Add SERIES RESISTORS on SB_STBS(R208), ADSTBS_0(R211), ADSTBS_1(R212) 2. Add SERIES RESISTORS DBI_LO(R198), DBI_HI(R197) 3. Change R72 from 10K to 20K 4.Add TEST POINT in GPIO(4:7) GPIO(9:14) 5. Add 1K pull down on GPIO8 6. Add 1K pull down RESISTOR to prvent leakage for some ZV_LCDDATA, ZV_LCDCNTL pin (ZV_LCDDATA(5:15), (18:19), ZV_LCDCNTL(0:3)) 7. Add 10K pull down RESISTOR to prevent leakage for ZV_LCDDATA(16:17) NOTE: Above items for ATI recommmed 8. R153,R154, change form 1K to 3K NOTE: For +V3S power rail (ATI- VREFG) 9. Add SERIES RESISTORS STOP(R192) DEVSEL(R199) TRDY(R200) IRDY(R217) FRAME(R216) 10. Add SERIES RESISTORS WBF(R205) RBF(R215) AD_STBF_0(R202) AD_STBF_1(R203) SB_STBF(R204) 11. R189 , R190 PIN2 TO GND 12. R72 PIN 1 TO LINK +V3S		SHEET 13 1.CHANGE R1122 FROM 220K TO 100K (REF.RUBY)		SHEET 42 1. OPEN L12 AND L21 PUT BLM21A12. CHANGE SIGNAL NAME R252 PIN2 FROM PCI_RESET TO NPCI_RESET																												
	SHEET 38 1. Add 0 OHM series resistor on the path of osc output to X'TAL and XTALOUT NOTE: Select frequency enter channnel		SHEET 29 1. ADD NOTE TO M9+X , M10 R153, R154 USED OR NOT 2. Change AGPTTEST DBI_LO & DBI_HI TO +V1.5ATIAGP RAIL TO MATCH RUBY		CHANGE LIST FEBRUARY 20 SHEET 11 1. U2010 PIN2(TON) TO ADD RESISTOR (OPEN) THEN PULL UP TO +VCCP																												
D	SHEET 30 1. Add 0 OHM series resistor on the path of osc output to X'TAL and XTALOUT NOTE: Select frequency enter channnel		SHEET 50 1.CHANGE USB POWER FROM +V5A TO +V5 TO MATCH RUBY SCH(REF.RUBY)		SHEET 13 1. CHANGE C66 VAULE FROM 22UF TO 68UF 2. ADD THE CAP 10UF C91 PIN1 TO LINK Q26 (PIN1,2,5,6) C91 PIN2 TO GND																												
	SHEET 29,30, 31, 32 1. Change for ATI M9+X , M10 dual footprint schematic		CHANGE LIST FEBRUARY 14 SHEET 33 1. CHANGE DEPEND RESISTORS PACKAGE TO LAYOUT ROUTES		CHANGE LIST FEBRUARY 21 SHEET 30 1. ADD THE BLUETOOTH SIGNAL AT CN1020 PIN43(CH_DATA) , PIN36(CH_CLK)																												
	SHEET 36 1. Add R219 TO JUMP M10 OR M9+X		CHANGE LIST FEBRUARY 15 SHEET 10 1. ADD U22 SCHMITT BUFFER FROM PIN2 R1096 AFTER THE RC CIRCUIT TO PIN10 OF U2008		SHEET 58 2. REMOVE C1671 C1673 C1674 C1675 THEN C1672 PIN2 CHAISY GND CHANGE TO DIGITAL GND																												
E	SHEET 38 1. U509 -V19 (PM_VGATE/VRMPWRGD) change from PM_VGATE to SB_VGATE 2. Add U509 GPIO42 resistors R236 pull up +V3S and R237 pull down to GND NOTE: To chose memory type		CHANGE LIST FEBRUARY 23 1. ADD RESISTOR 100K PULL UP TO +V3S ANOTHER PIN TO LINK Q53 PIN2(XMIT_OFF#)		CHANGE LIST FEBRUARY 27 SHEET 28 1. ADD BYPASS CAPACITORS C308, C309, C310 (220P)																												
	SHEET 41 1. Rotate CN8 (the touch-pad conect):180 point of view and pin5,6 link to GND, pin7,8 link to CN7 pin3 2. Change CN7 (stick point) geoetric (layout pattern)		CHANGE LIST FEBRUARY 27 SHEET 28 1. ADD BYPASS CAPACITORS , C311, C313 (150UF)																														
	SHEET 49 1. Change R155 from 10K to 100K																																
F									F																								
<table><tr><td colspan="2">Engineer David Du</td><td colspan="2" rowspan="5"> INVENTEC DIAMOND CHANGE LIST </td><td colspan="2" rowspan="5">TITLE Size A3 </td></tr><tr><td colspan="2">Drawn by David Du</td></tr><tr><td colspan="2">R&D CHK</td></tr><tr><td colspan="2">DOC CTRL CHK</td></tr><tr><td colspan="2">MFG ENGR CHK</td></tr><tr><td colspan="2">Changed by EE2</td><td colspan="2">Date Changed Thursday, July 31, 2003</td><td colspan="2">Time Changed 10:59:06 am</td><td>QA CHK</td><td>VER A02</td><td>Model Number PC8803</td><td>Sheet 65 of 67</td></tr></table>										Engineer David Du		INVENTEC DIAMOND CHANGE LIST		TITLE Size A3		Drawn by David Du		R&D CHK		DOC CTRL CHK		MFG ENGR CHK		Changed by EE2		Date Changed Thursday, July 31, 2003		Time Changed 10:59:06 am		QA CHK	VER A02	Model Number PC8803	Sheet 65 of 67
Engineer David Du		INVENTEC DIAMOND CHANGE LIST		TITLE Size A3																													
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R&D CHK																																	
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Changed by EE2		Date Changed Thursday, July 31, 2003		Time Changed 10:59:06 am		QA CHK	VER A02	Model Number PC8803	Sheet 65 of 67																								
1	2	3	4	5	6	7	8																										

	1	2	3	4	5	6	7	8	
A	FOR SI SCHEMATIC CHANGE								A
B	SHEET 17 1. FOR PSI# PULL HIGH +VCCP TO OPEN R1089			2003/03/24 update SHEET 31 & 34 1. ADD DDR_CSBI# SIGNAL FOR 8M*32 VRAN USED AND ADD R6016 RESISTOR DEPEND TO LINK U1018 & U1019 PIN M4			2003/03/30 update SHEET 5 1. ADD R6021 RESISTOR OPEN TO MODIFY OCP		
	SHEET 38 1. REMOVE PWM_GOOD_3 SIGNAL DELETE R1419			SHEETS 1.CHANGE R1068 VALUE FROM 150K TO 10K AND R1070 VALUE FROM 20K TO 1.3K FOR AC DETECT LEVEL			SHEET 33 1. CHANGE THE VALUE OF SERIAL TERMINATION RESISTORS ON THE DATA , DQM AND QS LINES TO 22-OHMS		
	SHEET 40 1. R1049 FOR PCI_SERR#_3 PULL DOWN TO OPEN 2. KBC INITIAL NEED TO PULL DOWN 100K RESISTOR R1 3. ADD U4004 , D6008 , R6014 , C6011 , C6012 THIS CIRCUIT TO FINE_TUNE POWER ON SEQUENCE TIME			SHEET15 1.CHANGE R6015 VALUE FROM OPEN TO 33 AND R1361 VALUE CHANGE TO OPEN FOR AC'97 FRQUENCE CHANGE 14.318MHZ			SHEET 34 1. CHANGE THE VALUE OF SERIAL TERMINATION RESISTOR OF QS LINE TO 22-HHMS		
	SHEET 41 1. RNET FOR KAHUNA PROGRAMMING. NEED ONLY FOR FLASH RECOVERY SATURATION			SHEET42 1.CHANGE R126 VALUE TO 0 AND R125 VALUE CHANGE FROM 0 TO OPEN FOR SUPER I/O USED_NPCI_RESET			SHEET 40 1. R105 SHOULD BE OPEN TO FINE TUNE KBC POWER UP SEQUENTIAL		
C	SHEET 50 1. REMOVE USB_OC#0 U2 , U3 PIN_2 TO OPEN 2. U2 , U3 PIN_1(EN) CONNECT +5V 3. U2 , U3 PIN_5 CONNECT +V5A 4. REMOVE L3 AND TO SHORT			SHEET44 1.CHANGE R1165_PIN-1 VOLTAGE FROM +5VA TO +V5			SHEET 55 1. ADD TWO 0.1uF DECAPS C6018 AND C6019 TO +V3S RAIL 2. ADD 0.1uF DECAPS C6019 TO +V3_LAN		
	SHEET 52 1. REMOVE R1126 , R1128 TO A_CCLKRUN# , B_CCLKRUN# SIGNAL PULL UP REPEAT			SHEET45 1.CHANGE R1460 FROM OPEN TO 1K FOR AC'97 CHOOSE FRQUENCE			SHEET 56 1. ADD SIX 0.1uF DECAPS C6021 , C6022 , C6023 , C6024 , C6025 , C6026 TO +V_LAN RAIL		
	SHEET 64 1. ADD Q6004 TO PREVENT THE CURRENT FROM D6006 INTO HDDLED LOGIC OUTPUT			2003/03/26 update SHEET13 1. CHANGE P MOS Q1041 , Q1046 , Q10 , Q11 HIGH CURRENT AND LOW RDS ON			2003/04/03 update SHEET 45 1. ISOLATE PCSPKB_3 USING A 2N7002 WITH DRAIN CONNECTED TO +V3S THROUGH A 10K PULL-UP , SOURCE TO GROUND , & GATE TO PCSPKB_3 . PIN 2 OF C28 SHOULD BE CONNECTED TO DRAIN OF 2N7002 , WHICH FEEDS THE AUX INPUT OF CODEC		
	2003/03/18 update SHEET 15 1. ADD R6013 RESISTOR OPEN TO JUMP CLK_SIO14_3R & ADI48M			SHEET36 1. CHANGE P MOS , Q1050 HIGH CURRENT AND LOW RDS ON			2. ISOLATE PCSPKB_ICH_3 USING A 2N7002 WITH DRAIN CONNECTED TO +V3S THROUGH A 10K PULL-UP , SOURCE TO GROUND , & GATE TO PCSPKB_ICH_3 . PIN 2 OF C15 SHOULD BE CONNECTED TO DRAIN OF 2N7002 , WHICH FEEDS THE AUX INPUT OF CODEC		
D	SHEET 40 1. ADD C6013 18P CAPICATOR BYPASS TO LOW_BAT#_3 THIS SIGNAL			SHEET 40 1. REMOVE R1409 , D1028 AND TO SHORT PCI_SERR#_3			SHEET 46 1. ISOLATE PHONE USING A 2N7002 WITH DRAIN CONNECTED TO +V3S THROUGH A 10K PULL-UP , SOURCE TO GROUND , & GATE TO PHONE . PIN 1 OF C24 SHOULD BE CONNECTED TO DRAIN OF 2N7002 , WHICH FEEDS THE AUX INPUT OF CODEC		
	2003/03/19 update SHEET 20 1. ADD U4005 , R6016 , Q6005 , D6009 , C6015 , C6014 THIS CIRCUIT AND CN6001 TO SECOND FAN			SHEET 50 1. REMOVE L2 AND TO SHORT			SHEET 47 , 64 1. CHANGING FDD ACTIVITY LED TO MULTI-BAY ACTIVITY LED AND HDD ACTIVITY LED TO JUST THE FIXED HDD		
	SHEET 52 1. CHANGE R1132 , R1133 FROM 47 OHM TO 22 OHM FOR A_CCLK & B_CCLK TIMING			SHEET 52 1. CHANGE VALUE C1358 , C1357 , C1398 FROM 0.01UF TO 0.1UF FOR BCM MODIFY			2003/04/05 update SHEET 31 1. CAPS C120 AND C125 ON CLOCK LINES CHANGE TO 10NF		
	SHEET 55 2. ADD CAPACITORS C6015 , C6014 10UF FOR BCM MODIFY			SHEET 55 1. REMOVE L2 AND TO SHORT			SHEET 57 1. THE HPD CIRCUIT REQUIRES A 2.5V ZENER (IN PARALLEL WITH R1264) , AND ADDITIONAL 20K IN SERIES. SEE THE ATI REFERENCE DESIGN BOARD		
E	SHEET 63 1.REMOVE CN4001 RTC CONNECTOR , CN1010_PIN-5-RTCBAT FOR MECHCNAL CHANGE			SHEET 64 1. CHANGE SW6001 AND SW6000 FOR MECHCNAL MODIFY 2. CHANGE D6005 I.R. SIZE FOR MECHCNAL MODIFY			Engineer David Du Drawn by David Du R&D CHK DOC CTRL CHK MFG ENGR CHK		
	SHEET 64 1. CHANGE SW6001 AND SW6000 FOR MECHCNAL MODIFY 2. CHANGE D6005 I.R. SIZE FOR MECHCNAL MODIFY			Changed by EE2 Date Changed Thursday, July 31, 2003 Time Changed 10:59:10 am			VER A02 Model Number PC8803 Sheet 66 of 67		
	SHEET 64 1. CHANGE SW6001 AND SW6000 FOR MECHCNAL MODIFY 2. CHANGE D6005 I.R. SIZE FOR MECHCNAL MODIFY			QA CHK			TITLE DIAMOND CHANGE LIST Size A3		
	SHEET 64 1. CHANGE SW6001 AND SW6000 FOR MECHCNAL MODIFY 2. CHANGE D6005 I.R. SIZE FOR MECHCNAL MODIFY			1			2		
F	1	2	3	4	5	6	7	8	F

	1	2	3	4	5	6	7	8			
A	FOR PV SCHEMATIC CHANGE				FOR PVR SCHEMATIC CHANGE 2003/07/17		FOR MV B SCHEMATIC CHANGE 2003/08/15				
	SHEET 30				SHEET 57		SHEET 5				
	1. ADD R6026 , R6027 , R6028 75 OHM RESISTORS TO PULL DOWN TO CRT SIGNAL R G B				1. CHANGE CAPACITOR VALUE C6027, C1335 FROM OPEN TO 100PF FOR EMI SOLUTION 2. ADD CAPACITOR C6030_100PF FOR EMI SOLUTION		FOR OCP circuit add R6043(0ohm) between U1002-8 and U1002-5 add R6044(open) and D6009(BAT54C) between U1002-13 and U1002-5				
	SHEET 35				SHEET 28		SHEET 46				
	1. ADD C6020 , CC6021 , C6022 CAPACITORS PULL DOWN TO OPEN 2. CHANGE L4 , L5 , L6 FROM BEAD TO 0 OHM RESISTORS				1. ADD CAPACITORS C6031 , C6032 , C6033 , C6034 , C6035 , C6036 , C6037 100PF FOR EMI SOLUTION 2. CHANGE CAPACITOR VALUE C256 FRPM 0.1uF TO 100PF		1. ADD CAPACITOR C6042_100PF between U1 pin 12 , pin 13 . 2. ADD CAPACITOR C6041 open between U1 pin 2 , pin 3 . 3. ADD CAPACITOR C6043_100PF between U1 pin 9 , pin 10 .				
B	SHEET 45				FOR PVR SCHEMATIC CHANGE 2003/07/23		FOR MV B SCHEMATIC CHANGE 2003/08/21				
	1. CHANGE C1435 FROM 4.7UF TO OPEN 2. SWAP MPC1_PWM THIS SIGNAL FROM C24 PIN1 TO Q1061 PIN 2 THAN Q1061 PIN 3 SWAP TO LINK R12 PIN 1				SHEET 9		SHEET 6				
	SHEET 46				1. R1582 , R1583 , R6019 , C1469 , Q1057 , Q1058 CHANGE TO OPEN FOR HP REQUIREMENT		1. CHANGE RESISTOR VALUE R1581 FROM 2.7K OHM TO 3.3K OHM FOR BATTERY SOLUTION				
	1. SWAP PHONE THIS SIGNAL FROM R12 PIN1 TO C24 PIN 2 2. CHANGE C5 , C6 , C23 FROM 0.1UF TO 0.01UF				SHEET 20		SHEET 9				
	SHEET 51				1. CN24 NOT TO USE		SHEET 13				
	1. ADD Q6005 NMOS TO CLEAR BACKWARD CURRENT				SHEET 47		1. CHANGE CAPACITOR VALUE C6016 FROM OPEN TO 12000PF FOR ATI RECOMMEND				
	SHEET 57				1. R1465 , R1449 CHANGE VALUE FROM OPEN TO 0 OHM 2. R1463 , R1448 CHANGE VALUE FROM 0 OHM TO OPEN		SHEET 13				
	1. KB_DAT_5 , KB_CLK_5 , EM_DAT_5 , EM_CLK_5, THIS SIGNAL ADD R6021 , R6022 , R6023 , R6024 , RESISTORS TO PULL UP +VSS 2. CHANGE L8 , L9 , L13 FROM 110 OHM FITTER BEAD TO 39 NH FITTER BEAD 3. CHANGE L4007 , L4006 , L4005 FROM 75 OHM FITTER BEAD TO 110 NH FITTER BEAD 4. CHANGE C137 , C138 , C165 FROM OPEN TO 18PF CAPICATORS				SHEET 5 54		1. CHANGE CAPACITOR VALUE C187 FROM 0.1UF TO 39000PF FOR ATI RECOMMEND				
					1. MOVE Q1009 , R1071 FOR LAYOUT PLACEMENT BECAUSE MECHANICAL COVER HAVE TO SHORT						
	FOR PV-R SCHEMATIC CHANGE 2003/07/15				FOR PVR SCHEMATIC CHANGE 2003/07/28						
					SHEET 5 57						
					1. Change resistors value R6021,R6022 ,R6023 , R6024 from 4.7k ohm to 1k ohm for KBC PS2 microsoft keyboard to match .						
					FOR PVR SCHEMATIC CHANGE 2003/07/31						
					SHEET 32						
					1. change capacitor value C1268 from 22uF to 68uF						
D					FOR PVR SCHEMATIC CHANGE 2003/08/01						
	SHEET 9				SHEET 35						
	1. CHANGE CAPACITOR C230 IMPROVE VGA/VCC BYPASS CAPACITOR ESR				1. change ferrite-bead from L4, L5 , L6 (BLM11B750S) to resistors R6037 , R6038 , R6039 0 ohm						
	SHEET 54				SHEET 57						
	1. CHANGE CAPACITOR C1075 , C1076 , C1077 , C1078 , C1079 , C1080 , SIZE FROM 0603 TO 0402 TO FIX INTEL WIRELESS CARD TO TOUCH CAPACITOR MECHANICAL ISSUE				1. change ferrite-bead from L4007, L4006 , L4005 (111XJBC_110NH) to resistors R6040 , R6041 , R6042 0 ohm						
	SHEET 56				2. change ferrite-bead from L8, L9 , L13 (111XJBC_39NH) to ferrite-bead L8 , L9 , L10 BLM11B750S						
	1. CHANGE RESISTOR R134 VALUE FROM 1.24K TO 1.21K TO INCREASE THE SIGNAL VOLTAGE LEVEL TO MEETING THE IEEE SPEC. 2. CHANGE R1351 FROM 4.70HM TO BLM11A601S FITTER BEAD (L4008) TO INCREASE THE SIGNAL VOLTAGE LEVEL TO MEETING THE IEEE SPEC.				FOR MV B SCHEMATIC CHANGE 2003/08/13						
	SHEET 57				SHEET 32						
	1. ADD CAPACITOR C328 , C329 , C330 TO 150PF FOR EMI SOLUTION				1. ADD P-MOS Q6006 PIN4 LINK TO SLP_S3_5R , PIN5,8 LINK TO +AGP_V3S & PIN1, 2, 3, 6, 7, LINK TO +V3S 2.REMOVE L1014						
	SHEET 58				SHEET 20						
	1. MOVE R1057 , R1058 FOR LAYOUT PLACEMENT BECAUSE MECHANICAL COVER HAVE TO SHORT				1. CHANGE CAPACITOR VALUE C286 2200pF TO NON-INSTALL						
	SHEET 59				SHEET 38						
	1. CLEAR PAD1003 LINK TO GND FOR EMI SOLUTION 2. ADD PAD1004 FOR THERMAL				1. ADD C6039 0.1UF BYPASS CAPACITOR .						
	SHEET 63				SHEET 47						
	1. DELETE R4010 AND LED_POWER_SWITCH CIRCUIT CHANGE TO SWITCH CIRCUIT FOR MECHANICAL REQUIREMENT				1. CHANGE RESISTOR R6035 0 OHM TO C6040 0.01UF CAPACITOR .						
	SHEET 64				SHEET 56						
	1. IR_PIN11 LINK TO GND				1. CHANGE RESISTOR VALUE R134 FROM 1.21K TO 1.18K OHM.						
F	LAYOUT NOTE:										
	1. CLK_CBIPCI_3R==>L5 CHANGE TO L3 2. CLK_MINIPCI_3R==>L5 CHANGE TO L3										
	1	2	3	4	5	6	7	8			
	Changed by EE2		Date Changed Thursday, August 21, 2003		Time Changed 11:48:22 am		QA CHK		VER A02	Model Number PC8803	Sheet 67 of 67

A

B

C

D

E

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