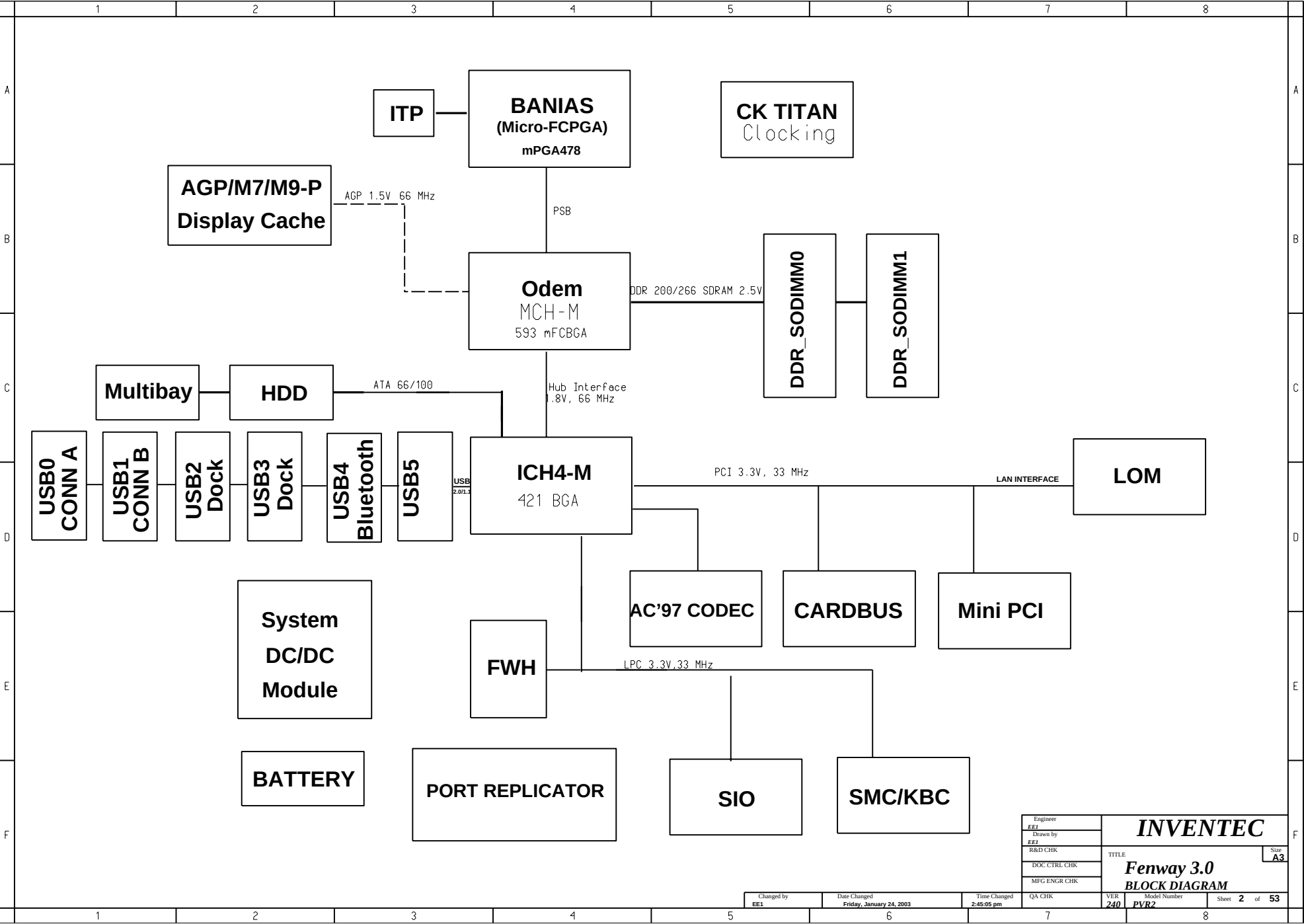
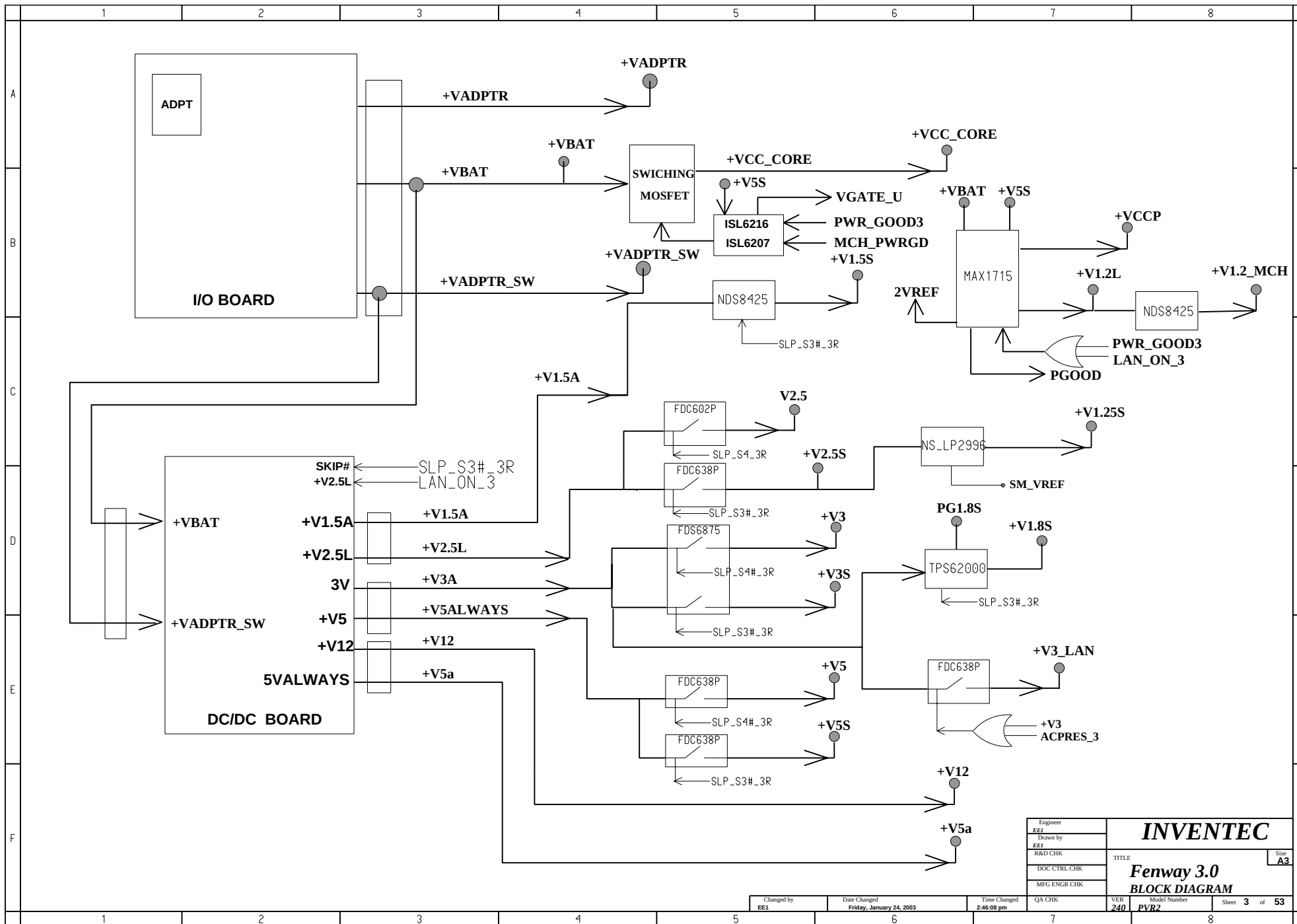


	1	2	3	4	5	6	7	8										
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					REV	ECN NO.	Description Of Change	DATE	DFTG.	ENGR.	REL.							
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C	5-7 - BANIAS PROCESSOR				33 - INT KBD./ POINTING DEVICE				C									
	8-10 - ODEM				34 - LID SWITCH/LEDS													
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	28 - EQ & MIC JACK				48-51 - POWER													
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	1	2	3	4	5	6	7	8										

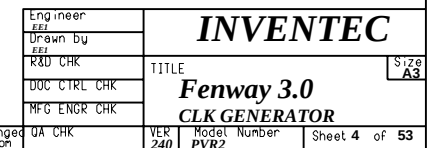


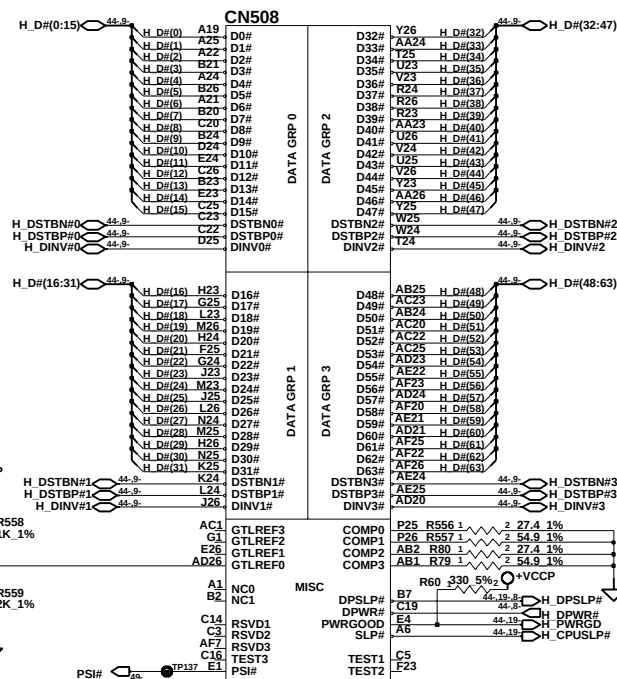
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R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK				
QA CHK				
VER 240	Model Number PVR2	Sheet 2	of 53	

Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:45:05 pm
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Engineer <i>EE1</i>		INVENTEC TITLE: Fenway 3.0 BLOCK DIAGRAM				Size A
Drawn by <i>EE1</i>						
R&D CHK						
DOC CTRL CHK						
MFG ENGR CHK						
Changed by <i>EE1</i>	Date Changed Friday, January 24, 2003	Time Changed 2:46:08 pm	QA CHK	VER 240	Model Number PVR2	Sheet 3 of 53
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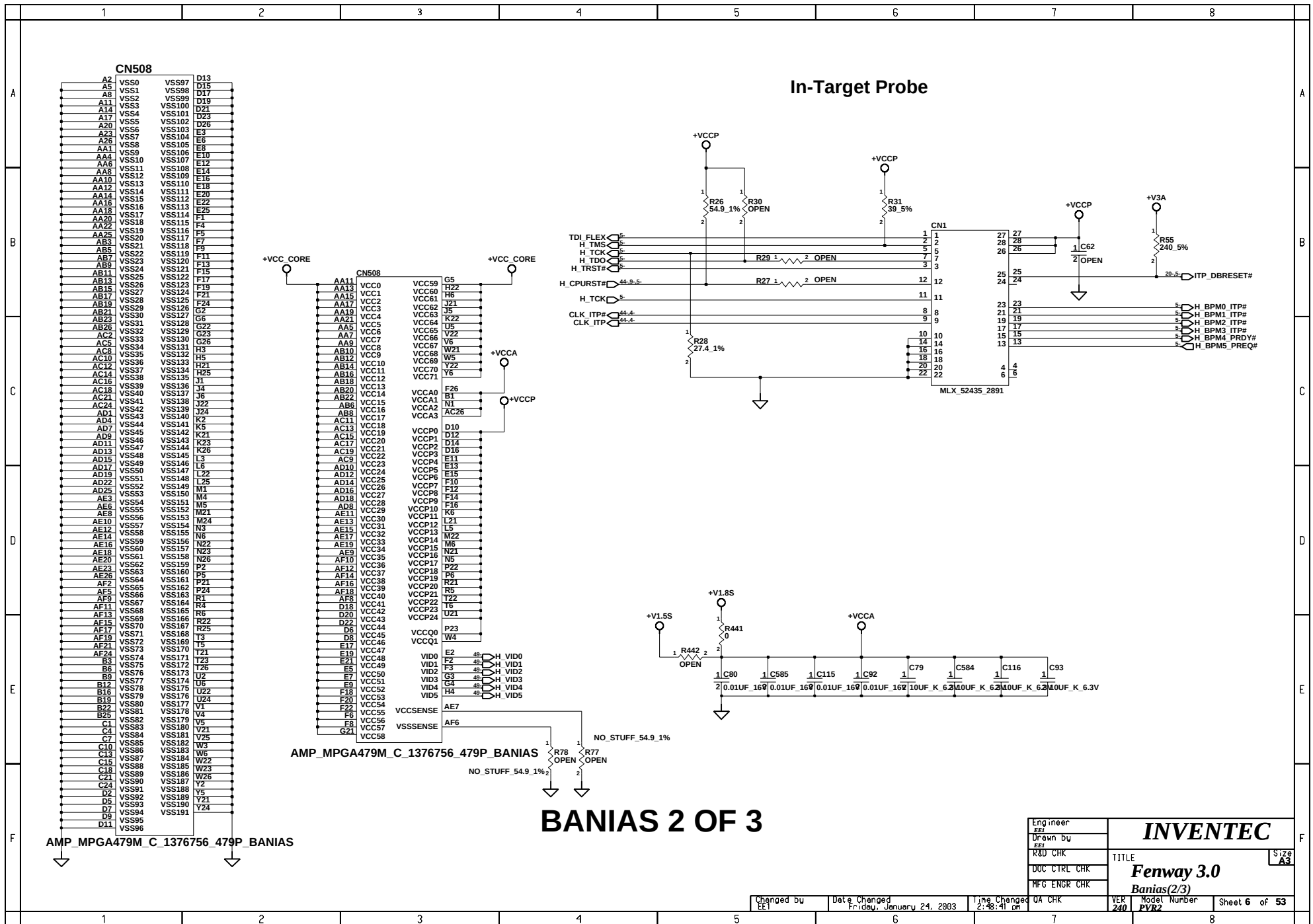


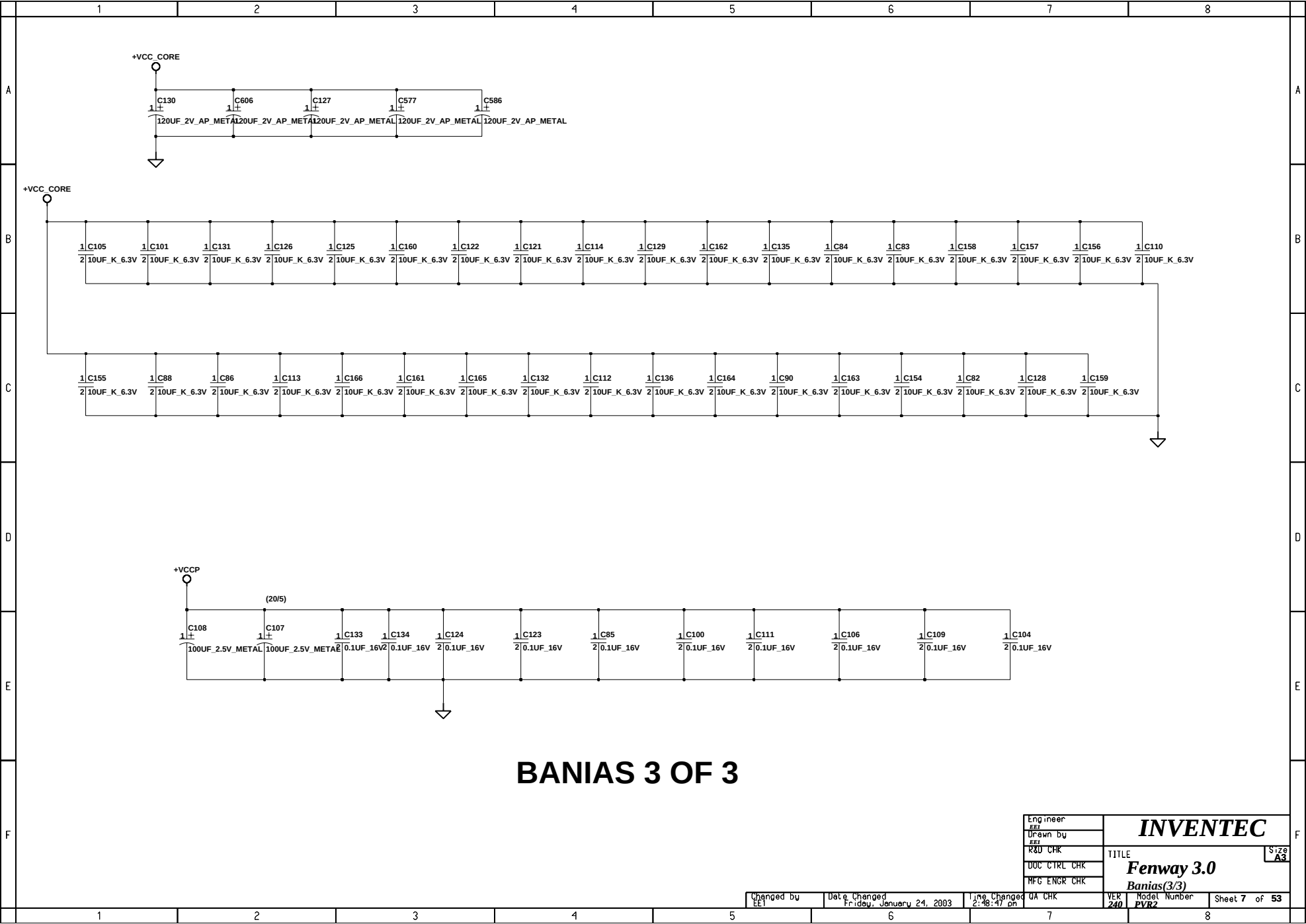


AMP_MPGA479M_C_1376756_479P_BANIAS

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Drawn by JEF					
K&L CHK					TITLE Fenway 3.0 Banias(1/3)
UOL CKL CHK					
MFC ENGR CHK					
QA CHK	VER 240	Model Number PVR2	Sheet 5 of 53		

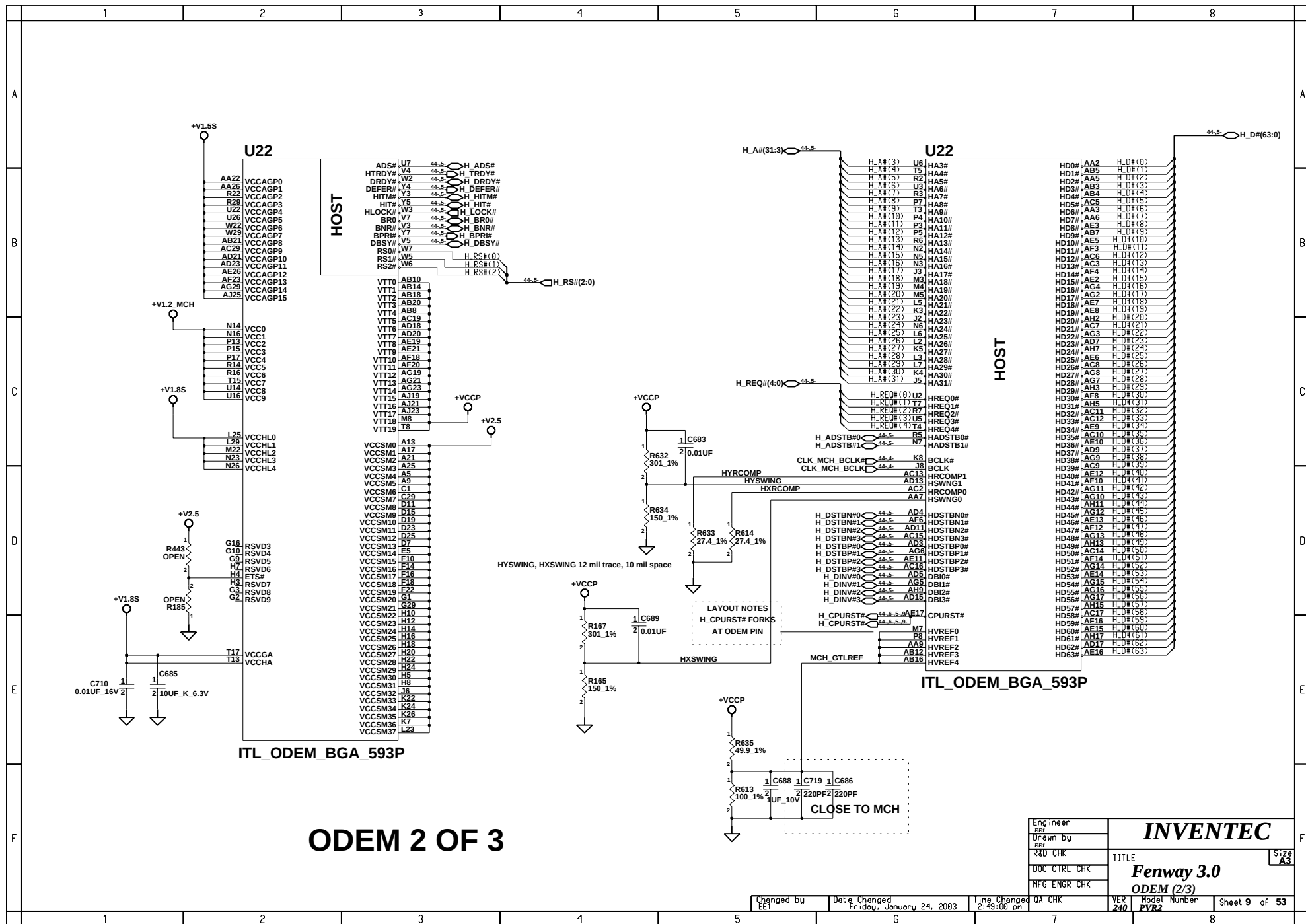
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:48:14 pm	QA CHK	VER 240	Model Number PVR2	Sheet 5 of 53
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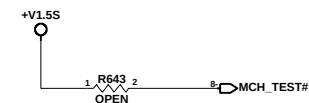
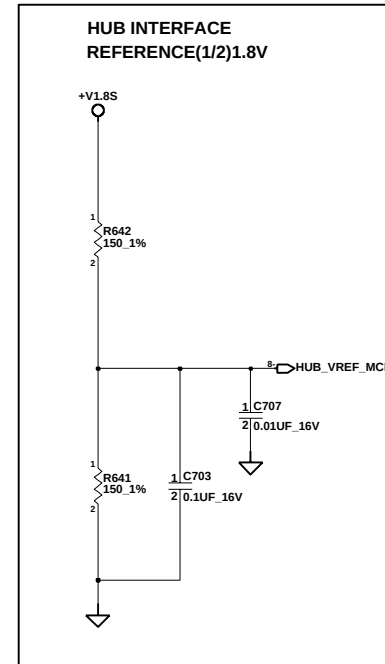
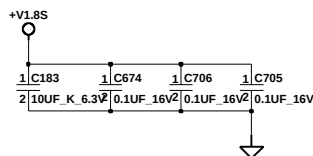
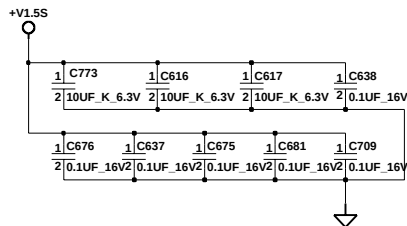
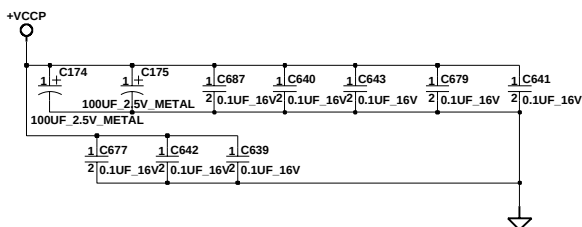
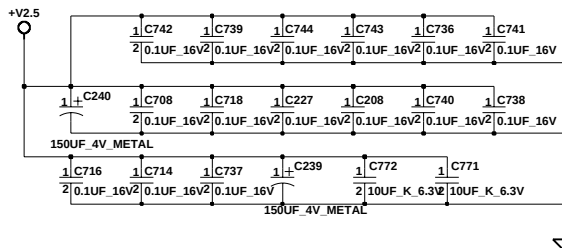
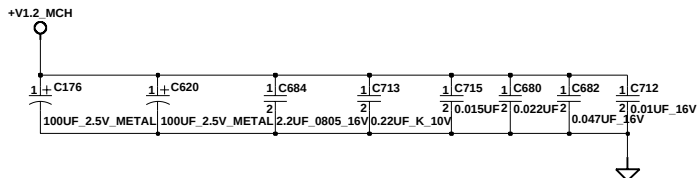
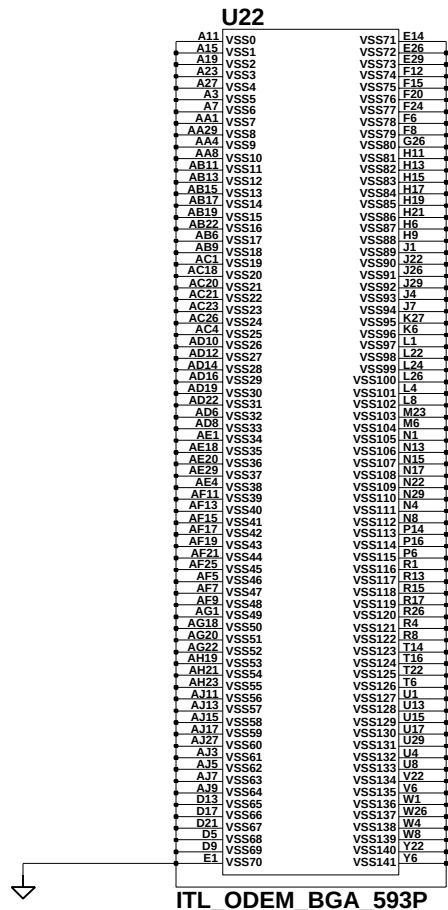




BANIAS 3 OF 3

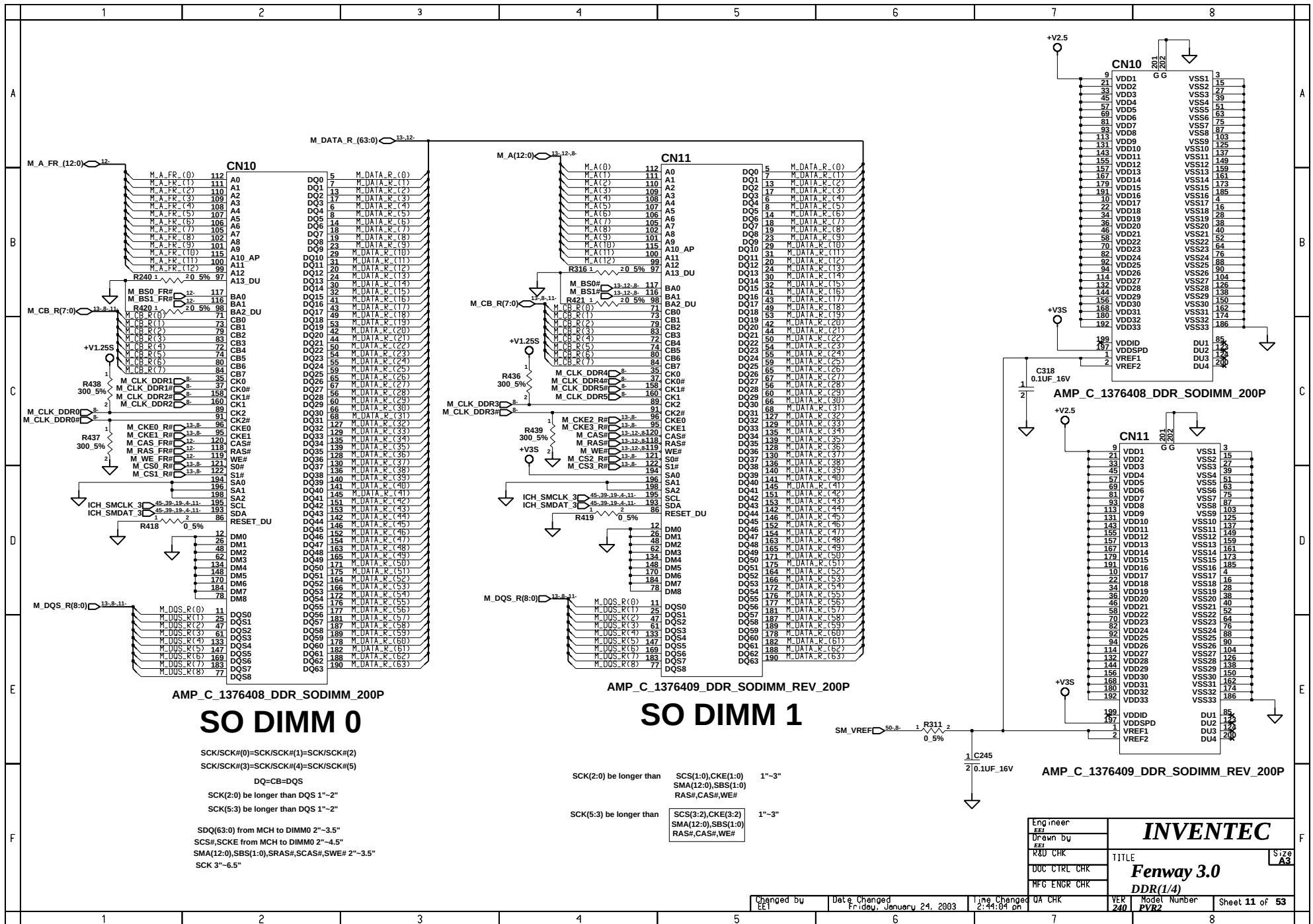
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Drawn by			
RDY CHK			
DOC CTRL CHK			
MFG ENGR CHK			
Changed by	Date Changed	Time Changed	QA CHK
EE1	Friday, January 24, 2003	2:48:47 pm	
VER	Model Number	Sheet 7 of 53	
240	PVR2		

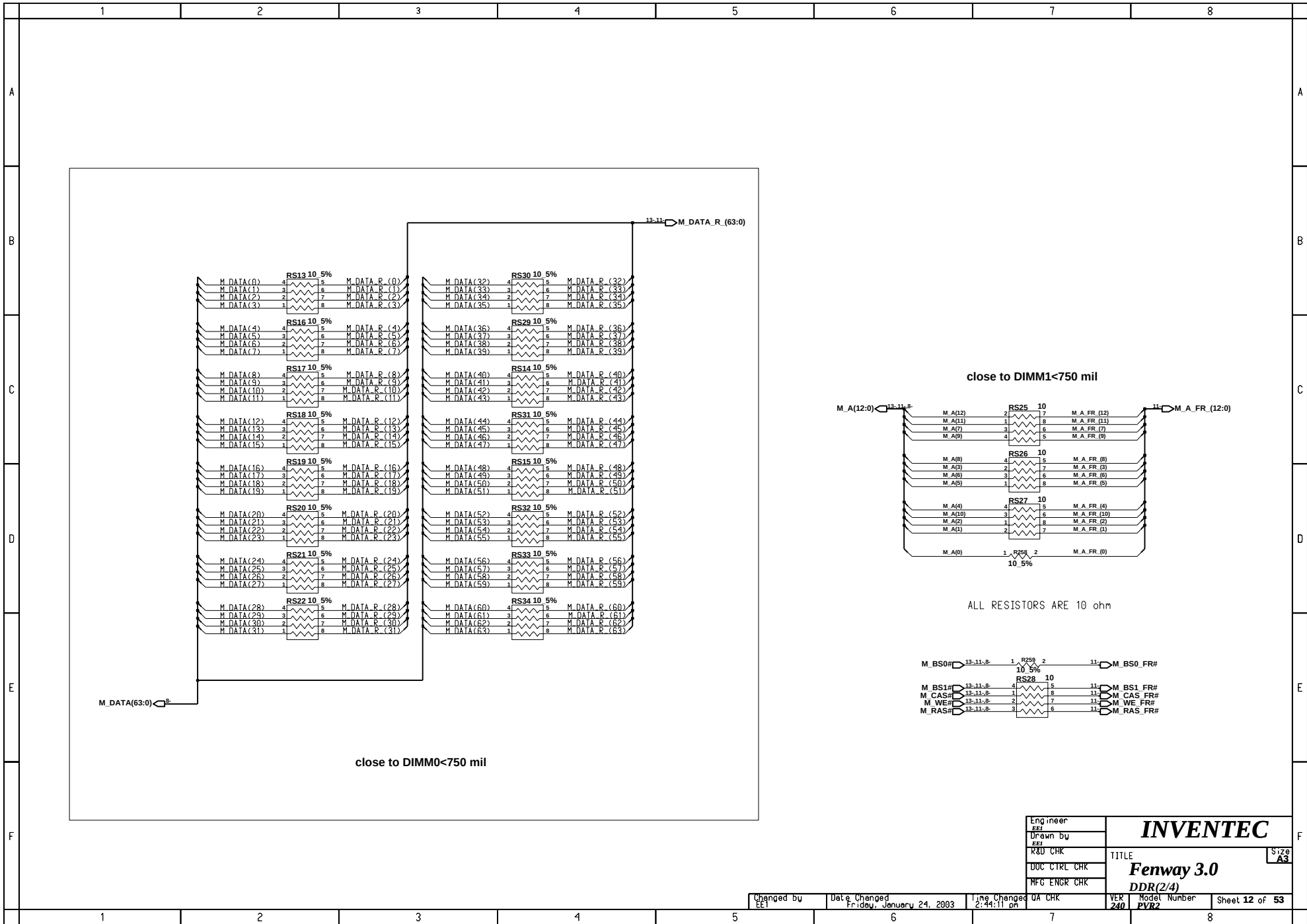


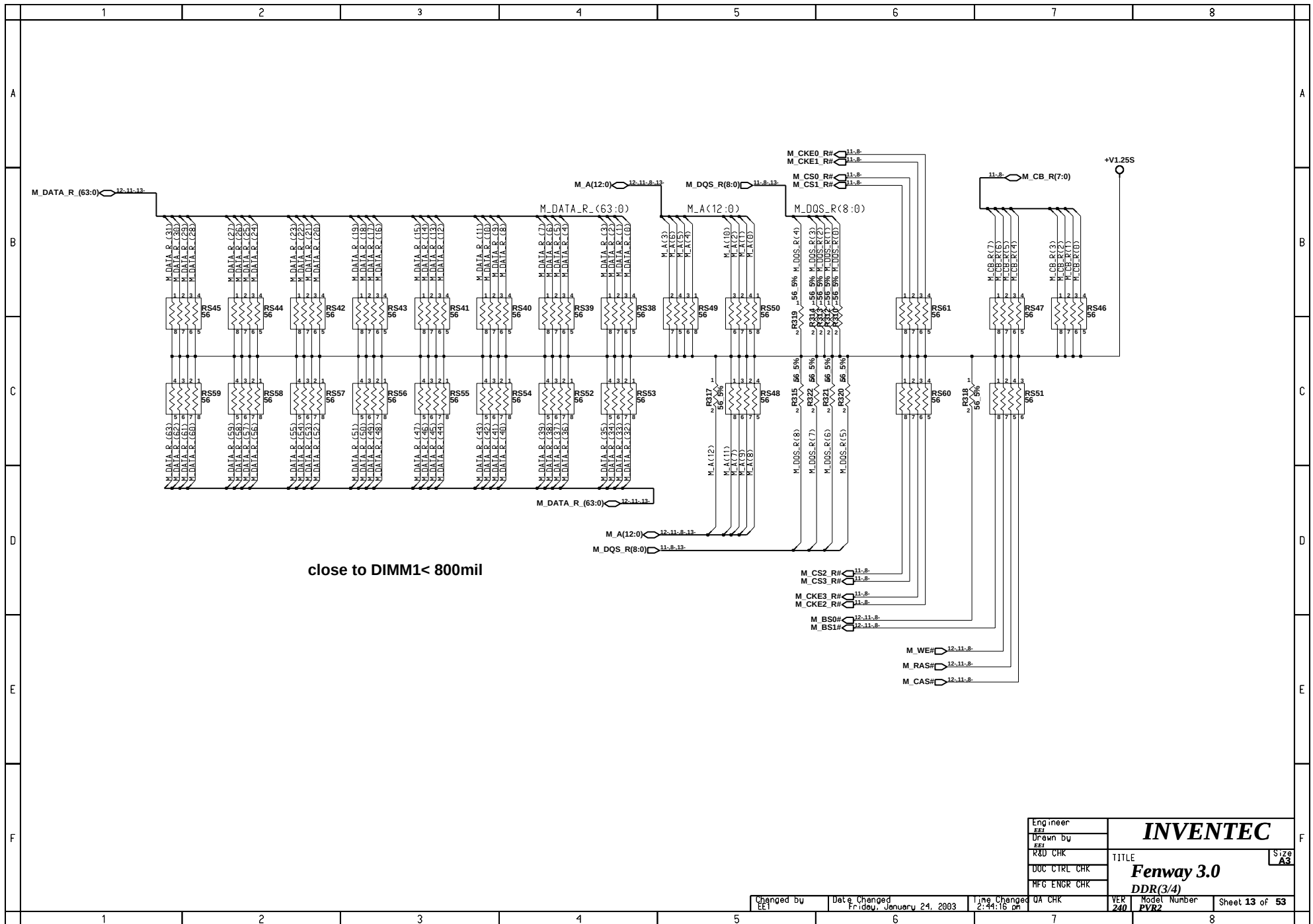


ODEM 3 OF 3

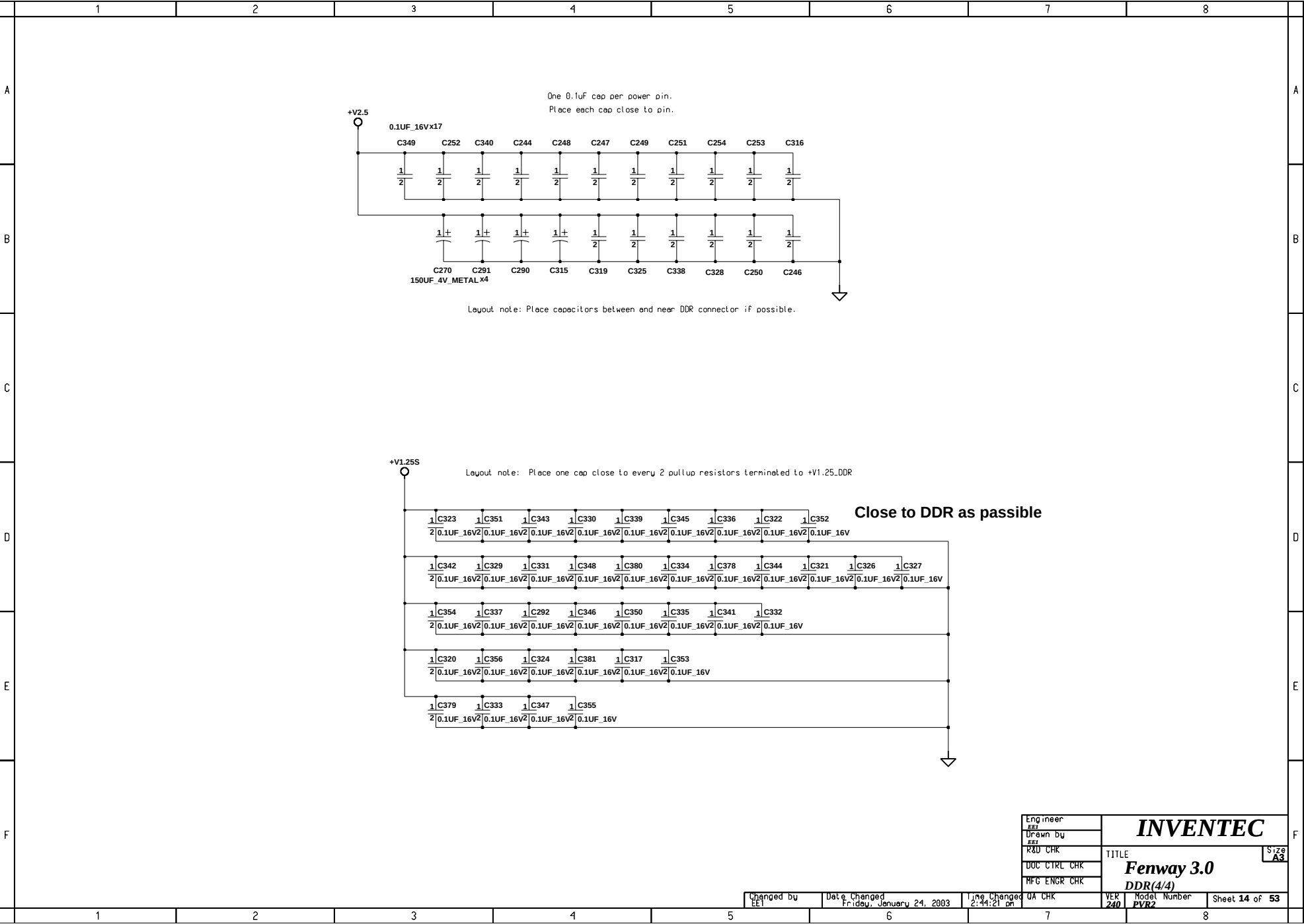
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Drawn by EE1		
R&D CHK EE1		
DOC CTRL CHK EE1		
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Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:43:57 pm
Model Number RVR2	VER 240	Sheet 10 of 53

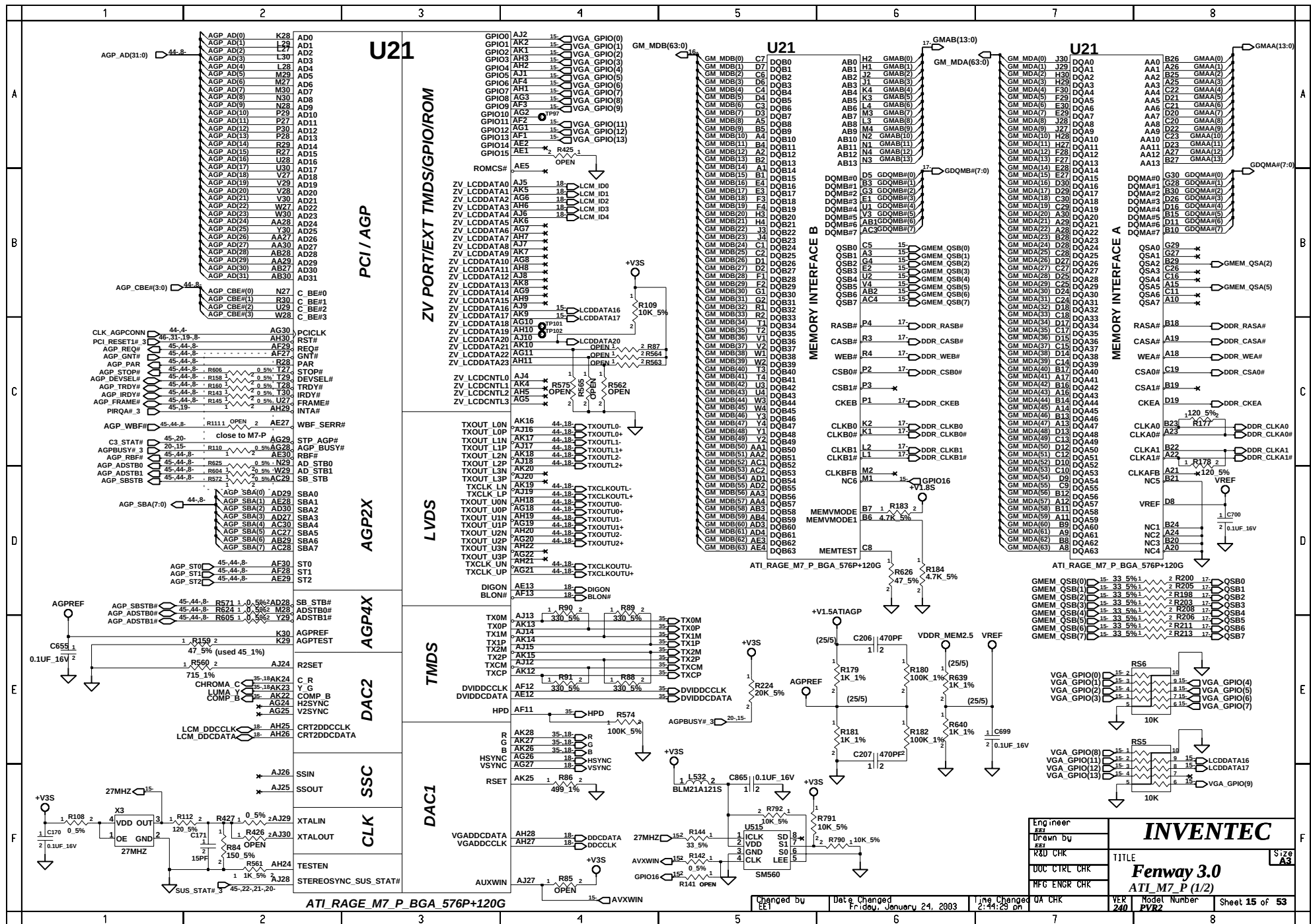


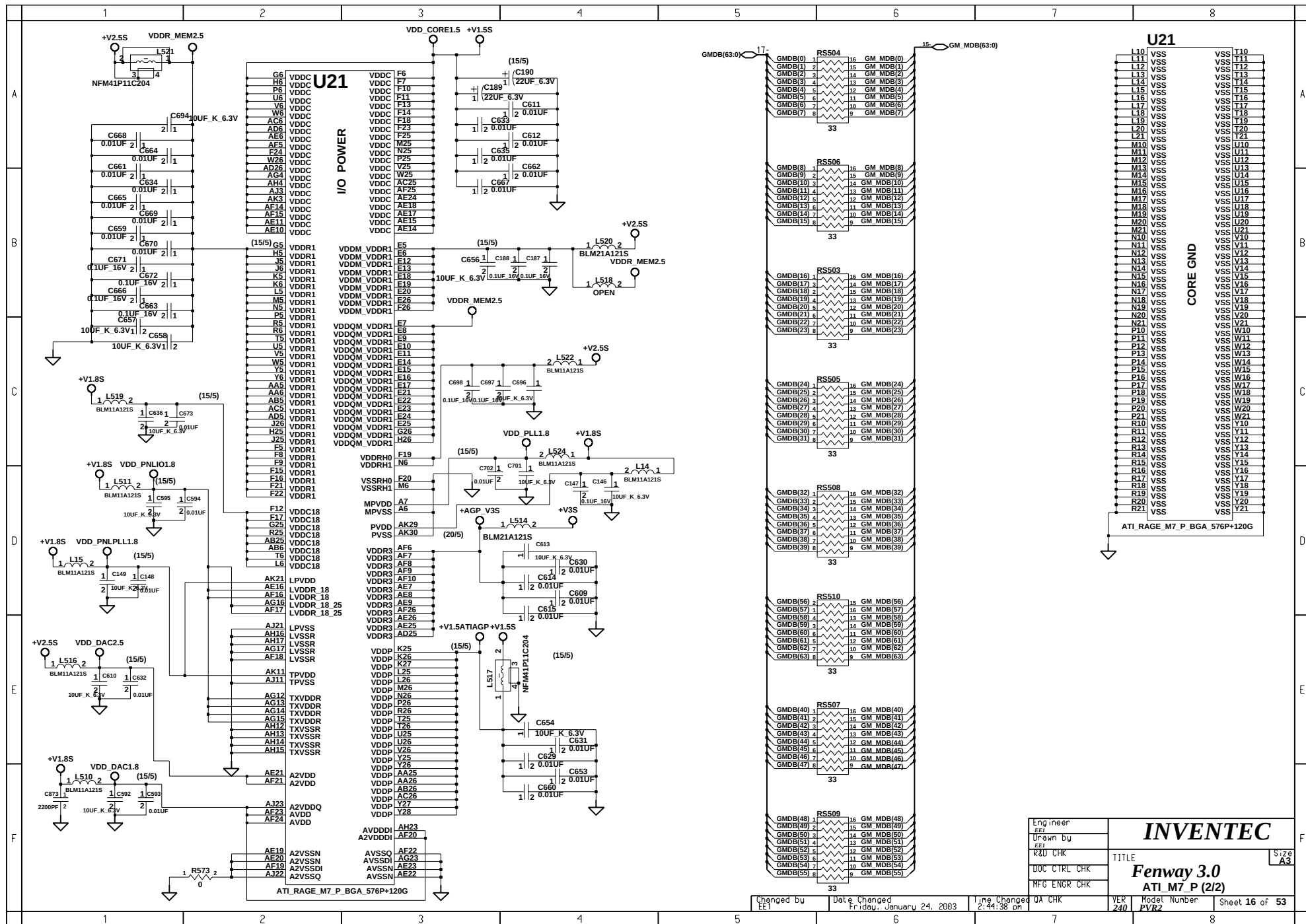


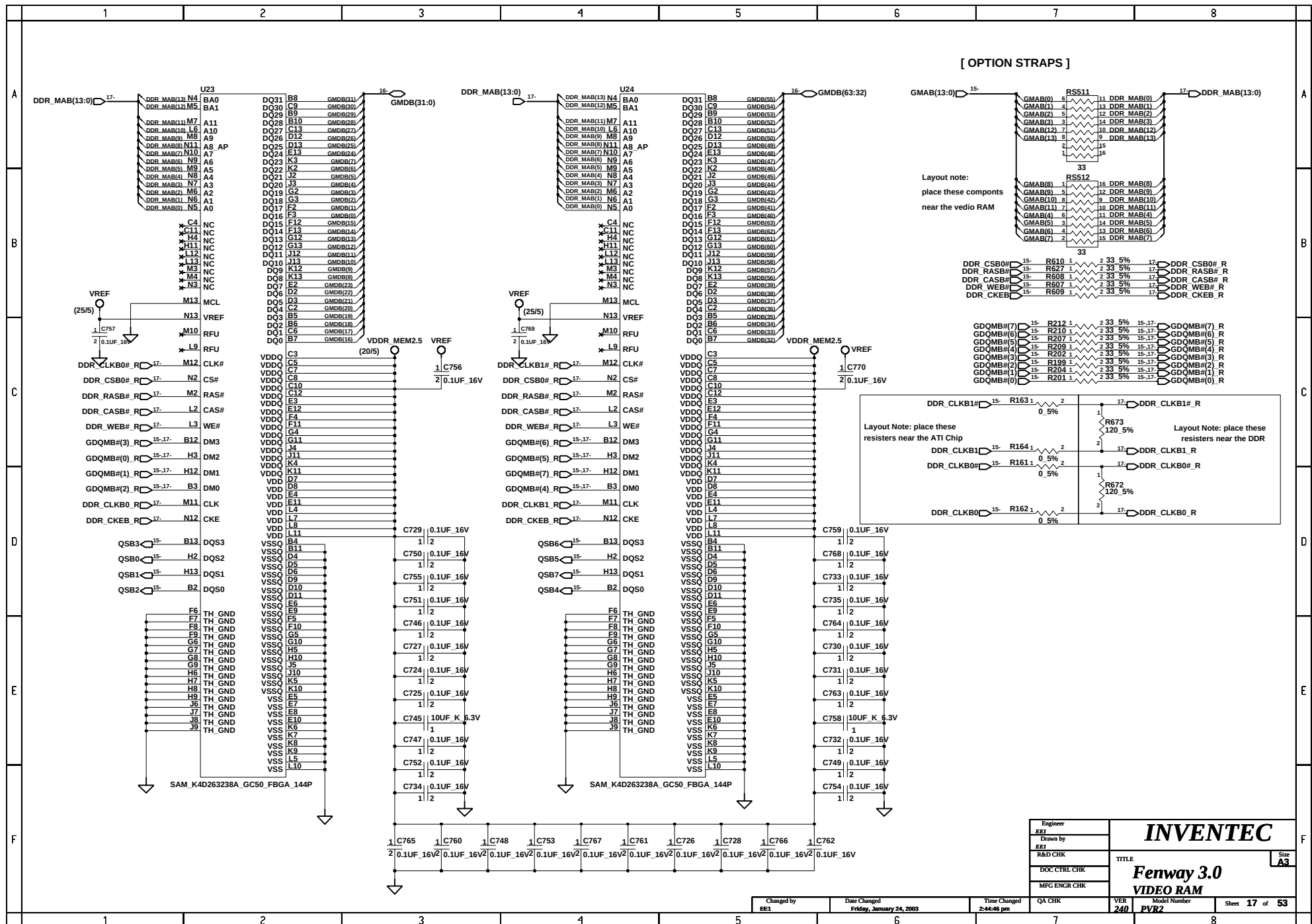


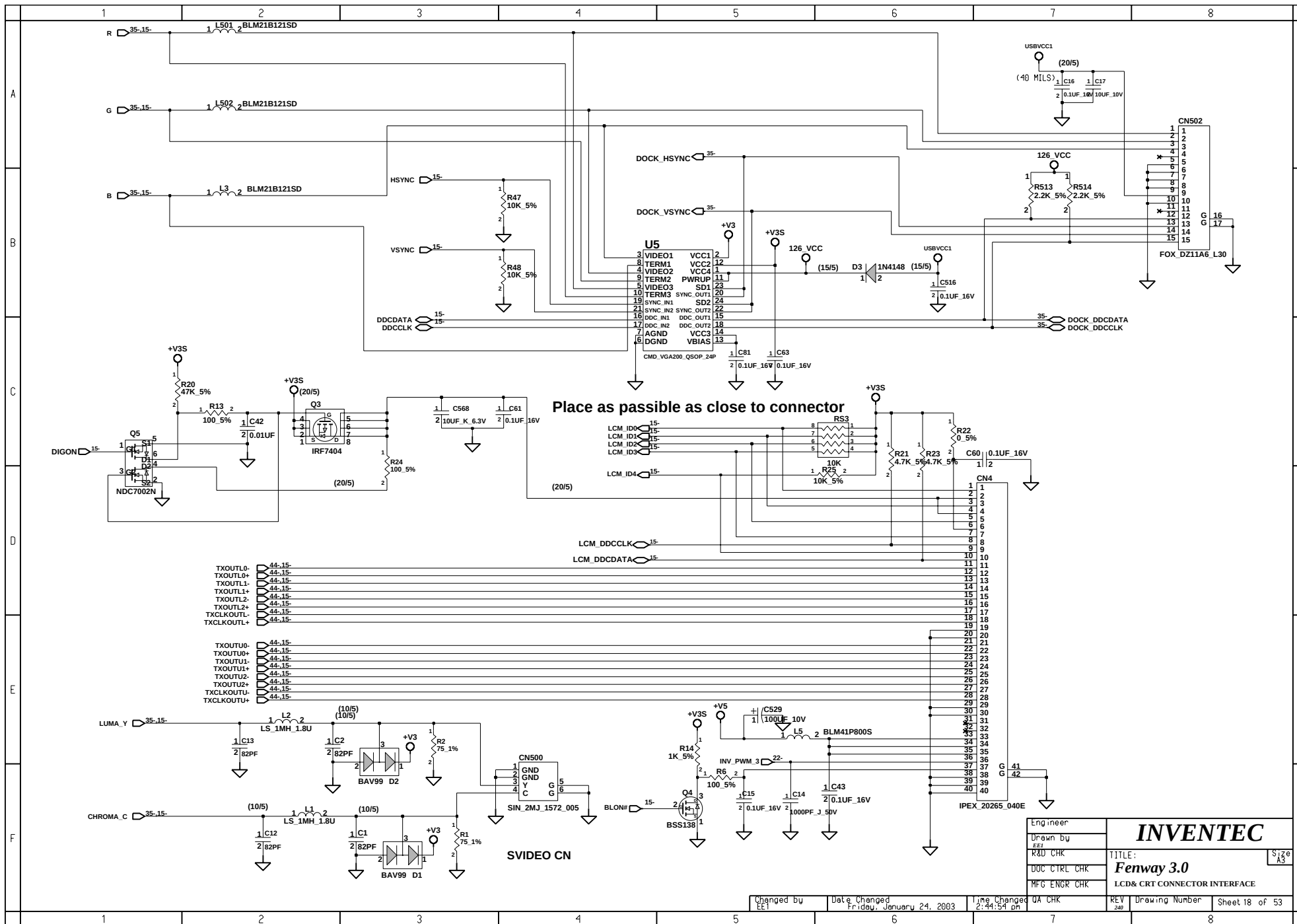
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Drawn by EE1	TITLE		
R&D CHK	Fenway 3.0		
DOC CTRL CHK	DDR(3/4)		
MFG ENGR CHK	Model Number		
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:44:16 pm	VER 240
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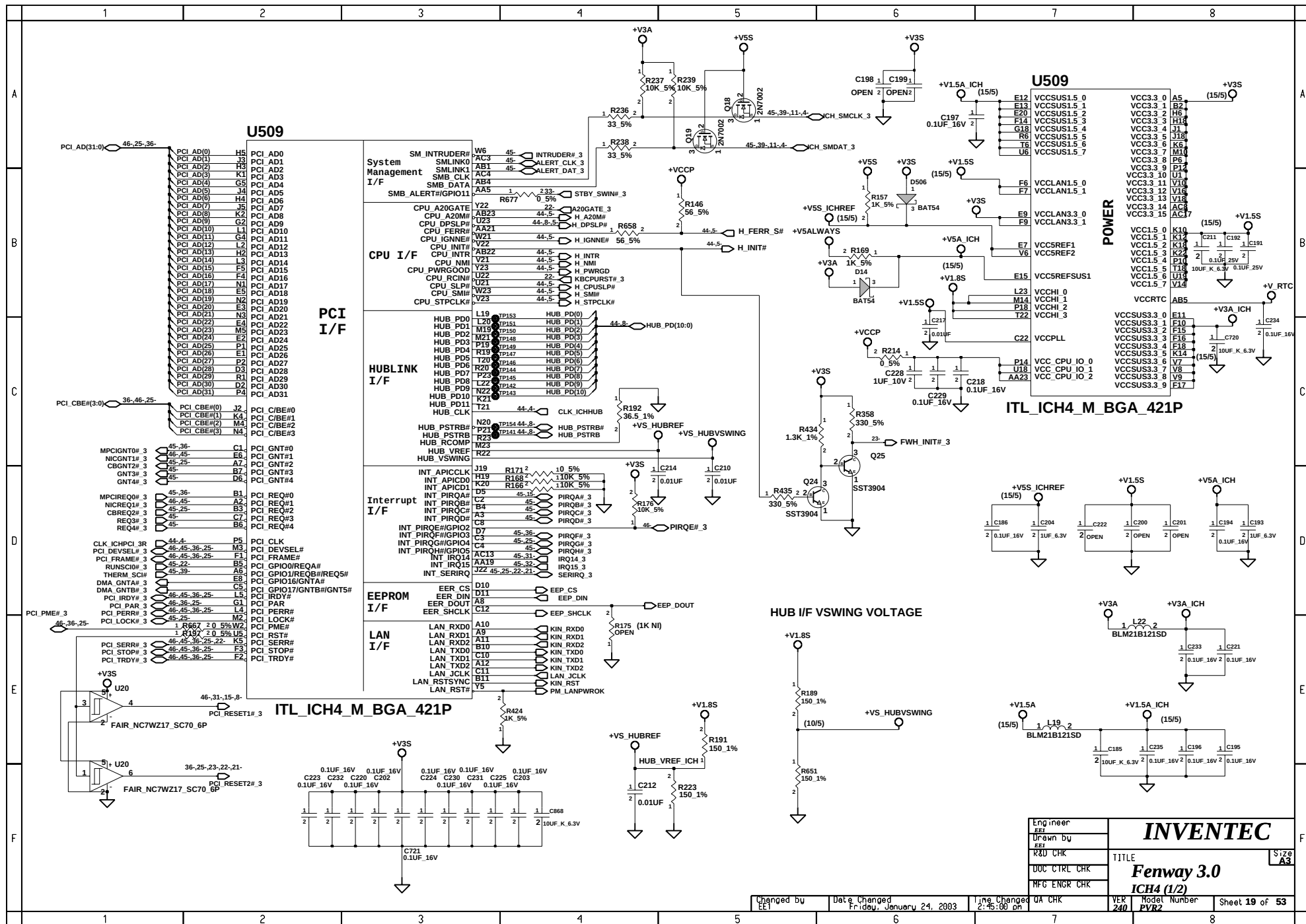


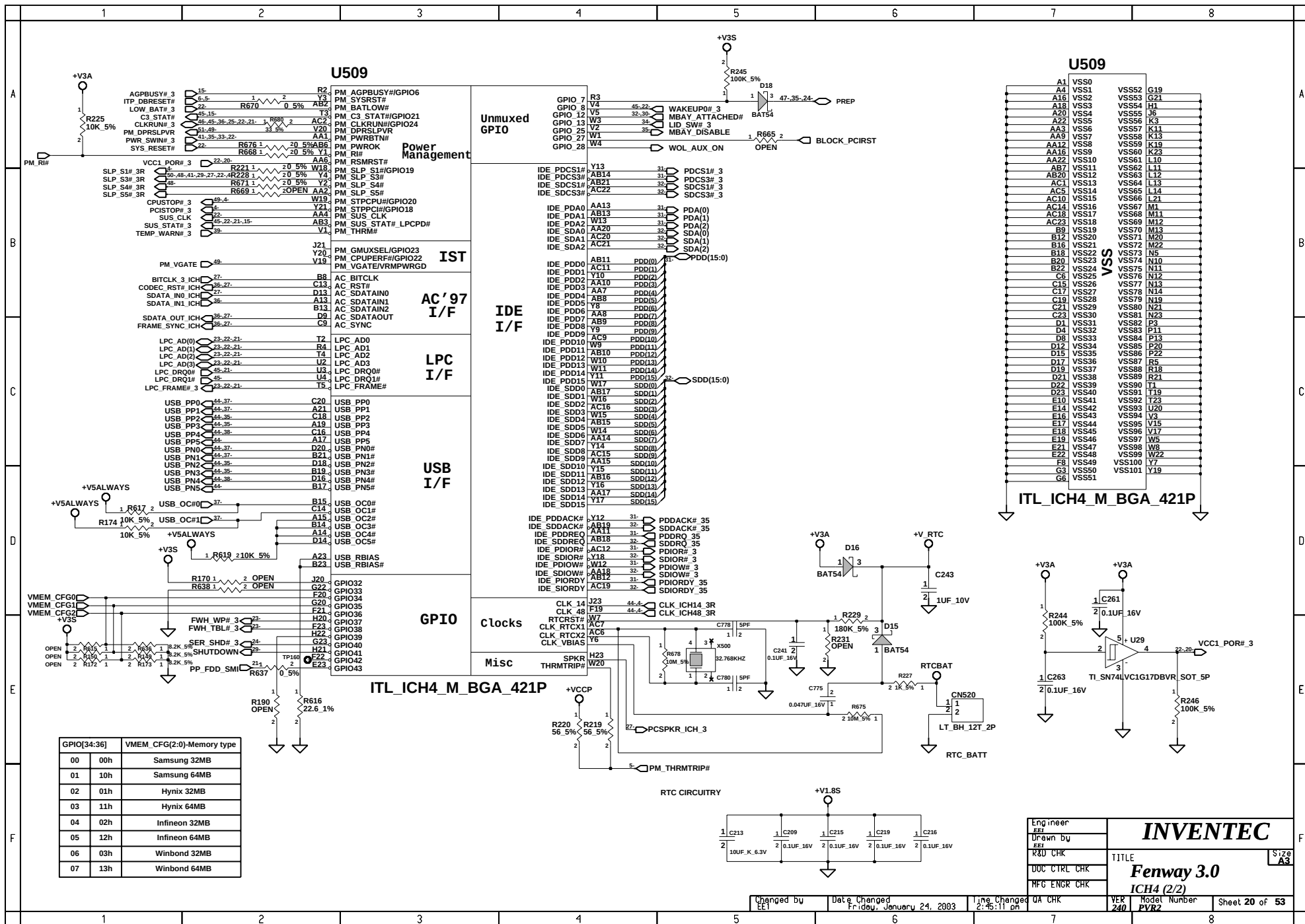


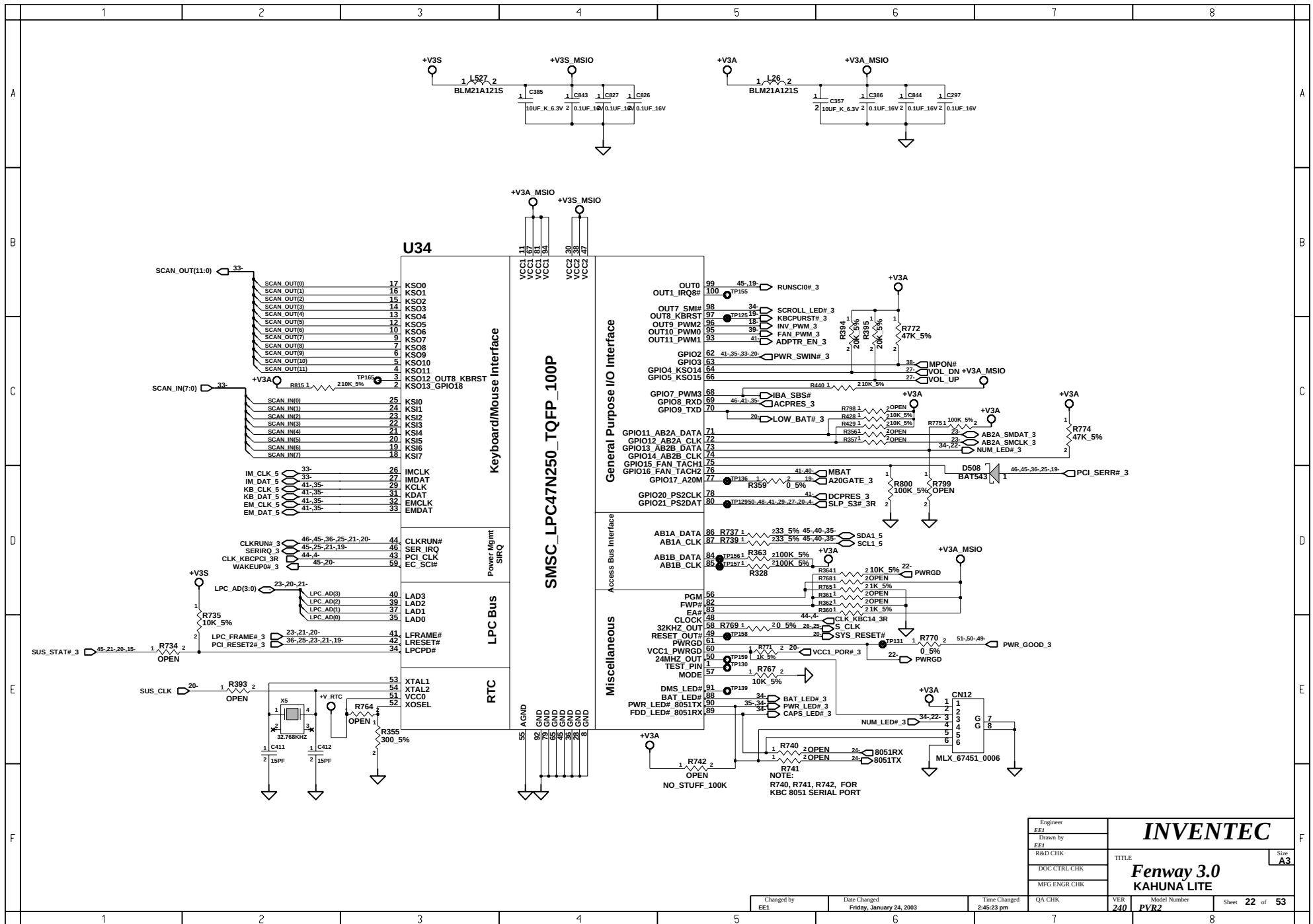




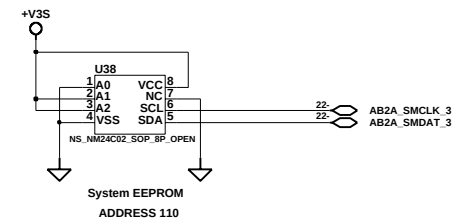
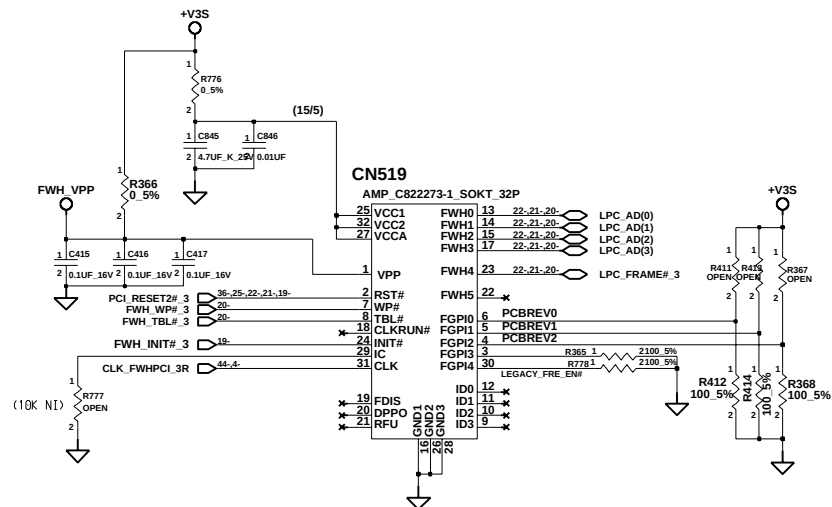






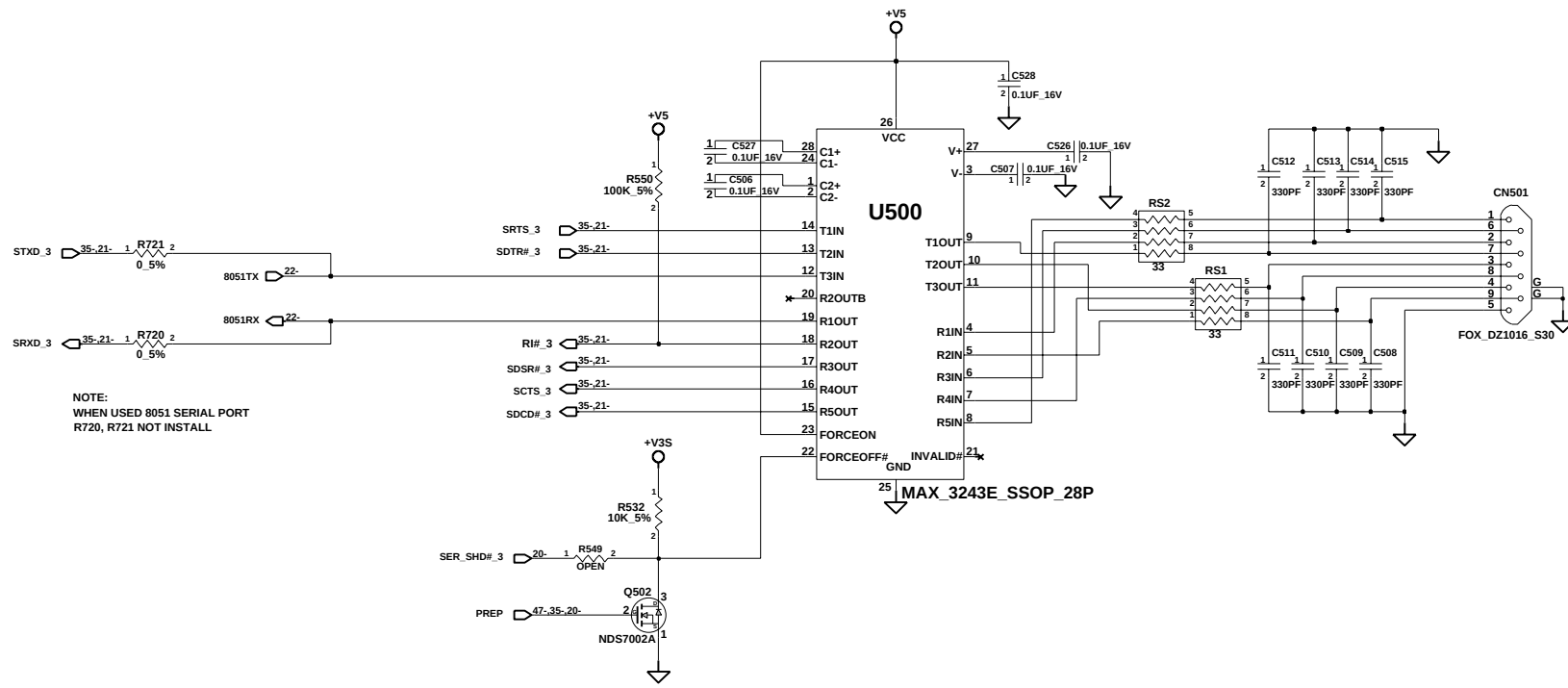


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Drawn by EE1		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:45:23 pm
QA CHK	VER 240	Model Number PVR2
Sheet 22 of 53		Size A3



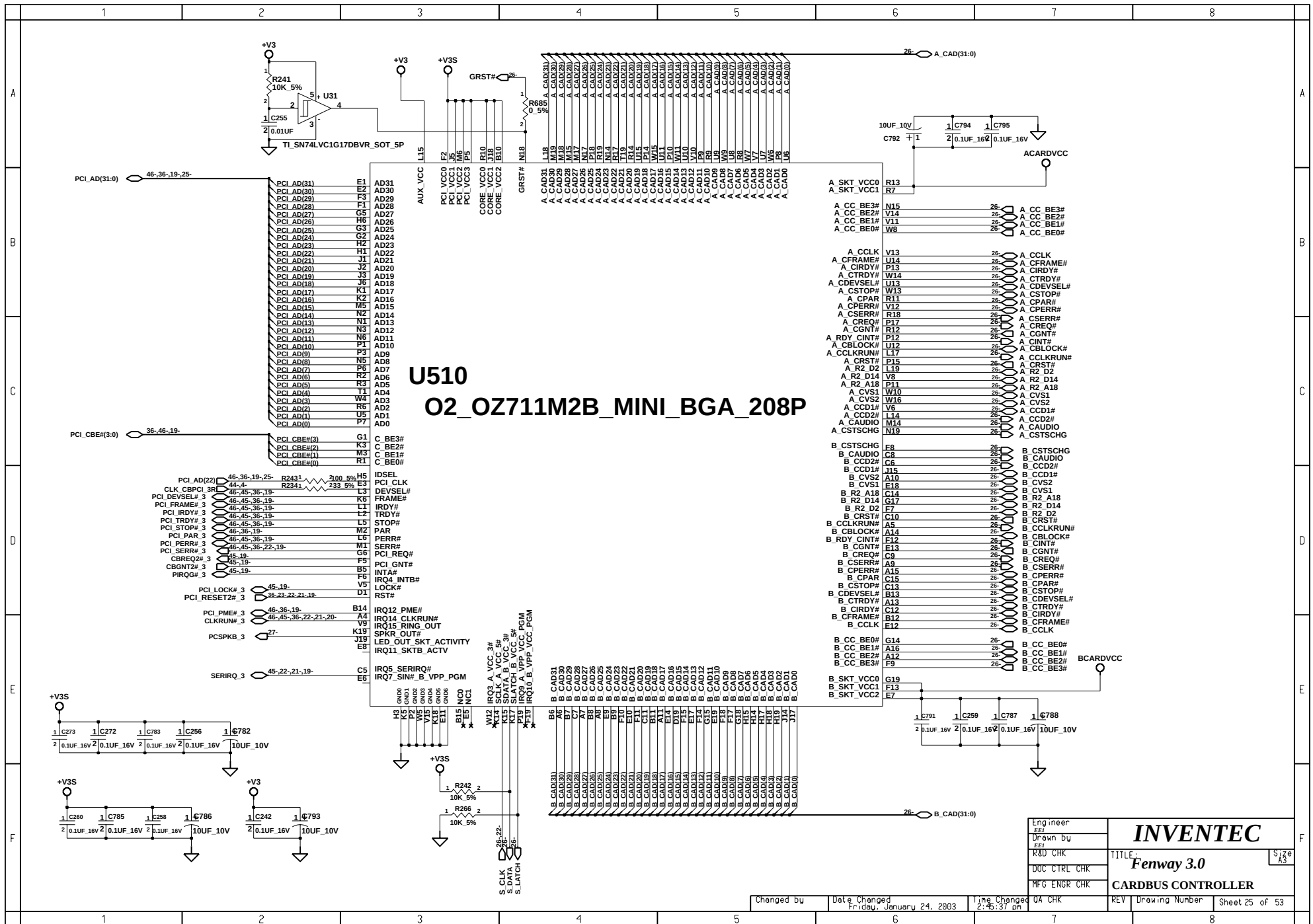
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Drawn by			
Rev. CHK			
DOC. CTRL. CHK			
MFG. ENGR. CHK	TITLE: Fenway 3.0 FWH&EEPROM		

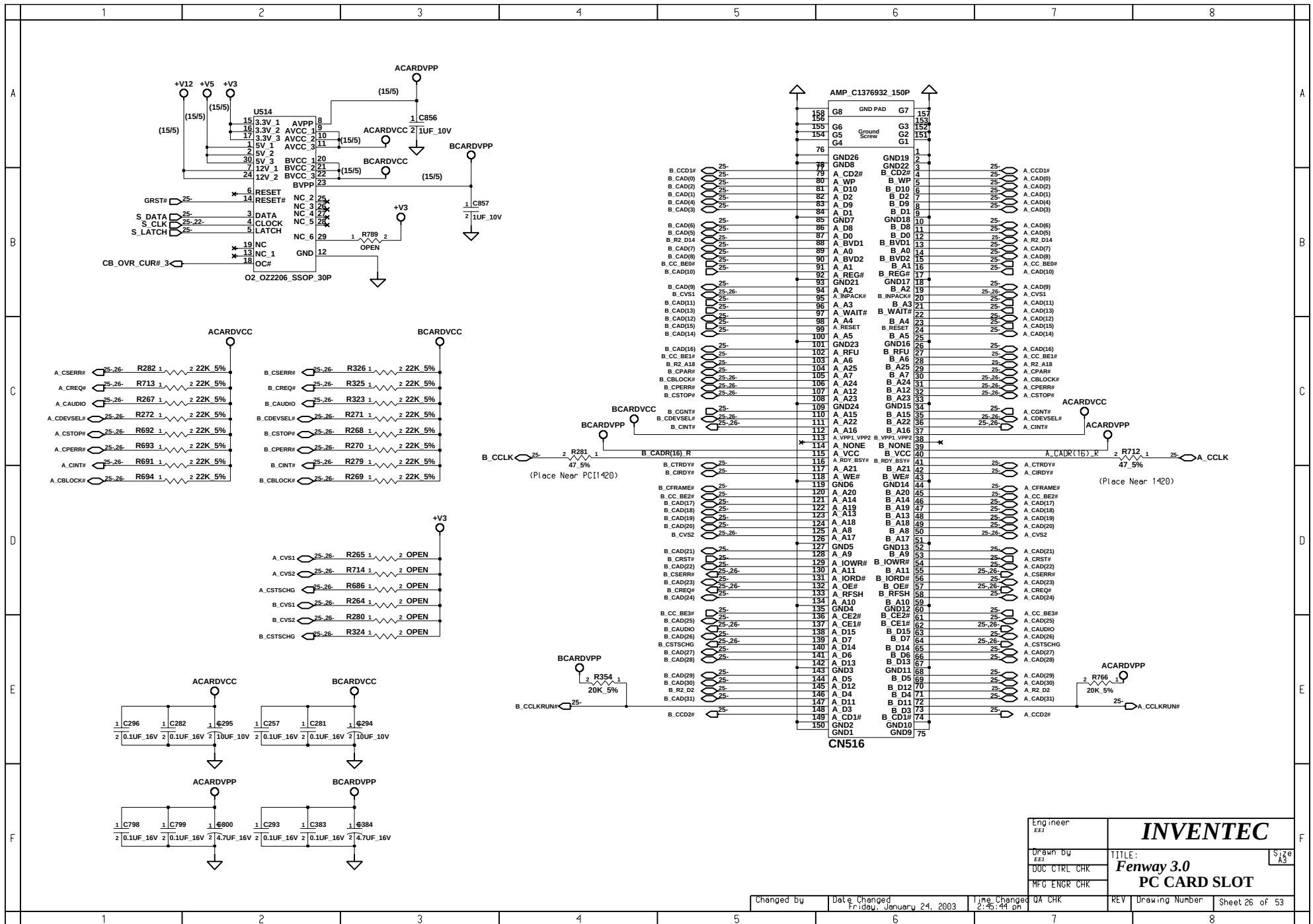
Changed by	Date Changed	Time Changed	Rev	Drawing Number	Sheet 23 of 53
	Friday, January 24, 2003	2:45:28 pm	UA CHK		

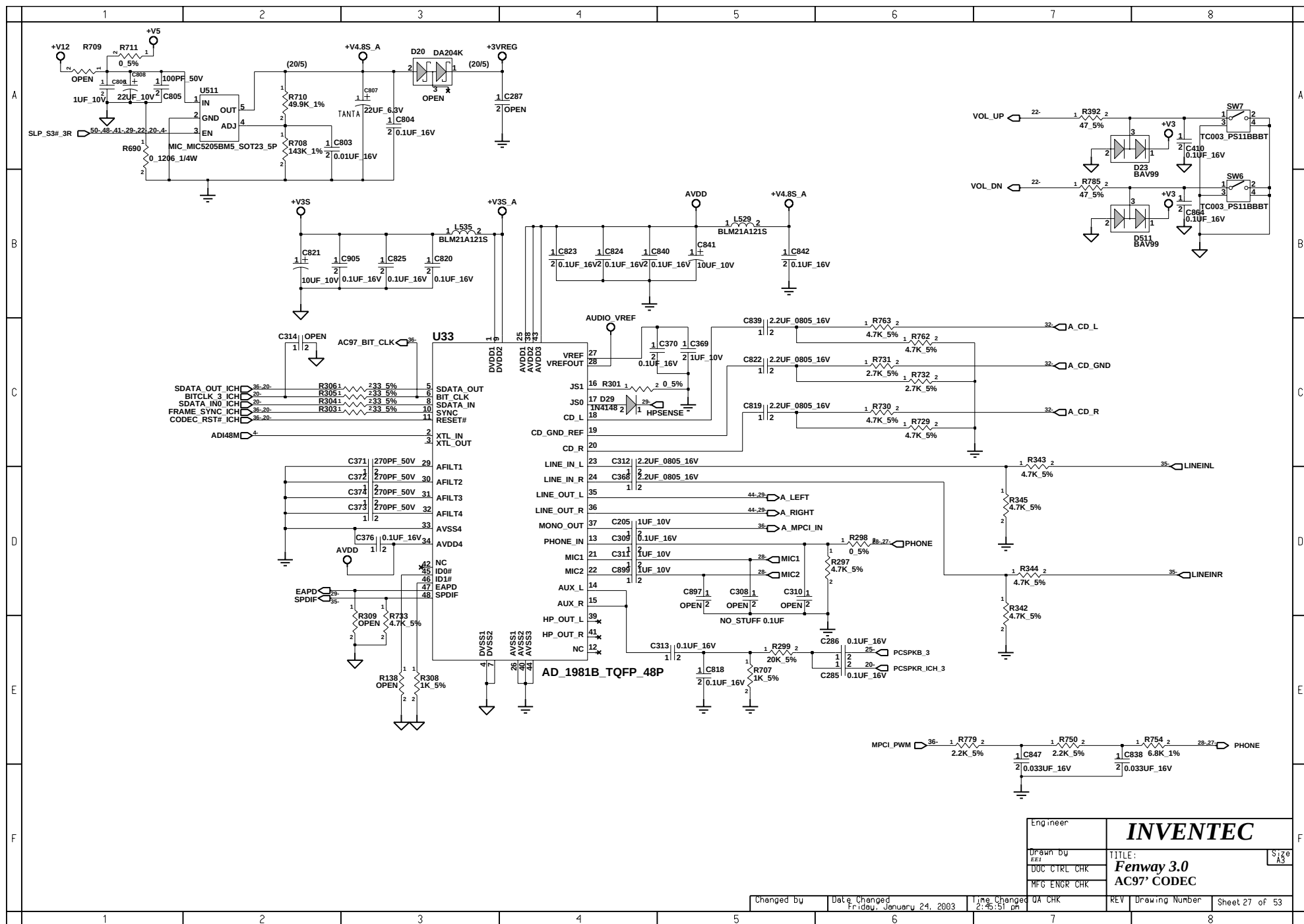


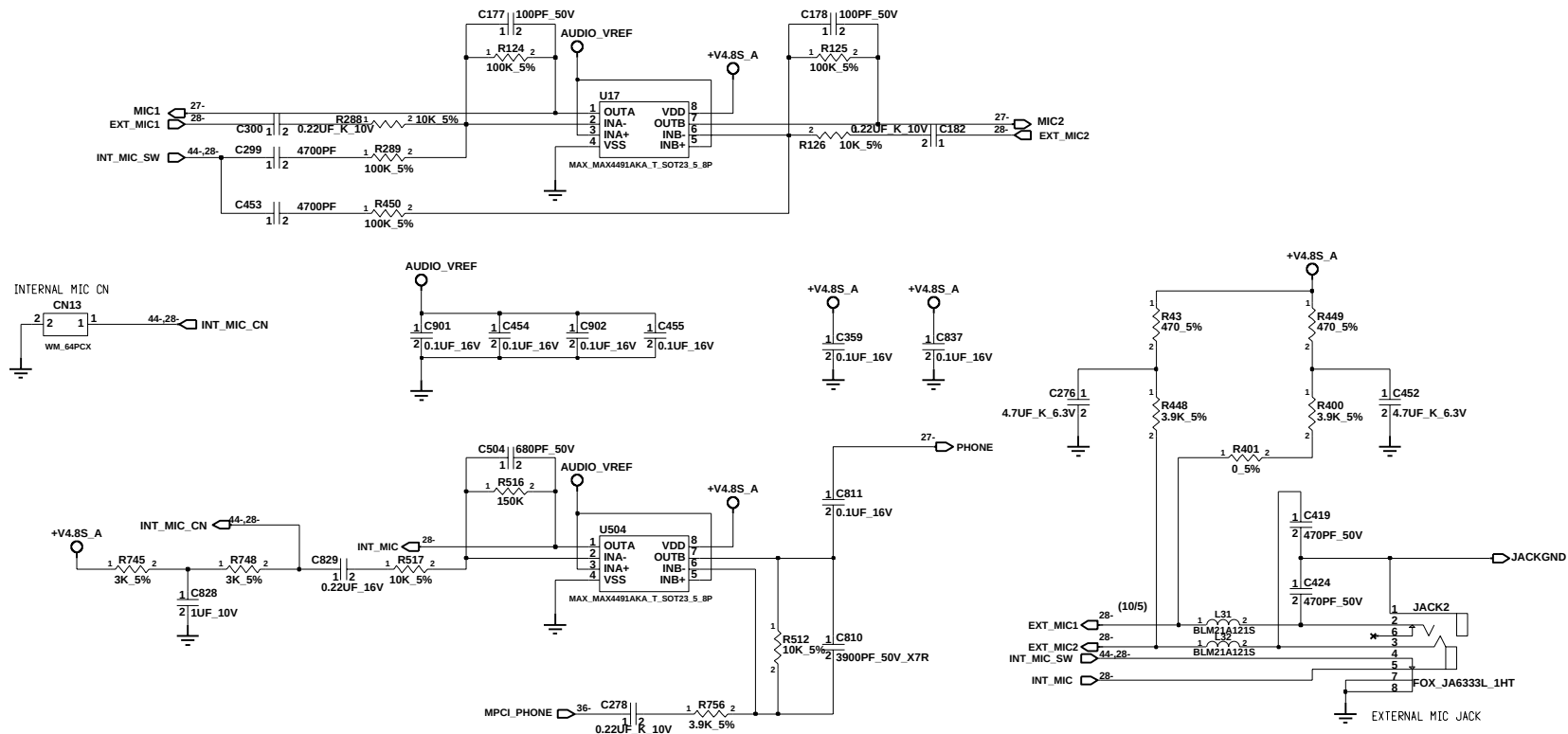
Engineer	INVENTEC TITLE: Fenway 3.0 SERIAL & PARALLEL PORTS		
Drawn by			
REV			
DOC CTRL CHK			
MFG ENGR CHK	REV	Drawing Number	Sheet 24 of 53

Changed by Friday, January 24, 2003 Time Changed 2:45:32 pm



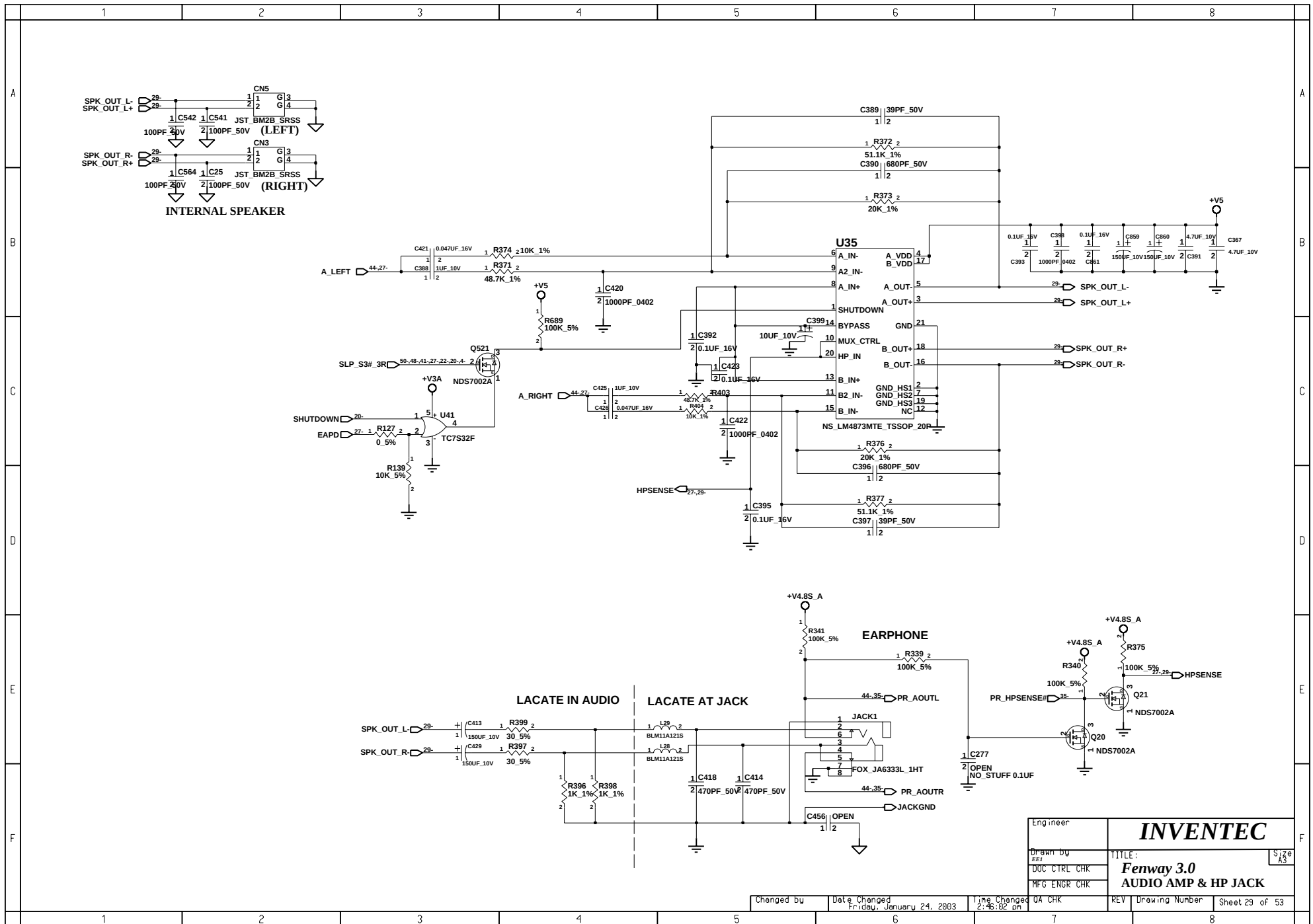


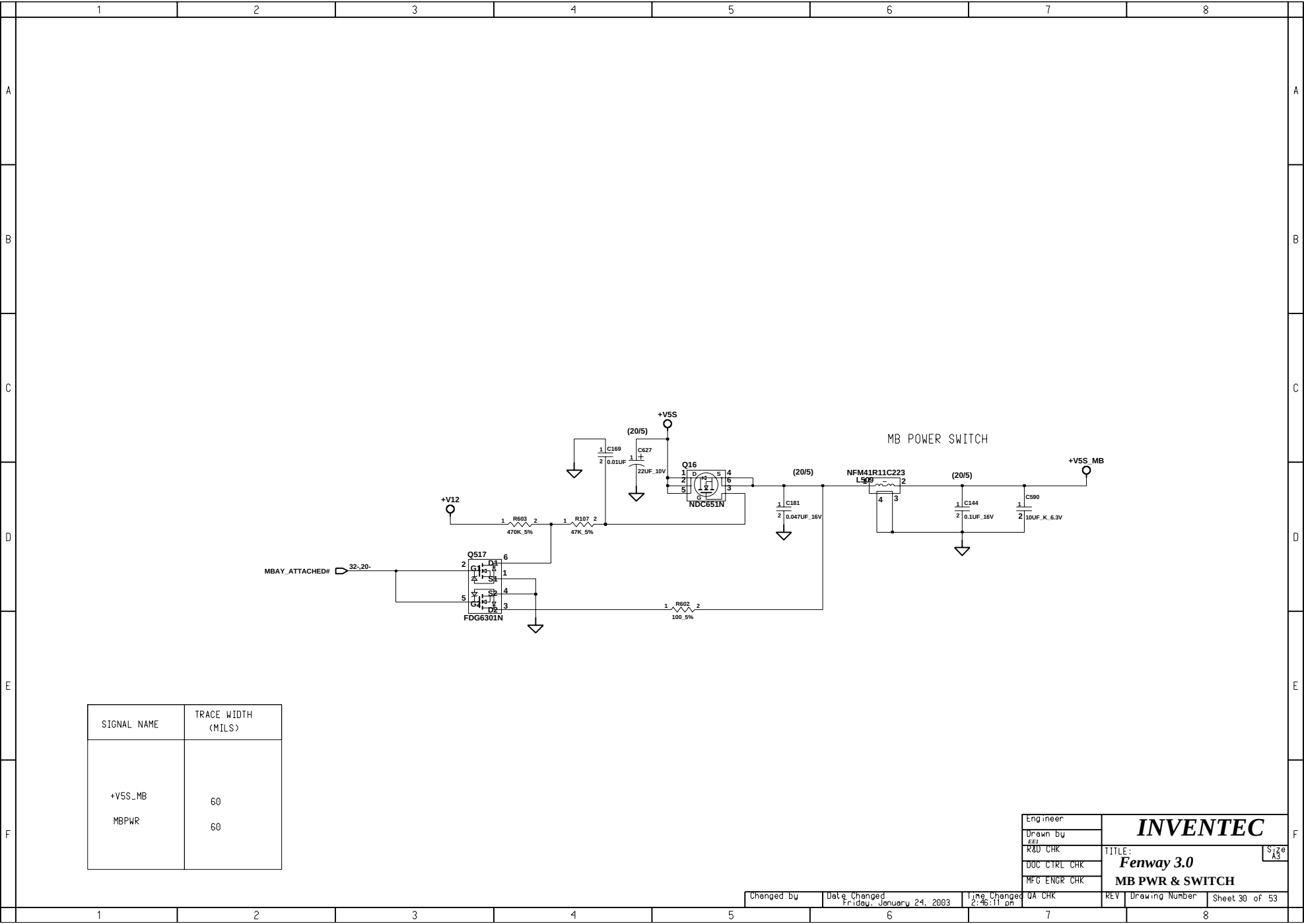


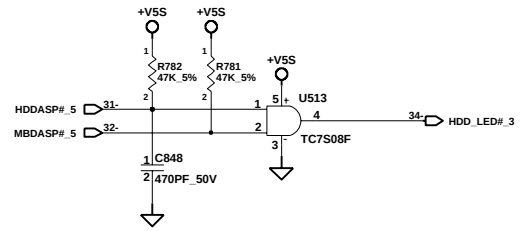
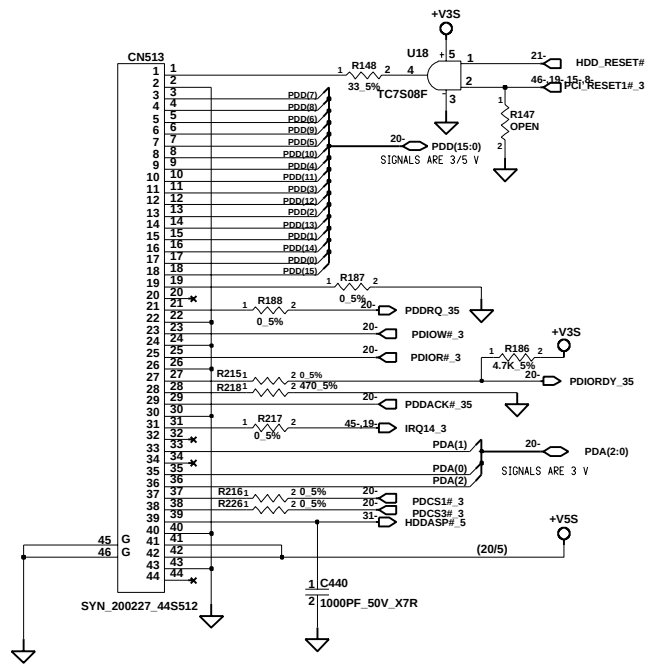


Engineer	INVENTEC		
Drawn by EEI	TITLE: Fenway 3.0		Size A3
DOC CTRL CHK	EQUALIZER & MIC JACK		
MFG ENGR CHK			

Changed by	Date Changed Friday, January 24, 2003	Time Changed 2:45:57 pm	QA CHK	REV	Drawing Number	Sheet 28 of 53
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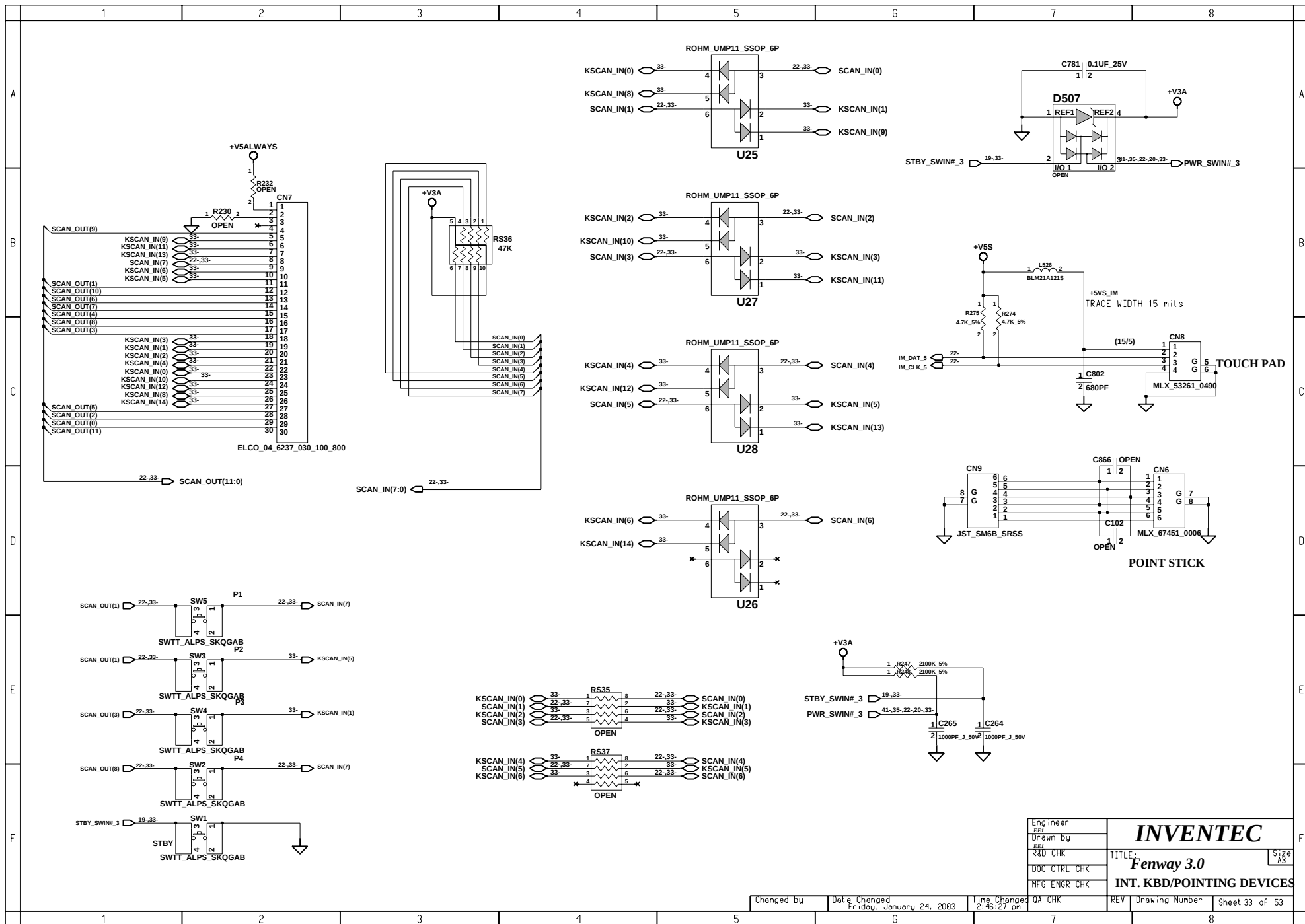


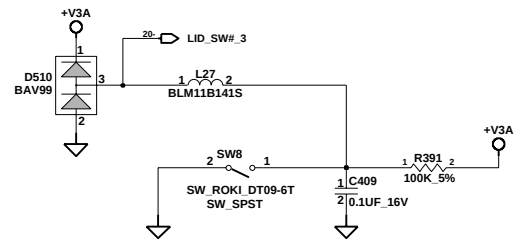


Engineer	INVENTEC		
Drawn by	Fenway 3.0		
Rev'd	HDD CONNECTOR		
DOC CTRL	REV		
MFG ENGR	Drawing Number		
QA CHK	Sheet 31 of 53		

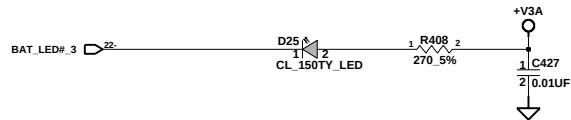
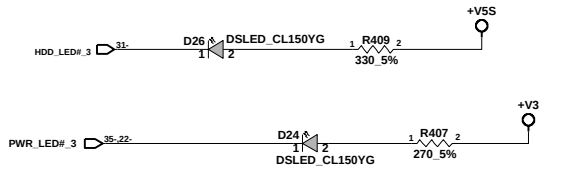
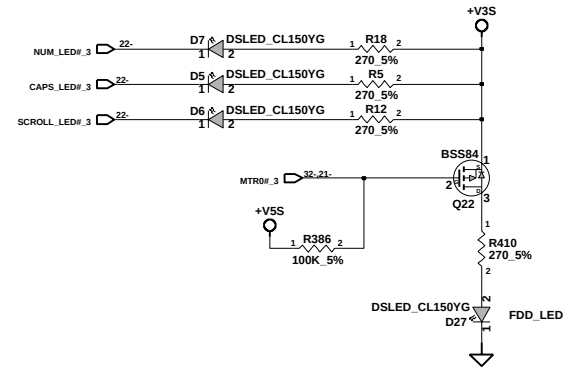
Changed by Friday, January 24, 2003 Time Changed 2:46:16 pm





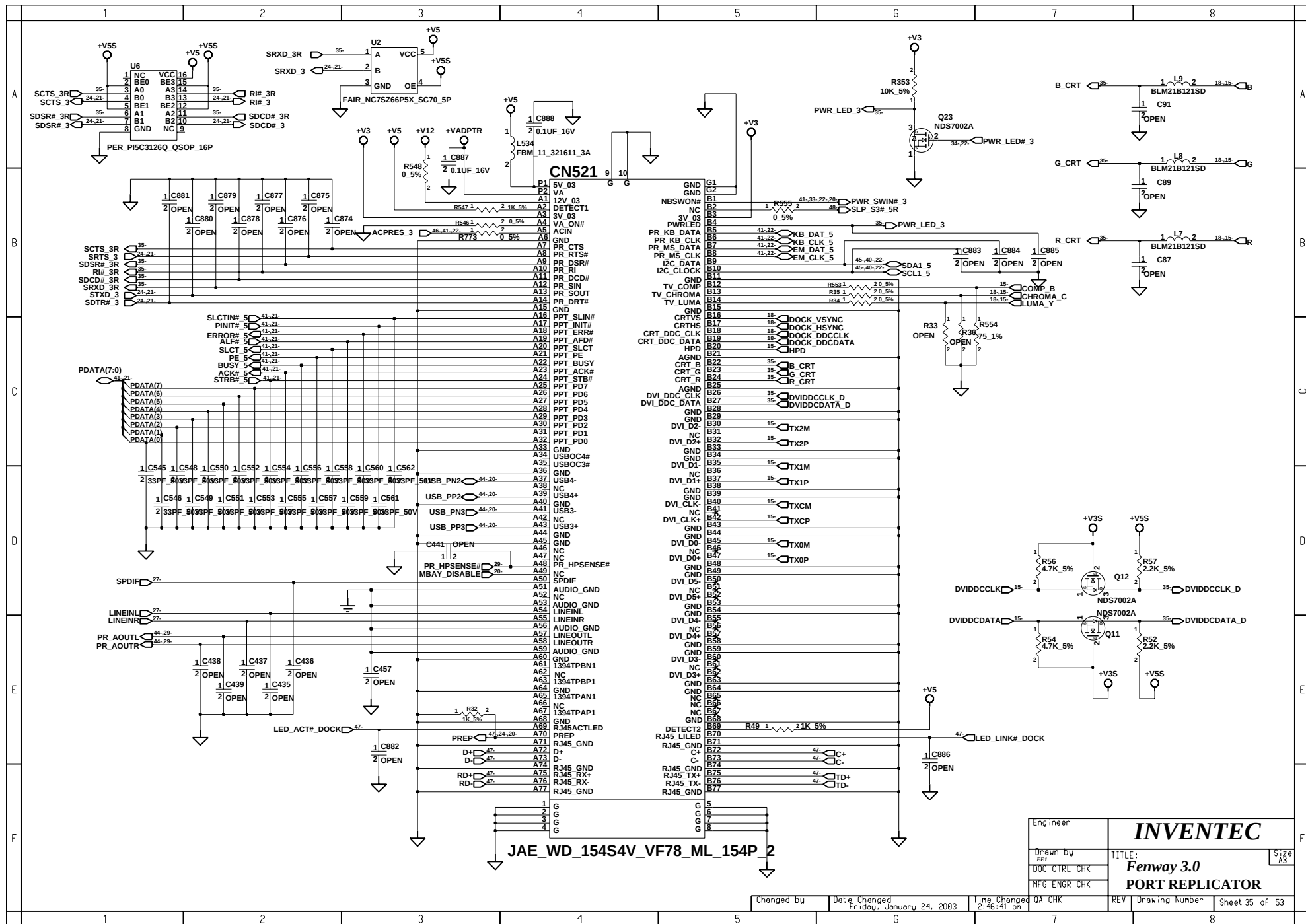


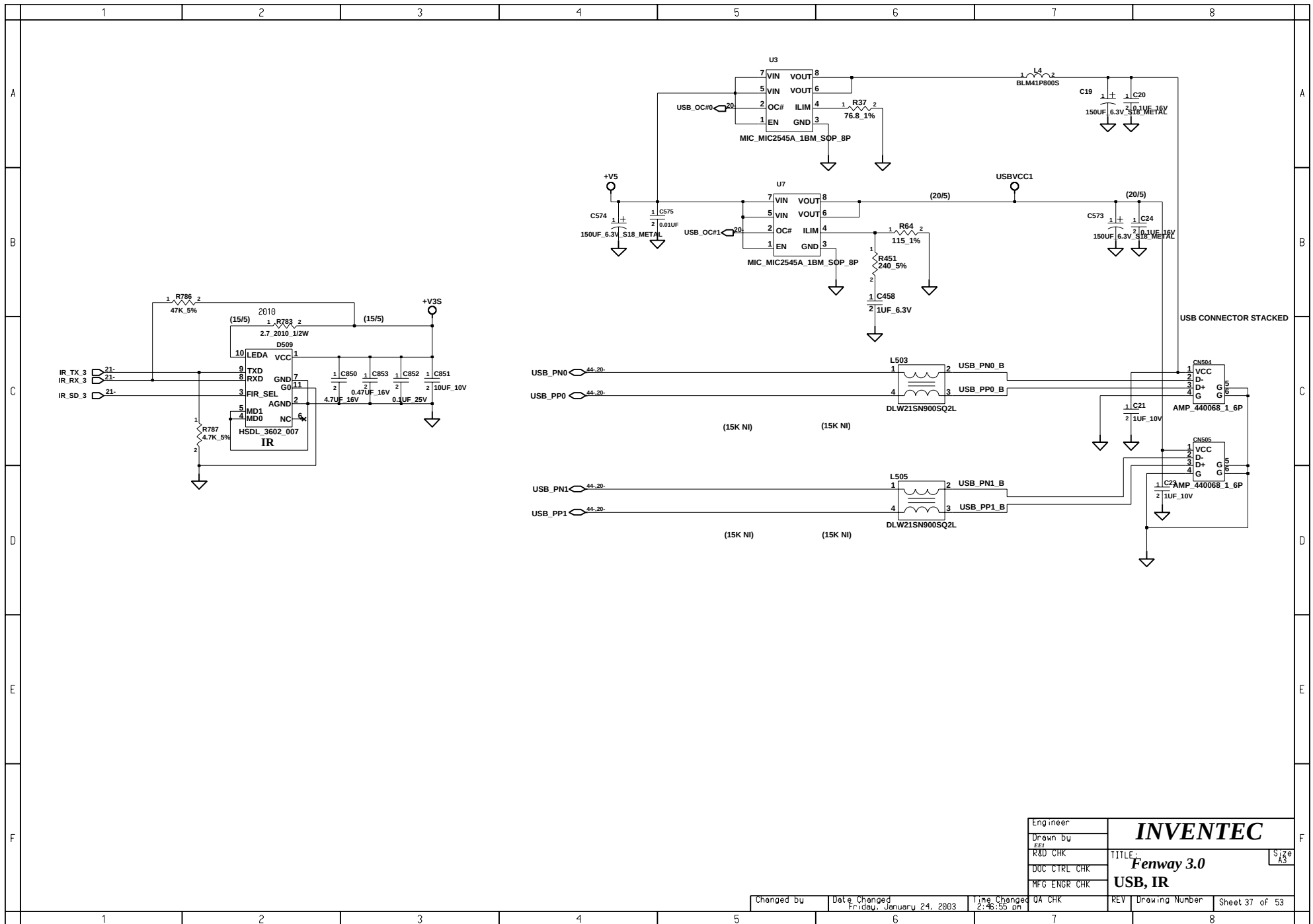
LID SWITCH



Engineer	INVENTEC		
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MFG ENGR CHK			

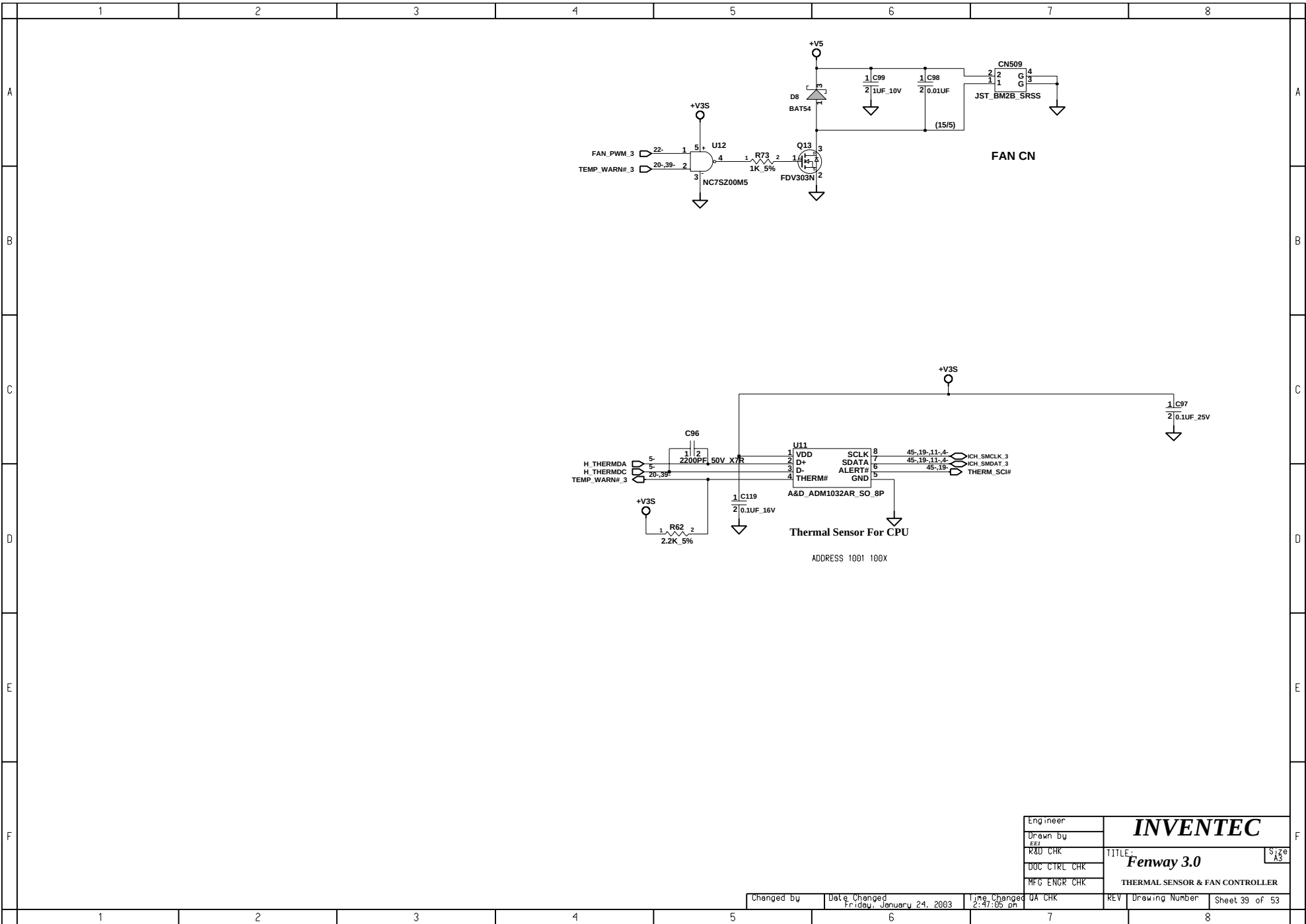
Changed by	Date Changed Friday, January 24, 2003	Time Changed 2:46:35 pm	UA CHK	REV	Drawing Number	Sheet 34 of 53
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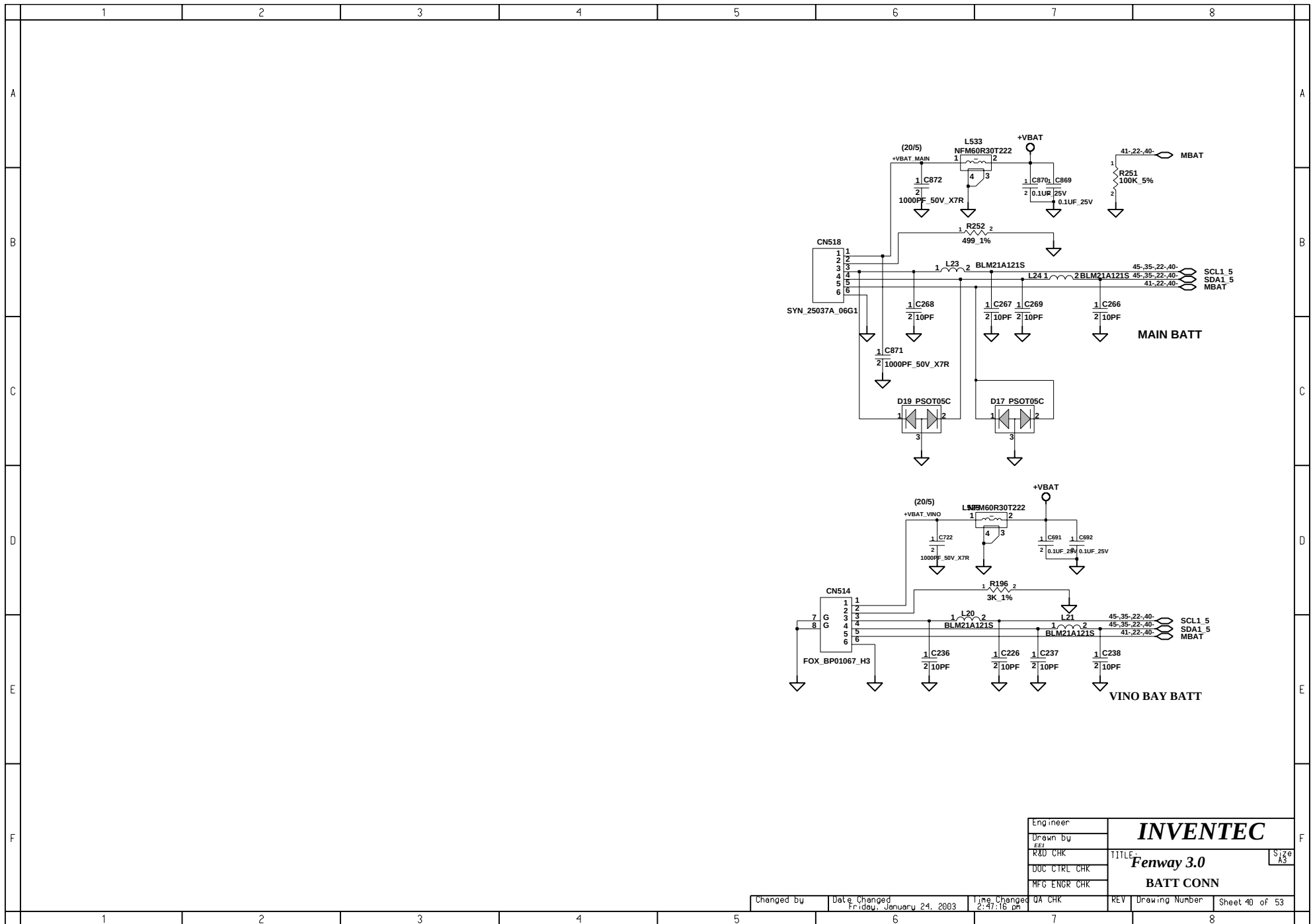


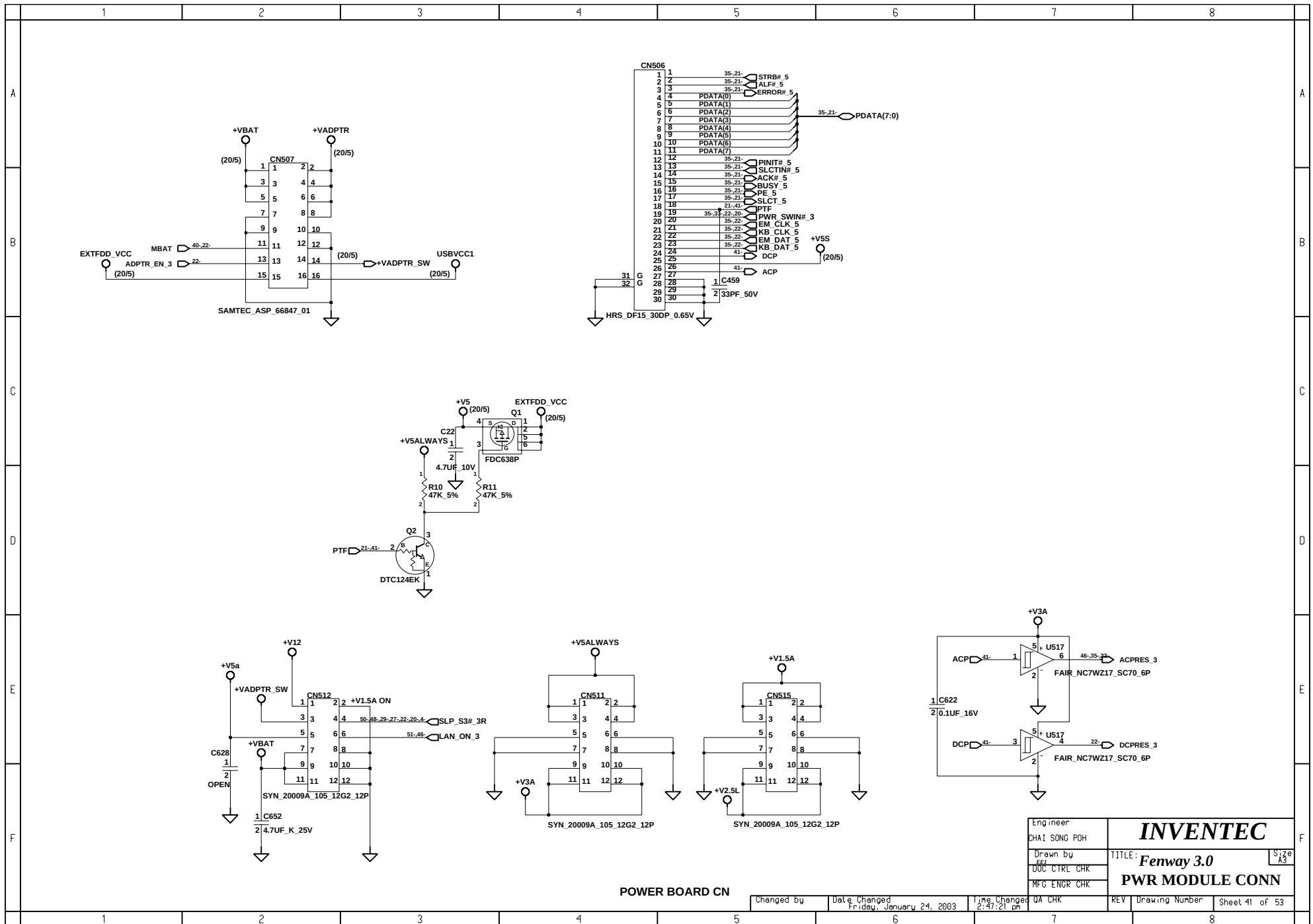


Engineer	INVENTEC TITLE: Fenway 3.0 USB, IR		
Drawn by			
REV			
QA CHK			
R&D CHK	REV		
DOC CTRL CHK	Drawing Number		
MFG ENGR CHK	Sheet 37 of 53		

Changed by Friday, January 24, 2003 Time Changed 2:46:55 pm



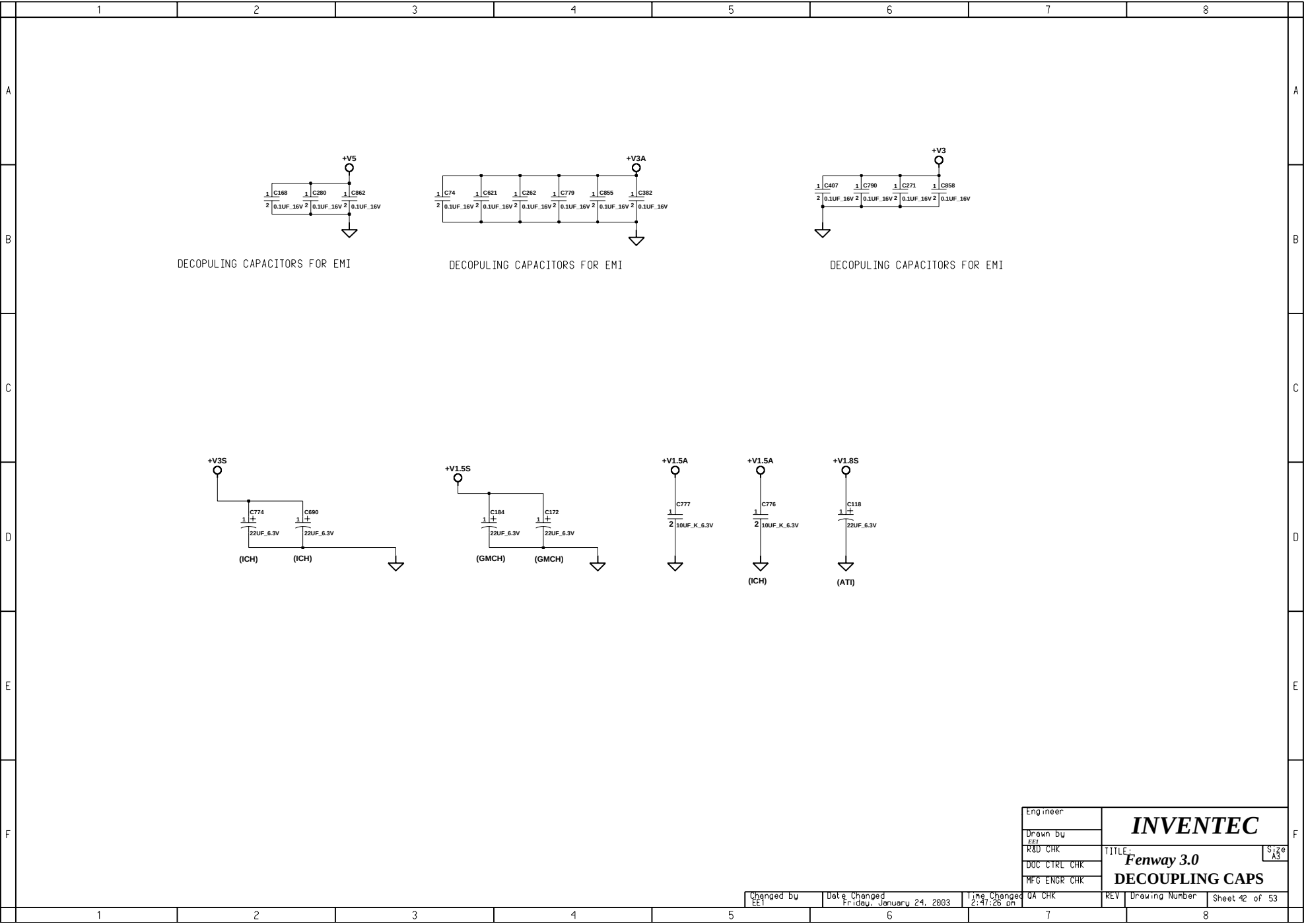




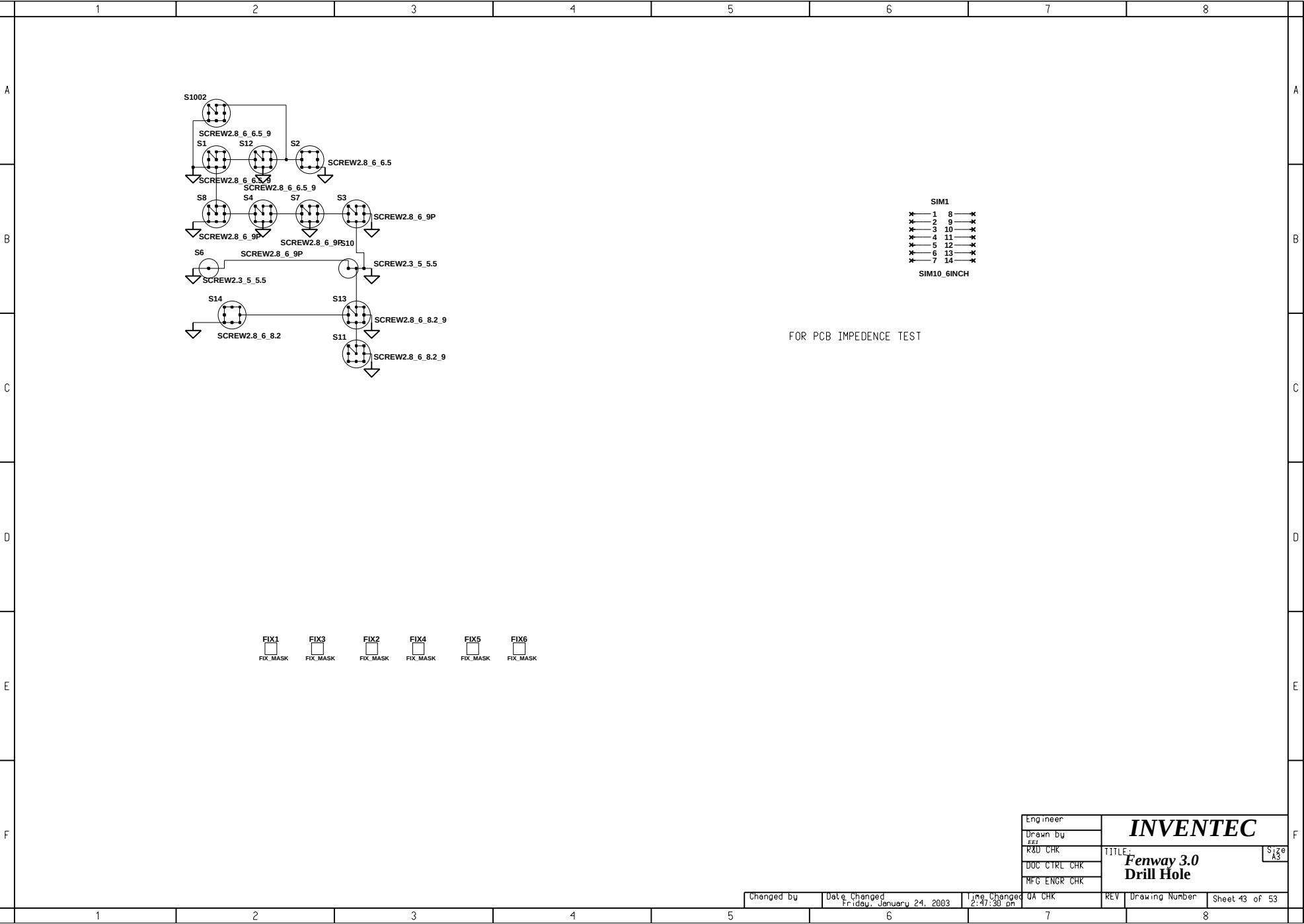
POWER BOARD CN

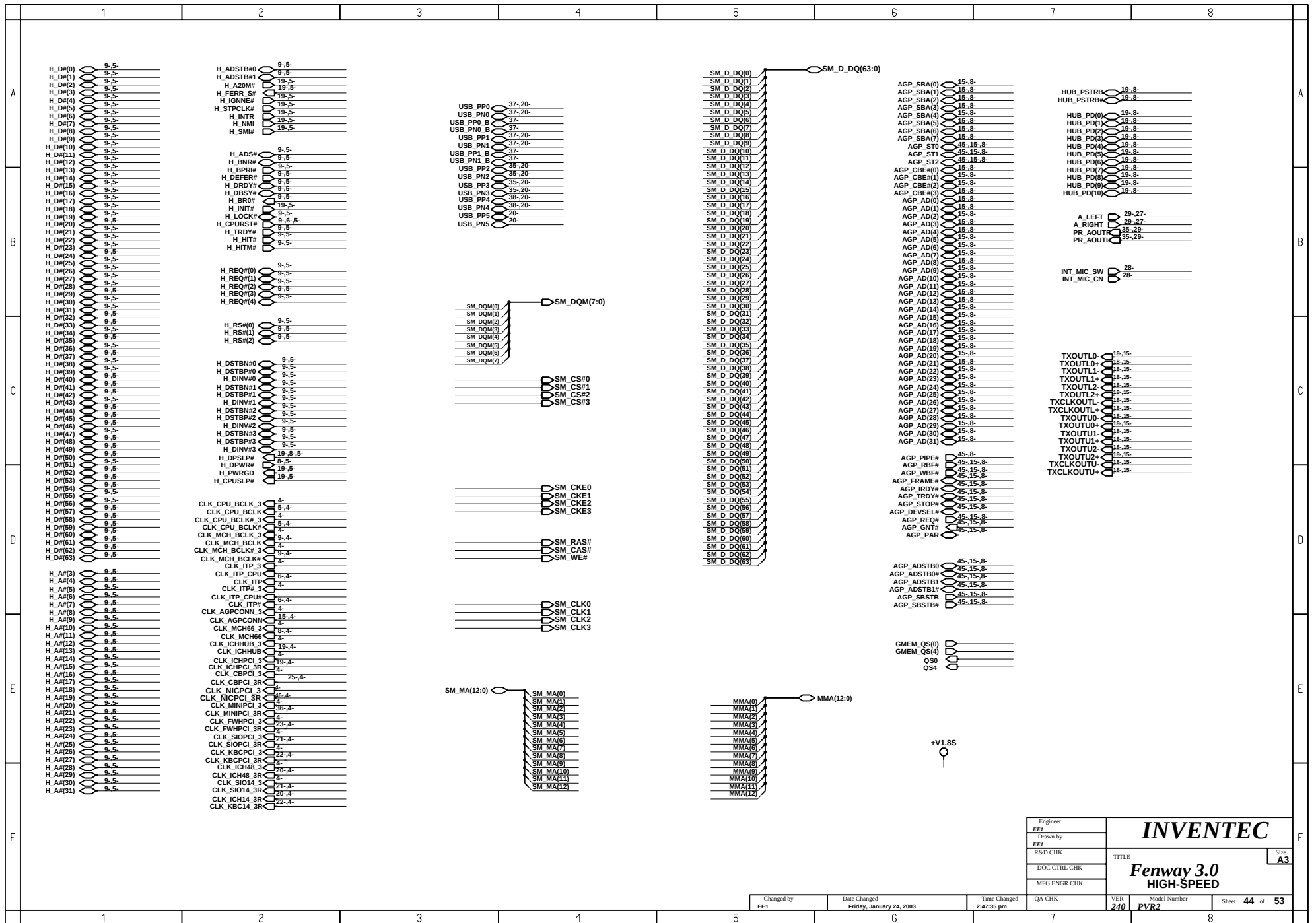
Engineer	CHAI SONG POH	INVENTEC TITLE: Fenway 3.0 PWR MODULE CONN	Size A3
Drawn by	EE		
DOC CTRL CHK			
MFG ENGR CHK			

Changed by	Date Changed	Time Changed	REV	Drawing Number	Sheet 41 of 53
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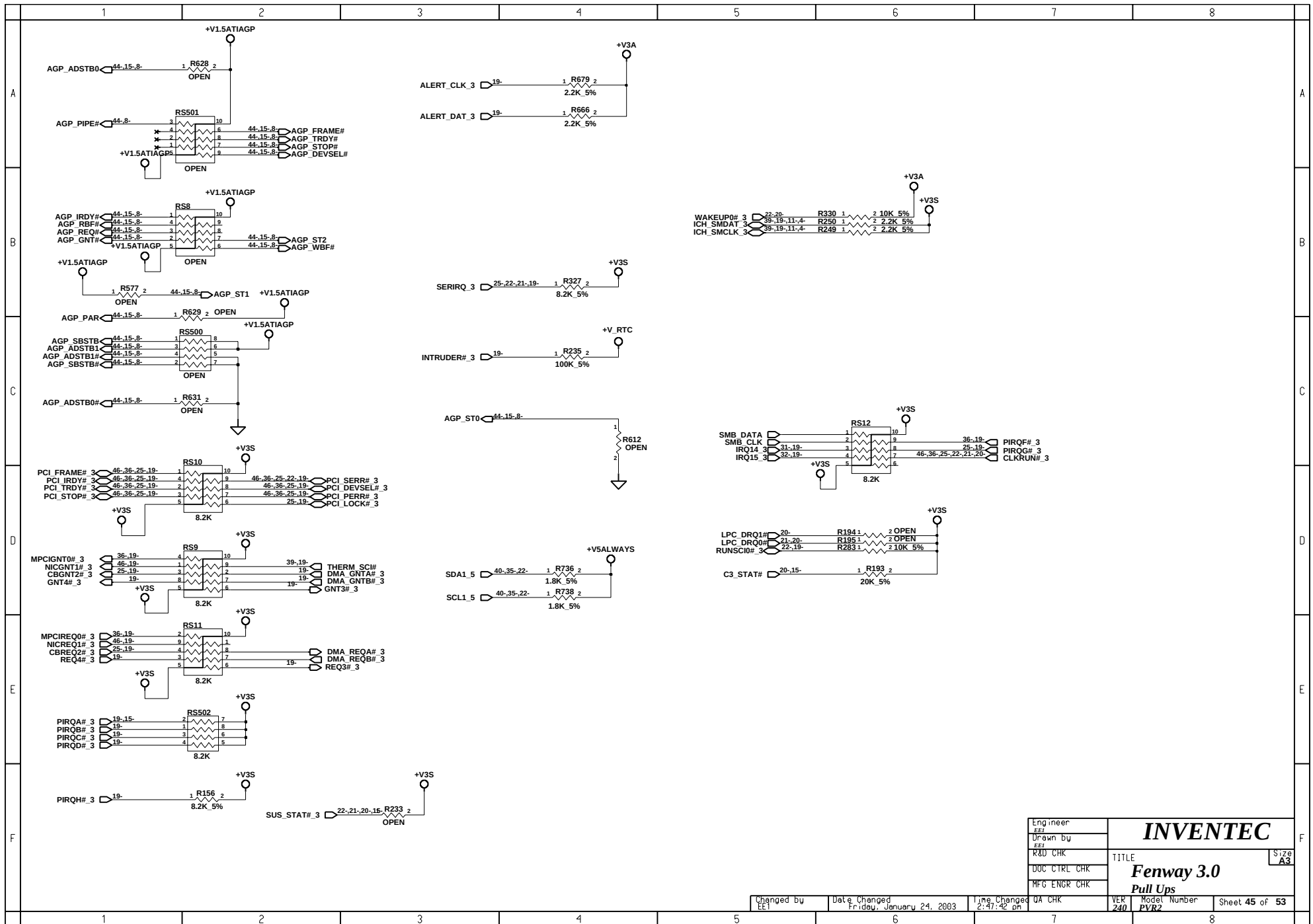
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DOC CTRL CHK			
MFG ENGR CHK			
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:47:26 pm	UA CHK
REV	Drawing Number	Sheet 42 of 53	



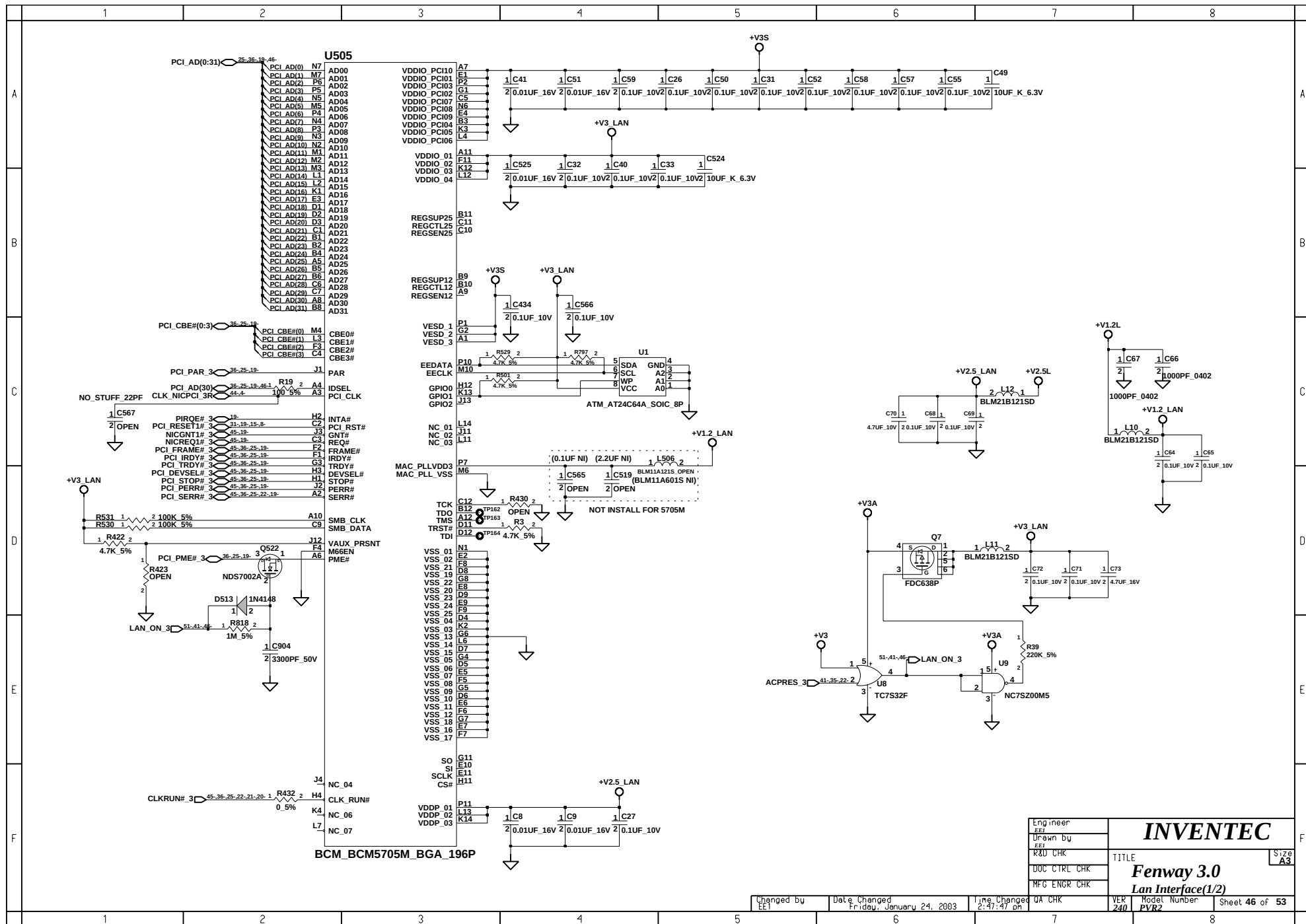


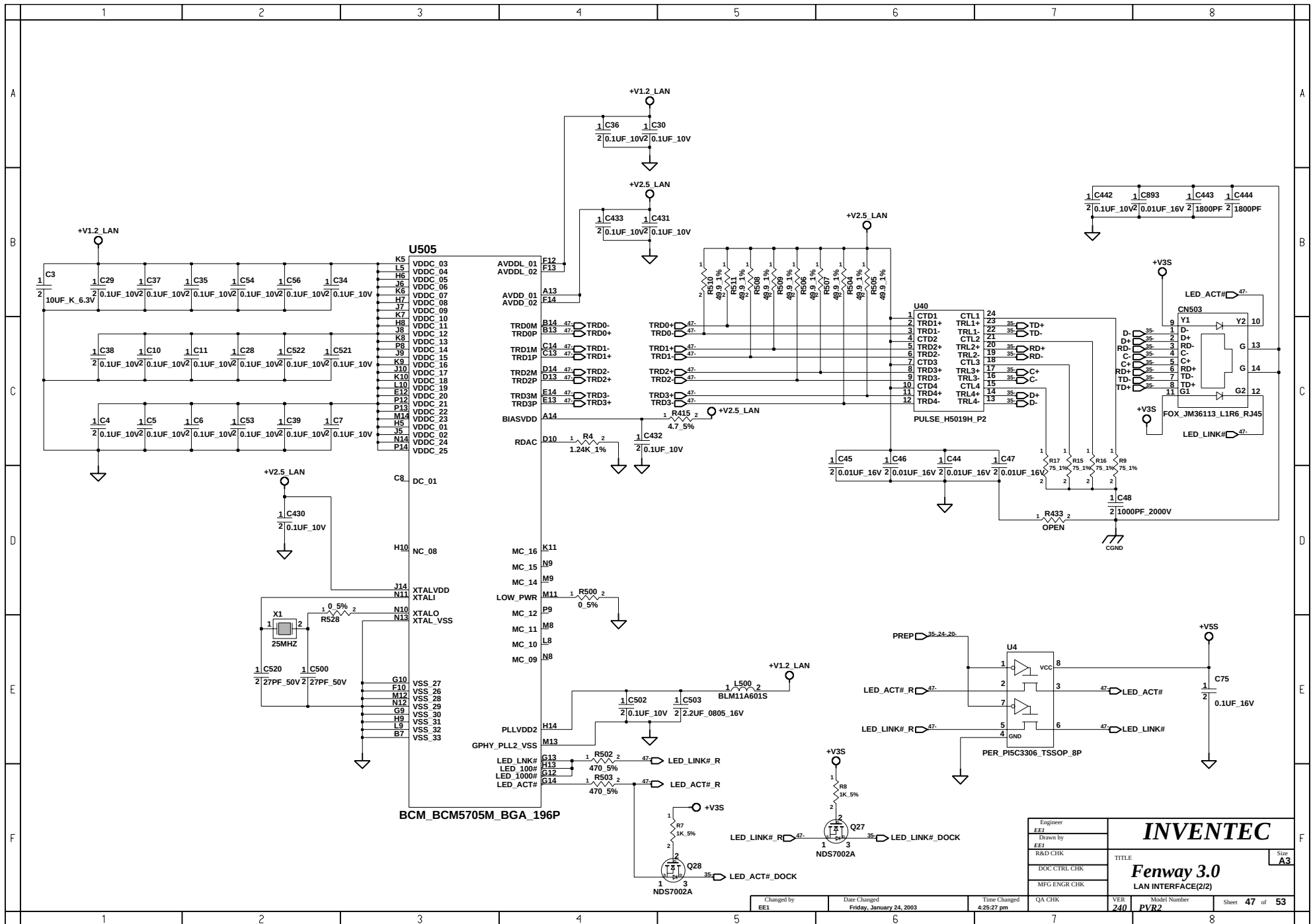
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Drawn by EE1				
R&D CHK				
DOC CTRL CHK				
MFG ENGR CHK	VER 240	Model Number PVR2	Sheet 44 of 53	

Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:47:35 pm
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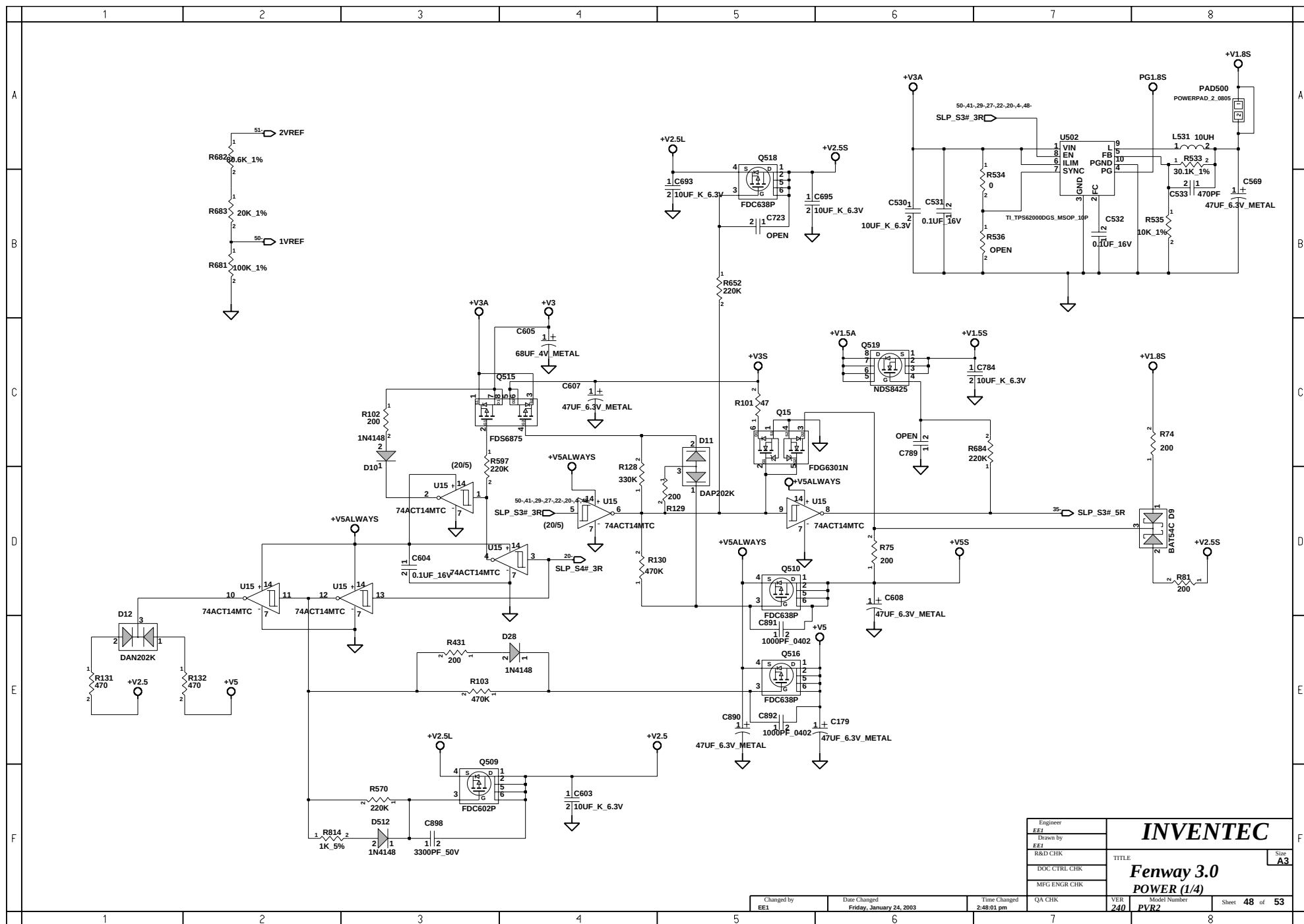
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Drawn by			
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DOC CTRL CHK			
MFG ENGR CHK			
Changed by	Date Changed	Time Changed	QA CHK
EE1	Friday, January 24, 2003	2:47:42 pm	UA
VER	Model Number	Sheet	of
240	PVR2	45	53

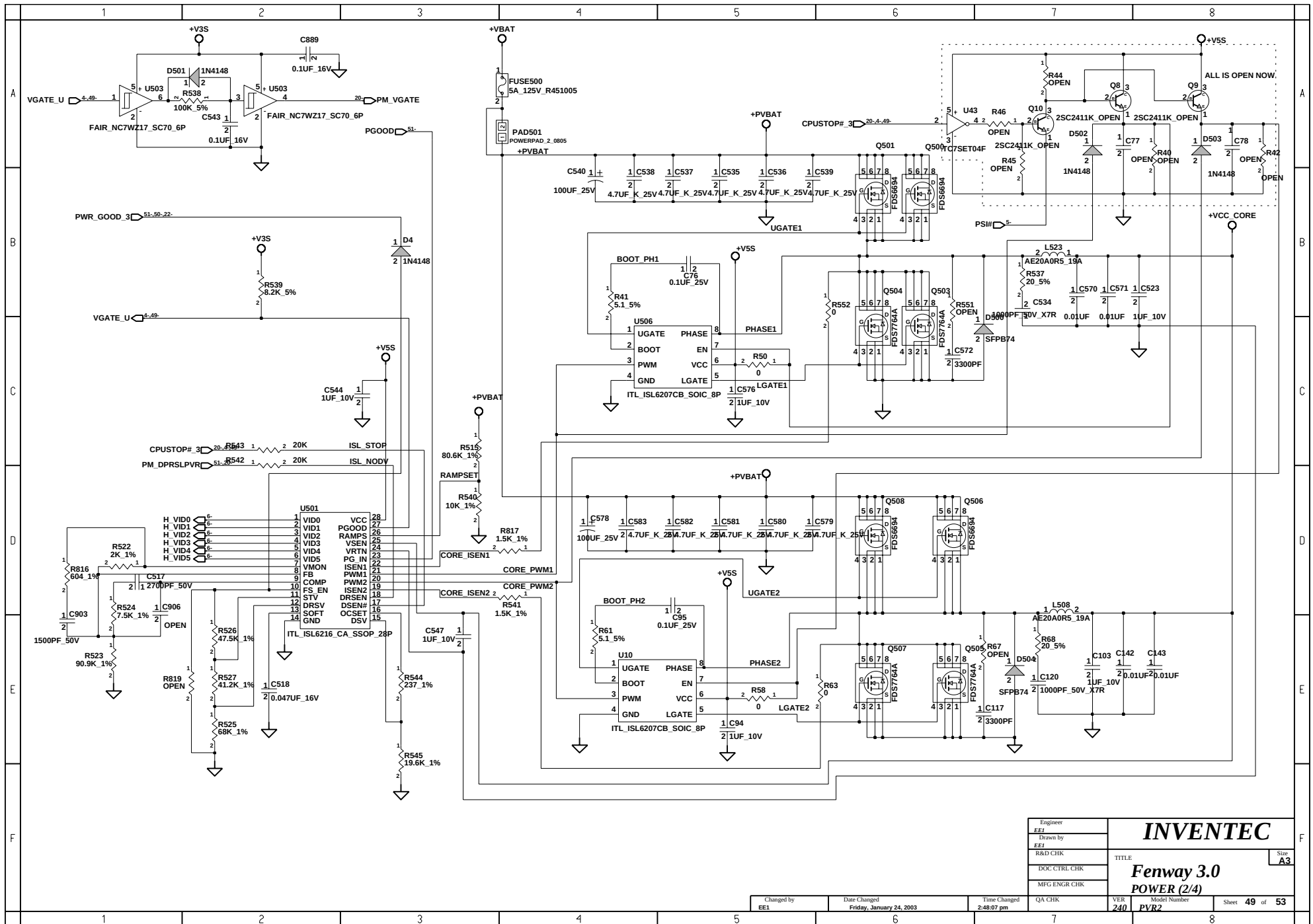




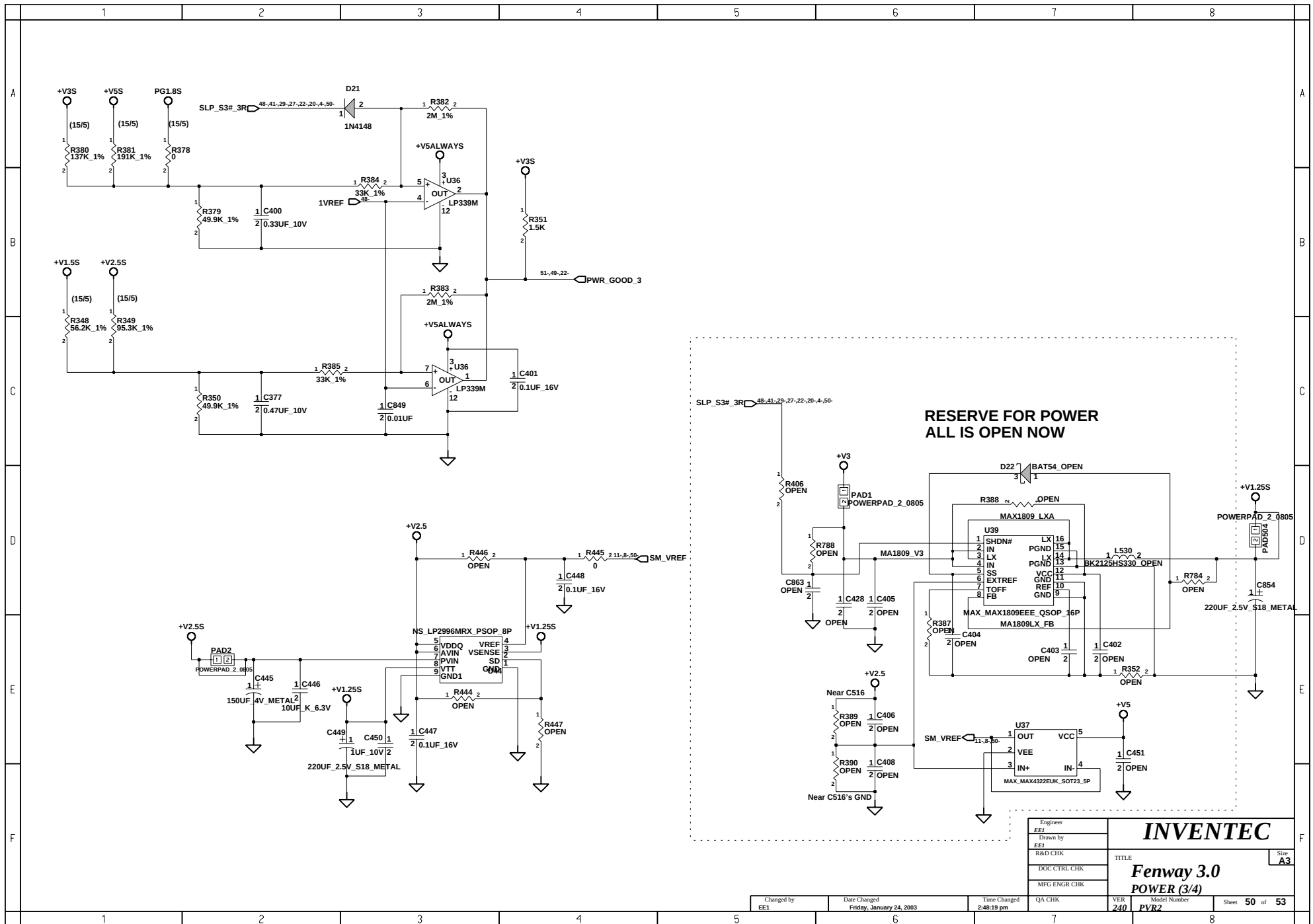
Engineer EE1	INVENTEC TITLE: Fenway 3.0 LAN INTERFACE(2/2)		Size A3
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DOC CTRL CHK			
MFG ENGR CHK			

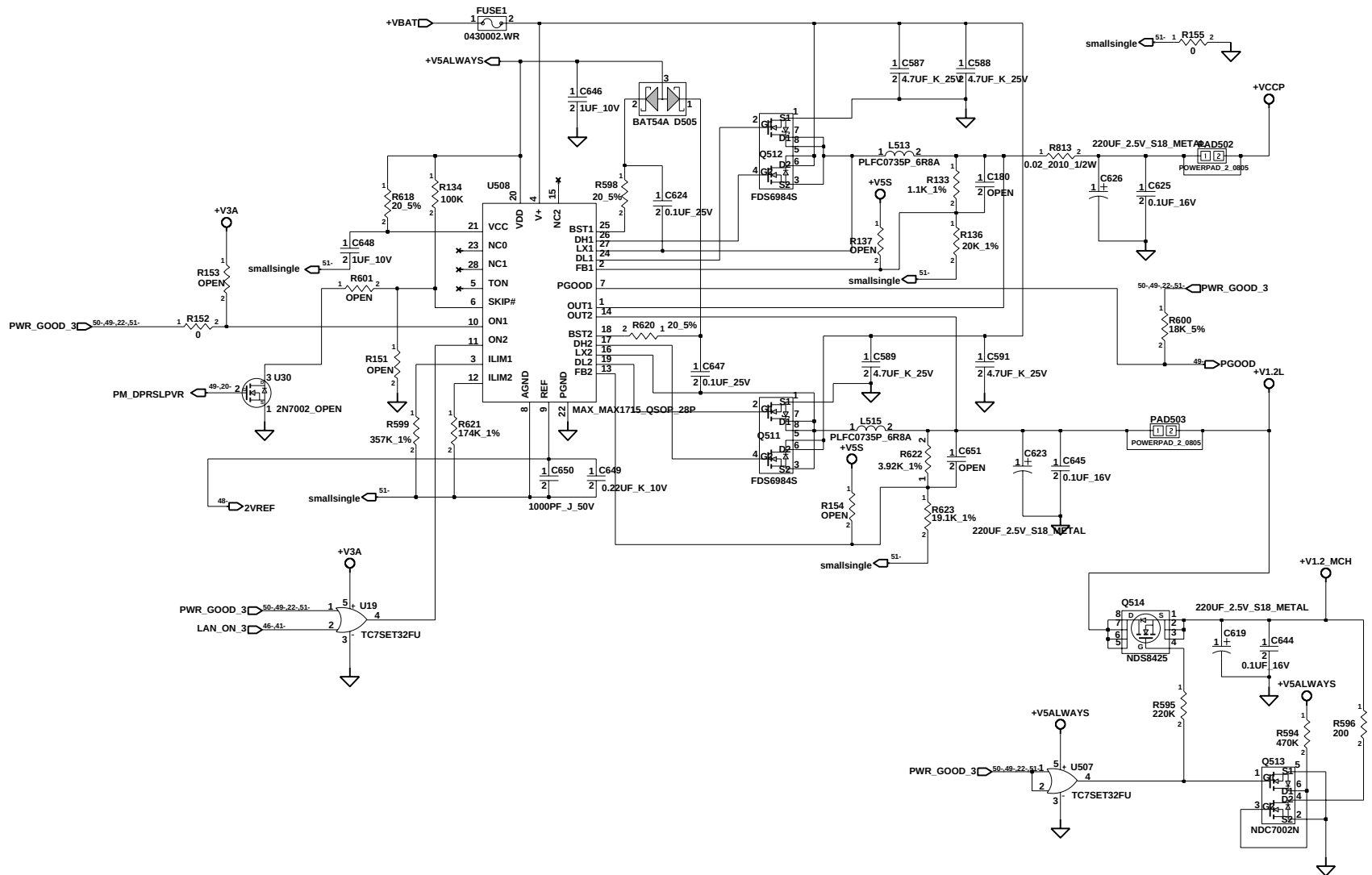
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 4:25:27 pm	QA CHK	VER 240	Model Number PVR2	Sheet 47 of 53
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Engineer EE1		INVENTEC TITLE Fenway 3.0 POWER (2/4) Model Number PVR2 Sheet 49 of 53 Size A3	
Drawn by EE1			
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DOC CTRL CHK			
MFG ENGR CHK			
Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:48:07 pm	QA CHK
VER 240	Model Number PVR2		Sheet 49 of 53





Engineer EE1	INVENTEC TITLE: Fenway 3.0 POWER(4/4) Model Number: PVR2	Size: A3 Sheet: 51 of 53
Drawn by EE1		
R&D CHK		
DOC CTRL CHK		
MFG ENGR CHK		

Changed by EE1	Date Changed Friday, January 24, 2003	Time Changed 2:48:25 pm	QA CHK	VER 240	Model Number PVR2	Sheet 51	of 53
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