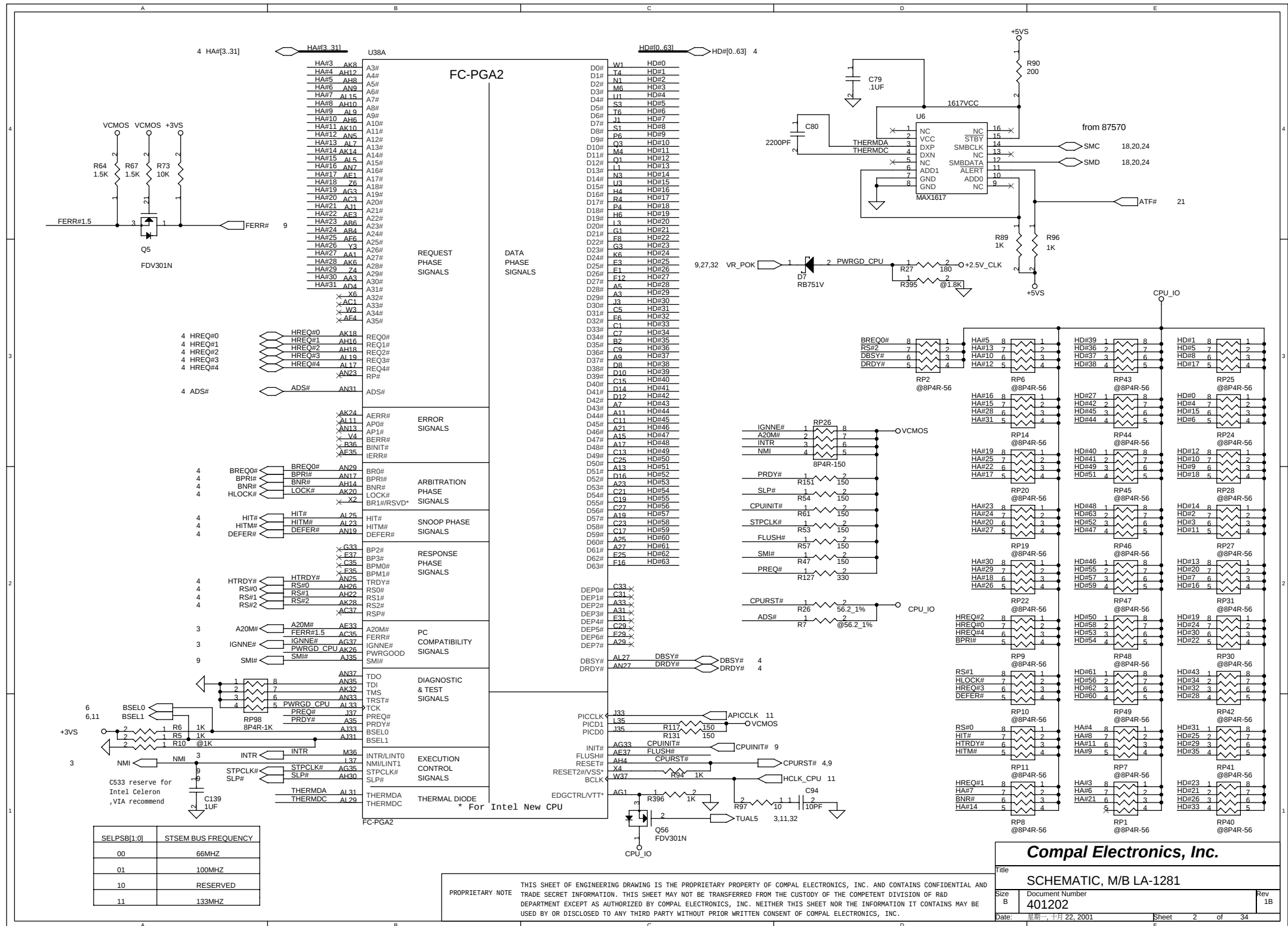
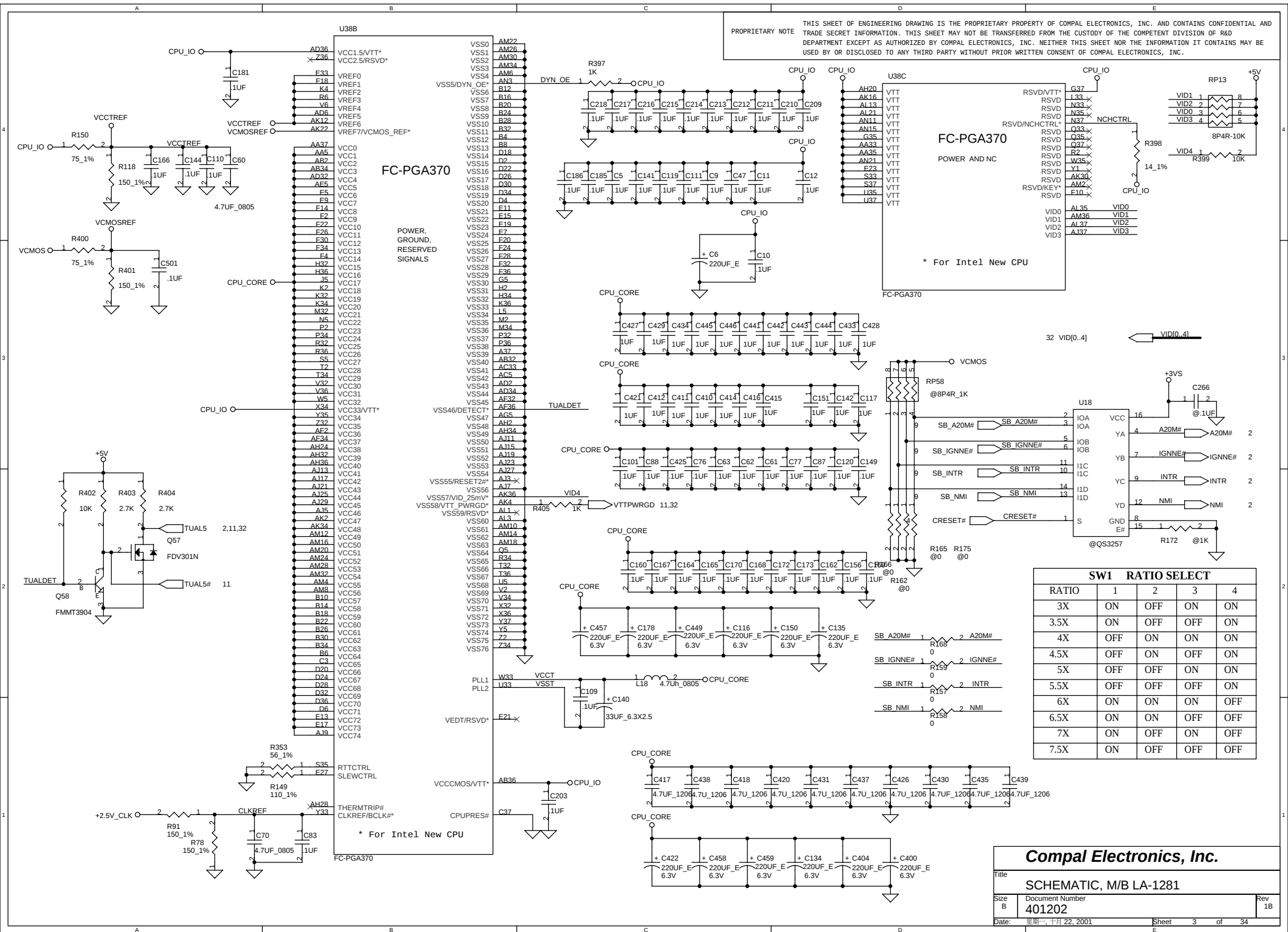
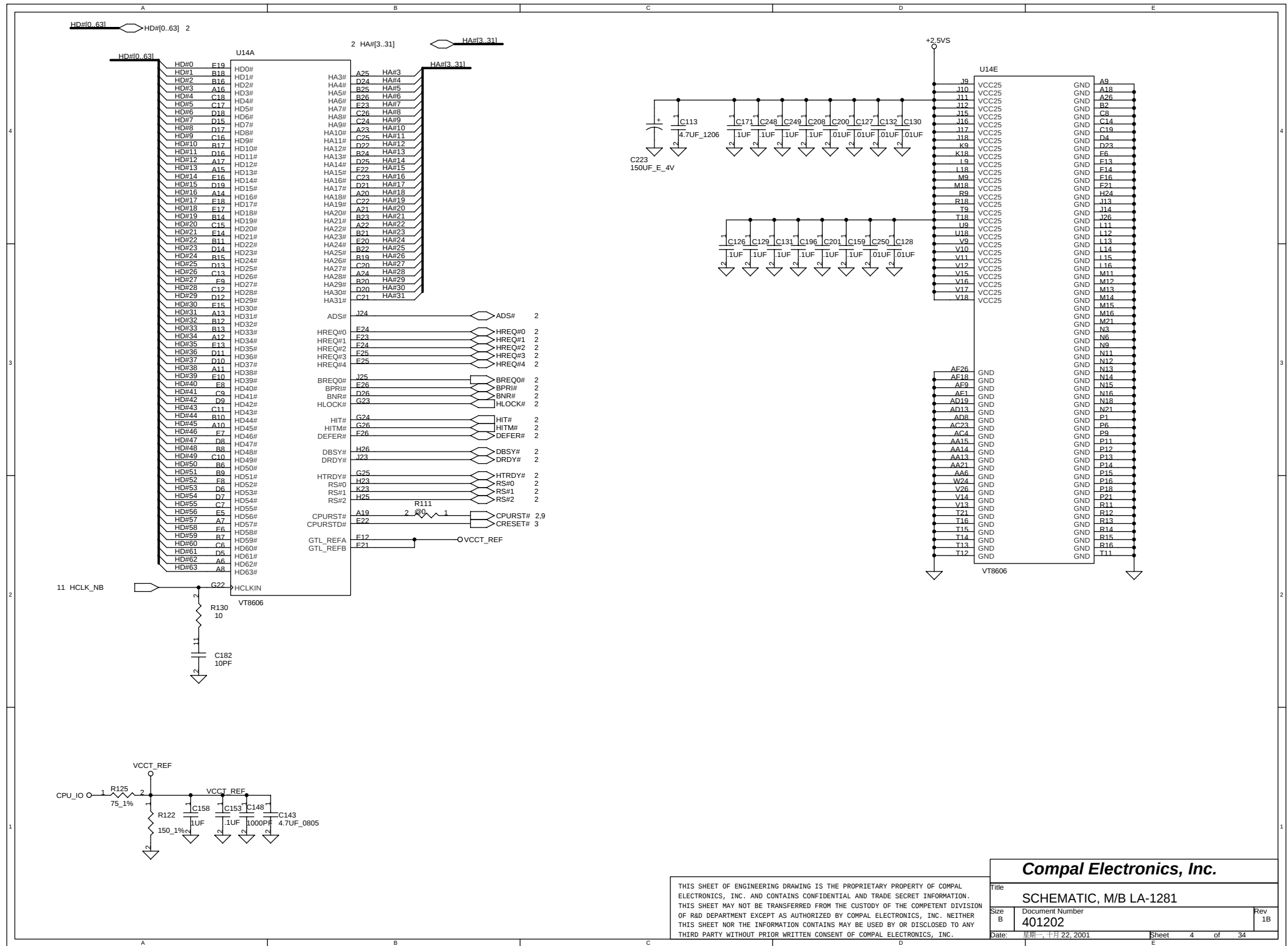


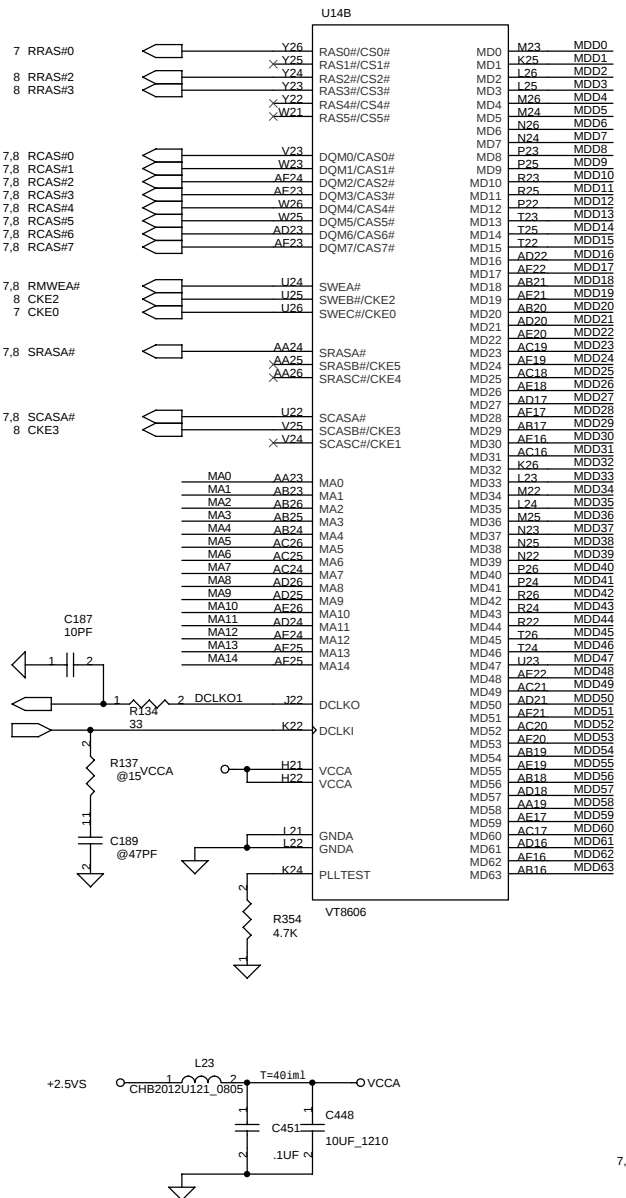


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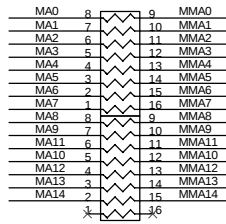
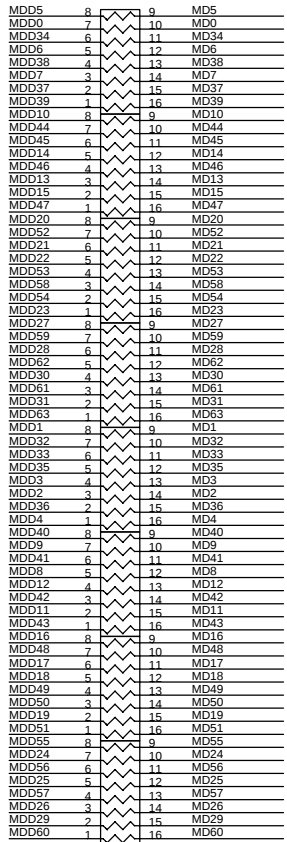




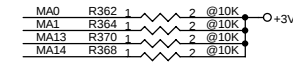




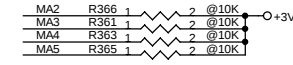
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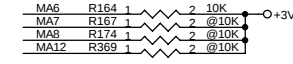
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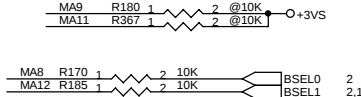
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RP54
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RP62
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RP38
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RP33
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RP57
16P8R-22

RP61
16P8R-22

RP53
16P8R-22

RP56
16P8R-22

Strap	Description	Setting
MA12,8	CPU Clock Frequency	01=100Mhz 11=133Mhz 00=66Mhz
MA2	PCI Base Address Mapping	0=Map0 1=Map1
MA3	Graphic IO Enable/Disable	0=Enable 1=Disable
MA4	PCI Interrupt	0=Enable 1=Disable
MA7	Graphic Test Mode	0=Normal 1=Test
MA9	VGA Clock Select	0=PLL 1= External
MA11	IOQ Level	0=4Level 1=1Level
MA0,1,13,14	Panel Type	

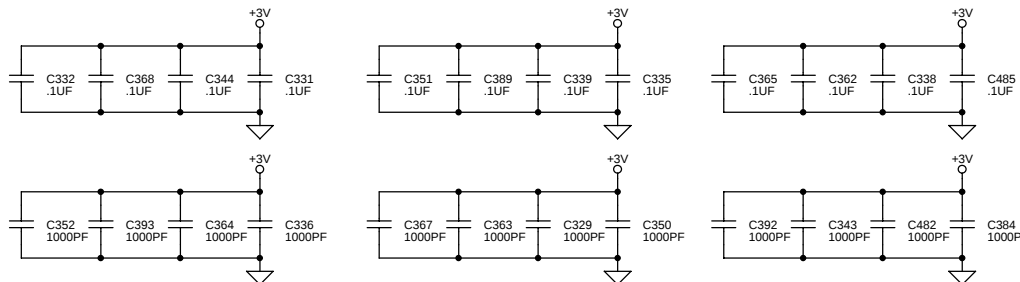
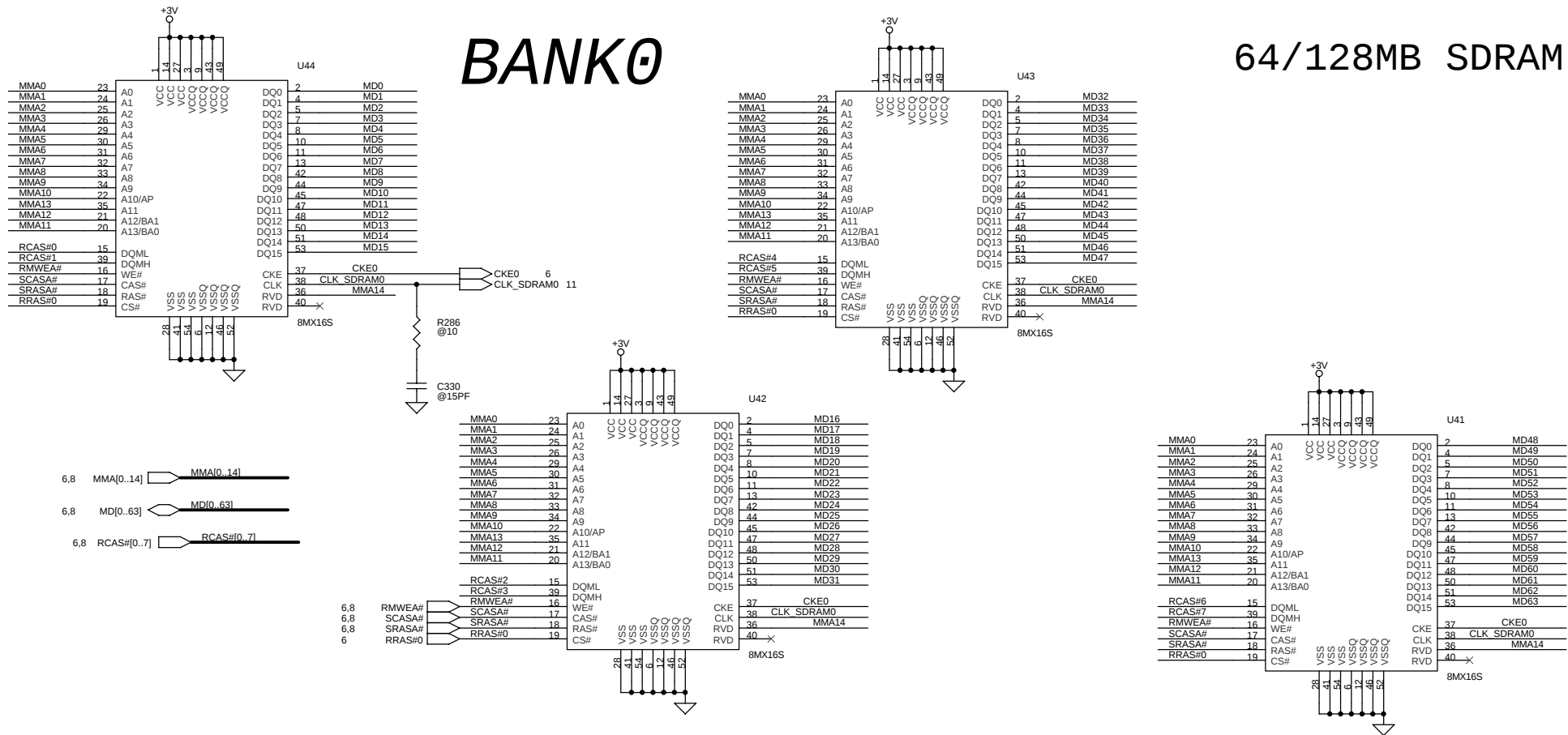
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BANK0

64/128MB SDRAM



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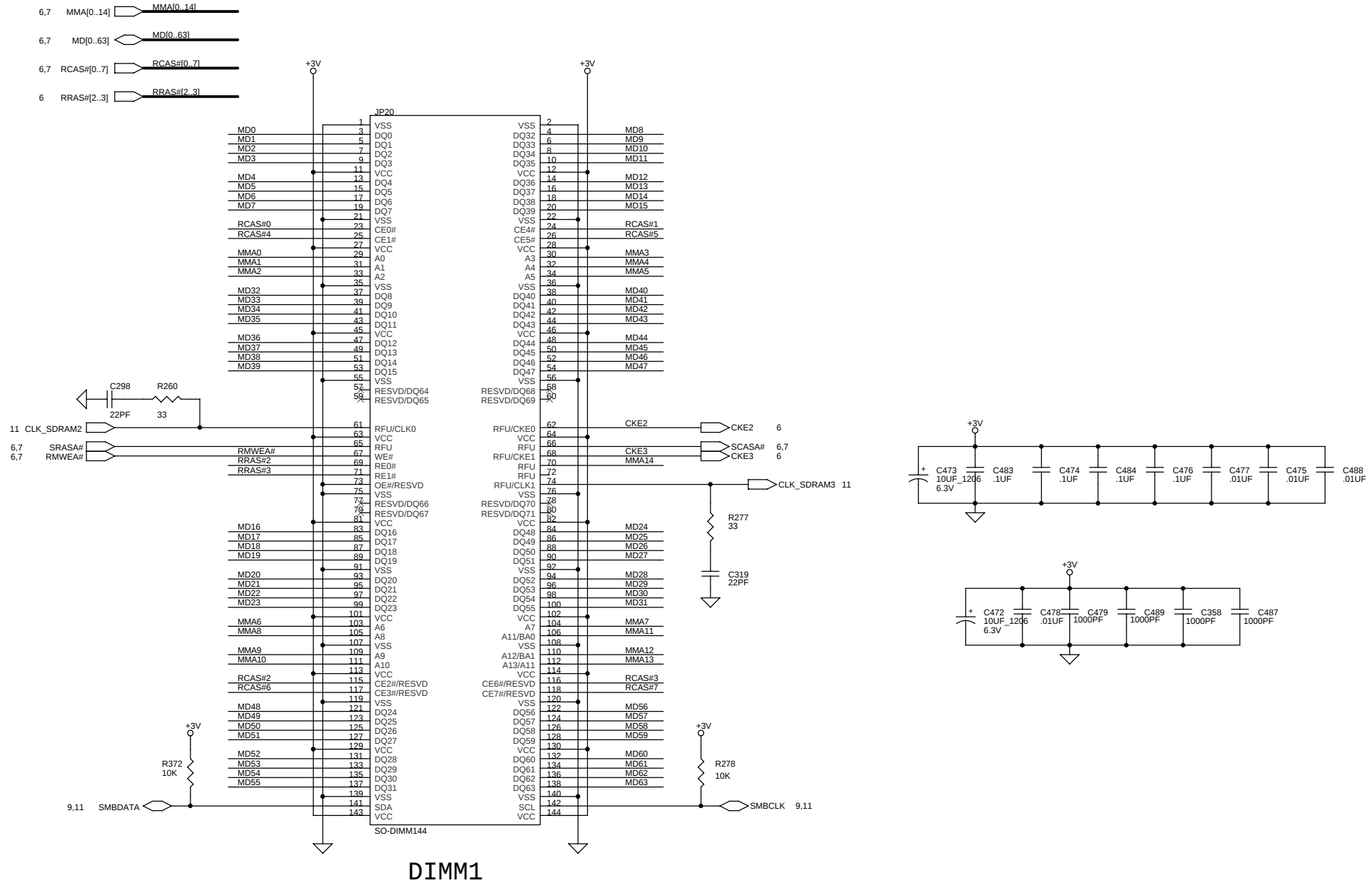
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SO-DIM 144 PINS
RAM MODULE CONN.

BANK2/3



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401202

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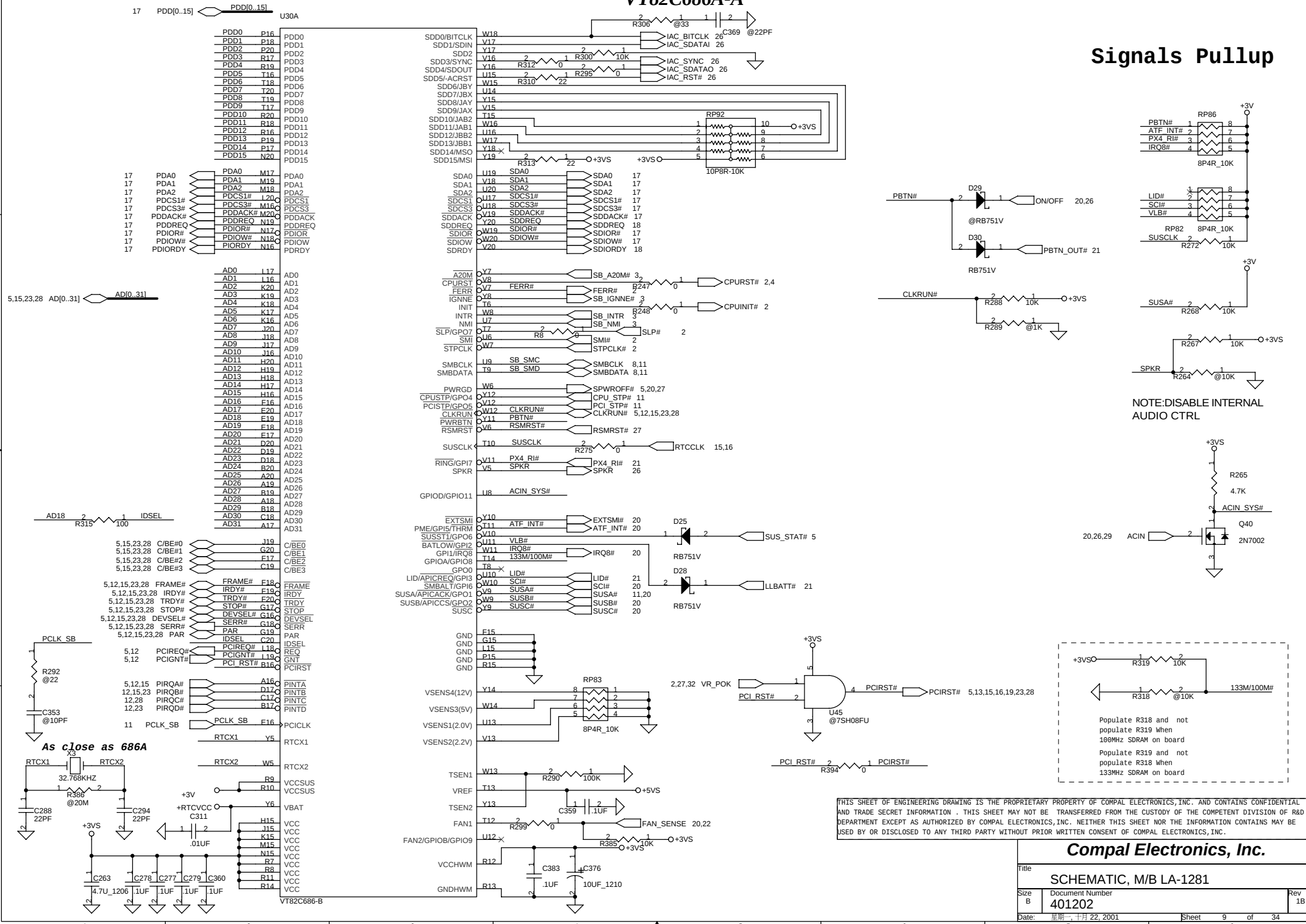
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VT82C686A-A

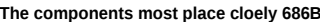
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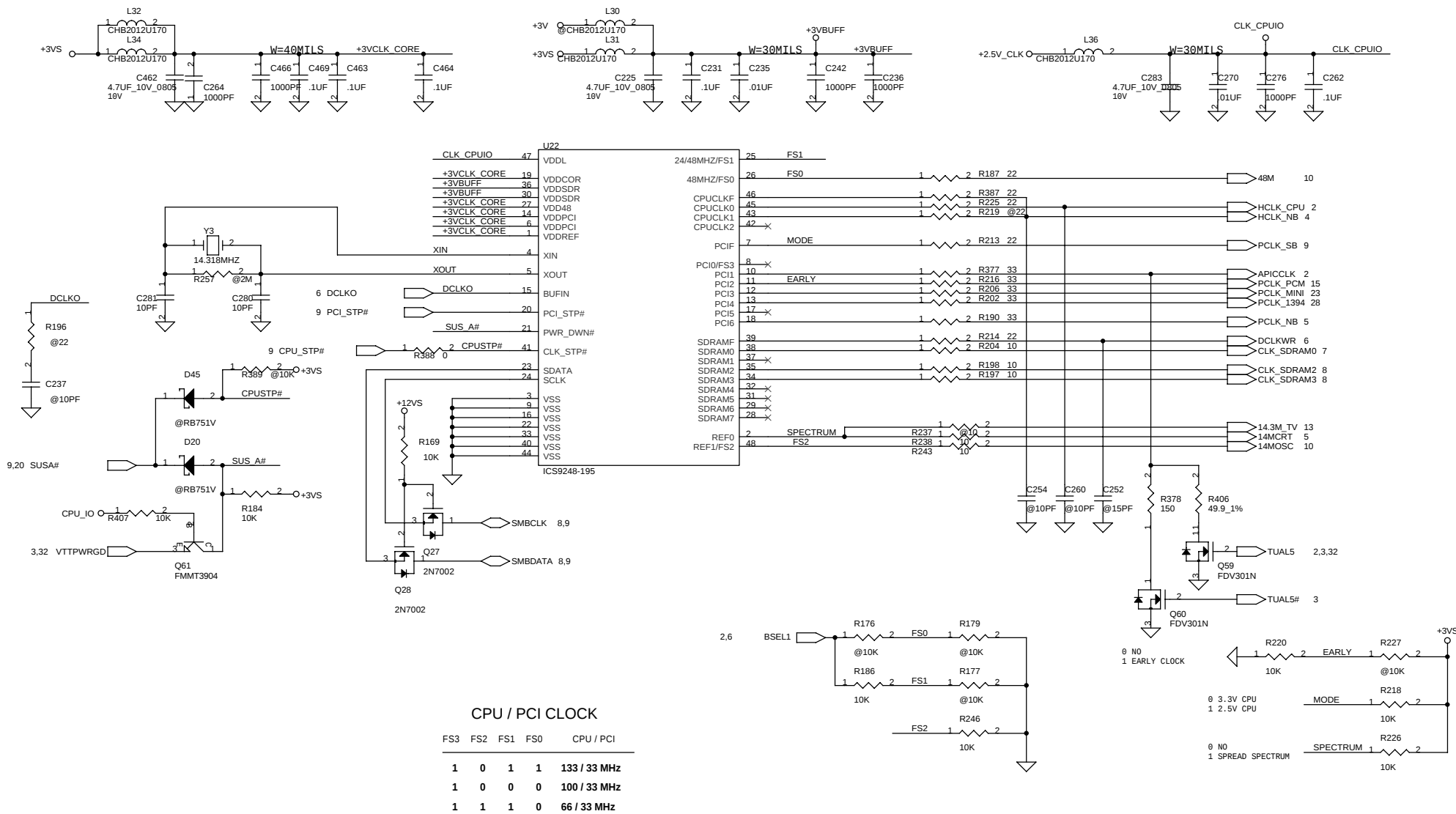
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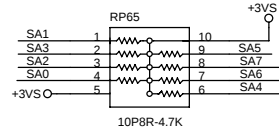
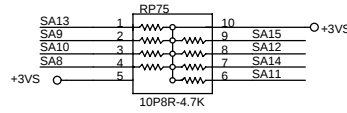
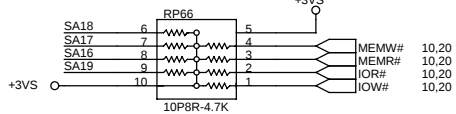
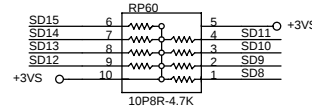
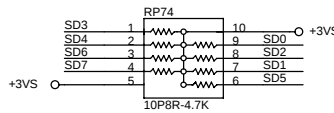
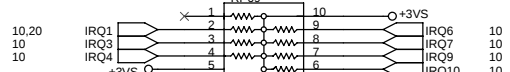
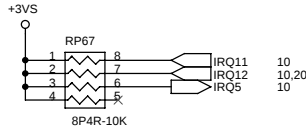
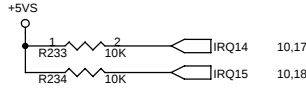
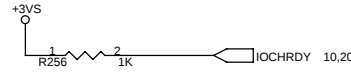
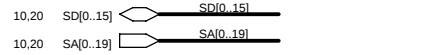
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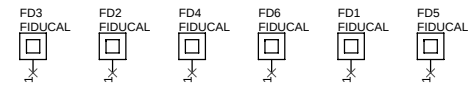
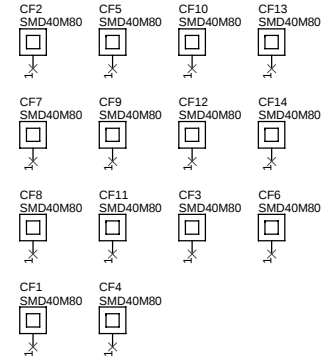
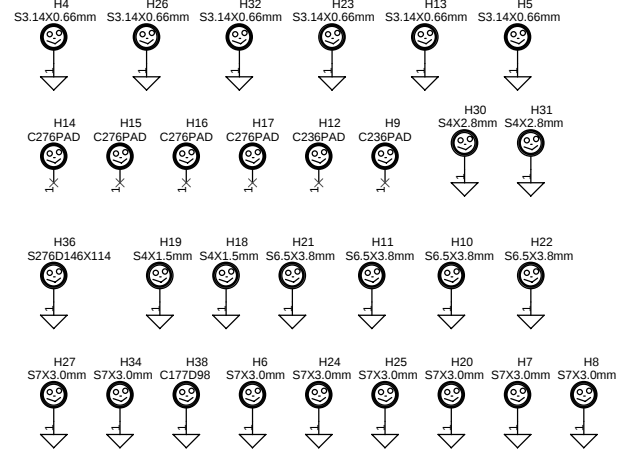
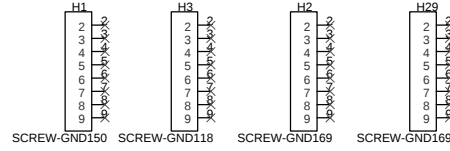
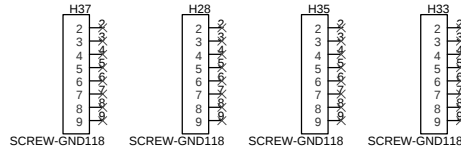
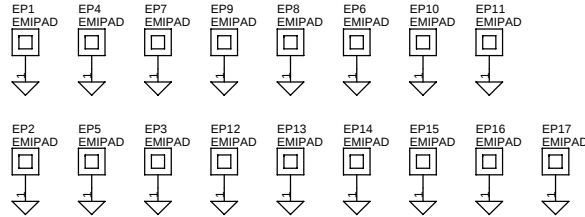
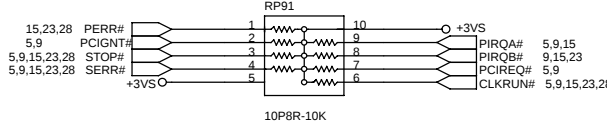
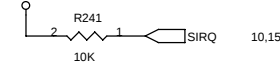
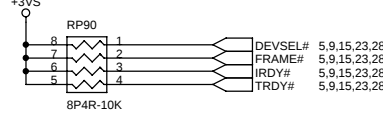
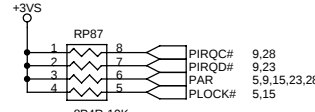
CLOCK GENERATOR & BUFFER



ISA BUS Pullup



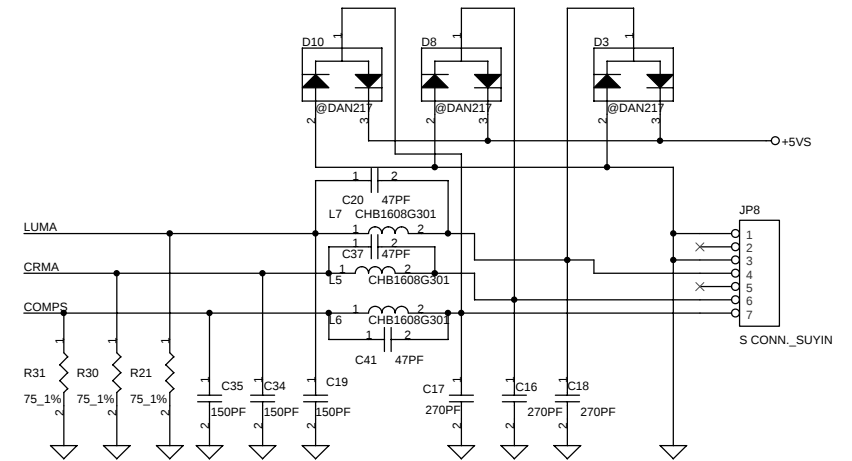
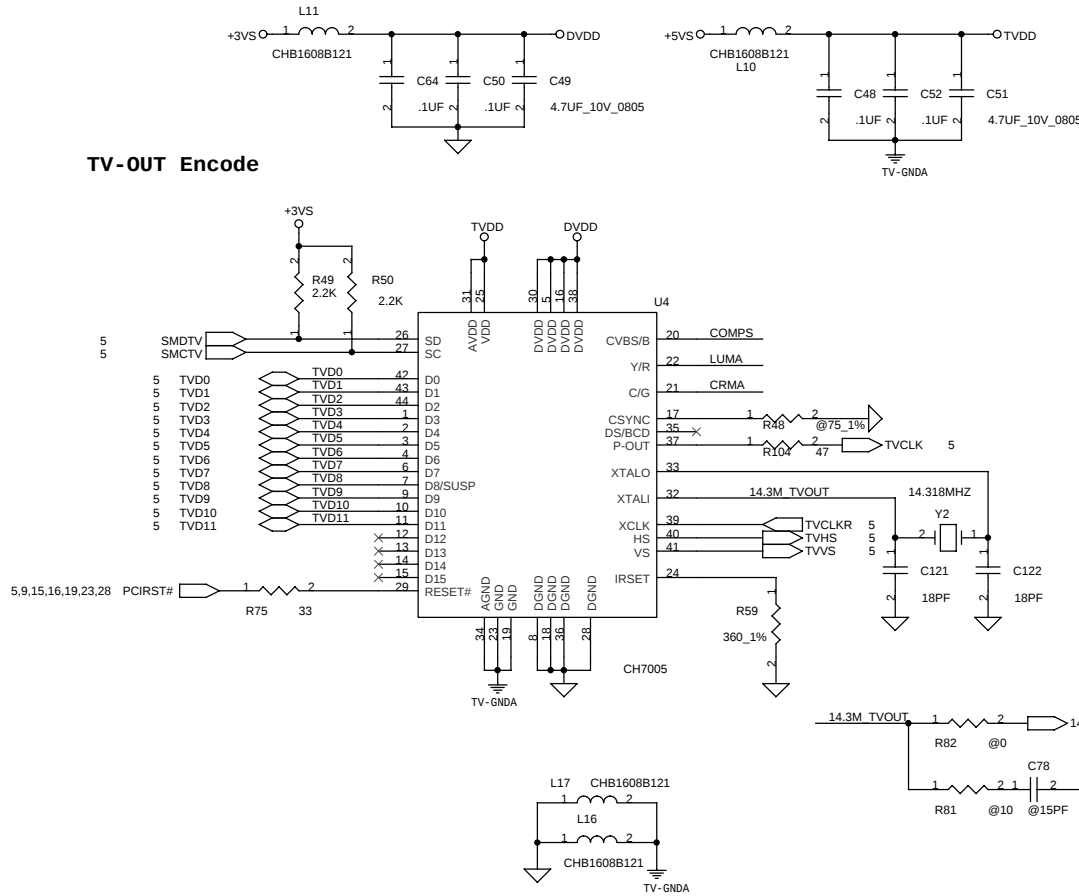
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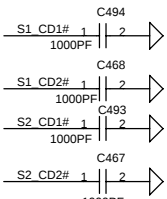
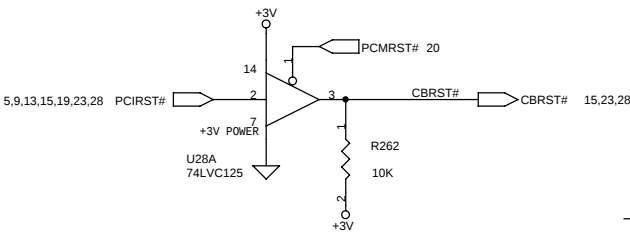
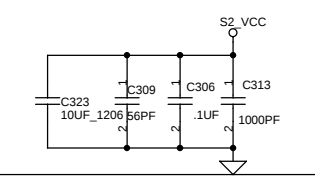
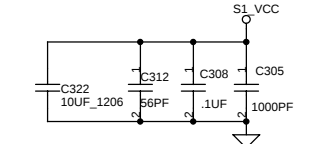
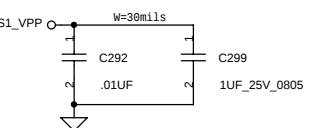
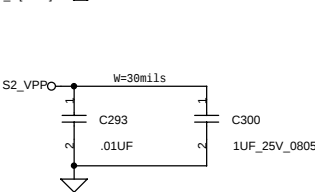
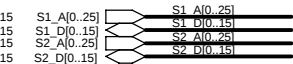
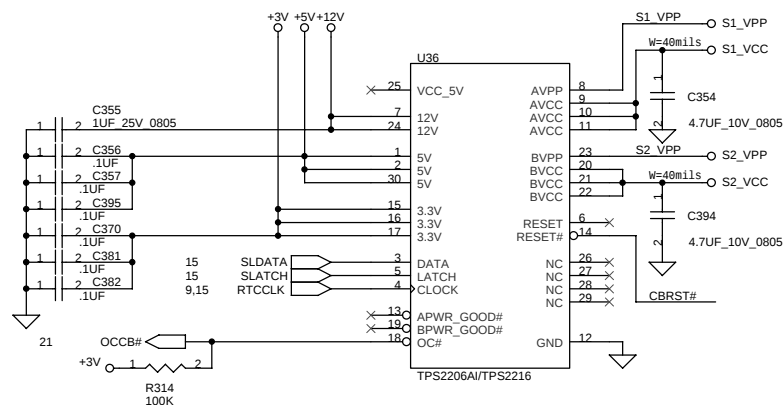
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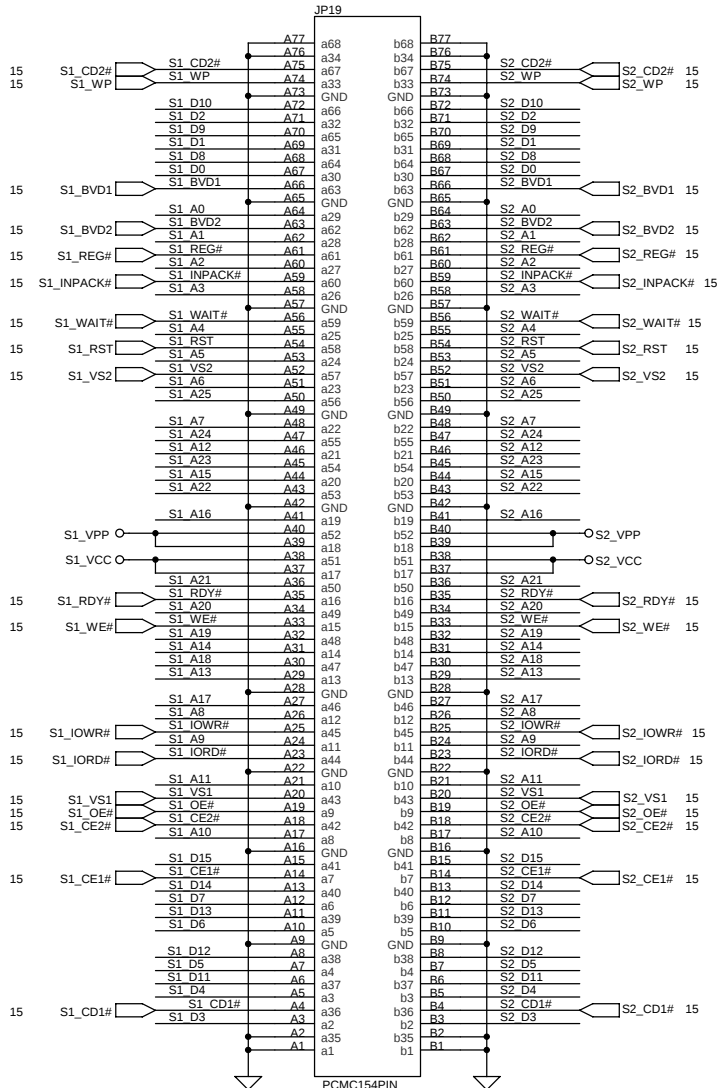
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PCMCIA POWER CTRL. Wire ZV P0RT to Slot A

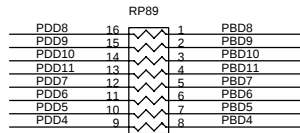
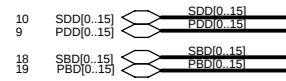


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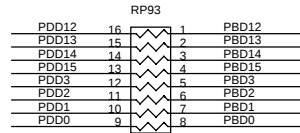


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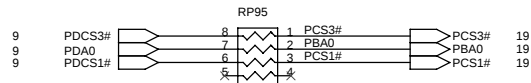
IDE Series Resistor Place them close SB



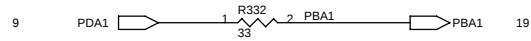
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16P8R-33



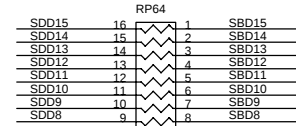
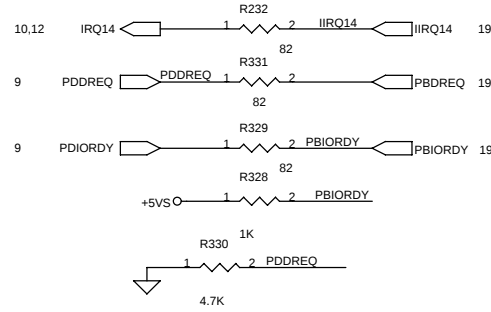
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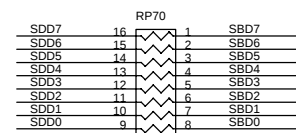
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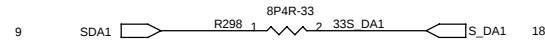
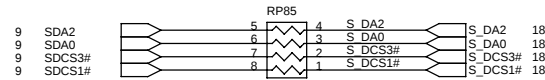
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16P8R-33

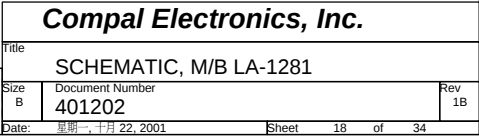


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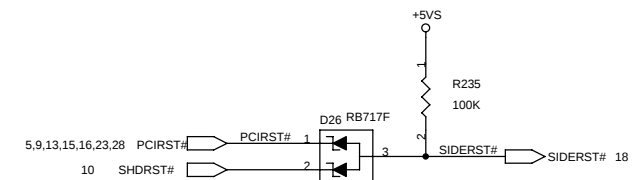
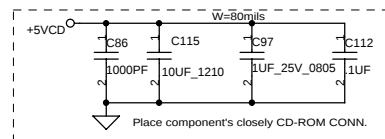
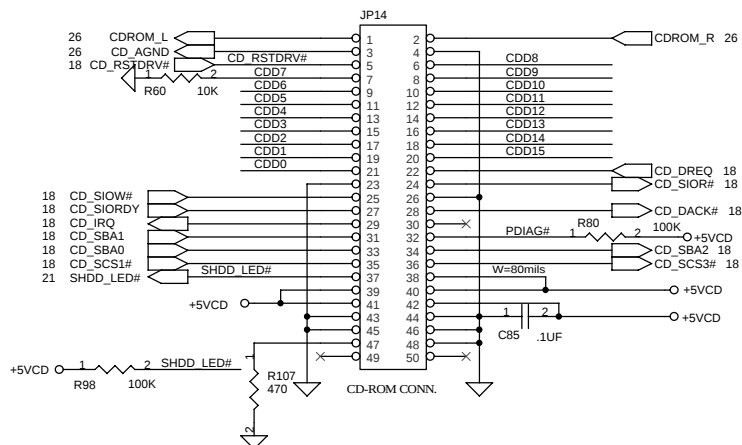
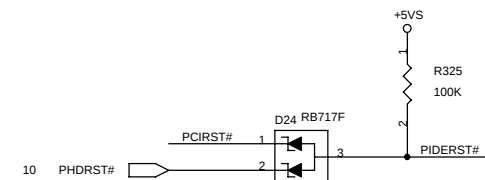
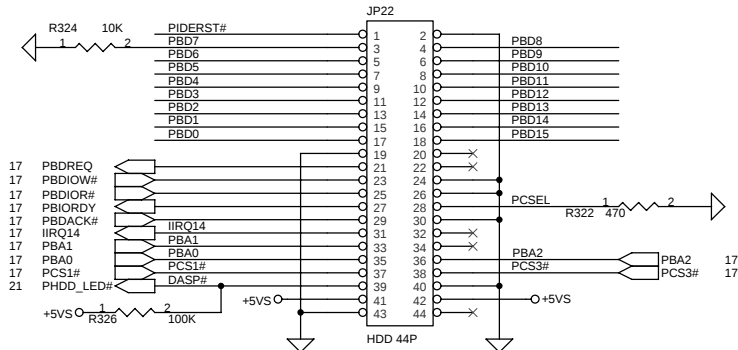
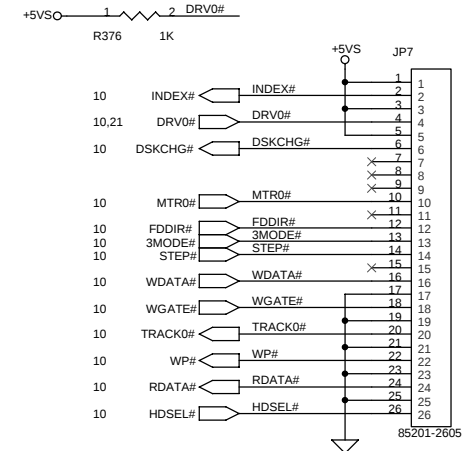
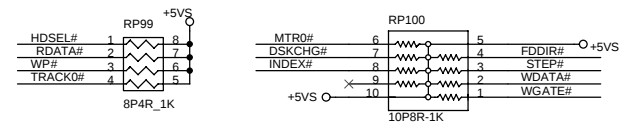
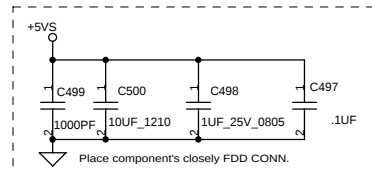
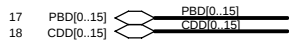
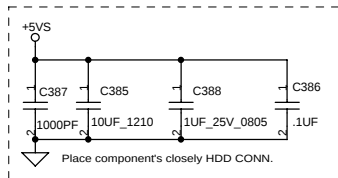
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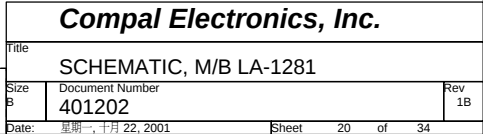
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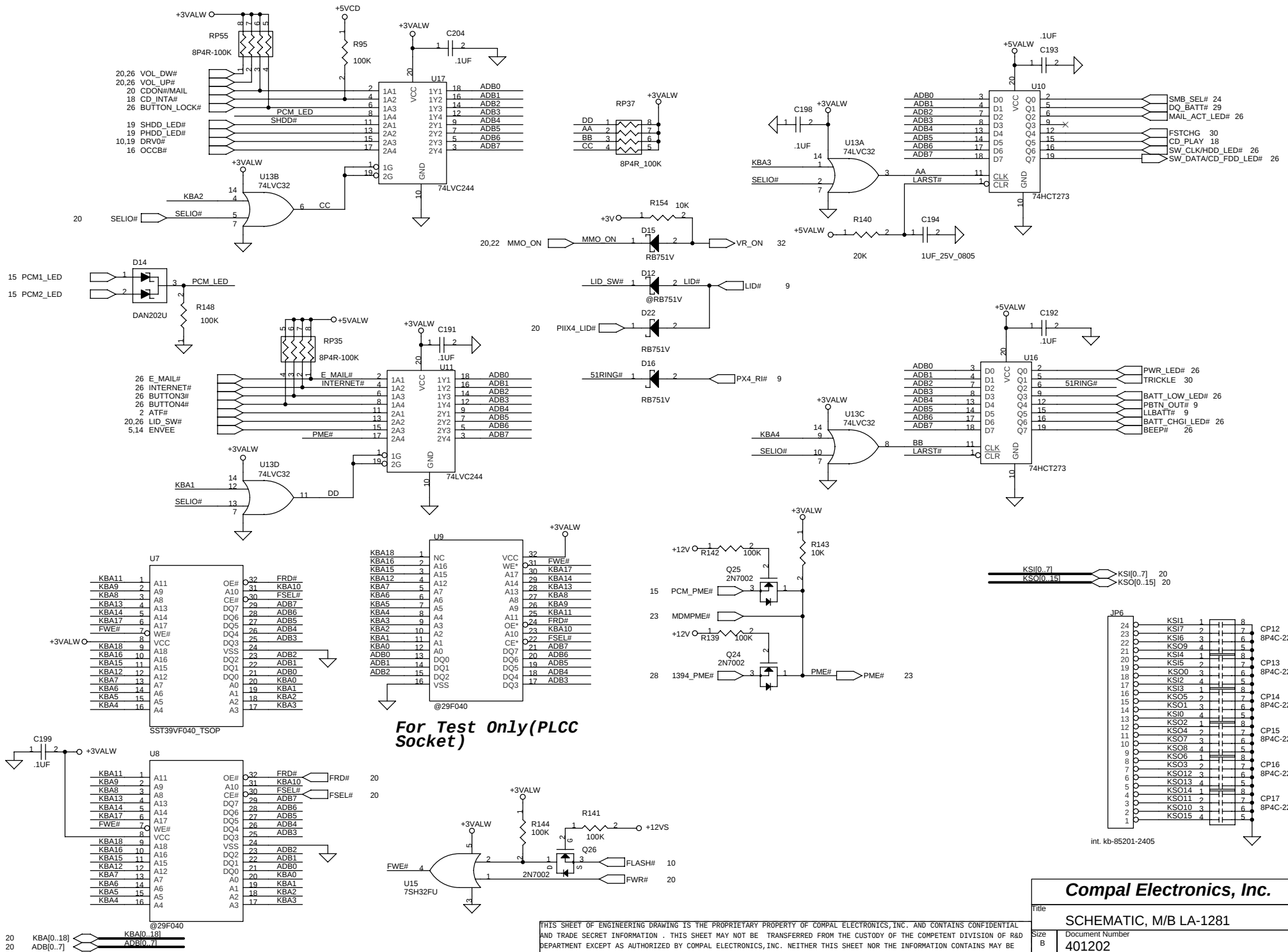


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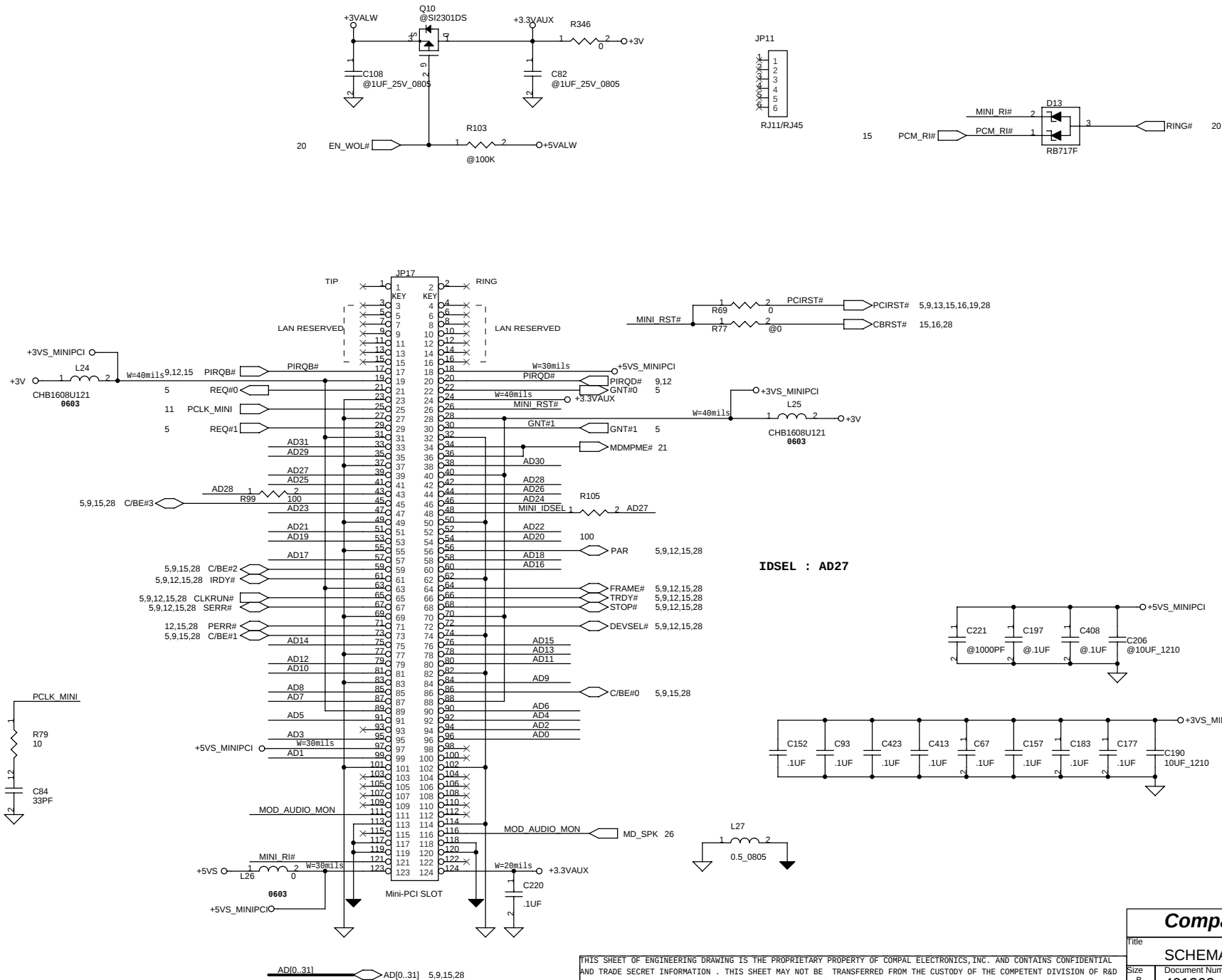
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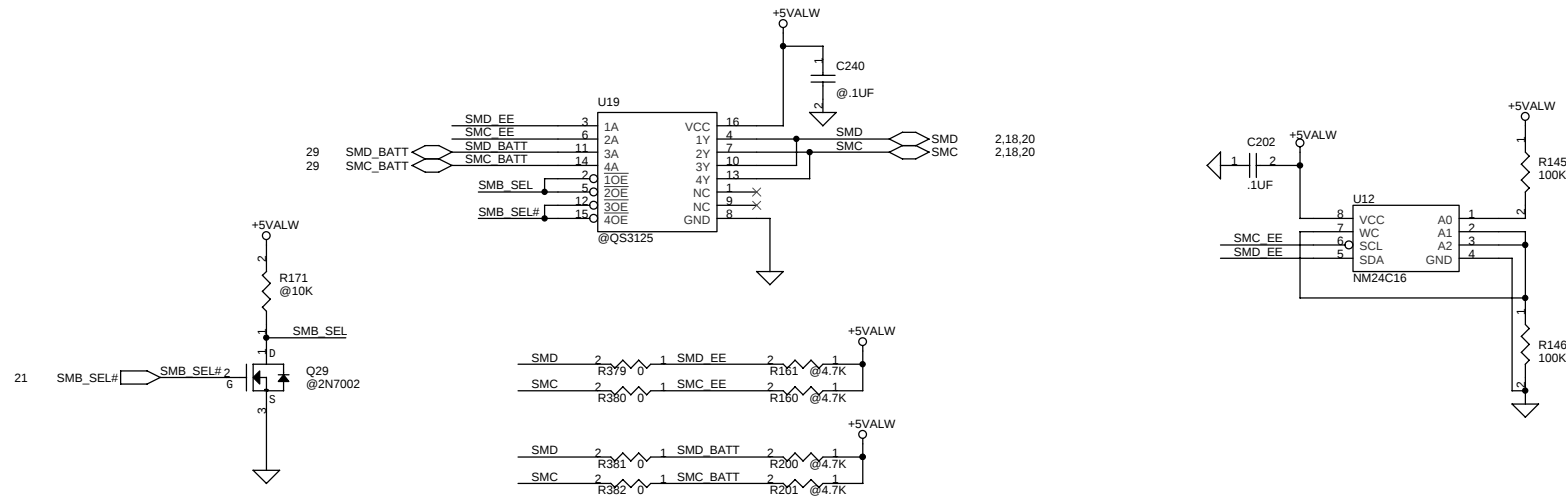
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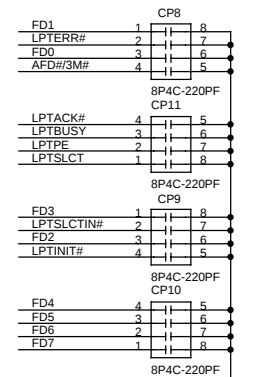
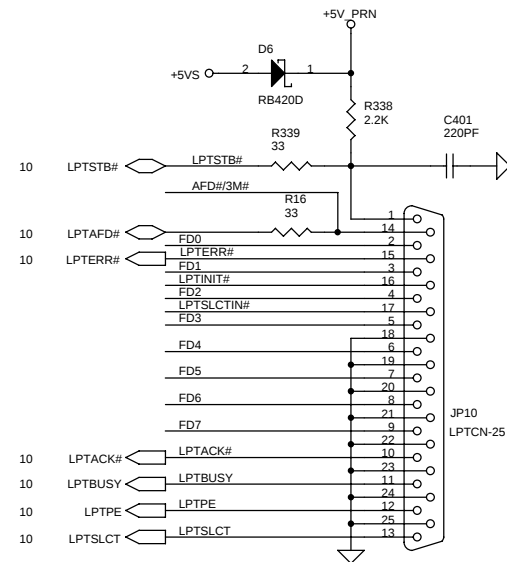
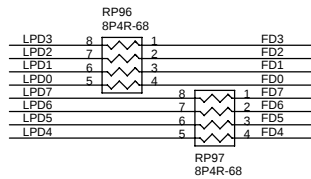
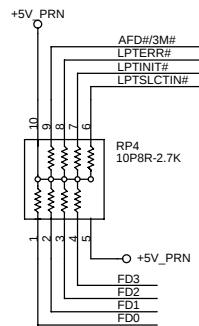
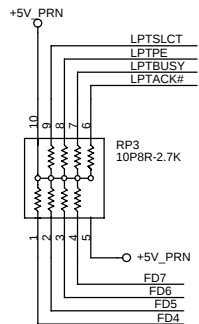
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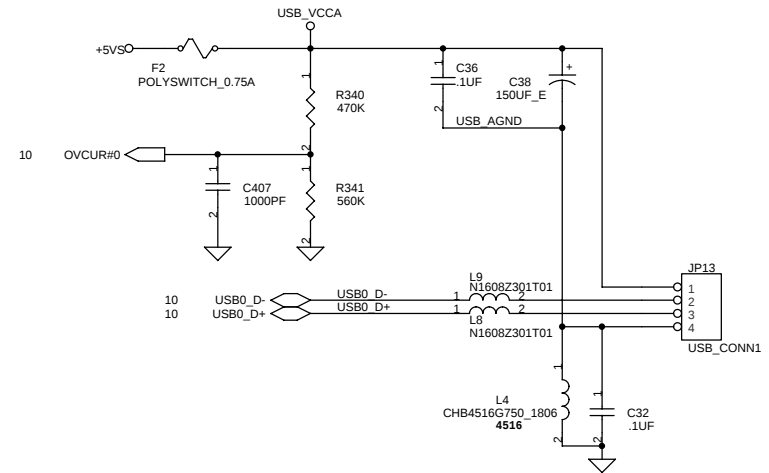
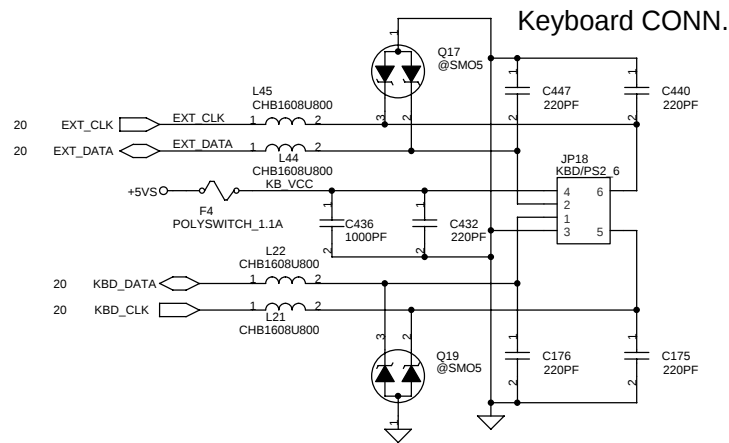
PARALLEL PORT



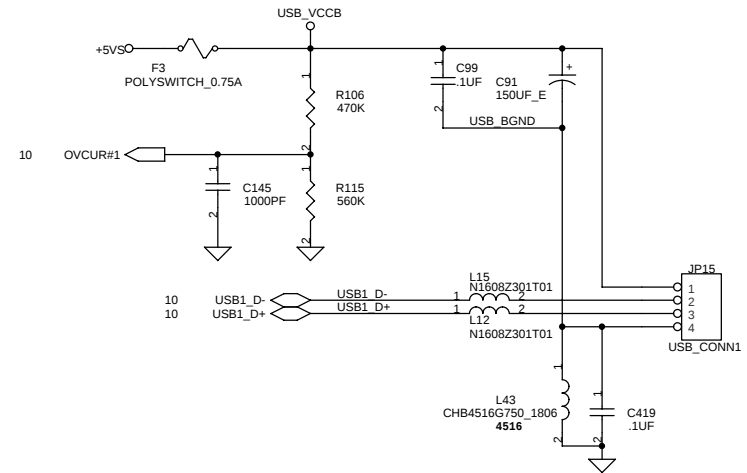
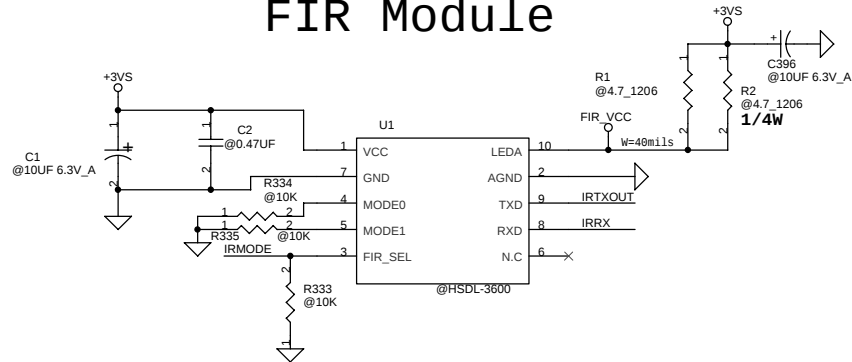
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FIR Module

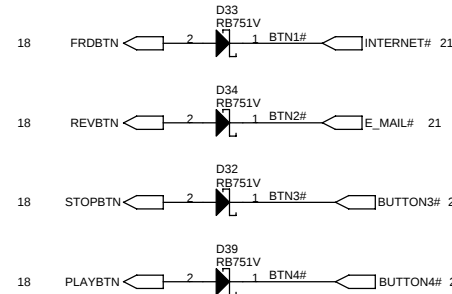
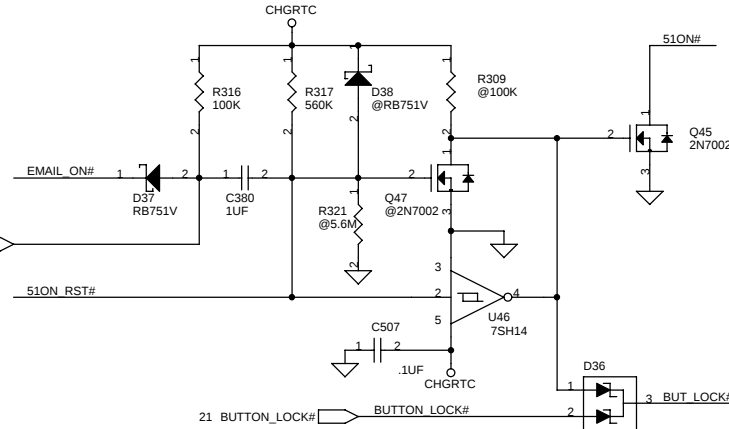
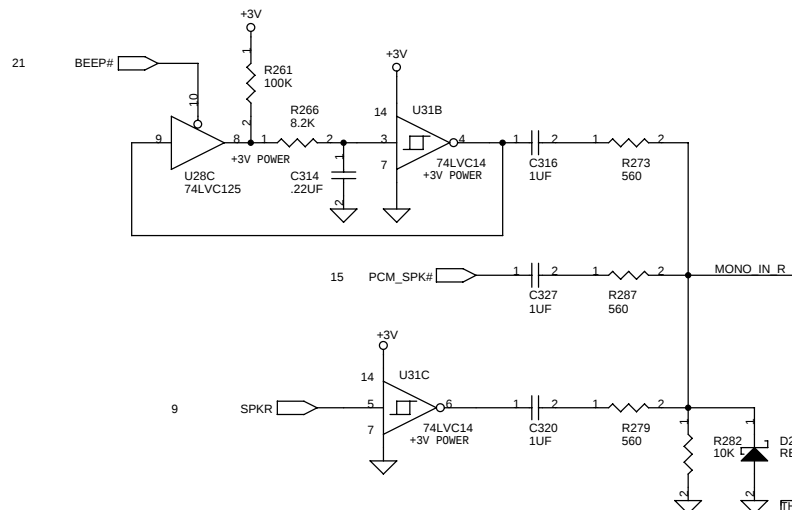
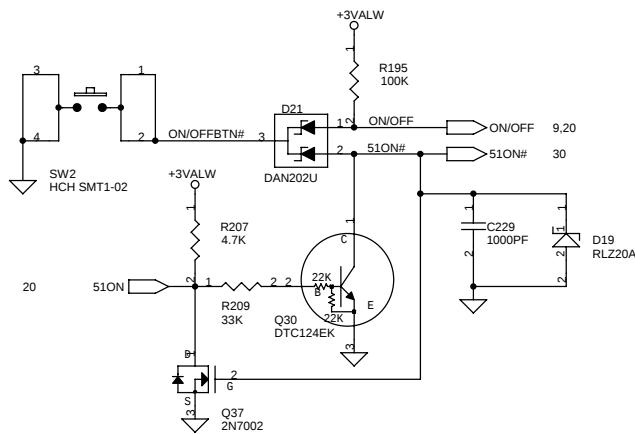
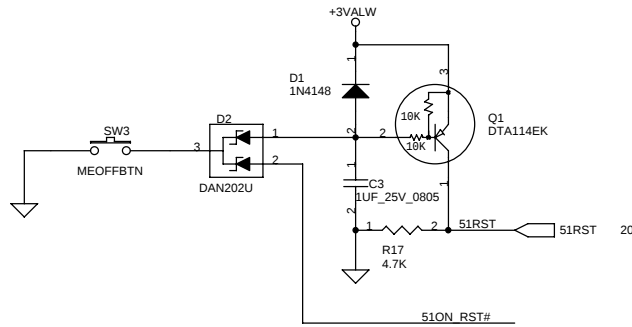


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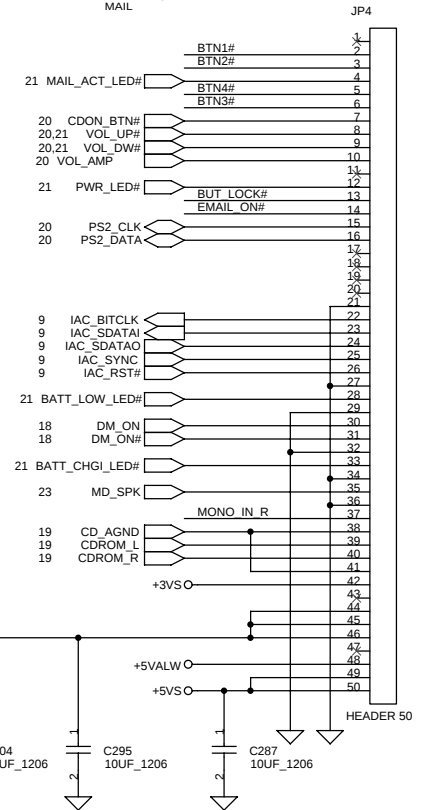
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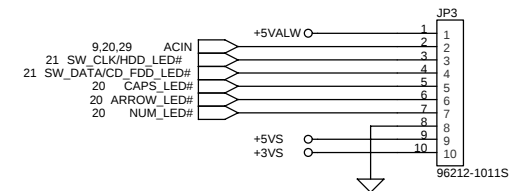
Reset Button



CDON_BTN# is Low, enable CD_PLAY
MAIL is High, enable
MAIL



System Window Connector



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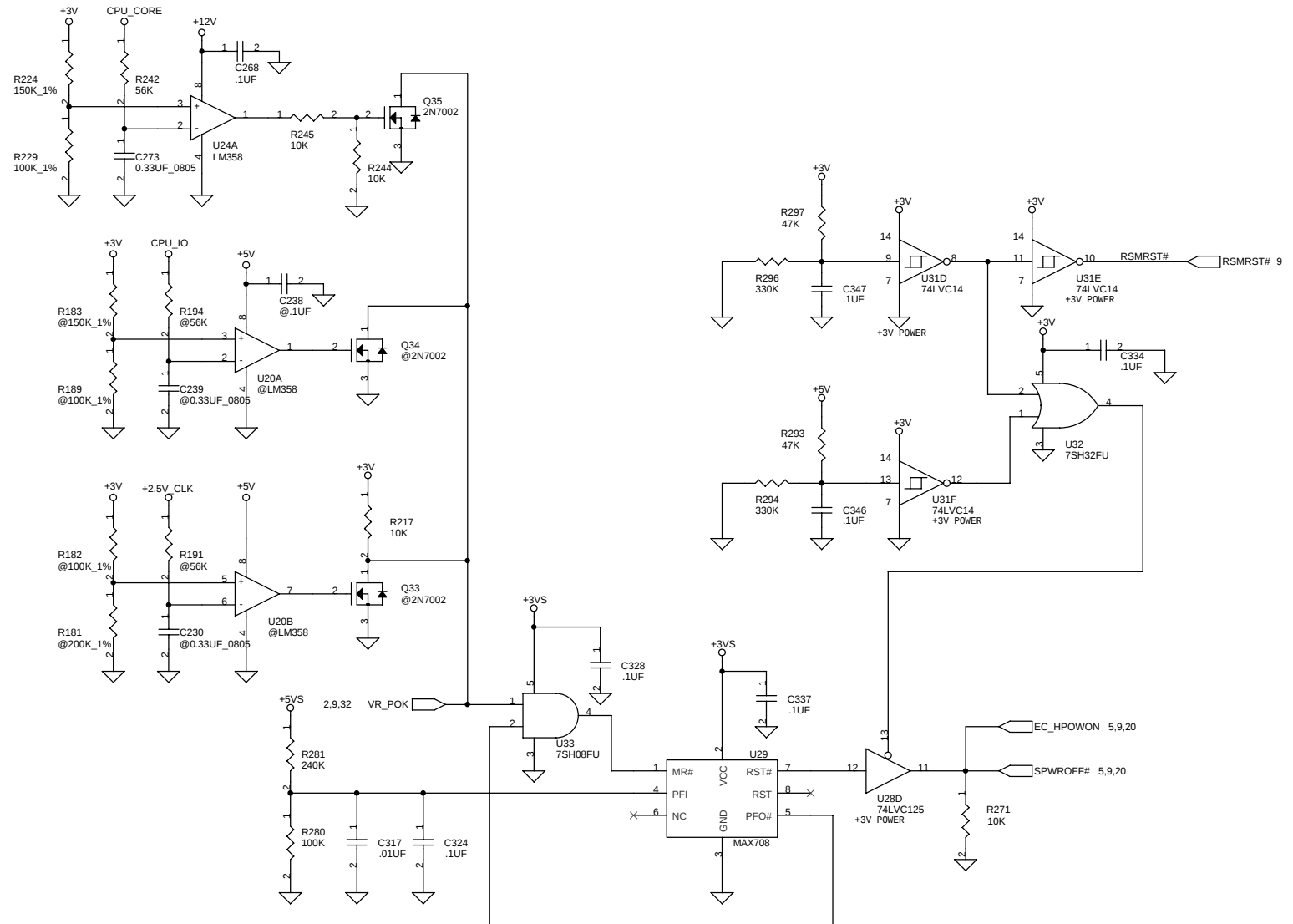
Pin 2: +12VALWP → J2 → JOPEN+12V → **Pin 1:** +12VALW (120mA)

Pin 1: +5VALWP → J1 → PAD-OPEN 4x4m → **Pin 2:** +5VALW (5A)

Pin 1: +3VALWP → J3 → PAD-OPEN 4x4m → **Pin 2:** +3VALW (4A)

Pin 2: +CPU_IOP → J5 → 3MMA/CPU_IO → **Pin 1:** CPU_IO (2.7A)

Pin 2: +2.5VSP → J6 → 3MMA+2.5VS → **Pin 1:** +2.5VS (2A)



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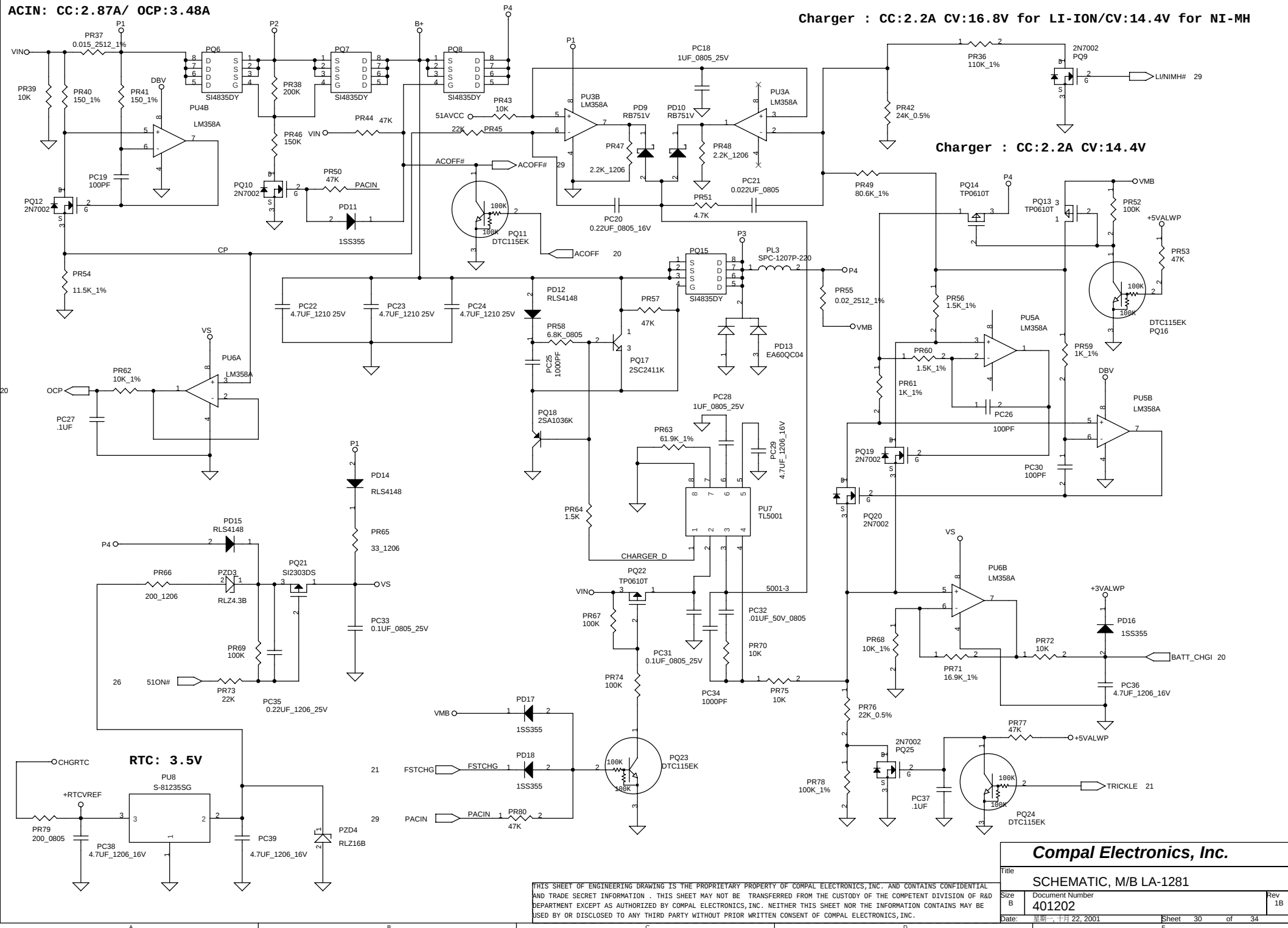
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ACIN: CC:2.87A/ OCP:3.48A

Charger : CC:2.2A CV:16.8V for LI-ION/CV:14.4V for NI-MH

Charger : CC:2.2A CV:14.4V

RTC: 3.5V

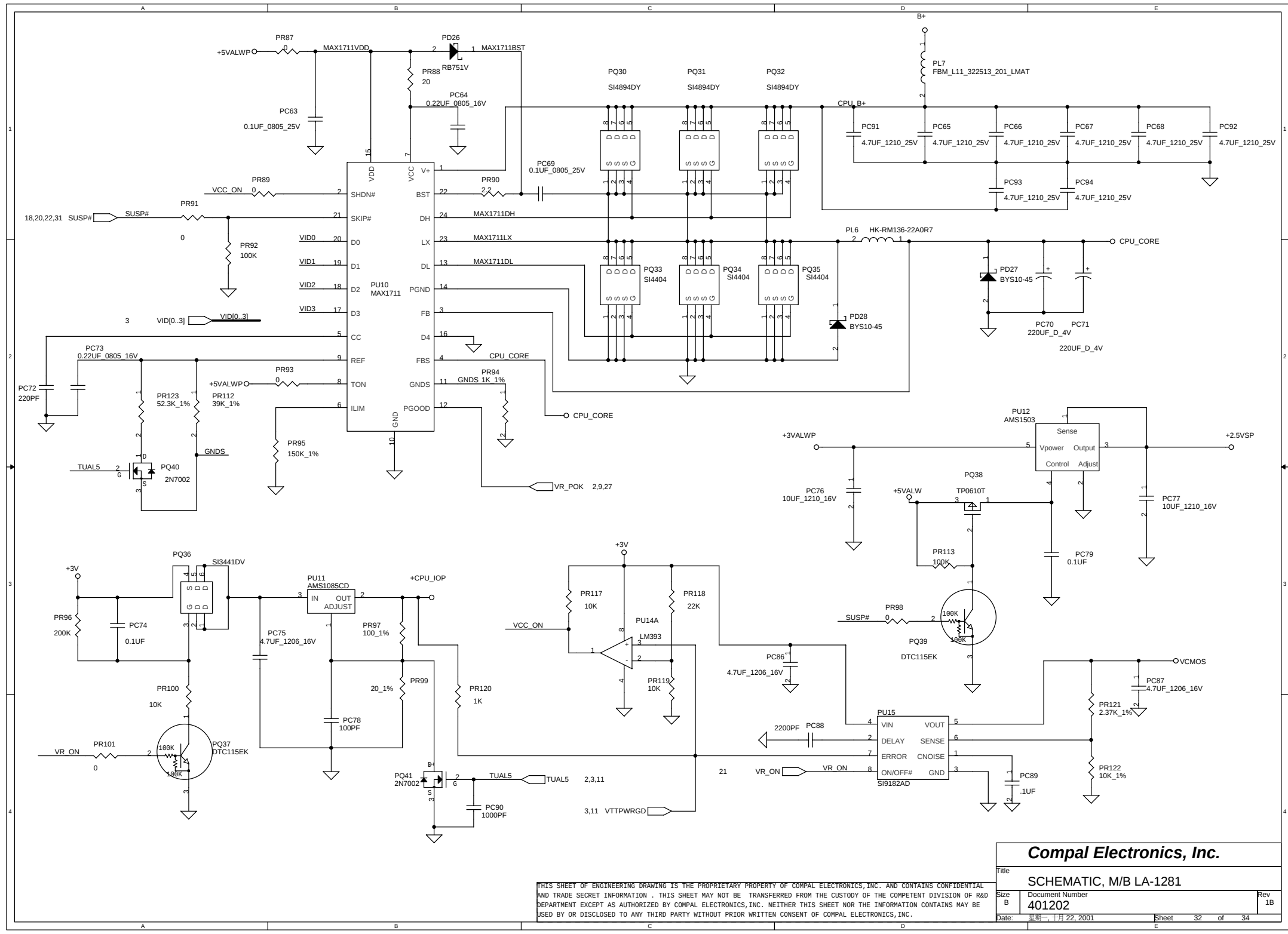


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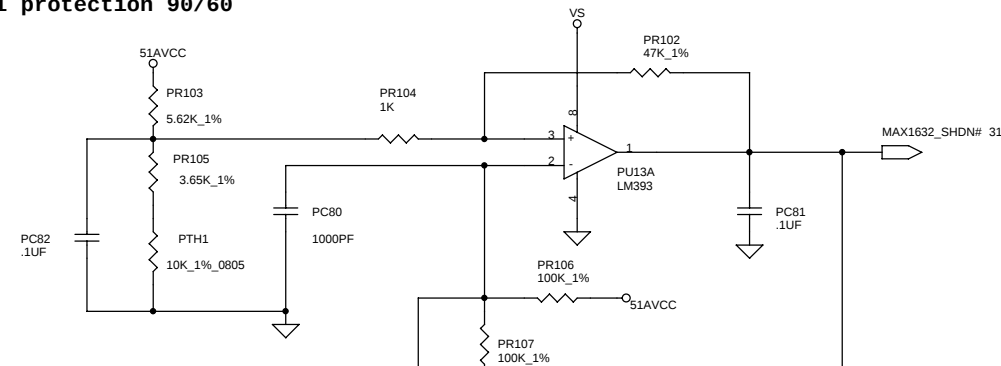
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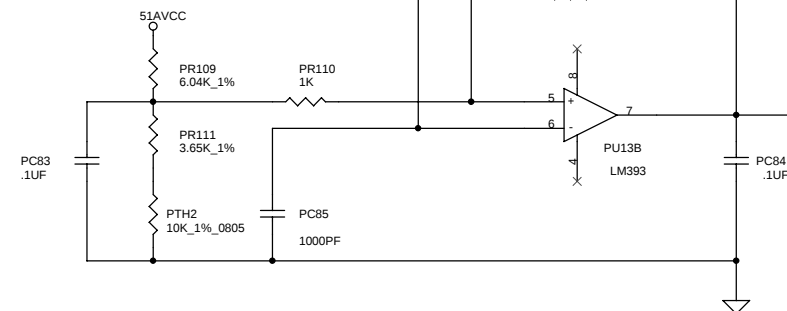
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For CPU thermal protection 90/60
度C



For Battery thermal protection 83/54
度C



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CY23 PIR LIST

08/28/01 Written by Jerry

P11: Add Q61 and R407 for VTTPWRGD

***** Rev1.0 PIR List *****

09/10/01 Written by Jerry

P26: Add @ at R309 and Q47

 Add C507 and U46

 Change value of R317 to 510K

P27: Remove @ at R224, R229, R242, R245, R244, C273, and Q35

P32: Add PC91, PC92, PC93, and PC94.

09/12/01 Written by Jerry

P06: Change value of RP34, RP50, RP54, RP62, RP38, RP33, RP57,

RP61, RP53, and RP56 to 16P8R-22

P26: Change value of R317 to 560K

09/19/01 Written by Jerry

P29: Change value of PR29 to 174K_1%

P32: Change value of PR123 to 52.3K_1%

***** Rev2.0 PIR List *****

10/03/01 Written by Jerry

P02: Change value of R27 to 180

 Delete R395

 R395's Pin 1 tie to RP98's pin5

P03: Change value of R353 to 56.2 1%

P28: Add R408 between +3V and pin 87 of U3

10/08/01 Written by Jerry

P11, P13, P28: Change footprint of Y1, Y2, and Y3

P23: Change footprint of JP11

P28: Change footprint of JP12

P31: Change footprint of PL5

P26: Change footprint of SW1

10/17/01 Written by Jerry

P02: Add @ at RP25, RP24, RP28, RP27, RP31, RP30, RP 42, RP41,

RP40, RP43, RP44, RP45, RP46, RP47, RP48, RP49, RP7, RP1, RP6,

RP14, RP20, RP19, RP22, RP9, RP10, RP11, RP8, RP2, and R7

P06: Remove @ at R164

P11: Add @ at R176

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